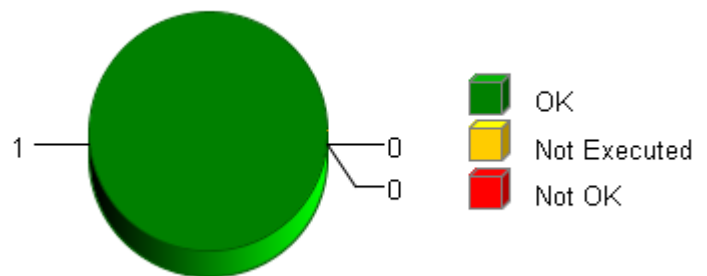


Summary

Total Test Objects: 1
Successful: 1
Failed: 0
Not Executed: 0
Date: 2015-03-11
Time: 17:42:13+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Collection "CBD_UnitTest"

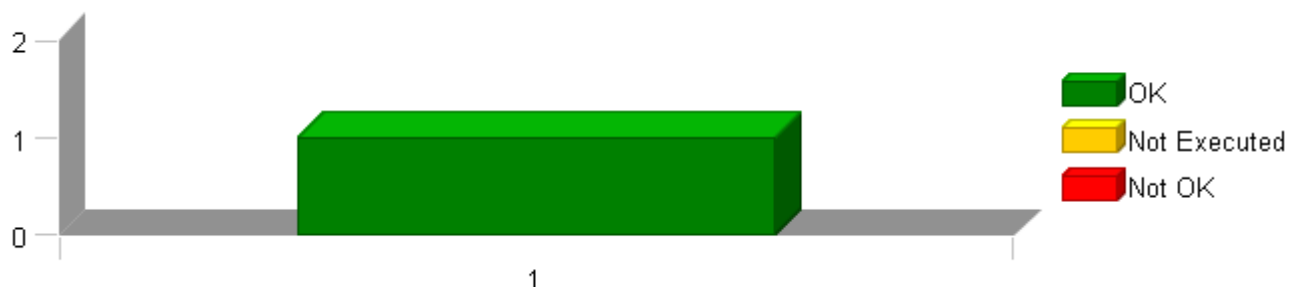
Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

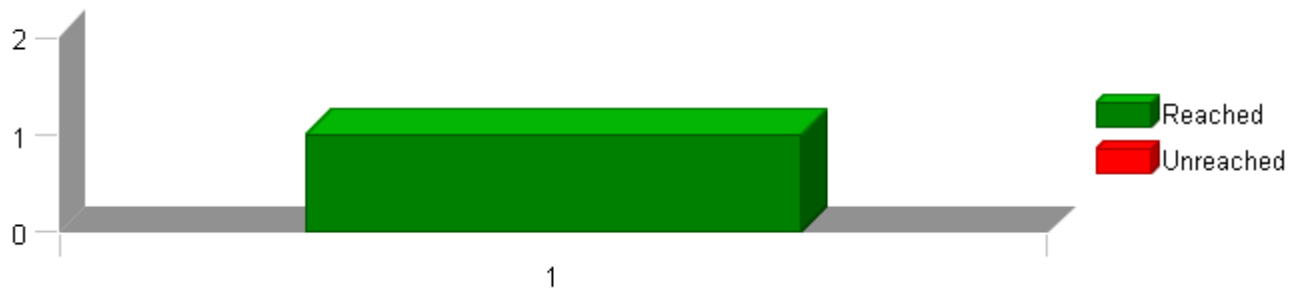
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)



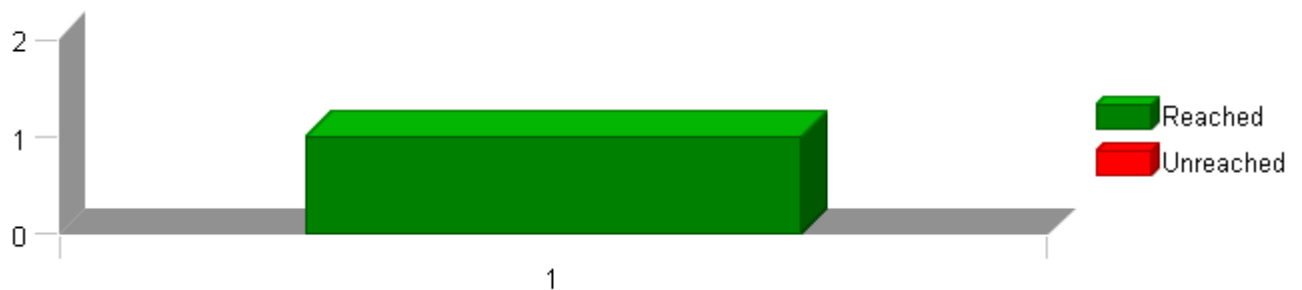
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

| No. | Name | C0 | C1 | Test Cases | Result |
|-----|--------------------------------|-------|-------|---------------|--------|
| | TmprlMon2 | 100 % | 100 % | 1 of 1 passed | ✓ |
| | CBD_UnitTest | 100 % | 100 % | 1 of 1 passed | ✓ |
| | TmprlMon2 | 100 % | 100 % | 1 of 1 passed | ✓ |
| 1 | TmprlMon2_Per1 | 100 % | 100 % | 1 of 1 passed | ✓ |

TEST DETAILS REPORT



| | |
|-------------|----------------|
| Project | TmprlMon2 |
| Module | TmprlMon2 |
| Test Object | TmprlMon2_Per1 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 1 |
| Successful | 1 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\FORD_TmprlMon2 |
| Configuration File | D:\Synergy_Work_Area\FORD_TmprlMon2\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\TmpriMon2\src\Sa_TmprlMon2.c |
| Compiler Options | -Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\TmpriMon2\utp\contract -I\$(PROJECTROOT)\TmpriMon2\utp\contract\Sa_TmprlMon2 -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|--------------------|--|
| Module 'TmpriMon2' | *****UNIT TEST DESCRIPTION***** Name of Tester:Surbhi Goel Code File(s) Under Test:Sa_TmprlMon2.c Code File(s) Version:1 Module Design Document:Temporal_Monitor_2_MDD Module Design Document Version:1 Data Dictionary Version:8 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.31 Total FLASH Used (Bytes):18 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements: Test Date:3/11/2015 Comments:"Note1:""CBD_Sandbox_dbg.map"" map file is embedded for reference. " ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 |
| Time Unit | Cycles |
| Timer Enabled | false |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\FORD_TmprlMon2\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TS1.1 583 Cycles

Description Vector Description:
TS1.1 Call Trace checked

Test Step 1.1 (Repeat Count = 1)

| T | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_TmprlMon2_Per1_CP0_CheckpointReached | 1 | Rte_Call_TmprlMon2_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Sa_TmprlMon2_WdMonitor_OP_SET | 1 | Rte_Call_Sa_TmprlMon2_WdMonitor_OP_SET | 1 | ✓ |
| Rte_Call_TmprlMon2_Per1_CP1_CheckpointReached | 1 | Rte_Call_TmprlMon2_Per1_CP1_CheckpointReached | 1 | ✓ |