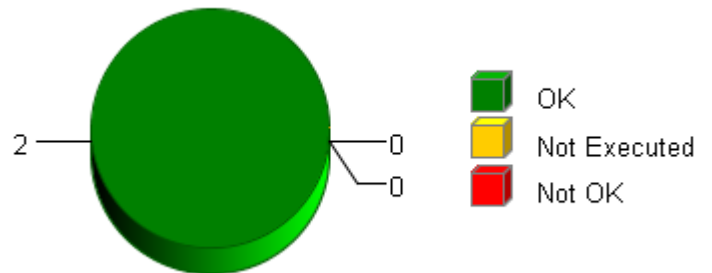


Summary

Total Test Objects: 2
Successful: 2
Failed: 0
Not Executed: 0
Date: 2018-01-29
Time: 17:02:01+0530

Overall Test Object Results (including Coverage)



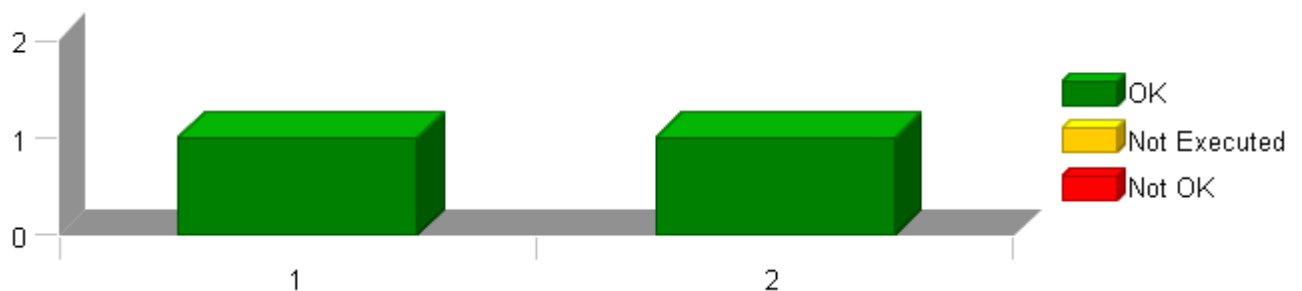
Selected Project Items

Test Collection "CBD_UnitTest"

Used Test Environments

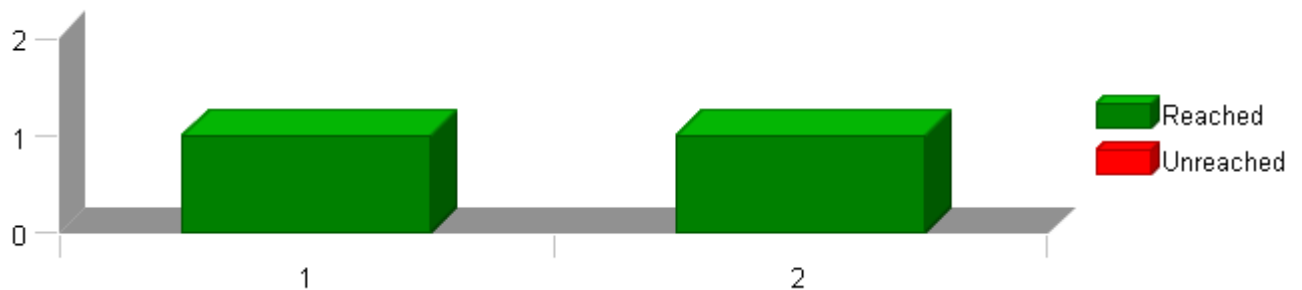
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



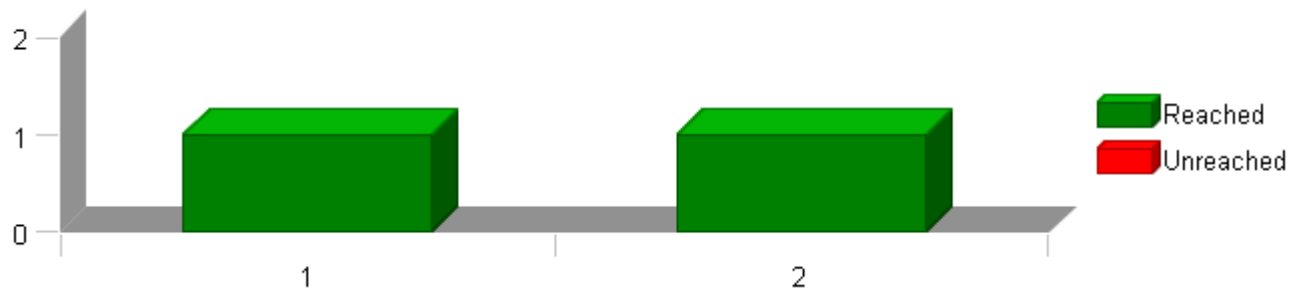
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

TEST OVERVIEW REPORT

2018-01-29, 17:02:01+0530

Project CMP_04.03.00_CDDInterface_11



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	Test Cases	Result
	CMP_04.03.00_CDDInterface_11	100 %	100 %	2 of 2 passed	✓
	CBD_UnitTest	100 %	100 %	2 of 2 passed	✓
	CDDInterface	100 %	100 %	2 of 2 passed	✓
1	CDDInterface11_Init1	100 %	100 %	1 of 1 passed	✓
2	CDDInterface11_Per1	100 %	100 %	1 of 1 passed	✓

TEST DETAILS REPORT

2018-01-29, 17:01:38+0530

CDDInterface11_Per1



Project	CMP_04.03.00_CDDInterface_11
Module	CDDInterface
Test Object	CDDInterface11_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11
Configuration File	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\CDDInterface\src\Sa_CDDInterface11.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract\Sa_CDDInterface11 -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include\TMS570_4_9_1\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Description***** Name of Tester: Kiran Thorat Code File(s) Under Test:Sa_CDDInterface11.c Code File(s) Version: 2 Module Design Document:CDDInterface11_MDD.doc Module Design Document Version:1 Data Dictionary Version: 6 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version:Nexteer EPS Unit Test Tool 1.0 Total FLASH Used (Bytes):52 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements:NA Test Date:1/29/2018 Comments: "NOTE1: Inline macros defined in "Global Macro.h" are not unit tested. NOTE2: "CBD_Sandbox_dbg.map" map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

TEST DETAILS REPORT

2018-01-29, 17:01:38+0530

CDDInterface11_Per1



Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

2018-01-29, 17:01:38+0530

CDDInterface11_Per1



Test Case 1: Range Test

Specification Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS1.1 25.00 Cycles
TS1.2 25.00 Cycles
TS1.3 25.00 Cycles

Description Vector Description:

TS1.1CDD_DesiredTuningSet_Cnt_G_u08=Min
TS1.2CDD_DesiredTuningSet_Cnt_G_u08=Max
TS1.3CDD_DesiredTuningSet_Cnt_G_u08=Mid

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
CDD_DesiredTuningSet_Cnt_G_u08	0		
Rte_Inst_Sa_CDDInterface11	target_Rte_Inst_Sa_CDDInterface11		
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Per1_DesiredTunSet_Cnt_u16	target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16		
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16.value	0	0	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
CDD_DesiredTuningSet_Cnt_G_u08	7		
Rte_Inst_Sa_CDDInterface11	target_Rte_Inst_Sa_CDDInterface11		
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Per1_DesiredTunSet_Cnt_u16	target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16		
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16.value	7	7	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.3 (Repeat Count = 1)

Name	Input Value		
CDD_DesiredTuningSet_Cnt_G_u08	5		
Rte_Inst_Sa_CDDInterface11	target_Rte_Inst_Sa_CDDInterface11		
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Per1_DesiredTunSet_Cnt_u16	target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16		
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Per1_DesiredTunSet_Cnt_u16.value	5	5	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

TEST DETAILS REPORT

2018-01-29, 17:01:17+0530



CDDInterface11_Init1

Project	CMP_04.03.00_CDDInterface_11
Module	CDDInterface
Test Object	CDDInterface11_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11
Configuration File	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\CDDInterface\src\Sa_CDDInterface11.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\CDDInterface\utp\contract\Sa_CDDInterface11 -I\$(PROJECTROOT)\CDDInterface\utp\contract -I\$(PROJECTROOT)\NtrlLib\include -I\$(PROJECTROOT)\StdDef\include\TMS570_4_9_1\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'CDDInterface'	*****Unit Test Description***** Name of Tester: Kiran Thorat Code File(s) Under Test:Sa_CDDInterface11.c Code File(s) Version: 2 Module Design Document:CDDInterface11_MDD.doc Module Design Document Version:1 Data Dictionary Version: 6 Unit Test Plan Version:2 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version:Nexteer EPS Unit Test Tool 1.0 Total FLASH Used (Bytes):52 Total RAM Used (Bytes):0 Total CALS Used (Bytes):0 Special Test Requirements:NA Test Date:1/29/2018 Comments: "NOTE1: Inline macros defined in "Global Macro.h" are not unit tested. NOTE2: "CBD_Sandbox_dbg.map" map file is embedded for reference." *****

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

TEST DETAILS REPORT

2018-01-29, 17:01:17+0530

CDDInterface11_Init1



Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\PSA_CMP_04.03.00_CDDInterface_11\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

2018-01-29, 17:01:17+0530

CDDInterface11_Init1



Test Case 1: Range Test

Specification Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS1.1 25.00 Cycles
TS1.2 25.00 Cycles
TS1.3 25.00 Cycles

Description Vector Description:

TS1.1CDD_DesiredTuningSet_Cnt_G_u08=Min
TS1.2CDD_DesiredTuningSet_Cnt_G_u08=Max
TS1.3CDD_DesiredTuningSet_Cnt_G_u08=Mid

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
CDD_DesiredTuningSet_Cnt_G_u08	0		
Rte_Inst_Sa_CDDInterface11	target_Rte_Inst_Sa_CDDInterface11		
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Init1_DesiredTunSet_Cnt_u1	target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16		
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16.value	0	0	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name		Input Value	
CDD_DesiredTuningSet_Cnt_G_u08		7	
Rte_Inst_Sa_CDDInterface11		target_Rte_Inst_Sa_CDDInterface11	
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Init1_DesiredTunSet_Cnt_u1		target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16	
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16.value	7	7	✔

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.3 (Repeat Count = 1)

Name		Input Value	
CDD_DesiredTuningSet_Cnt_G_u08		4	
Rte_Inst_Sa_CDDInterface11		target_Rte_Inst_Sa_CDDInterface11	
target_Rte_Inst_Sa_CDDInterface11.CDDInterface11_Init1_DesiredTunSet_Cnt_u1		target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16	
Name	Actual Value	Expected Value	Result
target_CDDInterface11_Init1_DesiredTunSet_Cnt_u16.value	4	4	✓

Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓