Summary

Total Test Objects:	5
Successful:	5
Failed:	0
Not Executed:	0
Date:	2014-09-05
Time:	17:35:09+0530

Overall Test Object Results (including Coverage)

- OK
- Not Executed
- Not OK
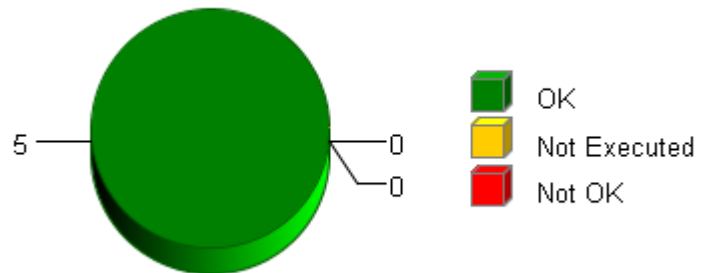
Selected Project Items

Test Object "CBD_UnitTest/AstLmt/AstLmt_Init"
Test Object "CBD_UnitTest/AstLmt/AstLmt_Per1"
Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_GetSteeringAssistDefeat"
Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_ManualTrqCmd"
Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_SetSteeringAssistDefeat"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

Check Interface:	No
Generate Driver:	Yes
Execute Test:	Yes
Create New Test Run:	No
Instrumentation:	Test Object Only
Coverage:	Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)
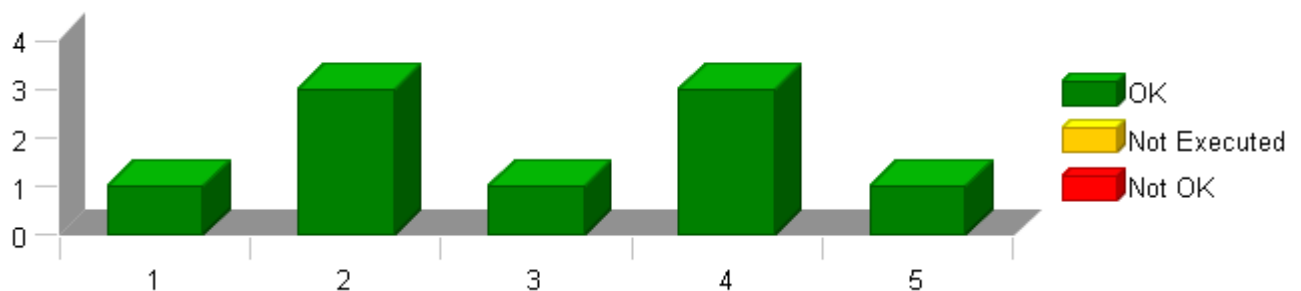
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

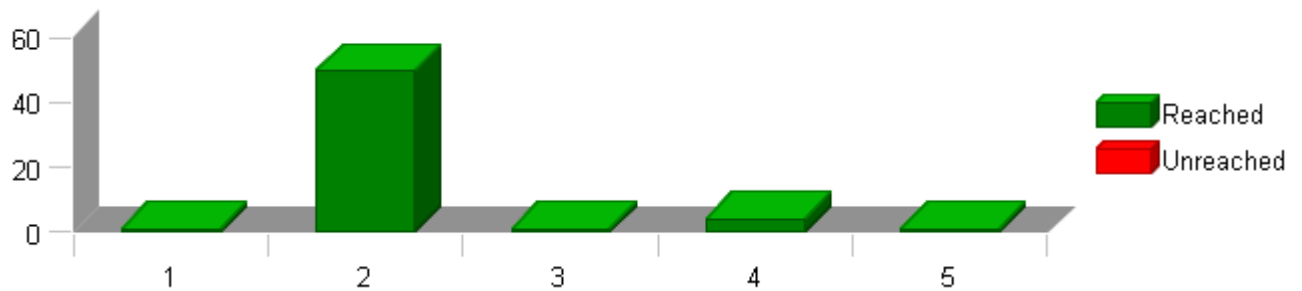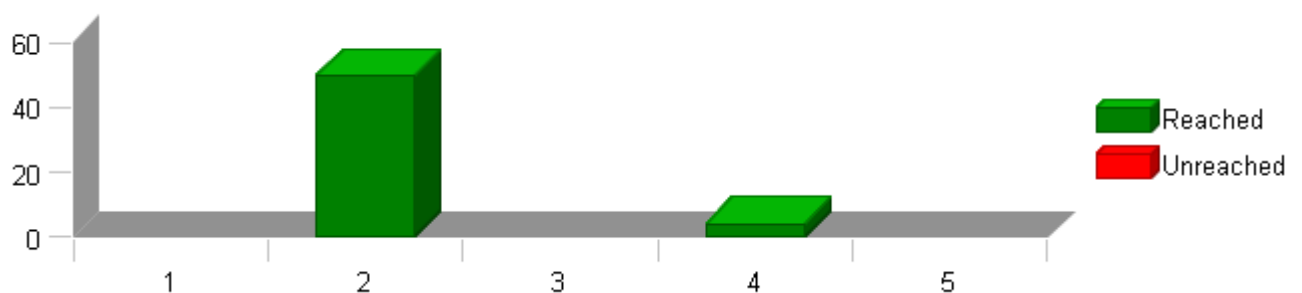## Statement (C0) Coverage: Total Statements for Each Test Object
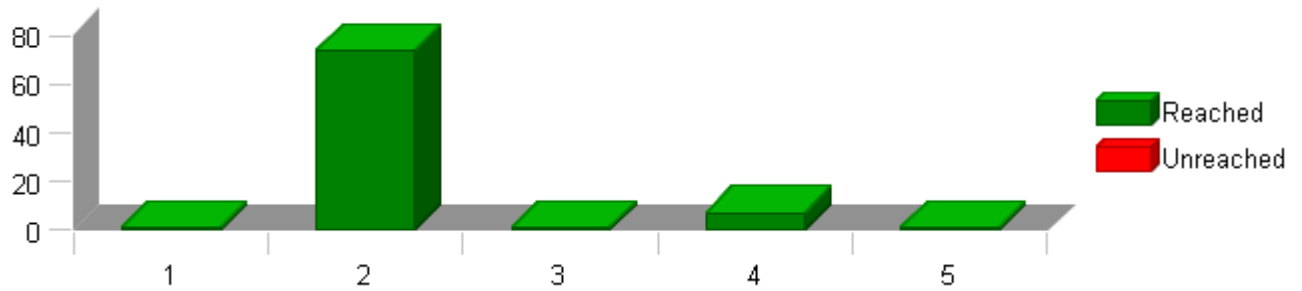
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.
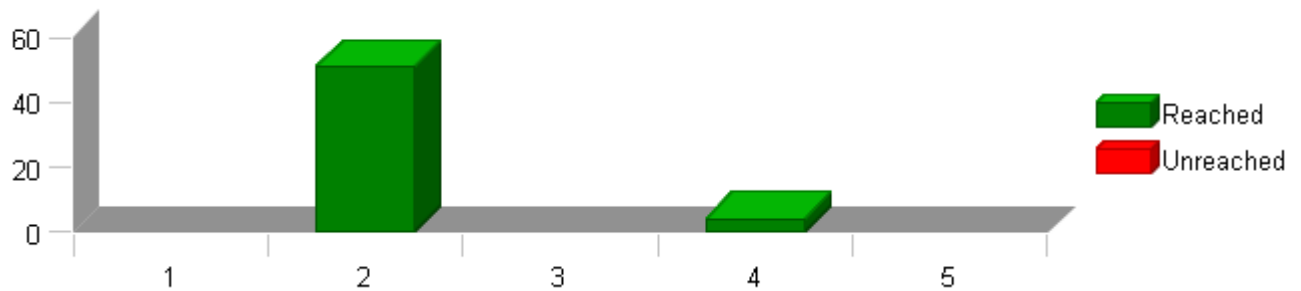
Decision Coverage: Total Decision Outcomes for Each Test Object

The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

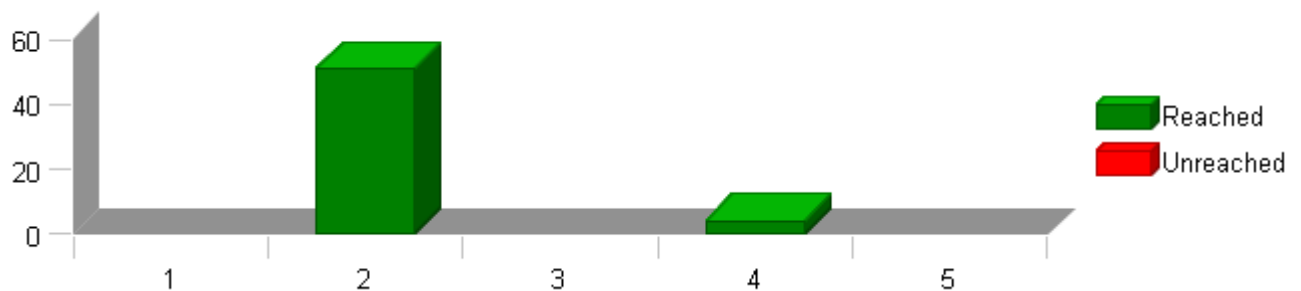Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	AstLmt_CM	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	✔
	AstLmt	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	✔
1	AstLmt_Init	100 %	100 %	-	-	-	1 of 1 passed	✔
2	AstLmt_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
3	AstLmt_Scom_GetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	✔
4	AstLmt_Scom_ManualTrqCmd	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
5	AstLmt_Scom_SetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	✔

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Scom_SetSteeringAssistDefeat

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\AstLmt_CM\utp\contract -I$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification	
Name	**Text**
Module 'AstLmt'	*********************************UNIT TEST INFORMATION*** Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." ***

Attributes	
Name	**Value**
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes	
Name	**Value**
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test ✔

Specification

```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

TS1.1    241.00 Cycles
TS1.2    241.00 Cycles
```

Description

Vector Description:

TS1.1 SteeringAsstDefeat1_Cnt_lgc = True
TS1.2 SteeringAsstDefeat1_Cnt_lgc = False

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	1		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		

Name	Actual Value	Expected Value	Result
target_Pim_SteerAsstDefeat	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	0		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		

Name	Actual Value	Expected Value	Result
target_Pim_SteerAsstDefeat	0	0	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	✔

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\AstLmt_CM\utp\contract -I$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification	
Name	**Text**
Module 'AstLmt'	**********************************UNIT TEST INFORMATION** Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." ***

Attributes	
Name	**Value**
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

Attributes	
Name	**Value**
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Matrix Test ✔

Specification	Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

TS1.1 1848.00 cycles   Shortest Execution Path
TS1.2 1948.00 cycles   Longest Execution Path

Description   Vector Description:

TS1.1:"Shortest execution Path:
(PosServCCDisable_Cnt_T_lgc = True) = False
(LrnPnCtrCCDisable_Cnt_T_lgc = True = False
((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON)) =True
(DefeatLimitService_Cnt_T_lgc = True)=True"
TS1.2"Longest Execution Path:
(PosServCCDisable_Cnt_T_lgc = True)=True
(LrnPnCtrCCDisable_Cnt_T_lgc = True)=True
((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
(LrnPnCtrEnable_Cnt_T_lgc = True)=False
(AstLmt_SteeringAsstDefeat_Cnt_M_lgc = True)=False
(DefeatLimitService_Cnt_T_lgc = True)=False"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 1.2 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

Name	Input Value
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.73999977	5.73999977 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Case 2: Boundary Test

Test Case 2: Boundary Test

Specification   Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

```
TS2.1 1985.00 cycles
TS2.2 1879.00 cycles
TS2.3 1821.00 cycles
TS2.4 1816.00 cycles
TS2.5 1747.00 cycles
TS2.6 1777.00 cycles
TS2.7 1747.00 cycles
TS2.8 1723.00 cycles
TS2.9 1747.00 cycles
TS2.10 1723.00 cycles
TS2.11 1747.00 cycles
TS2.12 1723.00 cycles
TS2.13 1747.00 cycles
TS2.14 1723.00 cycles
TS2.15 1747.00 cycles
TS2.16 1723.00 cycles
TS2.17 1723.00 cycles
TS2.18 1747.00 cycles
TS2.19 1723.00 cycles
TS2.20 1747.00 cycles
TS2.21 1723.00 cycles
TS2.22 1747.00 cycles
TS2.23 1723.00 cycles
TS2.24 1747.00 cycles
TS2.25 1723.00 cycles
TS2.26 1747.00 cycles
TS2.27 1723.00 cycles
TS2.28 1747.00 cycles
TS2.29 1723.00 cycles
TS2.30 1760.00 cycles
TS2.31 1781.00 cycles
TS2.32 1742.00 cycles
TS2.33 1742.00 cycles
TS2.34 1742.00 cycles
TS2.35 1742.00 cycles
TS2.36 1742.00 cycles
TS2.37 1742.00 cycles
TS2.38 1742.00 cycles
TS2.39 1742.00 cycles
TS2.40 1742.00 cycles
TS2.41 1742.00 cycles
TS2.42 1742.00 cycles
TS2.43 1758.00 cycles
TS2.44 1758.00 cycles
TS2.45 1760.00 cycles
TS2.46 1767.00 cycles
TS2.47 1742.00 cycles
TS2.48 1742.00 cycles
TS2.49 1742.00 cycles
TS2.50 1742.00 cycles
TS2.51 1742.00 cycles
TS2.52 1742.00 cycles
TS2.53 1742.00 cycles
TS2.54 1758.00 cycles
TS2.55 1742.00 cycles
TS2.56 1758.00 cycles
TS2.57 1742.00 cycles
TS2.58 1758.00 cycles
TS2.59 1742.00 cycles
TS2.60 1758.00 cycles
TS2.61 1723.00 cycles
TS2.62 1758.00 cycles
TS2.63 1723.00 cycles
TS2.64 1747.00 cycles
TS2.65 1723.00 cycles
TS2.66 1767.00 cycles
TS2.67 1723.00 cycles
TS2.68 1767.00 cycles
TS2.69 1723.00 cycles
TS2.70 1729.00 cycles
TS2.71 1723.00 cycles
TS2.72 1729.00 cycles
TS2.73 1723.00 cycles
TS2.74 1729.00 cycles
TS2.75 1723.00 cycles
TS2.76 1729.00 cycles
TS2.77 1723.00 cycles
TS2.78 1749.00 cycles
TS2.79 1723.00 cycles
TS2.80 1729.00 cycles
TS2.81 1758.00 cycles
TS2.82 1723.00 cycles
TS2.83 1729.00 cycles
TS2.84 1758.00 cycles
TS2.85 1767.00 cycles
TS2.86 1757.00 cycles
TS2.87 1829.00 cycles
TS2.88 1719.00 cycles
TS2.89 1712.00 cycles
TS2.90 1728.00 cycles
TS2.91 1714.00 cycles
TS2.92 1729.00 cycles
TS2.93 1719.00 cycles
TS2.94 1712.00 cycles
TS2.95 1731.00 cycles
TS2.96 1731.00 cycles
TS2.97 1729.00 cycles
TS2.98 1790.00 cycles
TS2.99 1758.00 cycles
```

Description   Vector Description :

TS2.1AssistCmd_MtrNm_f32 = Min
TS2.2AssistCmd_MtrNm_f32 = Max
TS2.3AssistCmd_MtrNm_f32 = Mid
TS2.4AssistCmd_MtrNm_f32 = Pos
TS2.5AssistCmd_MtrNm_f32 = Neg
TS2.6AssistEOTDamping_MtrNm_f32 = Min
TS2.7AssistEOTDamping_MtrNm_f32 = Max
TS2.8AssistEOTDamping_MtrNm_f32 = Mid
TS2.9AssistEOTDamping_MtrNm_f32 = Pos
TS2.10AssistEOTDamping_MtrNm_f32 = Neg
TS2.11AssistEOTGain_Uls_f32 = Min
TS2.12AssistEOTGain_Uls_f32 = Max
TS2.13AssistEOTGain_Uls_f32 = Mid
TS2.14AssistEOTLimit_MtrNm_f32 = Min
TS2.15AssistEOTLimit_MtrNm_f32 = Max
TS2.16AssistEOTLimit_MtrNm_f32 = Mid
TS2.17AssistStallLimit_MtrNm_f32 = Min
TS2.18AssistStallLimit_MtrNm_f32 = Max
TS2.19AssistStallLimit_MtrNm_f32 = Mid
TS2.20AssistVehSpdLimit_MtrNm_f32 = Min
TS2.21AssistVehSpdLimit_MtrNm_f32 = Max
TS2.22AssistVehSpdLimit_MtrNm_f32 = Mid
TS2.23CombinedDamping_MtrNm_f32 = Min
TS2.24CombinedDamping_MtrNm_f32 = Max
TS2.25CombinedDamping_MtrNm_f32 = Mid
TS2.26CombinedDamping_MtrNm_f32 = Pos
TS2.27CombinedDamping_MtrNm_f32 = Neg
TS2.28DefeatLimitService_Cnt_lgc = Min
TS2.29DefeatLimitService_Cnt_lgc = Max
TS2.30LimitedReturn_MtrNm_f32 = Min
TS2.31LimitedReturn_MtrNm_f32 = Max
TS2.32LimitedReturn_MtrNm_f32 = Mid
TS2.33LimitedReturn_MtrNm_f32 = Pos
TS2.34LimitedReturn_MtrNm_f32 = Neg
TS2.35LrnPnCtrCCDisable_Cnt_lgc = Min
TS2.36LrnPnCtrCCDisable_Cnt_lgc = Max
TS2.37LrnPnCtrEnable_Cnt_lgc = Min
TS2.38LrnPnCtrEnable_Cnt_lgc = Max
TS2.39LrnPnCtrTCmd_MtrNm_f32 = Min
TS2.40LrnPnCtrTCmd_MtrNm_f32 = Max
TS2.41LrnPnCtrTCmd_MtrNm_f32 = Mid
TS2.42LrnPnCtrTCmd_MtrNm_f32 = Pos
TS2.43LrnPnCtrTCmd_MtrNm_f32 = Neg
TS2.44OpTrqOvr_MtrNm_f32 = Min
TS2.45OpTrqOvr_MtrNm_f32 = Max
TS2.46OpTrqOvr_MtrNm_f32 = Mid
TS2.47OpTrqOvr_MtrNm_f32 = Pos
TS2.48OpTrqOvr_MtrNm_f32 = Neg
TS2.49OutputRampMult_Uls_f32 = Min
TS2.50OutputRampMult_Uls_f32 = Max
TS2.51OutputRampMult_Uls_f32 = Mid
TS2.52PosServCCDisable_Cnt_lgc = Min
TS2.53PosServCCDisable_Cnt_lgc = Max
TS2.54PowerLimitPerc_Uls_f32 = Min
TS2.55PowerLimitPerc_Uls_f32 = Max
TS2.56PowerLimitPerc_Uls_f32 = Mid
TS2.57PrkAssistCmd_MtrNm_f32 = Min
TS2.58PrkAssistCmd_MtrNm_f32 = Max
TS2.59PrkAssistCmd_MtrNm_f32 = Mid
TS2.60PrkAssistCmd_MtrNm_f32 = Pos
TS2.61PrkAssistCmd_MtrNm_f32 = Neg
TS2.62PullCompCmd_MtrNm_f32 = Min
TS2.63PullCompCmd_MtrNm_f32 = Max
TS2.64PullCompCmd_MtrNm_f32 = Mid
TS2.65PullCompCmd_MtrNm_f32 = Pos
TS2.66PullCompCmd_MtrNm_f32 = Neg
TS2.67ThermalLimitPerc_Uls_f32 = Min
TS2.68ThermalLimitPerc_Uls_f32 = Max
TS2.69ThermalLimitPerc_Uls_f32 = Mid
TS2.70ThermalLimit_MtrNm_f32 = Min
TS2.71ThermalLimit_MtrNm_f32 = Max
TS2.72ThermalLimit_MtrNm_f32 = Mid
TS2.73WheelImbalanceCmd_MtrNm_f32 = Min
TS2.74WheelImbalanceCmd_MtrNm_f32 = Max
TS2.75WheelImbalanceCmd_MtrNm_f32 = Mid
TS2.76WheelImbalanceCmd_MtrNm_f32 = Pos
TS2.77WheelImbalanceCmd_MtrNm_f32 = Neg
TS2.78k_SumLimPlCmpLimit_MtrNm_f32 = Min
TS2.79k_SumLimPlCmpLimit_MtrNm_f32 = Max
TS2.80k_SumLimPlCmpLimit_MtrNm_f32 = Mid
TS2.81VehSpd_Kph_f32 = Min
TS2.82VehSpd_Kph_f32 = Max
TS2.83VehSpd_Kph_f32 = Pos
TS2.84ManualTrqCmdEn_Cnt_M_lgc = Min
TS2.85ManualTrqCmdEn_Cnt_M_lgc = Max
TS2.86ManualTrqCmd_MtrNm_M_f32 = Min
TS2.87ManualTrqCmd_MtrNm_M_f32 = Max
TS2.88ManualTrqCmd_MtrNm_M_f32 = Zero
TS2.89ManualTrqCmd_MtrNm_M_f32 = Neg
TS2.90ManualTrqCmd_MtrNm_M_f32 = Pos
TS2.91SteeringAsstDefeat_Cnt_M_lgc = Min
TS2.92SteeringAsstDefeat_Cnt_M_lgc = Max
TS2.93TSMitCommand_MtrNm_f32=Min
TS2.94TSMitCommand_MtrNm_f32=Max

TS2.95TSMitCommand_MtrNm_f32=Zero
TS2.96TSMitCommand_MtrNm_f32=Neg
TS2.97TSMitCommand_MtrNm_f32=Pos
TS2.98All Min
TS2.99All max

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	8.10000038
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.100000001
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.79999995
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.0999985
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.0100000007	0.00999999978 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.100000001	0.100000001 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.2 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.36999989	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	7.19999981	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.89999998	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.60000002	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.2000008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1	1 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.200000003	0.200000003 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.3 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.30000019	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.70000005	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7999992	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.0900000036	0.0900000036 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.4 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	5.4000001	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.400000006	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	14.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7.19999981	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.79999995	2.79999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.400000006	0.400000006 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.5 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	4.5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.20000005
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.89999998
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.2999992
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.25	-0.25 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.5	0.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.6 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	36.4000015	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.720000029	-0.720000029 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.7 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.70000005	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.20000005	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.6600004	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.48999998	-0.49000001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.699999988	0.699999988 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1.79999995
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4.19999981
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34.7000008
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.9 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.19999981	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	65.9000015	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.0999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2.66000032	-2.66000009 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.809999943	-0.810000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.899999976	0.899999976 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.10 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5.63000011
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.699999988
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	25.5
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.330000013	0.330000013 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.699999988	0.699999988 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.11 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.23999977	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75.8000031	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.160000011	-0.159999996 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7.44999981
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.89999998
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.60000002
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.20000005
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.899999976
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.20000005
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	28.2000008
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.899999976	0.899999976 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.13 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.24000001		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.29999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.699999988		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.4000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.64000058	3.6400001 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.64000058	3.6400001 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.160000011	0.159999996 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.400000006	0.400000006 ± 0.0009765625	✔

T ✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 2.15 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.239999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.899999976		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.63999939	6.63999987 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-6.63999939	-6.63999987 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.360000014	-0.360000014 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.600000024	0.600000024 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.16 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1.36000001
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.699999988
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.100006
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.5	-3.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.699999988	0.699999988 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.17 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.48000002	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.39999998	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.70000005	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	12.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.18 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.79999995
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	510.899994
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.92000008	1.91999996 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.79999995	1.79999995 ± 0.0009765625	✔

_T_				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.19 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	4.71999979	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.89999998	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.19999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.700012	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.80000007	-1.79999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.89999998	1.89999998 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.20 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	7
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5.84000015
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.39999998
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.29999995
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.30000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	373.299988
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.8499999	6.8499999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-6.8499999	-6.8499999 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.21 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.96000004		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	324.600006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.98000002	1.98000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.22 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	6
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	8.07999992
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.899999976
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.20000005
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.60000002
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.5
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	456.399994
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.01999986	-1.01999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.713999867	-0.713999987 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.23 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.0599999987
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.29999995
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.70000005
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.5999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.100006
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1	1 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.800000012	-0.800000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.70000005	1.70000005 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 2.24 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	234.199997		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.61999989	-1.62 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.79999995	1.79999995 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.25 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.74000001	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.89999998	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	0	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.80000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	123.800003	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.20000005	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.89999998	1.89999998 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.26 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	4	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.57999992	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	500.700012	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.77999973	3.77999997 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.400000006	0.400000006 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2	2 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.27 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.42000008	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.70000005	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	400.899994	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.28 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	4.26000023	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.79999995	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.20000005	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	300.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.880000055	0.879999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	✔

T ✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 2.30 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.94000006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	58.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.9000001	2.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.31 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.78000021	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.5999985	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.32 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7.61999989
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.100000001
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.70000005
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.0999999
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.0999985
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.80000007	-1.79999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.0999999	3.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.33 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.46000004	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.79999995	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.20000005	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7000008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.20000005	3.20000005 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.34 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.600000024	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.9000001	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.70000005	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.19999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	3.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.98000002	1.98000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.35 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.959999979	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.400000006	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.4000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.79999995	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.10000038	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.30000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	253.600006	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.4000001	3.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.36 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1.32000005
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.0999999
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.9000001
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.4000001
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	342.100006
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-3
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.37 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.67999995	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.19999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	484.799988	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.5999999	3.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.38 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.03999996	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.30000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.70000005	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	155.899994	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.5	-0.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.70000005	3.70000005 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.39 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.4000001		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.79999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	278.600006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.79999995	3.79999995 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.40 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	2.75999999
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.899999976
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	312.399994
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.9000001	3.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.41 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.11999989	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	0	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	328.299988	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.42 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.48000002	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.10000002	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1.5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	429.399994	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.0999999	4.0999999 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.43 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.20000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.80000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	52.7999992	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.19999981	4.19999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.44 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.19999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.44000006	1.44000006 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.45 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	7
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	4.55999994
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.39999998
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.9000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.30000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	83.9000015
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2	-2 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.9000001	4.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.46 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.92000008		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	73.6999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.47 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.28000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.69999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	500.200012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4.20000029	-4.19999981 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.0999999	5.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.48 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5.63999987
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.70000005
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.19999981
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.10000038
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	58.2000008
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.19999981	5.19999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.49 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.69999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.2999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.30000019	5.30000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.50 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.36000013	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.89999998	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.4000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.80000019	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.4000015	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.4000001	5.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.51 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	4	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.71999979	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.0999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.9000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.39999962	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.52 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7.07999992
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.0999999
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.19999981
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	14.6000004
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.53 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	7.44000006	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.20000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.30000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.69999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.60000038	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.7000008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-3	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.69999981	5.69999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.54 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	36.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.19999981		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2.32000017	-2.31999993 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.80000019	5.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.55 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	2	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.15999985	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.8999996	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.56 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.52000046	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.19999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.81999969	2.81999993 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.69199991	1.69200003 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.57 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.36999989
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.400000006
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5999999
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.69999981
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.0999999
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	65.3000031
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.840000033	-0.839999974 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.0999999	6.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.58 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.829999983		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6.69999981		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	25.1000004		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.62999988	3.63000011 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.62999988	3.63000011 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.90400004	2.90400004 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.59 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1.25999999
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.79999995
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.9000001
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.30000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.69999981
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75.6999969
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4.04999971	-4.05000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.30000019	6.30000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.60 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.69000006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.9000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	28.8999996		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19000006	6.19000006 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	6.19000006	6.19000006 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.619000018	0.619000018 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.4000001	6.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.61 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.11999989	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.9000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.62 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	2.54999995
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5999999
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.05000019	2.04999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.615000069	0.61500001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.5999999	6.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.63 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.98000002	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.69999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	2	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.399994	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.400000006	-0.400000006 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.69999981	6.69999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.64 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.41000009		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	363.799988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.4000001	3.4000001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.80000019	6.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.65 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.9000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.30000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.10000038
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	246.100006
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.80000007	-1.79999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.9000001	6.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.66 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	4.26999998	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.5999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.300003	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7	7 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.67 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	4.69999981	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.69999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	84.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4	-4 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.0999999	7.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.68 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	5.13000011	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.80000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.19999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.39999962	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	27.7000008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.19999981	7.19999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.69 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	5.55999994	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.9000001	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.30000019	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.69999981	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	2.9000001	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.699999988	-0.699999988 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.30000019	7.30000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.70 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5.98999977
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.4000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.71 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.42000008	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.10000038	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.5	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.9000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	53.4000015	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.5	7.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.72 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	6.8499999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.19999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	12.6000004	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.73 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7.28000021
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.89999998
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.30000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.69999981
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.10000038
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	510.799988
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.74 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7.71000004
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.39999962
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.19999981
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.5
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.75 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.14000034	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.5	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.9000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.30000019	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	373.100006	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	0	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.29999995	2.29999995 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.76 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.56999969	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.60000038	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.39999962	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	324.200012	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.4000001	2.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.77 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.600000024	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.69999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.10000038	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	456.299988	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	6.29999971	6.30000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.5	2.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.78 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.39999998	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.19999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.60000038	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.399994	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.2000005	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.5999999	2.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.79 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.30000019	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.69999981	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	234.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.70000005	2.70000005 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.80 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	7
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5.5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.39999962
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	123.599998
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.79999995	2.79999995 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.81 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	4	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.300000012	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.9000001	3.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.82 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.579999983	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.11999989	5.11999989 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.11999989	-5.11999989 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.60000002	-1.60000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.83 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	0.860000014	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.899999976	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	125.260002	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.5	0.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.0999999	4.0999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.84 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.13999999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.80000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75.3000031	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2.29999995	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.19999981	4.19999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.85 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	2	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.41999996	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.9000001	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.30000019	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.69999981	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.19999981	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.19999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.4000015	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.4000001	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.30999994	2.30999994 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.30000019	4.30000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.86 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.70000005	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5.30000019	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.4000001	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.5	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.4000001	4.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.87 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	1.98000002	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.0999999	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.9000001	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.38000009e-008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.88 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.25999999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5999999	4.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.89 (Repeat Count = 1)			✔
Name	**Input Value**		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.91 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	3.0999999
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.92 (Repeat Count = 1)			✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.93 (Repeat Count = 1)	✔

Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	0
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	2.25999999
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5999999	4.5999999 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.94 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.95 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	2.81999993	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.96 (Repeat Count = 1)		✔
Name	**Input Value**	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.0999999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5	
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.97 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.98 (Repeat Count = 1)	✔
Name	**Input Value**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	**Actual Value**	**Expected Value**	**Result**
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 2.99 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Case 3: Path Coverage Test ✔

Specification

```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:
TS3.1 1985.00 cycles
TS3.2 1867.00 cycles
TS3.3 1821.00 cycles
TS3.4 1745.00 cycles
TS3.5 1747.00 cycles
TS3.6 1723.00 cycles
TS3.7 1747.00 cycles
TS3.8 1723.00 cycles
TS3.9 1747.00 cycles
TS3.10 1723.00 cycles
TS3.11 1747.00 cycles
TS3.12 1723.00 cycles
TS3.13 1770.00 cycles
TS3.14 1804.00 cycles
TS3.15 1851.00 cycles
TS3.16 1790.00 cycles
TS3.17 1757.00 cycles
TS3.18 1702.00 cycles
TS3.19 1718.00 cycles
TS3.20 1745.00 cycles
```

Description

Vector Description :

TS3.1"(PosServCCDisable_Cnt_T_lgc == TRUE)=True
and
(LrnPnCtrCCDisable_Cnt_T_lgc == TRUE)=True
and
(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = True
and
((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =False && (VehSpd_Kph_T_f32 < FLT_EPSILON))
and
(LrnPnCtrEnable_Cnt_T_lgc == TRUE)=True
and
(PreLimitTorque_MtrNm_T_f32 >= 0)=True
and
(AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = True
and
(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=True
and
(DefeatLimitService_Cnt_T_lgc == TRUE)=True
and
"
TS3.2"(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = False
and
(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = false
and
((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
and
(Sign_f32_m(AssistPowerLimit_MtrNm_T_f32) >= D_ZERO_CNT_S8)=False
and(ThermalLimitPerc_Uls_T_f32 >= PowerLimitPerc_Uls_T_f32) = True"
TS3.3"((HwTrqPolarity_Cnt_T_f32 * SignMRFMtrVel_Cnt_T_f32 * SignPreLimitForPower_Cnt_T_f32) >= D_ZERO_ULS_F32)=False
and
(AssistPowerLimit_MtrNm_T_f32 >= Abs_f32_m(k_SumLimOvrSpdMtrTrq2QLmt_MtrNm_f32))=False
and
(AbsPreLimitForPower_MtrNm_T_f32 <= ForPowerLimit_MtrNm_T_f32)=True"
TS3.4"(DefeatLimitService_Cnt_T_lgc == TRUE)= False
and
(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = True
and
(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = True"
TS3.5"(LrnPnCtrEnable_Cnt_T_lgc == TRUE)=False
and
(AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = False
and
(PreLimitForStall_MtrNm_T_f32 <= 0)=False
and
(PreLimitForPower_MtrNm_T_f32 <= 0)=False"
TS3.6"(LrnPnCtrCCDisable_Cnt_T_lgc == TRUE)=False
and
(AbsPreLimitForPower_MtrNm_T_f32 <= AssistPowerLimit_MtrNm_T_f32)=False"
TS3.7(PosServCCDisable_Cnt_T_lgc == TRUE)=False
TS3.8"(PreLimitTorque_MtrNm_T_f32 >= 8.8) = True
and
(PreLimitForStall_MtrNm_T_f32 >= 8.8)=True"
TS3.9"(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=False
"
TS3.10(PostLimitForAssistSumCC_MtrNm_T_f32 >= 8.8)=True
TS3.11"(PreLimitTorque_MtrNm_T_f32 >= 0)=False
and
and(PreLimitTorque_MtrNm_T_f32 <= -8.8) = True
"
TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
TS3.13(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = False
TS3.14(LimitPercentFiltered_Uls_T_f32 >=1)=True
TS3.15(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = False
TS3.16(LimitPercentFiltered_Uls_T_f32 <= 0) = True
TS3.17"((ManualTrqCmdEn_Cnt_M_lgc == TRUE)=True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=True
(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=False"
TS3.18(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=True
TS3.19(TrqLimitMin_MtrNm_T_f32 >= 8.8)=True
TS3.20(PreLimitForStall_MtrNm_T_f32 <= 0)=True

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	1
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	1
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	✔

T ✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.2 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	6
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.3 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	2
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	8
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.4 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	6
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	348
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2	2 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.5 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	3
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.75	2.75 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.6 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	245
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	4.9000001	4.9000001 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.7 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	4
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	166
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	✔

T ✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.8 (Repeat Count = 1)

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	437
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	7.04000044	7.03999996 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.9 (Repeat Count = 1)

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	6
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	367
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.10 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	5
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.11 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	6
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	7
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	24
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.699999988	0.699999988 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.12 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.699999988
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.13 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.200000003
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.14 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	2
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.15 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8	8 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	✔

T ✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.16 (Repeat Count = 1)	✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2	-2 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.17 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.4000001	4.4000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Test Step 3.18 (Repeat Count = 1)

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.4000001	4.4000001 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.19 (Repeat Count = 1)

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached		1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached		1	✔

Test Step 3.20 (Repeat Count = 1)

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	2
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32

Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	✔
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	✔
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	✔
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✔

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Scom_GetSteeringAssistDefeat

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\AstLmt_CM\utp\contract -I$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification	
Name	**Text**
Module 'AstLmt'	*********************************UNIT TEST INFORMATION*** Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **

Attributes	
Name	**Value**
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes	
Name	**Value**
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test ✔

Specification
```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

TS1.1   20.00 Cycles
TS1.2   20.00 Cycles
```

Description   Vector Description:

TS1.1   Rte_Pim_SteerAsstDefeat = True
TS1.2   Rte_Pim_SteerAsstDefeat = False

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc		
tgt_Pim_SteerAsstDefeat	1		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_SteeringAsstDefeat_Cnt_lgc	1	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	**Actual Value**	**Expected Value**	**Result**
tgt_SteeringAsstDefeat_Cnt_lgc	0	0	✔

TEST DETAILS REPORT

2014-09-05, 17:33:02+0530

AstLmt_Scom_ManualTrqCmd

Razorcat

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Scom_ManualTrqCmd

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\AstLmt_CM\utp\contract -I$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification	
Name	**Text**
Module 'AstLmt'	**********************************UNIT TEST INFORMATION*** Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." ***

Attributes	
Name	**Value**
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

Attributes	
Name	**Value**
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Matrix Test ✔

Specification	Performance Metrics: (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 269.00 Cycles Shortest Execution Path TS1.2 274.00 Cycles Longest Execution Path
Description	Vector Description: TS1.1 "Shortest Execution Path: (TRUE == EnableManualCtrl) =False" TS1.2 "Longest Execution Path: (TRUE == EnableManualCtrl) = True (VehSpd_Kph_T_f32 < FLT_EPSILON) = True"

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6	-6 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Case 2: Boundary Test ✔

Specification

```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

TS2.1   269.00 Cycles
TS2.2   272.00 Cycles
TS2.3   275.00 Cycles
TS2.4   275.00 Cycles
TS2.5   274.00 Cycles
TS2.6   274.00 Cycles
TS2.7   274.00 Cycles
TS2.8   274.00 Cycles
TS2.9   272.00 Cycles
TS2.10   272.00 Cycles
TS2.11   269.00 Cycles
TS2.12   272.00 Cycles
```

Description

Vector Description:

TS2.1 EnableManualCtrl = min
TS2.2 EnableManualCtrl = max
TS2.3 MtrTrqCmd_MtrNm_f32 = min
TS2.4 MtrTrqCmd_MtrNm_f32 = max
TS2.5 MtrTrqCmd_MtrNm_f32 = zero
TS2.6 MtrTrqCmd_MtrNm_f32 = neg
TS2.7 MtrTrqCmd_MtrNm_f32 = pos
TS2.8 VehSpd_Kph_f32 = min
TS2.9 VehSpd_Kph_f32 = max
TS2.10 VehSpd_Kph_f32 = pos
TS2.11 All min
TS2.12 All max

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-15.9949999		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3.4000001		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54.6699982		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.2 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6.19999981		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29.9200001		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999	-12.9949999 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	34	34	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-9.99499989
EnableManualCtrl	1

Name	Input Value		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32		1	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	6.38000017e-008		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32		1	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	0		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	7.27999989e-008		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32		1	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-0.995000005		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-2.5		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	5.28000008e-008		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32		1	✔

Test Step 2.7 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	4.55999994		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	8.38000034e-008		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.55999994	4.55999994 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.8 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.1199999		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.1199999	15.1199999 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.9 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-3.67000008		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011	8.00500011 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	34	34	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.10 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	9.82999992		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	123.260002		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001	11.0050001 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	34	34	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.11 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 2.12 (Repeat Count = 1) ✔

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	34	34	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Case 3: Path Coverage Test

Specification

```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

TS3.1    269.00 Cycles
TS3.2    272.00 Cycles
TS3.3    275.00 Cycles
```

Description

Vector Description:

TS3.1 (TRUE == EnableManualCtrl) = False
TS3.2 "(TRUE == EnableManualCtrl) = True
and(VehSpd_Kph_T_f32 < FLT_EPSILON)=False"
TS3.3 (VehSpd_Kph_T_f32 < FLT_EPSILON)=True

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	2	2 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	34	34	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008		

Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✔
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	✔
AstLmt_Scom_ManualTrqCmd()	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	✔

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✔
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\AstLmt_CM\utp\contract -I$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470\include

Comments/Description/Specification	
Name	**Text**
Module 'AstLmt'	********************************UNIT TEST INFORMATION** Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." **

Attributes	
Name	**Value**
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Attributes	
Name	**Value**
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Boundary Test ✔

Specification

```
Performance Metrics:

(With "None" Instrumentation and WithPS
Environment)

CPU Cycles:

TS1.1    25.00 Cycles
TS1.2    25.00 Cycles
```

Description   Vector Description:

TS1.1   SteeringAsstDefeat1_Cnt_lgc = True
TS1.2   SteeringAsstDefeat1_Cnt_lgc = False

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	1		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	**Actual Value**	**Expected Value**	**Result**
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	0	✔