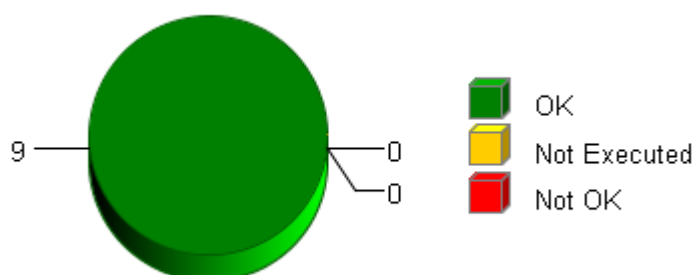


Summary

Total Test Objects: 9
Successful: 9
Failed: 0
Not Executed: 0
Date: 2015-01-29
Time: 19:02:29+0530

Overall Test Object Results (including Coverage)



Selected Project Items

Test Object "CBD_UnitTest/MtrDrvDiag/GateDrvWaitTime"
 Test Object "CBD_UnitTest/MtrDrvDiag/MotorDriverInit"
 Test Object "CBD_UnitTest/MtrDrvDiag/MtrDrvDiag_Per1"
 Test Object "CBD_UnitTest/MtrDrvDiag/MtrDrvDiag_Per2"
 Test Object "CBD_UnitTest/MtrDrvDiag/MtrDrvDiag_Trns1"
 Test Object "CBD_UnitTest/MtrDrvDiag/ProcBridgeFlt"
 Test Object "CBD_UnitTest/MtrDrvDiag/ProcGateDriveFlt"
 Test Object "CBD_UnitTest/MtrDrvDiag/ReadMtrDrvFltData"
 Test Object "CBD_UnitTest/MtrDrvDiag/ResetGateDrive"

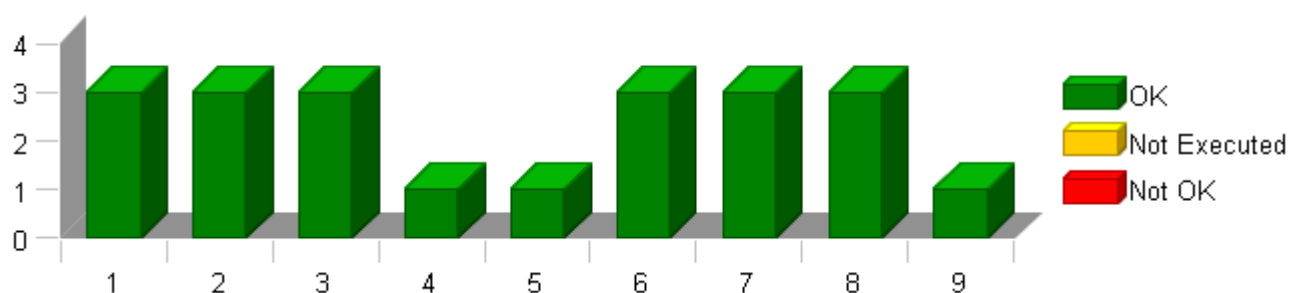
Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

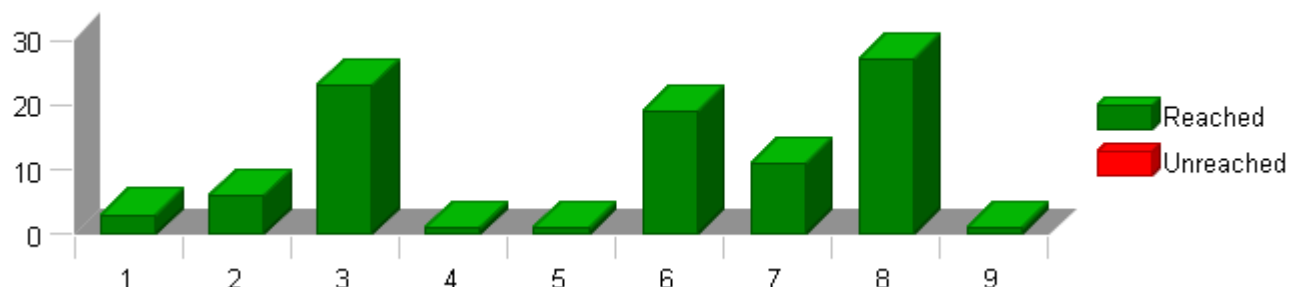
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No
Instrumentation: Test Object Only
Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition / Decision Coverage, Multiple Condition Coverage

Test Case Results for Each Test Object (without Coverage)



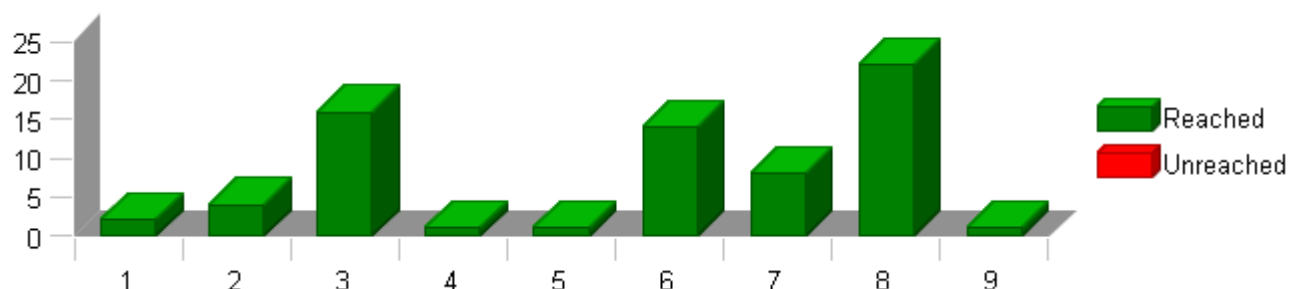
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object



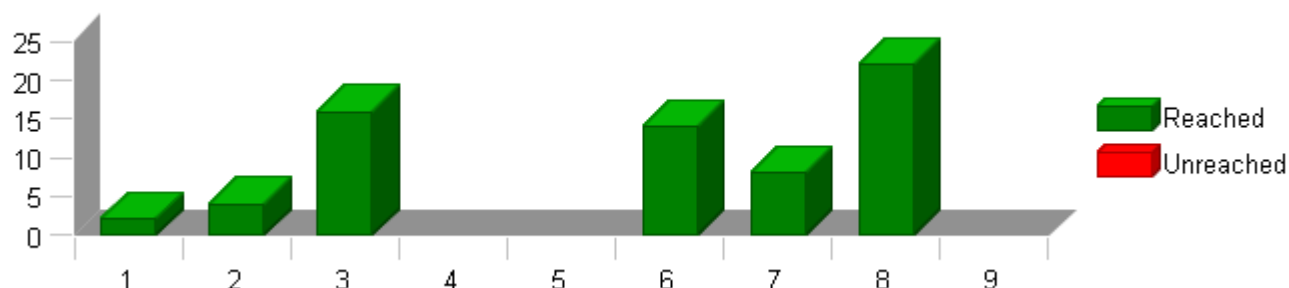
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

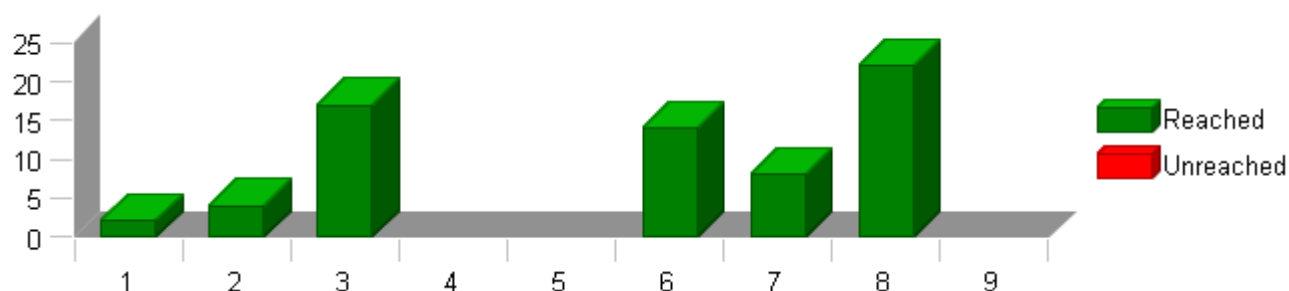
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

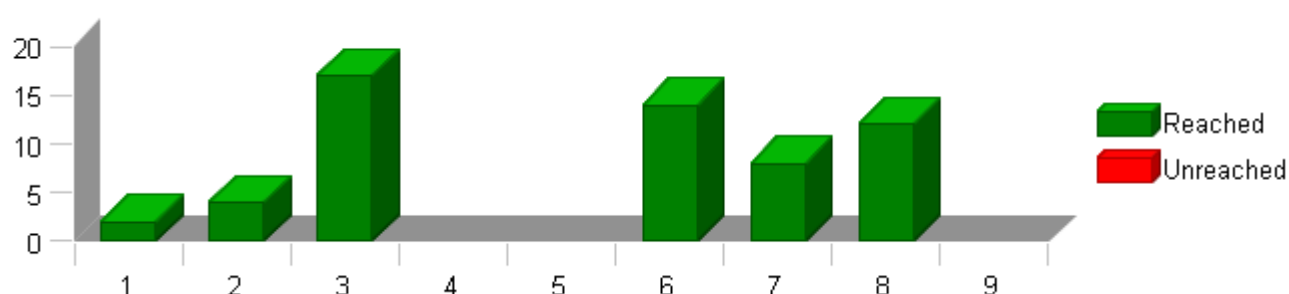
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	MtrDrvDiag	100 %	100 %	100 %	100 %	100 %	21 of 21 passed	✓
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	21 of 21 passed	✓
	MtrDrvDiag	100 %	100 %	100 %	100 %	100 %	21 of 21 passed	✓
1	GateDrvWaitTime	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
2	MotorDriverInit	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
3	MtrDrvDiag_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
4	MtrDrvDiag_Per2	100 %	100 %	-	-	-	1 of 1 passed	✓
5	MtrDrvDiag_Trns1	100 %	100 %	-	-	-	1 of 1 passed	✓
6	ProcBridgeFlt	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
7	ProcGateDriveFlt	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
8	ReadMtrDrvFltData	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✓
9	ResetGateDrive	100 %	100 %	-	-	-	1 of 1 passed	✓

TEST DETAILS REPORT

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ResetGateDrive



Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	ResetGateDrive

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrDrvDiag'	<p>*****UNIT TEST DESCRIPTION*****</p> <p>Name of Tester: Spoorti Mali Code File(s) Under Test: Sa_MtrDrvDiag.c Code File(s) Version: 22 Module Design Document:Motor_Driver_Diagnostics_MDD.doc Module Design Document Version: 11 Data Dictionary Version: 11 Unit Test Plan Version: 3 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31 Total FLASH Used (Bytes): 1334 Total RAM Used (Bytes): 17 Total CALS Used (Bytes): 50 Special Test Requirements: Test Date:01-29-2015 Comments:</p> <p>Note 1:Inline function defined in ""Gloabalmacro.h"" is not unit Tested.</p> <p>Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.</p> <p>Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and ""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.</p> <p>Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"",""ReadMtrDrvFitData"" functions, ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.</p> <p>Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."</p> <p>*****</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj

TEST DETAILS REPORT

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ResetGateDrive



Attributes	
Name	Value
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC1.1 1817.00 Cycles

Description Vector Description

TS1.1 Check for Call Trace

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	20

T				
Actual Function	Count	Expected Function	Count	Result
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

TEST DETAILS REPORT

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MotorDriverInit



Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	MotorDriverInit

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
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TEST DETAILS REPORT

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MotorDriverInit



Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in "Gloabalmacro.h"
is not unit Tested.

Note 2:In "GateDrvWaitTime" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In "ReadMtrDrvFitData"function,"DataHigh_Cnt_T_lgc" temporary variable and
"Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16" stub are used as "DataHigh_Cnt_T_lgc_Array[10]" and
"Rte_Call_FetFit1Data_OP_GET_Array[11]" to replicate the usercode functionality of TESSY.

Note 4:In functions "ProcGateDriveFit" , "ProcBridgeFit" , "MtrDrvDiag_Per1","ReadMtrDrvFitData" functions,
"Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16" is not tested with its Min value, to avoid the infinite loop.

Note 5:"CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

TEST DETAILS REPORT

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MotorDriverInit

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 251 Cycles TC1.2 504 Cycles
Description	Vector Description TS1.1"Shortest execution path: (0==SVDiag_MtrDrvInitActive_Cnt_M_lgc)=false && (ElapsedTime_mS_T_u16p0>10)=true" TS1.2"Longest execution path (0==SVDiag_MtrDrvInitActive_Cnt_M_lgc)=true && (ElapsedTime_mS_T_u16p0>10)=true"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	755		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	83462		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	231		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	235		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	6653		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	562		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 504 Cycles
TC2.2 251 Cycles
TC2.3 505 Cycles
TC2.4 505 Cycles
TC2.5 505 Cycles
TC2.6 505 Cycles
TC2.7 505 Cycles
TC2.8 505 Cycles
TC2.9 251 Cycles
TC2.10 505 Cycles
TC2.11 251 Cycles
TC2.12 505 Cycles
TC2.13 251 Cycles

Description Vector Description

TS2.1SVDiag_MtrDrvInitActive_Cnt_M_lgc=False
TS2.2SVDiag_MtrDrvInitActive_Cnt_M_lgc=True
TS2.3SVDiag_MtrDrvInitStartTime_mS_M_u32p0=min
TS2.4SVDiag_MtrDrvInitStartTime_mS_M_u32p0=max
TS2.5SVDiag_MtrDrvInitStartTime_mS_M_u32p0=pos
TS2.6Rte_Call_SystemTime_GetSystemTime_mS_u32=min
TS2.7Rte_Call_SystemTime_GetSystemTime_mS_u32=max
TS2.8Rte_Call_SystemTime_GetSystemTime_mS_u32=pos
TS2.9Rte_Call_SystemTime_DtrmnElapsedTime_mS_u16=min
TS2.10Rte_Call_SystemTime_DtrmnElapsedTime_mS_u16=max
TS2.11Rte_Call_SystemTime_DtrmnElapsedTime_mS_u16=pos
TS2.12All Min
TS2.13All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	235		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	6653		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	562		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	755		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	83462		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	231		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔

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Name	Actual Value	Expected Value	Result
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	235		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	6653		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	562		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	755		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	83462		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	231		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462	83462 ± 1	✔

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.5 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	235			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	6653			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	562			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	1	1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✓	

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.6 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	762			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	0			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	3215			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	1	1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0	0 ± 1	✓	

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.7 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	107			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	4294967295			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	3961			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	1	1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295	4294967295 ± 1	✓	

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.8 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1233			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	455			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	66547			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4707			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	1	1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	66547	66547 ± 1	✓	

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.9 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	0			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	562			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5453			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	0	0	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832	832 ± 1	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓

Test Step 2.10 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4234			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	65535			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	231			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	6199			
Name	Actual Value	Expected Value	Result	
MotorDriverInit()	1	1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	231	231 ± 1	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.11 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			

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MotorDriverInit

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	344		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	20		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	1233		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	3961		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	344	344 ± 1	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	0		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	0		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	0	0	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0	0 ± 1	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	65535		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	4294967295		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4294967295		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295	4294967295 ± 1	✔

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MotorDriverInit



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Case 3: Path Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC3.1 504 Cycles TC3.2 251 Cycles TC3.3 251 Cycles
Description	Vector Description TS3.1"If (False = SVDiag_MtrDrvInitActive_Cnt_M_lgc))=>True If (ElapsedTime_mS_T_u16p0 > k_GateDrvInitDwellTime_mS_u16p0)=>True" TS3.2"If (False = SVDiag_MtrDrvInitActive_Cnt_M_lgc))=>False " TS3.3If (ElapsedTime_mS_T_u16p0 > k_GateDrvInitDwellTime_mS_u16p0)=>False

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	235		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	6653		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	562		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	755		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	83462		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	231		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	1	1	✓

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MotorDriverInit

Name	Actual Value	Expected Value	Result
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓

Test Step 3.3 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	0		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_Curr	562		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5453		
Name	Actual Value	Expected Value	Result
MotorDriverInit()	0	0	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832	832 ± 1	✔

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓

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GateDrvWaitTime



Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	GateDrvWaitTime

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
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GateDrvWaitTime



Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in ""Gloabalmacro.h""
is not unit Tested.

Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and
""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.

Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"" ,""ReadMtrDrvFitData"" functions,
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.

Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:
	TC1.1 511 Cycles TC1.2 791 Cycles
Description	Vector Description TS1.1"Shortest Execution Path: (WaitElapsedTime_uS_T_u16p0 < TimeToWait_uS_T_u16p0)=False" TS1.2"Longest Execution Path: (WaitElapsedTime_uS_T_u16p0 < TimeToWait_uS_T_u16p0)=True"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	10000
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	500000000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	✓

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GateDrvWaitTime



Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 520 Cycles
TC2.2 520 Cycles
TC2.3 519 Cycles
TC2.4 520 Cycles
TC2.5 520 Cycles
TC2.6 519 Cycles
TC2.7 519 Cycles
TC2.8 519 Cycles
TC2.9 801 Cycles
TC2.10 519 Cycles
TC2.11 519 Cycles

Description Vector Description

TS2.1TimeToWait_uS_T_u16p0=Min
TS2.2TimeToWait_uS_T_u16p0=Max
TS2.3TimeToWait_uS_T_u16p0=Pos
TS2.4Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16=Min
TS2.5Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16=Max
TS2.6Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16=Pos
TS2.7Rte_Call_SystemTime_GetSystemTime_uS_u32=Min
TS2.8Rte_Call_SystemTime_GetSystemTime_uS_u32=Max
TS2.9Rte_Call_SystemTime_GetSystemTime_uS_u32=Pos
TS2.10All Min
TS2.11All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	65535
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	70000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	100
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

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GateDrvWaitTime



Test Step 2.4 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	3000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.5 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	300
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.6 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	400
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.7 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	500
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.8 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4294967295

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

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GateDrvWaitTime

Test Step 2.9 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	10000
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	500000000

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Step 2.11 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
TimeToWait_uS_T_u16p0	65535
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4294967295

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓

Test Case 3: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

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GateDrvWaitTime

Name	Input Value
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	500000000

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	2	✓	

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MtrDrvDiag_Per2

Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	MtrDrvDiag_Per2

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrDrvDiag'	<p>*****UNIT TEST DESCRIPTION*****</p> <p>Name of Tester: Spoorti Mali Code File(s) Under Test: Sa_MtrDrvDiag.c Code File(s) Version: 22 Module Design Document:Motor_Driver_Diagnostics_MDD.doc Module Design Document Version: 11 Data Dictionary Version: 11 Unit Test Plan Version: 3 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31 Total FLASH Used (Bytes): 1334 Total RAM Used (Bytes): 17 Total CALS Used (Bytes): 50 Special Test Requirements: Test Date:01-29-2015 Comments:</p> <p>Note 1:Inline function defined in ""Gloabalmacro.h"" is not unit Tested.</p> <p>Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.</p> <p>Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and ""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.</p> <p>Note 4:In functions ""ProcGateDriveFit"", ""ProcBridgeFit"", ""MtrDrvDiag_Per1"", ""ReadMtrDrvFitData"" functions, ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" is not tested with its Min value, to avoid the infinite loop.</p> <p>Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."</p> <p>*****</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj

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MtrDrvDiag_Per2



Attributes	
Name	Value
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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MtrDrvDiag_Per2



Test Case 1: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
TC1.1 569 Cycles

Description Vector Description
TS1.1 Check for Call Trace

Test Step 1.1 (Repeat Count = 1)

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET(signal)	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓

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ReadMtrDrvFltData

Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	ReadMtrDrvFltData

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
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TEST DETAILS REPORT

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ReadMtrDrvFitData



Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in ""Gloabalmacro.h""
is not unit Tested.

Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and
""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.

Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"" ,""ReadMtrDrvFitData"" functions,
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.

Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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ReadMtrDrvFitData



Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 644073 Cycles TC1.2 648550 Cycles
Description	Vector Description TS1.1"Shortest Execution Path: (ClockBit_Cnt_T_u16<10U)=True &&(ClockBit_Cnt_T_u16<10U)=False && (0U == Status_Cnt_T_b16)=True && (FaultedPhases_Cnt_T_u16 == 1U)=False" TS1.2"Longest Execution Path: (ClockBit_Cnt_T_u16<10U)=True &&(ClockBit_Cnt_T_u16<10U)=False && (TRUE == DataHigh_Cnt_T_lgc)=True && (0U == Status_Cnt_T_b16)=False && (FaultedPhases_Cnt_T_u16 == 1U)=False && (case 0=True)&& (case 1=True)&&(case 2=True)&&(case 3=True)&&(case 4=True)&&(case 5=True)&&(case 6=True)&&(case 7=True)&&(case 8=True)"

Test Step 1.1 (Repeat Count = 1)

Name		Input Value	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)		tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	
StatusPtr_Cnt_T_b16		tgt_StatusPtr_Cnt_T_b16	
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		0	
tgt_StatusPtr_Cnt_T_b16		0	
Name	Actual Value	Expected Value	Result
ReadMtrDrvFitData()	0	0	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
tgt_StatusPtr_Cnt_T_b16	1024	1024	✓



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ReadMtrDrvFltData



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ReadMtrDrvFltData

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 1.2 (Repeat Count = 1)				✓
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000			
tgt_StatusPtr_Cnt_T_b16	65535			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	6	6		✓
SVDiag_FETFaultPhase_Cnt_M_enum	3	3		✓
SVDiag_FETFaultType_Cnt_M_enum	2	2		✓
tgt_StatusPtr_Cnt_T_b16	1023	1023		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 648625 Cycles
TC2.2 653236 Cycles
TC2.3 655425 Cycles
TC2.4 660030 Cycles
TC2.5 664822 Cycles
TC2.6 669589 Cycles
TC2.7 674087 Cycles

Description Vector Description

TS2.1StatusPtr_Cnt_T_b16=Min
TS2.2StatusPtr_Cnt_T_b16=Max
TS2.3StatusPtr_Cnt_T_b16=Pos
TS2.4Rte_Call_FetFlt1Data_OP_GET=Min
TS2.5Rte_Call_FetFlt1Data_OP_GET=Max
TS2.6All Min
TS2.7All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5000		
tgt_StatusPtr_Cnt_T_b16	0		
Name	Actual Value	Expected Value	Result
ReadMtrDrvFltData()	6	6	✔
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
tgt_StatusPtr_Cnt_T_b16	1023	1023	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.2 (Repeat Count = 1)				✓
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000			
tgt_StatusPtr_Cnt_T_b16	65535			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	6	6		✓
SVDiag_FETFaultPhase_Cnt_M_enum	3	3		✓
SVDiag_FETFaultType_Cnt_M_enum	2	2		✓
tgt_StatusPtr_Cnt_T_b16	1023	1023		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.3 (Repeat Count = 1) ✓				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000			
tgt_StatusPtr_Cnt_T_b16	5000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
tgt_StatusPtr_Cnt_T_b16	1	1	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.4 (Repeat Count = 1)				✓
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	50000			
tgt_StatusPtr_Cnt_T_b16	12000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	0	0		✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0		✓
SVDiag_FETFaultType_Cnt_M_enum	0	0		✓
tgt_StatusPtr_Cnt_T_b16	1024	1024		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.5 (Repeat Count = 1) ✓				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	60000			
tgt_StatusPtr_Cnt_T_b16	13000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	6	6	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓	
tgt_StatusPtr_Cnt_T_b16	1023	1023	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.6 (Repeat Count = 1)				✓
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0			
tgt_StatusPtr_Cnt_T_b16	0			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	0	0		✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0		✓
SVDiag_FETFaultType_Cnt_M_enum	0	0		✓
tgt_StatusPtr_Cnt_T_b16	1024	1024		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓

Test Step 2.7 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4294967295			
tgt_StatusPtr_Cnt_T_b16	65535			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFitData()	6	6	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC3.1 650867 Cycles
TC3.2 655658 Cycles
TC3.3 660158 Cycles
TC3.4 664790 Cycles
TC3.5 669419 Cycles

Description

Vector Description

TS3.1"
(0 = Status_Cnt_T_b16)=True"
TS3.2"(ClockBit_Cnt_T_u16 = 0 To D_NUMOFGDSTATUSBITS_CNT_U16 - 1)=True
(True = DataHigh_Cnt_T_lgc_Array(ClockBit_Cnt_T_u16))=True"
TS3.3(PHASEA = FETFaultPhase_Cnt_T_enum)=True
TS3.4(PHASEB = FETFaultPhase_Cnt_T_enum)=True
TS3.5"(PHASEA = FETFaultPhase_Cnt_T_enum)=False
(PHASEB = FETFaultPhase_Cnt_T_enum)=False"

Test Step 3.1 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5000		
tgt_StatusPtr_Cnt_T_b16	0		
Name	Actual Value	Expected Value	Result
ReadMtrDrvFltData()	0	0	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
tgt_StatusPtr_Cnt_T_b16	1024	1024	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.2 (Repeat Count = 1)				✓
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000			
tgt_StatusPtr_Cnt_T_b16	65535			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	6	6		✓
SVDiag_FETFaultPhase_Cnt_M_enum	3	3		✓
SVDiag_FETFaultType_Cnt_M_enum	2	2		✓
tgt_StatusPtr_Cnt_T_b16	1023	1023		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.3 (Repeat Count = 1) ✓				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000			
tgt_StatusPtr_Cnt_T_b16	5000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
tgt_StatusPtr_Cnt_T_b16	1	1	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.4 (Repeat Count = 1) ✓				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000			
tgt_StatusPtr_Cnt_T_b16	5000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
tgt_StatusPtr_Cnt_T_b16	2	2	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.5 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
StatusPtr_Cnt_T_b16	tgt_StatusPtr_Cnt_T_b16			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000			
tgt_StatusPtr_Cnt_T_b16	5000			
Name	Actual Value	Expected Value	Result	
ReadMtrDrvFltData()	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
tgt_StatusPtr_Cnt_T_b16	4	4	✓	



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ReadMtrDrvFltData



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ReadMtrDrvFitData

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓

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Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	ProcGateDriveFlt

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
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Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in ""Gloabalmacro.h""
is not unit Tested.

Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and
""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.

Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"",""ReadMtrDrvFitData"" functions,
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.

Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 62 Cycles TC1.2 1213900 Cycles
Description	Vector Description TS1.1"Shortest execution path: ((*GateDriveFitAccPtr_Cnt_T_u16)<((k_OnStateDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.Pstep)))=false && ((*GateDriveFitAccPtr_Cnt_T_u16)<((k_OnStateDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.Pstep)))=true e &&(((1==((*GateDriveFitAccPtr_Cnt_T_u16)>=(k_OnStateDiag_Cnt_str.Threshold)?(((1):((0))))=false" TS1.2"Longest execution path: ((*GateDriveFitAccPtr_Cnt_T_u16)<((k_OnStateDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.Pstep)))=false && ((*GateDriveFitAccPtr_Cnt_T_u16)<((k_OnStateDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.Pstep)))=true e &&(((1==((*GateDriveFitAccPtr_Cnt_T_u16)>=(k_OnStateDiag_Cnt_str.Threshold)?(((1):((0))))=true && (0==FF2Set_Cnt_T_lgc)=true"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
FF2Set_Cnt_T_lgc	1		
GateDriveFitAccPtr_Cnt_T_u16	tgt_GateDriveFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	0		
SVDiag_FETFaultType_Cnt_M_enum	2		
k_GateDriveDiag_Cnt_str.Threshold	65535		
k_GateDriveDiag_Cnt_str.PStep	1314		
tgt_GateDriveFitAccPtr_Cnt_T_u16	134		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
tgt_GateDriveFitAccPtr_Cnt_T_u16	1448	1448	✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
FF2Set_Cnt_T_lgc	1		
GateDriveFitAccPtr_Cnt_T_u16	tgt_GateDriveFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
k_GateDriveDiag_Cnt_str.Threshold	366		
k_GateDriveDiag_Cnt_str.PStep	123		
tgt_GateDriveFitAccPtr_Cnt_T_u16	419		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
tgt_GateDriveFitAccPtr_Cnt_T_u16	366	366	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓

Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 595 Cycles
TC2.2 1226150 Cycles
TC2.3 588 Cycles
TC2.4 1218839 Cycles
TC2.5 569 Cycles
TC2.6 1218520 Cycles
TC2.7 71 Cycles
TC2.8 55 Cycles
TC2.9 55 Cycles
TC2.10 1219546 Cycles
TC2.11 55 Cycles
TC2.12 591 Cycles
TC2.13 1226445 Cycles

Description Vector Description

TS2.1FF2Set_Cnt_T_lgc=False
TS2.2FF2Set_Cnt_T_lgc=True
TS2.3GateDriveFitAccPtr_Cnt_T_u16=min
TS2.4GateDriveFitAccPtr_Cnt_T_u16=max
TS2.5GateDriveFitAccPtr_Cnt_T_u16=pos
TS2.6k_GateDriveDiag_Cnt_str.Threshold=min
TS2.7k_GateDriveDiag_Cnt_str.Threshold=max
TS2.8k_GateDriveDiag_Cnt_str.Threshold=pos
TS2.9k_GateDriveDiag_Cnt_str.Pstep=min
TS2.10k_GateDriveDiag_Cnt_str.Pstep=max
TS2.11k_GateDriveDiag_Cnt_str.Pstep=pos
TS2.12All Min
TS2.13All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value	Expected Value	Result
FF2Set_Cnt_T_lgc	0		
GateDriveFitAccPtr_Cnt_T_u16	tgt_GateDriveFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	0		
SVDiag_FETFaultType_Cnt_M_enum	0		
k_GateDriveDiag_Cnt_str.Threshold	134		
k_GateDriveDiag_Cnt_str.PStep	5632		
tgt_GateDriveFitAccPtr_Cnt_T_u16	563		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
tgt_GateDriveFitAccPtr_Cnt_T_u16	134	134	✓

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1		✓

Test Step 2.2 (Repeat Count = 1)					✓
Name		Input Value			
FF2Set_Cnt_T_lgc		1			
GateDriveFltAccPtr_Cnt_T_u16		tgt_GateDriveFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)		tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_FETFaultPhase_Cnt_M_enum		1			
SVDiag_FETFaultType_Cnt_M_enum		1			
k_GateDriveDiag_Cnt_str.Threshold		366			
k_GateDriveDiag_Cnt_str.PStep		123			
tgt_GateDriveFltAccPtr_Cnt_T_u16		419			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		1000			
Name	Actual Value	Expected Value	Result		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓		
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓		
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓		
tgt_GateDriveFltAccPtr_Cnt_T_u16	366	366	✓		



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.3 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_lgc	0
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	2
k_GateDriveDiag_Cnt_str.Threshold	854
k_GateDriveDiag_Cnt_str.PStep	900
tgt_GateDriveFltAccPtr_Cnt_T_u16	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	2000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	854	854	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.4 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_lgc	1
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	3
SVDiag_FETFaultType_Cnt_M_enum	0
k_GateDriveDiag_Cnt_str.Threshold	713
k_GateDriveDiag_Cnt_str.PStep	44
tgt_GateDriveFltAccPtr_Cnt_T_u16	65535
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	713	713	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.5 (Repeat Count = 1) ✓

Name	Input Value		
FF2Set_Cnt_T_lgc	0		
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	1		
k_GateDriveDiag_Cnt_str.Threshold	624		
k_GateDriveDiag_Cnt_str.PStep	2345		
tgt_GateDriveFltAccPtr_Cnt_T_u16	1445		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4294967295		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✔
SVDiag_FETFaultType_Cnt_M_enum	1	1	✔
tgt_GateDriveFltAccPtr_Cnt_T_u16	624	624	✔

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.6 (Repeat Count = 1) ✓

Name	Input Value		
FF2Set_Cnt_T_lgc	1		
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	2		
k_GateDriveDiag_Cnt_str.Threshold	0		
k_GateDriveDiag_Cnt_str.PStep	632		
tgt_GateDriveFltAccPtr_Cnt_T_u16	4577		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	15000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
tgt_GateDriveFltAccPtr_Cnt_T_u16	0	0	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.7 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_Igc	1
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	2
k_GateDriveDiag_Cnt_str.Threshold	65535
k_GateDriveDiag_Cnt_str.PStep	1314
tgt_GateDriveFltAccPtr_Cnt_T_u16	134
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	1448	1448	✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_Igc	0
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	1
k_GateDriveDiag_Cnt_str.Threshold	3426
k_GateDriveDiag_Cnt_str.PStep	733
tgt_GateDriveFltAccPtr_Cnt_T_u16	366
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	1099	1099	✓

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Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.9 (Repeat Count = 1)

Name	Input Value			
FF2Set_Cnt_T_lgc	0			
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_FETFaultPhase_Cnt_M_enum	3			
SVDiag_FETFaultType_Cnt_M_enum	2			
k_GateDriveDiag_Cnt_str.Threshold	5632			
k_GateDriveDiag_Cnt_str.PStep	0			
tgt_GateDriveFltAccPtr_Cnt_T_u16	854			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	4500			
Name	Actual Value	Expected Value	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	*none*	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	*none*	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓	
tgt_GateDriveFltAccPtr_Cnt_T_u16	854	854	✓	

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Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.10 (Repeat Count = 1)

Name	Input Value			
FF2Set_Cnt_T_lgc	1			
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_FETFaultPhase_Cnt_M_enum	1			
SVDiag_FETFaultType_Cnt_M_enum	0			
k_GateDriveDiag_Cnt_str.Threshold	123			
k_GateDriveDiag_Cnt_str.PStep	65535			
tgt_GateDriveFltAccPtr_Cnt_T_u16	713			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	50000			
Name	Actual Value	Expected Value	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
tgt_GateDriveFltAccPtr_Cnt_T_u16	123	123	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.11 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_Igc	1
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	1
k_GateDriveDiag_Cnt_str.Threshold	900
k_GateDriveDiag_Cnt_str.PStep	206
tgt_GateDriveFltAccPtr_Cnt_T_u16	92
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	60000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	298	298	✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.12 (Repeat Count = 1) ✓

Name	Input Value
FF2Set_Cnt_T_Igc	0
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
k_GateDriveDiag_Cnt_str.Threshold	0
k_GateDriveDiag_Cnt_str.PStep	0
tgt_GateDriveFltAccPtr_Cnt_T_u16	0
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	0	0	✓

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ProcGateDriveFlt



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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1		✓

Test Step 2.13 (Repeat Count = 1)					✓
Name		Input Value			
FF2Set_Cnt_T_lgc		1			
GateDriveFltAccPtr_Cnt_T_u16		tgt_GateDriveFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)		tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr			
SVDiag_FETFaultPhase_Cnt_M_enum		3			
SVDiag_FETFaultType_Cnt_M_enum		2			
k_GateDriveDiag_Cnt_str.Threshold		65535			
k_GateDriveDiag_Cnt_str.PStep		65535			
tgt_GateDriveFltAccPtr_Cnt_T_u16		65535			
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		4294967295			
Name	Actual Value	Expected Value	Result		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓		
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓		
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓		
tgt_GateDriveFltAccPtr_Cnt_T_u16	65535	65535	✓		



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ProcGateDriveFlt



Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	

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ProcGateDriveFlt

Name	Input Value		
SVDiag_FETFaultType_Cnt_M_enum	1		
k_GateDriveDiag_Cnt_str.Threshold	366		
k_GateDriveDiag_Cnt_str.PStep	123		
tgt_GateDriveFltAccPtr_Cnt_T_u16	419		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	1000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	4	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
tgt_GateDriveFltAccPtr_Cnt_T_u16	366	366	✓



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ProcGateDriveFlt



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ProcGateDriveFlt

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.3 (Repeat Count = 1) ✓

Name	Input Value		
FF2Set_Cnt_T_Igc	1		
GateDriveFltAccPtr_Cnt_T_u16	tgt_GateDriveFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr		
SVDiag_FETFaultPhase_Cnt_M_enum	0		
SVDiag_FETFaultType_Cnt_M_enum	2		
k_GateDriveDiag_Cnt_str.Threshold	65535		
k_GateDriveDiag_Cnt_str.PStep	1314		
tgt_GateDriveFltAccPtr_Cnt_T_u16	134		
tgt_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_Curr	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
tgt_GateDriveFltAccPtr_Cnt_T_u16	1448	1448	✔

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Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

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MtrDrvDiag_Per1

Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	MtrDrvDiag_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
------	------

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MtrDrvDiag_Per1

Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in ""Gloabalmacro.h""
is not unit Tested.

Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and
""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.

Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"",""ReadMtrDrvFitData"" functions,
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.

Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 1127 Cycles TC1.2 11366 Cycles
Description	Vector Description TS1.1"Shortest Execution Path: (TRUE == MtrDrvInitComp_Cnt_T_Igc)=False && (TRUE == MtrDrvInitStart_Cnt_T_Igc)=False " TS1.2"Longest Execution Path: (TRUE == MtrDrvInitComp_Cnt_T_Igc)=True && (TRUE == SVDiag_ResetWaitLoop_Cnt_M_Igc)=False && ((FALSE == FF1Set_Cnt_T_Igc) && (FALSE == FF2Set_Cnt_T_Igc))=True && ((GateDriveFitAcc_Cnt_T_u16)>(k_GateDriveDiag_Cnt_str.Nstep))=True && ((OnStateFitAcc_Cnt_T_u16)>(k_OnStateDiag_Cnt_str.Nstep))=False"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailedNTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTim@mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	10
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1
SVDiag_MtrDrvInitComp_Cnt_M_Igc	0
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125
SVDiag_OnStateFitAcc_Cnt_M_u16	1111
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	1000
k_GateDriveDiag_Cnt_str.PStep	5000
k_GateDriveDiag_Cnt_str.NStep	11110
k_OnStateDiag_Cnt_str.Threshold	1111
k_OnStateDiag_Cnt_str.PStep	11000
k_OnStateDiag_Cnt_str.NStep	1000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	

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MtrDrvDiag_Per1

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_GateDriveFltAcc_Cnt_M_u16	0		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	70		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462		
SVDiag_OnStateFltAcc_Cnt_M_u16	7777		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	15000		
k_GateDriveDiag_Cnt_str.PStep	35000		
k_GateDriveDiag_Cnt_str.NStep	20000		
k_OnStateDiag_Cnt_str.Threshold	7777		
k_OnStateDiag_Cnt_str.PStep	17000		
k_OnStateDiag_Cnt_str.NStep	7000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	17000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	17100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	23000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	700000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	15000	15000 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	70	70 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462	83462 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	7777	7777 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 1123 Cycles
TC2.2 1035 Cycles
TC2.3 1035 Cycles
TC2.4 4281 Cycles
TC2.5 11447 Cycles
TC2.6 11351 Cycles
TC2.7 8692 Cycles
TC2.8 8693 Cycles
TC2.9 8646 Cycles
TC2.10 8654 Cycles
TC2.11 18464 Cycles
TC2.12 18317 Cycles
TC2.13 18301 Cycles
TC2.14 18298 Cycles
TC2.15 18340 Cycles
TC2.16 18260 Cycles
TC2.17 18348 Cycles
TC2.18 18339 Cycles
TC2.19 18298 Cycles
TC2.20 8846 Cycles
TC2.21 4123 Cycles
TC2.22 18387 Cycles
TC2.23 11511 Cycles
TC2.24 1035 Cycles
TC2.25 4156 Cycles

Description Vector Description

TS2.1MtrDrvInitStart_Cnt_lgc=Min
TS2.2MtrDrvInitStart_Cnt_lgc=Max
TS2.3SVDiag_MtrDrvInitComp_Cnt_M_lgc=Min
TS2.4SVDiag_MtrDrvInitComp_Cnt_M_lgc=Max
TS2.5SVDiag_GateDriveFitAcc_Cnt_M_u16=Min
TS2.6SVDiag_GateDriveFitAcc_Cnt_M_u16=Max
TS2.7SVDiag_GateDriveFitAcc_Cnt_M_u16=Pos
TS2.8SVDiag_GenGateDriveFitAcc_Cnt_M_u16=Min
TS2.9SVDiag_GenGateDriveFitAcc_Cnt_M_u16=Max
TS2.10SVDiag_GenGateDriveFitAcc_Cnt_M_u16=Pos
TS2.11SVDiag_OnStateFitAcc_Cnt_M_u16=Min
TS2.12SVDiag_OnStateFitAcc_Cnt_M_u16=Max
TS2.13SVDiag_OnStateFitAcc_Cnt_M_u16=Pos
TS2.14k_GateDriveDiag_Cnt_str.NStep=Min
TS2.15k_GateDriveDiag_Cnt_str.NStep=Max
TS2.16k_GateDriveDiag_Cnt_str.NStep=Pos
TS2.17k_OnStateDiag_Cnt_str.NStep=Min
TS2.18k_OnStateDiag_Cnt_str.NStep=Max
TS2.19k_OnStateDiag_Cnt_str.NStep=Pos
TS2.20SVDiag_ResetWaitLoop_Cnt_M_lgc=Min
TS2.21SVDiag_ResetWaitLoop_Cnt_M_lgc=Max
TS2.22Rte_Call_FetFit1Data_OP_GET and Rte_Call_FetFit2Clk_OP_GET=False
TS2.23Rte_Call_FetFit1Data_OP_GET and Rte_Call_FetFit2Clk_OP_GET=True
TS2.24All Min
TS2.25All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	10
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125
SVDiag_OnStateFitAcc_Cnt_M_u16	1111
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	1000
k_GateDriveDiag_Cnt_str.PStep	5000
k_GateDriveDiag_Cnt_str.NStep	11110
k_OnStateDiag_Cnt_str.Threshold	1111
k_OnStateDiag_Cnt_str.PStep	11000
k_OnStateDiag_Cnt_str.NStep	1000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	1000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	11100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	5000

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	100000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvrInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvrInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000	10000 ± 1	✔
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	10	10 ± 1	✔
SVDiag_MtrDrvrInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvrInitComp_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvrInitStartTime_mS_M_u32p0	3125	3125 ± 1	✔
SVDiag_OnStateFitAcc_Cnt_M_u16	1111	1111 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✔
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✔
target_MtrDrvDiag_Per1_MtrDrvrInitComplete_Cnt_lgc.value	0	0	✔

T					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓	
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.2 (Repeat Count = 1)				
Name		Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)		target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)		target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag		target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum		1		
SVDiag_FETFaultType_Cnt_M_enum		1		
SVDiag_GateDriveFitAcc_Cnt_M_u16		20000		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16		20		
SVDiag_MtrDrvInitActive_Cnt_M_lgc		1		
SVDiag_MtrDrvInitComp_Cnt_M_lgc		0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0		747		
SVDiag_OnStateFitAcc_Cnt_M_u16		2222		
SVDiag_ResetWaitLoop_Cnt_M_lgc		1		
k_GateDriveDiag_Cnt_str.Threshold		2000		
k_GateDriveDiag_Cnt_str.PStep		10000		
k_GateDriveDiag_Cnt_str.NStep		22220		
k_OnStateDiag_Cnt_str.Threshold		2222		
k_OnStateDiag_Cnt_str.PStep		12000		
k_OnStateDiag_Cnt_str.NStep		2000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value		1		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse		2000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse		12100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		8000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		200000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum		target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result	
SVDiag_FETFaultPhase_Cnt_M_enum	1	1		
SVDiag_FETFaultType_Cnt_M_enum	1	1		
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000	20000 ± 1		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	20	20 ± 1		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1		

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Name	Actual Value	Expected Value	Result
SVDiag_OnStateFltAcc_Cnt_M_u16	2222	2222 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
MotorDriverInit	1	MotorDriverInit	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.3 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_GateDriveFltAcc_Cnt_M_u16	50000		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	50		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	344		
SVDiag_OnStateFltAcc_Cnt_M_u16	5555		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	5000		
k_GateDriveDiag_Cnt_str.PStep	25000		
k_GateDriveDiag_Cnt_str.NStep	55550		
k_OnStateDiag_Cnt_str.Threshold	5555		
k_OnStateDiag_Cnt_str.PStep	15000		
k_OnStateDiag_Cnt_str.NStep	5000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	11000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	15100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	17000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	500000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✔
SVDiag_FETFaultType_Cnt_M_enum	1	1	✔
SVDiag_GateDriveFltAcc_Cnt_M_u16	50000	50000 ± 1	✔
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	50	50 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	344	344 ± 1	✔
SVDiag_OnStateFltAcc_Cnt_M_u16	5555	5555 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✔

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Name	Actual Value	Expected Value	Result
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	0	0	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.4 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_GateDriveFitAcc_Cnt_M_u16	60000		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	60		
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1		
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653		
SVDiag_OnStateFitAcc_Cnt_M_u16	6666		
SVDiag_ResetWaitLoop_Cnt_M_Igc	1		
k_GateDriveDiag_Cnt_str.Threshold	10000		
k_GateDriveDiag_Cnt_str.PStep	30000		
k_GateDriveDiag_Cnt_str.NStep	10000		
k_OnStateDiag_Cnt_str.Threshold	6666		
k_OnStateDiag_Cnt_str.PStep	16000		
k_OnStateDiag_Cnt_str.NStep	6000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	14000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	16100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	20000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	600000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_GateDriveFitAcc_Cnt_M_u16	60000	60000 ± 1	✔
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	60	60 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✔
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✔
SVDiag_OnStateFitAcc_Cnt_M_u16	6666	6666 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✔
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✔
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✔

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_GateDriveFltAcc_Cnt_M_u16	0		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	70		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462		
SVDiag_OnStateFltAcc_Cnt_M_u16	7777		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	15000		
k_GateDriveDiag_Cnt_str.PStep	35000		
k_GateDriveDiag_Cnt_str.NStep	20000		
k_OnStateDiag_Cnt_str.Threshold	7777		
k_OnStateDiag_Cnt_str.PStep	17000		
k_OnStateDiag_Cnt_str.NStep	7000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	17000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	17100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	23000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	700000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	15000	15000 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	70	70 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462	83462 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	7777	7777 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.6 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	0	
SVDiag_FETFaultType_Cnt_M_enum	1	
SVDiag_GateDriveFitAcc_Cnt_M_u16	65535	
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	80	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1233	
SVDiag_OnStateFitAcc_Cnt_M_u16	8888	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	
k_GateDriveDiag_Cnt_str.Threshold	20000	
k_GateDriveDiag_Cnt_str.PStep	40000	
k_GateDriveDiag_Cnt_str.NStep	30000	
k_OnStateDiag_Cnt_str.Threshold	8888	
k_OnStateDiag_Cnt_str.PStep	18000	
k_OnStateDiag_Cnt_str.NStep	8000	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0	
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	20000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	18100	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	26000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	800000	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000	20000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	80	80 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1233	1233 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	8888	8888 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ProcGateDriveFit	1	ProcGateDriveFit	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	1
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFitAcc_Cnt_M_u16	3000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	90
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832
SVDiag_OnStateFitAcc_Cnt_M_u16	9999
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	25000
k_GateDriveDiag_Cnt_str.PStep	45000
k_GateDriveDiag_Cnt_str.NStep	40000
k_OnStateDiag_Cnt_str.Threshold	9999
k_OnStateDiag_Cnt_str.PStep	19000
k_OnStateDiag_Cnt_str.NStep	9000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	0
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	23000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	19100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	100000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	900000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	3000	3000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	90	90 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832	832 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9999	9999 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	2	
SVDiag_FETFaultType_Cnt_M_enum	2	
SVDiag_GateDriveFltAcc_Cnt_M_u16	4000	
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	0	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4234	
SVDiag_OnStateFltAcc_Cnt_M_u16	11110	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	
k_GateDriveDiag_Cnt_str.Threshold	30000	
k_GateDriveDiag_Cnt_str.PStep	50000	
k_GateDriveDiag_Cnt_str.NStep	50000	
k_OnStateDiag_Cnt_str.Threshold	11110	
k_OnStateDiag_Cnt_str.PStep	20000	
k_OnStateDiag_Cnt_str.NStep	10000	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1	
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	1	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	26000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	20100	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	200000000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1000000	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	4000	4000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4234	4234 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	11110	11110 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ProcBridgeFit	1	ProcBridgeFit	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	1
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFitAcc_Cnt_M_u16	5000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	200
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	2929
SVDiag_OnStateFitAcc_Cnt_M_u16	12221
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	35000
k_GateDriveDiag_Cnt_str.PStep	55000
k_GateDriveDiag_Cnt_str.NStep	60000
k_OnStateDiag_Cnt_str.Threshold	12221
k_OnStateDiag_Cnt_str.PStep	21000
k_OnStateDiag_Cnt_str.NStep	11000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	0
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	29000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	21100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	300000000

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	1100000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	5000	5000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	200	200 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_ms_M_u32p0	2929	2929 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	12221	12221 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓

T	Actual Function	Count	Expected Function	Count	Result
	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
	ProcBridgeFit	1	ProcBridgeFit	1	✓
	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
	ResetGateDrive	1	ResetGateDrive	1	✓
	SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
	GateDrvWaitTime	1	GateDrvWaitTime	1	✓
	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
	ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)	Name	Input Value
	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_ms_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_ms_u16_ElapsedTime
	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_ms_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_ms_u32_CurrentTime
	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
	Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
	SVDiag_FETFaultPhase_Cnt_M_enum	2
	SVDiag_FETFaultType_Cnt_M_enum	2
	SVDiag_GateDriveFitAcc_Cnt_M_u16	6000
	SVDiag_GenGateDriveFitAcc_Cnt_M_u16	75
	SVDiag_MtrDrvInitActive_Cnt_M_lgc	1
	SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
	SVDiag_MtrDrvInitStartTime_ms_M_u32p0	10000
	SVDiag_OnStateFitAcc_Cnt_M_u16	13332
	SVDiag_ResetWaitLoop_Cnt_M_lgc	0
	k_GateDriveDiag_Cnt_str.Threshold	40000
	k_GateDriveDiag_Cnt_str.PStep	60000
	k_GateDriveDiag_Cnt_str.NStep	11000
	k_OnStateDiag_Cnt_str.Threshold	13332
	k_OnStateDiag_Cnt_str.PStep	22000
	k_OnStateDiag_Cnt_str.NStep	12000
	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1
	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	32000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	22100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	400000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1200000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	6000	6000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	75	75 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	10000	10000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	13332	13332 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.11 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	3	
SVDiag_FETFaultType_Cnt_M_enum	0	
SVDiag_GateDriveFltAcc_Cnt_M_u16	7000	
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	100	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	20000	
SVDiag_OnStateFltAcc_Cnt_M_u16	0	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	
k_GateDriveDiag_Cnt_str.Threshold	45000	
k_GateDriveDiag_Cnt_str.PStep	65000	
k_GateDriveDiag_Cnt_str.NStep	3333	
k_OnStateDiag_Cnt_str.Threshold	14443	
k_OnStateDiag_Cnt_str.PStep	23000	
k_OnStateDiag_Cnt_str.NStep	1111	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1	
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	35000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	23100	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	500000000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1300000	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	3667	3667 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	100	100 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	20000	20000 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	1
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFltAcc_Cnt_M_u16	8000
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	110
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	100000
SVDiag_OnStateFltAcc_Cnt_M_u16	65535
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	50000
k_GateDriveDiag_Cnt_str.PStep	11000
k_GateDriveDiag_Cnt_str.NStep	4444
k_OnStateDiag_Cnt_str.Threshold	11000
k_OnStateDiag_Cnt_str.PStep	24000
k_OnStateDiag_Cnt_str.NStep	2222
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	0
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	38000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	24100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	600000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1400000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	3556	3556 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	110	110 ± 1	✓
SVDiag_MtrDrvrInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvrInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvrInitStartTime_mS_M_u32p0	100000	100000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	63313	63313 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvrInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.13 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	2
SVDiag_GateDriveFitAcc_Cnt_M_u16	9000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	120
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	200000
SVDiag_OnStateFitAcc_Cnt_M_u16	3000
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	55000
k_GateDriveDiag_Cnt_str.PStep	12000
k_GateDriveDiag_Cnt_str.NStep	5555
k_OnStateDiag_Cnt_str.Threshold	12000
k_OnStateDiag_Cnt_str.PStep	25000
k_OnStateDiag_Cnt_str.NStep	3333
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	41000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	25100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	700000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	1500000
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	3445	3445 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	120	120 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	200000	200000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.14 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	3
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	130
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	300000
SVDiag_OnStateFitAcc_Cnt_M_u16	14100
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	60000
k_GateDriveDiag_Cnt_str.PStep	13000
k_GateDriveDiag_Cnt_str.NStep	0
k_OnStateDiag_Cnt_str.Threshold	13000
k_OnStateDiag_Cnt_str.PStep	26000
k_OnStateDiag_Cnt_str.NStep	4444
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	44000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	26100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	800000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1600000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000	10000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	130	130 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	300000	300000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9656	9656 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.15 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFitAcc_Cnt_M_u16	11000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	140
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	400000
SVDiag_OnStateFitAcc_Cnt_M_u16	15100
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	65000
k_GateDriveDiag_Cnt_str.PStep	14000
k_GateDriveDiag_Cnt_str.NStep	65535
k_OnStateDiag_Cnt_str.Threshold	14000
k_OnStateDiag_Cnt_str.PStep	27000
k_OnStateDiag_Cnt_str.NStep	5555
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	47000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	2222
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	900000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1700000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	140	140 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	400000	400000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9545	9545 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.16 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	1
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFitAcc_Cnt_M_u16	12000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	150
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	500000
SVDiag_OnStateFitAcc_Cnt_M_u16	16100
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	11111
k_GateDriveDiag_Cnt_str.PStep	15000
k_GateDriveDiag_Cnt_str.NStep	3000
k_OnStateDiag_Cnt_str.Threshold	15000
k_OnStateDiag_Cnt_str.PStep	28000
k_OnStateDiag_Cnt_str.NStep	6666
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	50000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	3333
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	15000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1800000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	9000	9000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	150	150 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	500000	500000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9434	9434 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.17 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal			
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime			
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag			
SVDiag_FETFaultPhase_Cnt_M_enum	2			
SVDiag_FETFaultType_Cnt_M_enum	0			
SVDiag_GateDriveFitAcc_Cnt_M_u16	13000			
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	160			
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0			
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	600000			
SVDiag_OnStateFitAcc_Cnt_M_u16	17100			
SVDiag_ResetWaitLoop_Cnt_M_lgc	0			
k_GateDriveDiag_Cnt_str.Threshold	22222			
k_GateDriveDiag_Cnt_str.PStep	16000			
k_GateDriveDiag_Cnt_str.NStep	9999			
k_OnStateDiag_Cnt_str.Threshold	16000			
k_OnStateDiag_Cnt_str.PStep	29000			
k_OnStateDiag_Cnt_str.NStep	0			
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1			
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0			
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	53000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	4444			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	20000000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	1900000			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓	
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓	
SVDiag_GateDriveFitAcc_Cnt_M_u16	3001	3001 ± 1	✓	
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	160	160 ± 1	✓	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	600000	600000 ± 1	✓	
SVDiag_OnStateFitAcc_Cnt_M_u16	17100	17100 ± 1	✓	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓	
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓	
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓	
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓	
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓	
NTC	0x4D	0x4D	✓	
Param	0x00	0x00	✓	
Status	0x00	0x00	✓	
NTC	0x4E	0x4E	✓	
Param	0x00	0x00	✓	
Status	0x00	0x00	✓	
NTC	0x4F	0x4F	✓	
Param	0x00	0x00	✓	
Status	0x00	0x00	✓	

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.18 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_GateDriveFltAcc_Cnt_M_u16	14000		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	170		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	700000		
SVDiag_OnStateFltAcc_Cnt_M_u16	65535		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	33333		
k_GateDriveDiag_Cnt_str.PStep	17000		
k_GateDriveDiag_Cnt_str.NStep	11110		
k_OnStateDiag_Cnt_str.Threshold	17000		
k_OnStateDiag_Cnt_str.PStep	30000		
k_OnStateDiag_Cnt_str.NStep	65535		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	56000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	5555		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	25000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	2000000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_GateDriveFltAcc_Cnt_M_u16	2890	2890 ± 1	✔
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	170	170 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	700000	700000 ± 1	✔
SVDiag_OnStateFltAcc_Cnt_M_u16	0	0 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✔
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✔
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✔
NTC	0x4D	0x4D	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x4E	0x4E	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x4F	0x4F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.19 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	2
SVDiag_GateDriveFltAcc_Cnt_M_u16	15000
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	180
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	800000
SVDiag_OnStateFltAcc_Cnt_M_u16	19100
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_GateDriveDiag_Cnt_str.Threshold	44444
k_GateDriveDiag_Cnt_str.PStep	18000
k_GateDriveDiag_Cnt_str.NStep	12221
k_OnStateDiag_Cnt_str.Threshold	18000
k_OnStateDiag_Cnt_str.PStep	31000
k_OnStateDiag_Cnt_str.NStep	3000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	59000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	6666
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	30000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	2100000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	2779	2779 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	180	180 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	800000	800000 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	16100	16100 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 2.20 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_GateDriveFitAcc_Cnt_M_u16	16000		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	190		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	900000		
SVDiag_OnStateFitAcc_Cnt_M_u16	20100		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	55555		
k_GateDriveDiag_Cnt_str.PStep	19000		
k_GateDriveDiag_Cnt_str.NStep	13332		
k_OnStateDiag_Cnt_str.Threshold	19000		
k_OnStateDiag_Cnt_str.PStep	32000		
k_OnStateDiag_Cnt_str.NStep	11110		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	62000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	7777		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	35000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	2200000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_GateDriveFitAcc_Cnt_M_u16	16000	16000 ± 1	✔
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	190	190 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	900000	900000 ± 1	✔
SVDiag_OnStateFitAcc_Cnt_M_u16	19000	19000 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✔
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✔
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✔
NTC	0x4C	0x4C	✔
Param	0x01	0x01	✔
Status	0x01	0x01	✔



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	1	
SVDiag_FETFaultType_Cnt_M_enum	1	
SVDiag_GateDriveFitAcc_Cnt_M_u16	17000	
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	85	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1000000	
SVDiag_OnStateFitAcc_Cnt_M_u16	21100	
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	
k_GateDriveDiag_Cnt_str.Threshold	11000	
k_GateDriveDiag_Cnt_str.PStep	20000	
k_GateDriveDiag_Cnt_str.NStep	11100	
k_OnStateDiag_Cnt_str.Threshold	20000	
k_OnStateDiag_Cnt_str.PStep	33000	
k_OnStateDiag_Cnt_str.NStep	12221	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0	
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	65000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	8888	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	40000000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	2300000	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	17000	17000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	85	85 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1000000	1000000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	21100	21100 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_FetFit2Clik_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clik_OP_GET_signal			
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime			
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag			
SVDiag_FETFaultPhase_Cnt_M_enum	2			
SVDiag_FETFaultType_Cnt_M_enum	2			
SVDiag_GateDriveFitAcc_Cnt_M_u16	30000			
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	115			
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1			
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1321			
SVDiag_OnStateFitAcc_Cnt_M_u16	3333			
SVDiag_ResetWaitLoop_Cnt_M_Igc	0			
k_GateDriveDiag_Cnt_str.Threshold	3000			
k_GateDriveDiag_Cnt_str.PStep	15000			
k_GateDriveDiag_Cnt_str.NStep	33330			
k_OnStateDiag_Cnt_str.Threshold	3333			
k_OnStateDiag_Cnt_str.PStep	13000			
k_OnStateDiag_Cnt_str.NStep	3000			
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1			
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clik_OP_GET_signal	0			
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	5000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	13100			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	45000000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	300000			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc			
Name	Actual Value	Expected Value	Result	
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓	

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Name	Actual Value	Expected Value	Result
SVDiag_GateDriveFltAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	115	115 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	1321	1321 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	333	333 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_GateDriveFltAcc_Cnt_M_u16	40000		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	178		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	900		
SVDiag_OnStateFltAcc_Cnt_M_u16	4444		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	4000		
k_GateDriveDiag_Cnt_str.PStep	20000		
k_GateDriveDiag_Cnt_str.NStep	44440		
k_OnStateDiag_Cnt_str.Threshold	4444		
k_OnStateDiag_Cnt_str.PStep	14000		
k_OnStateDiag_Cnt_str.NStep	4000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	8000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	14100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	50000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	400000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_GateDriveFltAcc_Cnt_M_u16	4000	4000 ± 1	✔
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	178	178 ± 1	✔

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Name	Actual Value	Expected Value	Result
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	900	900 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	4444	4444 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag_Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	0	
SVDiag_FETFaultType_Cnt_M_enum	0	
SVDiag_GateDriveFitAcc_Cnt_M_u16	0	
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	0	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0	
SVDiag_OnStateFitAcc_Cnt_M_u16	0	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	
k_GateDriveDiag_Cnt_str.Threshold	0	
k_GateDriveDiag_Cnt_str.PStep	0	
k_GateDriveDiag_Cnt_str.NStep	0	
k_OnStateDiag_Cnt_str.Threshold	0	
k_OnStateDiag_Cnt_str.PStep	0	
k_OnStateDiag_Cnt_str.NStep	0	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0	
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	50	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	0	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	0	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	0	0 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_GateDriveFitAcc_Cnt_M_u16	65535		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	200		
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1		
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295		
SVDiag_OnStateFitAcc_Cnt_M_u16	65535		
SVDiag_ResetWaitLoop_Cnt_M_Igc	1		
k_GateDriveDiag_Cnt_str.Threshold	65535		
k_GateDriveDiag_Cnt_str.PStep	65535		
k_GateDriveDiag_Cnt_str.NStep	65535		
k_OnStateDiag_Cnt_str.Threshold	65535		
k_OnStateDiag_Cnt_str.PStep	65535		
k_OnStateDiag_Cnt_str.NStep	65535		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	65535		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	65535		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	4294967295		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	4294967295		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_GateDriveFitAcc_Cnt_M_u16	65535	65535 ± 1	✔
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	200	200 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✔
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	4294967295	4294967295 ± 1	✔
SVDiag_OnStateFitAcc_Cnt_M_u16	65535	65535 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✔

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MtrDrvDiag_Per1

Name	Actual Value	Expected Value	Result
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Case 3: Path Test

Specification	Description
<p>Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:</p> <p>TC3.1 1123 Cycles TC3.2 4263 Cycles TC3.3 11559 Cycles TC3.4 1035 Cycles TC3.5 8755 Cycles TC3.6 18481 Cycles TC3.7 18303 Cycles TC3.8 18385 Cycles TC3.9 18481 Cycles</p>	<p>Vector Description</p> <p>TS3.1"(TRUE == MtrDrvInitComp_Cnt_T_Igc)=False && (TRUE == MtrDrvInitStart_Cnt_T_Igc)=False" TS3.2"(TRUE == MtrDrvInitComp_Cnt_T_Igc)=True && (TRUE == SVDiag_ResetWaitLoop_Cnt_M_Igc) =True " TS3.3"(TRUE == SVDiag_ResetWaitLoop_Cnt_M_Igc) =False && (FALSE == FF1Set_Cnt_T_Igc)=False && (TRUE == FF1Set_Cnt_T_Igc)=True && (TRUE != SVDiag_ResetWaitLoop_Cnt_M_Igc)= True" TS3.4"(TRUE == MtrDrvInitStart_Cnt_T_Igc) =True" TS3.5"(FALSE == FF1Set_Cnt_T_Igc) =True && (FALSE == FF2Set_Cnt_T_Igc)=False && (TRUE == FF1Set_Cnt_T_Igc)=False" TS3.6"(FALSE == FF1Set_Cnt_T_Igc) =True && (FALSE == FF2Set_Cnt_T_Igc)=True && ((GateDriveFltAcc_Cnt_T_u16)>(k_GateDriveDiag_Cnt_str.Nstep))=True && ((OnStateFltAcc_Cnt_T_u16)>(k_OnStateDiag_Cnt_str.Nstep))=False " TS3.7((GateDriveFltAcc_Cnt_T_u16)>(k_GateDriveDiag_Cnt_str.Nstep))=False TS3.8((OnStateFltAcc_Cnt_T_u16)>(k_OnStateDiag_Cnt_str.Nstep))=True TS3.9 If (True = MtrDrvInitComp_Cnt_T_Igc) =True,If (True = SVDiag_ResetWaitLoop_Cnt_M_Igc)=False,If (True = FF1Set_Cnt_T_Igc)=False</p>

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_GateDriveFltAcc_Cnt_M_u16	10000
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	10
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1
SVDiag_MtrDrvInitComp_Cnt_M_Igc	0
SVDiag_MtrDrvInitStartTime_mS_u32p0	3125
SVDiag_OnStateFltAcc_Cnt_M_u16	1111
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	1000

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MtrDrvDiag_Per1

Name	Input Value
k_GateDriveDiag_Cnt_str.PStep	5000
k_GateDriveDiag_Cnt_str.NStep	11110
k_OnStateDiag_Cnt_str.Threshold	1111
k_OnStateDiag_Cnt_str.PStep	11000
k_OnStateDiag_Cnt_str.NStep	1000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	0
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	1000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	11100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	5000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	100000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	10000	10000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	10	10 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	3125	3125 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	1111	1111 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)

Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_GateDriveFitAcc_Cnt_M_u16	60000
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	60
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653
SVDiag_OnStateFitAcc_Cnt_M_u16	6666
SVDiag_ResetWaitLoop_Cnt_M_Igc	1
k_GateDriveDiag_Cnt_str.Threshold	10000
k_GateDriveDiag_Cnt_str.PStep	30000
k_GateDriveDiag_Cnt_str.NStep	10000
k_OnStateDiag_Cnt_str.Threshold	6666
k_OnStateDiag_Cnt_str.PStep	16000
k_OnStateDiag_Cnt_str.NStep	6000
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	14000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	16100
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	20000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	600000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	60000	60000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	60	60 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	6653	6653 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	6666	6666 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)				
Name	Input Value			
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal			
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime			
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag			
SVDiag_FETFaultPhase_Cnt_M_enum	3			
SVDiag_FETFaultType_Cnt_M_enum	2			
SVDiag_GateDriveFitAcc_Cnt_M_u16	0			
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	70			
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0			
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1			
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462			
SVDiag_OnStateFitAcc_Cnt_M_u16	7777			
SVDiag_ResetWaitLoop_Cnt_M_Igc	0			
k_GateDriveDiag_Cnt_str.Threshold	15000			
k_GateDriveDiag_Cnt_str.PStep	35000			
k_GateDriveDiag_Cnt_str.NStep	20000			
k_OnStateDiag_Cnt_str.Threshold	7777			
k_OnStateDiag_Cnt_str.PStep	17000			
k_OnStateDiag_Cnt_str.NStep	7000			
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1			
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1			
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	1			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	17000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	17100			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	23000			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	700000			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc			
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc			
Name	Actual Value	Expected Value	Result	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓	

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MtrDrvDiag_Per1

Name	Actual Value	Expected Value	Result
SVDiag_GateDriveFltAcc_Cnt_M_u16	15000	15000 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	70	70 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	83462	83462 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	7777	7777 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime	
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag	
SVDiag_FETFaultPhase_Cnt_M_enum	1	
SVDiag_FETFaultType_Cnt_M_enum	1	
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000	
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	20	
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0	
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	
SVDiag_OnStateFitAcc_Cnt_M_u16	2222	
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	
k_GateDriveDiag_Cnt_str.Threshold	2000	
k_GateDriveDiag_Cnt_str.PStep	10000	
k_GateDriveDiag_Cnt_str.NStep	22220	
k_OnStateDiag_Cnt_str.Threshold	2222	
k_OnStateDiag_Cnt_str.PStep	12000	
k_OnStateDiag_Cnt_str.NStep	2000	
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1	
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	1	
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	2000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	12100	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	8000	
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	200000	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum	
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000	20000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	20	20 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	2222	2222 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	1	1	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓

T	Actual Function	Count	Expected Function	Count	Result
	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
	MotorDriverInit	1	MotorDriverInit	1	✓
	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16	1	✓
	ResetGateDrive	1	ResetGateDrive	1	✓
	SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
	GateDrvWaitTime	1	GateDrvWaitTime	1	✓
	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
	ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)				
Name		Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)		target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)		target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)		target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag		target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum		1		
SVDiag_FETFaultType_Cnt_M_enum		1		
SVDiag_GateDriveFitAcc_Cnt_M_u16		3000		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16		90		
SVDiag_MtrDrvInitActive_Cnt_M_lgc		0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc		1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0		832		
SVDiag_OnStateFitAcc_Cnt_M_u16		9999		
SVDiag_ResetWaitLoop_Cnt_M_lgc		0		
k_GateDriveDiag_Cnt_str.Threshold		25000		
k_GateDriveDiag_Cnt_str.PStep		45000		
k_GateDriveDiag_Cnt_str.NStep		40000		
k_OnStateDiag_Cnt_str.Threshold		9999		
k_OnStateDiag_Cnt_str.PStep		19000		
k_OnStateDiag_Cnt_str.NStep		9000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value		0		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal		1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc		0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse		23000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse		19100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		100000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		900000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum		target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3		

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MtrDrvDiag_Per1

Name	Actual Value	Expected Value	Result
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	3000	3000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	90	90 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	832	832 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9999	9999 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	2	2	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓
NTC	0x4C	0x4C	✓
Param	0x01	0x01	✓
Status	0x01	0x01	✓



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
ResetGateDrive	1	ResetGateDrive	1	✓
SuspendAllInterrupts	1	SuspendAllInterrupts	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓
ResumeAllInterrupts	1	ResumeAllInterrupts	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET_signal	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)		

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	3667	3667 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	100	100 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	20000	20000 ± 1	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	3	3	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_Igc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_GateDriveFltAcc_Cnt_M_u16	11000
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	140
SVDiag_MtrDrvInitActive_Cnt_M_Igc	0
SVDiag_MtrDrvInitComp_Cnt_M_Igc	1
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	400000
SVDiag_OnStateFltAcc_Cnt_M_u16	15100
SVDiag_ResetWaitLoop_Cnt_M_Igc	0
k_GateDriveDiag_Cnt_str.Threshold	65000
k_GateDriveDiag_Cnt_str.PStep	14000
k_GateDriveDiag_Cnt_str.NStep	65535
k_OnStateDiag_Cnt_str.Threshold	14000
k_OnStateDiag_Cnt_str.PStep	27000
k_OnStateDiag_Cnt_str.NStep	5555
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_Igc.value	1
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_Igc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	47000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	2222
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	900000000
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	1700000
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_Igc

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MtrDrvDiag_Per1

Name	Input Value		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_Igc	target_MtrDrvDiag_Per1_MtrDrvrInitStart_Cnt_Igc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	140	140 ± 1	✓
SVDiag_MtrDrvrInitActive_Cnt_M_Igc	0	0	✓
SVDiag_MtrDrvrInitComp_Cnt_M_Igc	1	1	✓
SVDiag_MtrDrvrInitStartTime_mS_M_u32p0	400000	400000 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	9545	9545 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_Igc	0	0	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	0	0	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_Igc.value	0	0	✓
target_MtrDrvDiag_Per1_MtrDrvrInitComplete_Cnt_Igc.value	1	1	✓
NTC	0x4D	0x4D	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4E	0x4E	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓
NTC	0x4F	0x4F	✓
Param	0x00	0x00	✓
Status	0x00	0x00	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 3.8 (Repeat Count = 1)

Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_GateDriveFltAcc_Cnt_M_u16	8000		
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	110		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	100000		
SVDiag_OnStateFltAcc_Cnt_M_u16	65535		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	50000		
k_GateDriveDiag_Cnt_str.PStep	11000		
k_GateDriveDiag_Cnt_str.NStep	4444		
k_OnStateDiag_Cnt_str.Threshold	11000		
k_OnStateDiag_Cnt_str.PStep	24000		
k_OnStateDiag_Cnt_str.NStep	2222		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	0		
target_Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_GET_signal	0		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	38000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	24100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	600000000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	1400000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✔
SVDiag_FETFaultType_Cnt_M_enum	1	1	✔
SVDiag_GateDriveFltAcc_Cnt_M_u16	3556	3556 ± 1	✔
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	110	110 ± 1	✔
SVDiag_MtrDrvInitActive_Cnt_M_lgc	0	0	✔
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✔
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	100000	100000 ± 1	✔
SVDiag_OnStateFltAcc_Cnt_M_u16	63313	63313 ± 1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✔
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✔
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	0	0	✔
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✔
NTC	0x4D	0x4D	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x4E	0x4E	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔
NTC	0x4F	0x4F	✔
Param	0x00	0x00	✔
Status	0x00	0x00	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP0_CheckpointReached	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	3	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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MtrDrvDiag_Per1

Test Step 3.9 (Repeat Count = 1)			
Name	Input Value		
Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET(signal)	target_Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET_signal		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16(ElapsedTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_ElapsedTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTime		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
Rte_Inst_Sa_MtrDrvDiag	target_Rte_Inst_Sa_MtrDrvDiag		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000		
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	20		
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1		
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1		
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747		
SVDiag_OnStateFitAcc_Cnt_M_u16	2222		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_GateDriveDiag_Cnt_str.Threshold	2000		
k_GateDriveDiag_Cnt_str.PStep	10000		
k_GateDriveDiag_Cnt_str.NStep	22220		
k_OnStateDiag_Cnt_str.Threshold	2222		
k_OnStateDiag_Cnt_str.PStep	12000		
k_OnStateDiag_Cnt_str.NStep	2000		
target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc.value	1		
target_Rte_Call_Sa_MtrDrvDiag_FetFit2Cik_OP_GET_signal	1		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_mS_u16_Elapse	2000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16_Elapse	12100		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_mS_u32_CurrentTi	8000		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTi	200000		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_FETFaultType_Cnt_enum	target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc		
target_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc	target_MtrDrvDiag_Per1_MtrDrvInitStart_Cnt_lgc		
Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_GateDriveFitAcc_Cnt_M_u16	20000	20000 ± 1	✓
SVDiag_GenGateDriveFitAcc_Cnt_M_u16	20	20 ± 1	✓
SVDiag_MtrDrvInitActive_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	1	1	✓
SVDiag_MtrDrvInitStartTime_mS_M_u32p0	747	747 ± 1	✓
SVDiag_OnStateFitAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_MtrDrvDiag_Per1_FETFaultPhase_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_FETFaultType_Cnt_enum.value	1	1	✓
target_MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc.value	1	1	✓
target_MtrDrvDiag_Per1_MtrDrvInitComplete_Cnt_lgc.value	1	1	✓



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MtrDrvDiag Per1



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MtrDrvDiag_Per1

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	Rte_Call_MtrDrvDiag_Per1_CP1_CheckpointReached	1	✓

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Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	ProcBridgeFlt

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
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Module 'MtrDrvDiag'

*****UNIT TEST DESCRIPTION*****

Name of Tester: Spoorti Mali
Code File(s) Under Test: Sa_MtrDrvDiag.c
Code File(s) Version: 22
Module Design Document:Motor_Driver_Diagnostics_MDD.doc
Module Design Document Version: 11
Data Dictionary Version: 11
Unit Test Plan Version: 3
Optimization Level:Level 2
Compiler (CodeGen) Version: TMS470_4.9.5
Model Type: Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31
Total FLASH Used (Bytes): 1334
Total RAM Used (Bytes): 17
Total CALS Used (Bytes): 50
Special Test Requirements:
Test Date:01-29-2015
Comments:

Note 1:Inline function defined in ""Gloabalmacro.h""
is not unit Tested.

Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.

Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and
""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.

Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"" ,""ReadMtrDrvFitData"" functions,
""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.

Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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ProcBridgeFit



Test Case 1: Metrics Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 68 Cycles TC1.2 1190725 Cycles
Description	Vector Description TS1.1"Shortest Execution Path: ((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep))=False && ((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep))=True && (TRUE == DiagFailed_m(*OnStateFitAccPtr_Cnt_T_u16, k_OnStateDiag_Cnt_str))=False " TS1.2"Longest Execution Path: ((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep))=False && ((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep))=True && (TRUE == DiagFailed_m(*OnStateFitAccPtr_Cnt_T_u16, k_OnStateDiag_Cnt_str))=True && (TRUE == OnStateFitActive_Cnt_T_lgc)=False && FaultedPhases_Cnt_T_u16 > 1U)=False && (FaultedPhases_Cnt_T_u16 == 1U)=False && (*GenGateDriveFitAccPtr_Cnt_T_u16 >= 100U)"

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	65535		
k_OnStateDiag_Cnt_str.PStep	1318		
target_GenGateDriveFitAccPtr_Cnt_T_u16	20		
target_OnStateFitAccPtr_Cnt_T_u16	713		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	20	20	✔
target_OnStateFitAccPtr_Cnt_T_u16	2031	2031	✔

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 1.2 (Repeat Count = 1)

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	455		
k_OnStateDiag_Cnt_str.PStep	376		
target_GenGateDriveFitAccPtr_Cnt_T_u16	200		
target_OnStateFitAccPtr_Cnt_T_u16	134		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔

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Name	Actual Value	Expected Value	Result
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	200	200	✓
target_OnStateFitAccPtr_Cnt_T_u16	455	455	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

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ProcBridgeFit



Test Case 2: Boundary Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TC2.1 1202989 Cycles
TC2.2 1198188 Cycles
TC2.3 1198036 Cycles
TC2.4 1197712 Cycles
TC2.5 1198050 Cycles
TC2.6 1195791 Cycles
TC2.7 1203125 Cycles
TC2.8 68 Cycles
TC2.9 1198621 Cycles
TC2.10 54 Cycles
TC2.11 1197449 Cycles
TC2.12 54 Cycles
TC2.13 1207741 Cycles
TC2.14 1205823 Cycles
TC2.15 1198495 Cycles
TC2.16 1329144 Cycles

Description Vector Description

TS2.1OnStateFitAccPtr_Cnt_T_u16=min
TS2.2OnStateFitAccPtr_Cnt_T_u16=max
TS2.3OnStateFitAccPtr_Cnt_T_u16=pos
TS2.4GenGateDriveFitAccPtr_Cnt_T_u16=min
TS2.5GenGateDriveFitAccPtr_Cnt_T_u16=max
TS2.6GenGateDriveFitAccPtr_Cnt_T_u16=pos
TS2.7k_OnStateDiag_Cnt_str.Threshold=min
TS2.8k_OnStateDiag_Cnt_str.Threshold=max
TS2.9k_OnStateDiag_Cnt_str.Threshold=pos
TS2.10k_OnStateDiag_Cnt_str.Pstep=min
TS2.11k_OnStateDiag_Cnt_str.Pstep=max
TS2.12k_OnStateDiag_Cnt_str.Pstep=pos
TS2.13Rte_Call_NxtrDiagMgr_GetNTCFailed=min
TS2.14Rte_Call_NxtrDiagMgr_GetNTCFailed=max
TS2.15All Min
TS2.16All Max

Test Step 2.1 (Repeat Count = 1)

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	0		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_OnStateDiag_Cnt_str.Threshold	123		
k_OnStateDiag_Cnt_str.PStep	567		
target_GenGateDriveFitAccPtr_Cnt_T_u16	120		
target_OnStateFitAccPtr_Cnt_T_u16	0		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	120	120	✔
target_OnStateFitAccPtr_Cnt_T_u16	123	123	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.2 (Repeat Count = 1) ✓

Name	Input Value
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	900
k_OnStateDiag_Cnt_str.PStep	675
target_GenGateDriveFltAccPtr_Cnt_T_u16	150
target_OnStateFltAccPtr_Cnt_T_u16	65535
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	1000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFltAccPtr_Cnt_T_u16	150	150	✓
target_OnStateFltAccPtr_Cnt_T_u16	900	900	✓

T ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.3 (Repeat Count = 1) ✓

Name	Input Value
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	3
SVDiag_FETFaultType_Cnt_M_enum	2
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	44
k_OnStateDiag_Cnt_str.PStep	842
target_GenGateDriveFltAccPtr_Cnt_T_u16	180
target_OnStateFltAccPtr_Cnt_T_u16	477
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0

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Name		Input Value		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir		2000		
Name	Actual Value	Expected Value	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	78	78	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓	
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓	
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓	
target_GenGateDriveFitAccPtr_Cnt_T_u16	180	180	✓	
target_OnStateFitAccPtr_Cnt_T_u16	0	0	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.4 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16		
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	2345		
k_OnStateDiag_Cnt_str.PStep	9211		
target_GenGateDriveFltAccPtr_Cnt_T_u16	0		
target_OnStateFltAccPtr_Cnt_T_u16	4577		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	78	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFltAccPtr_Cnt_T_u16	1	1	✔
target_OnStateFltAccPtr_Cnt_T_u16	0	0	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 2.5 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16		
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	455		
k_OnStateDiag_Cnt_str.PStep	376		
target_GenGateDriveFltAccPtr_Cnt_T_u16	200		
target_OnStateFltAccPtr_Cnt_T_u16	134		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFltAccPtr_Cnt_T_u16	100	100	✔
target_OnStateFltAccPtr_Cnt_T_u16	0	0	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.6 (Repeat Count = 1)				✓
Name	Input Value			
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16			
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime			
SVDiag_FETFaultPhase_Cnt_M_enum	0			
SVDiag_FETFaultType_Cnt_M_enum	2			
SVDiag_ResetWaitLoop_Cnt_M_lgc	0			
k_OnStateDiag_Cnt_str.Threshold	100			
k_OnStateDiag_Cnt_str.PStep	690			
target_GenGateDriveFltAccPtr_Cnt_T_u16	100			
target_OnStateFltAccPtr_Cnt_T_u16	366			
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	15000			
Name	Actual Value	Expected Value	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77		✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2		✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1		✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0		✓
SVDiag_FETFaultType_Cnt_M_enum	0	0		✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0		✓
target_GenGateDriveFltAccPtr_Cnt_T_u16	100	100		✓
target_OnStateFltAccPtr_Cnt_T_u16	0	0		✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.7 (Repeat Count = 1)				✓
Name	Input Value			
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16			
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16			
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc			
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime			
SVDiag_FETFaultPhase_Cnt_M_enum	1			
SVDiag_FETFaultType_Cnt_M_enum	1			
SVDiag_ResetWaitLoop_Cnt_M_lgc	0			
k_OnStateDiag_Cnt_str.Threshold	0			
k_OnStateDiag_Cnt_str.PStep	1004			
target_GenGateDriveFltAccPtr_Cnt_T_u16	10			
target_OnStateFltAccPtr_Cnt_T_u16	854			
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0			
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	0			
Name	Actual Value	Expected Value	Result	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓	
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓	
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓	
target_GenGateDriveFltAccPtr_Cnt_T_u16	10	10	✓	
target_OnStateFltAccPtr_Cnt_T_u16	0	0	✓	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.8 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	65535		
k_OnStateDiag_Cnt_str.PStep	1318		
target_GenGateDriveFitAccPtr_Cnt_T_u16	20		
target_OnStateFitAccPtr_Cnt_T_u16	713		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	20	20	✔
target_OnStateFitAccPtr_Cnt_T_u16	2031	2031	✔

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Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓



Test Step 2.9 (Repeat Count = 1)





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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.10 (Repeat Count = 1) ✓

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	1
SVDiag_FETFaultType_Cnt_M_enum	2
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_OnStateDiag_Cnt_str.Threshold	900
k_OnStateDiag_Cnt_str.PStep	0
target_GenGateDriveFitAccPtr_Cnt_T_u16	40
target_OnStateFitAccPtr_Cnt_T_u16	66
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	50000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	40	40	✔
target_OnStateFitAccPtr_Cnt_T_u16	66	66	✔

T ✓				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

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Test Step 2.11 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	44
k_OnStateDiag_Cnt_str.PStep	65535
target_GenGateDriveFitAccPtr_Cnt_T_u16	50
target_OnStateFitAccPtr_Cnt_T_u16	200
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	60000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	50	50	✓
target_OnStateFitAccPtr_Cnt_T_u16	44	44	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.12 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	2345
k_OnStateDiag_Cnt_str.PStep	555
target_GenGateDriveFitAccPtr_Cnt_T_u16	60
target_OnStateFitAccPtr_Cnt_T_u16	546
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	83000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	60	60	✓
target_OnStateFitAccPtr_Cnt_T_u16	1101	1101	✓

Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

Test Step 2.13 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	2

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Name	Input Value		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	20		
k_OnStateDiag_Cnt_str.PStep	555		
target_GenGateDriveFitAccPtr_Cnt_T_u16	100		
target_OnStateFitAccPtr_Cnt_T_u16	546		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	83000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	100	100	✔
target_OnStateFitAccPtr_Cnt_T_u16	0	0	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.14 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16		
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_ResetWaitLoop_Cnt_M_lgc	0		
k_OnStateDiag_Cnt_str.Threshold	500		
k_OnStateDiag_Cnt_str.PStep	235		
target_GenGateDriveFltAccPtr_Cnt_T_u16	110		
target_OnStateFltAccPtr_Cnt_T_u16	892		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	104350		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	1	1	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✔
target_GenGateDriveFltAccPtr_Cnt_T_u16	110	110	✔
target_OnStateFltAccPtr_Cnt_T_u16	500	500	✔

T ✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.15 (Repeat Count = 1) ✓

Name	Input Value
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_OnStateDiag_Cnt_str.Threshold	0
k_OnStateDiag_Cnt_str.PStep	0
target_GenGateDriveFltAccPtr_Cnt_T_u16	0
target_OnStateFltAccPtr_Cnt_T_u16	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0

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Name	Input Value		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTir	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✓
SVDiag_FETFaultType_Cnt_M_enum	0	0	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	1	1	✓
target_OnStateFitAccPtr_Cnt_T_u16	0	0	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	255	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓

Test Step 2.16 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16		
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	3		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	65535		
k_OnStateDiag_Cnt_str.PStep	65535		
target_GenGateDriveFltAccPtr_Cnt_T_u16	200		
target_OnStateFltAccPtr_Cnt_T_u16	65535		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFltAccPtr_Cnt_T_u16	200	200	✔
target_OnStateFltAccPtr_Cnt_T_u16	65535	65535	✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation
and WithPS Environment)
CPU Cycles:

TC3.1 1168975 Cycles
TC3.2 68 Cycles
TC3.3 1173591 Cycles
TC3.4 1173766 Cycles
TC3.5 1175178 Cycles
TC3.6 1180297 Cycles

Description

Vector Description

TS3.1'((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep)
=False &&
(TRUE == OnStateFitActive_Cnt_T_lgc)= False &&
(FaultedPhases_Cnt_T_u16 > 1U)
=True &&

1=(*OnStateFitAccPtr_Cnt_T_u16)>=(k_GateDriveDiag_Cnt_str.Threshold)?1:0=True &&
(*OnStateFitAccPtr_Cnt_T_u16)>=((k_GateDriveDiag_Cnt_str.Threshold))= True"
TS3.2'((*OnStateFitAccPtr_Cnt_T_u16)<((k_GateDriveDiag_Cnt_str.Threshold)-(k_GateDriveDiag_Cnt_str.PStep)
=True &&
1=(*OnStateFitAccPtr_Cnt_T_u16)>=(k_GateDriveDiag_Cnt_str.Threshold)?1:0=False &&
(*OnStateFitAccPtr_Cnt_T_u16)>=((k_GateDriveDiag_Cnt_str.Threshold))= False"
TS3.3'(TRUE == OnStateFitActive_Cnt_T_lgc)= True &&
(FaultedPhases_Cnt_T_u16 == 1U)= True"
TS3.4" (FaultedPhases_Cnt_T_u16 > 1U)
=False"
TS3.5"(FaultedPhases_Cnt_T_u16 == 1U)
=False &&
(*GenGateDriveFitAccPtr_Cnt_T_u16 >=100U)=False "
TS3.6(*GenGateDriveFitAccPtr_Cnt_T_u16 >=100U)=True

Test Step 3.1 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	0
SVDiag_FETFaultType_Cnt_M_enum	0
SVDiag_ResetWaitLoop_Cnt_M_lgc	0
k_OnStateDiag_Cnt_str.Threshold	123
k_OnStateDiag_Cnt_str.PStep	567
target_GenGateDriveFitAccPtr_Cnt_T_u16	120
target_OnStateFitAccPtr_Cnt_T_u16	0
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	5000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	3	3	✓
SVDiag_FETFaultType_Cnt_M_enum	2	2	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	0	0	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	120	120	✓
target_OnStateFitAccPtr_Cnt_T_u16	123	123	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 3.2 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	2		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	65535		
k_OnStateDiag_Cnt_str.PStep	1318		
target_GenGateDriveFitAccPtr_Cnt_T_u16	20		
target_OnStateFitAccPtr_Cnt_T_u16	713		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4000		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✔
SVDiag_FETFaultType_Cnt_M_enum	2	2	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	20	20	✔
target_OnStateFitAccPtr_Cnt_T_u16	2031	2031	✔

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Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	✓

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Test Step 3.3 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	2
SVDiag_FETFaultType_Cnt_M_enum	1
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	900
k_OnStateDiag_Cnt_str.PStep	675
target_GenGateDriveFitAccPtr_Cnt_T_u16	150
target_OnStateFitAccPtr_Cnt_T_u16	65535
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	1
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	1000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	79	79	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	2	2	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	150	150	✓
target_OnStateFitAccPtr_Cnt_T_u16	900	900	✓

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 3.4 (Repeat Count = 1)

Name	Input Value
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime
SVDiag_FETFaultPhase_Cnt_M_enum	3
SVDiag_FETFaultType_Cnt_M_enum	2
SVDiag_ResetWaitLoop_Cnt_M_lgc	1
k_OnStateDiag_Cnt_str.Threshold	44
k_OnStateDiag_Cnt_str.PStep	842
target_GenGateDriveFitAccPtr_Cnt_T_u16	180
target_OnStateFitAccPtr_Cnt_T_u16	477
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	2000

Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	78	78	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
SVDiag_FETFaultPhase_Cnt_M_enum	1	1	✓
SVDiag_FETFaultType_Cnt_M_enum	1	1	✓
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✓
target_GenGateDriveFitAccPtr_Cnt_T_u16	180	180	✓
target_OnStateFitAccPtr_Cnt_T_u16	0	0	✓



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 3.5 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFitAccPtr_Cnt_T_u16	target_GenGateDriveFitAccPtr_Cnt_T_u16		
OnStateFitAccPtr_Cnt_T_u16	target_OnStateFitAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	1		
SVDiag_FETFaultType_Cnt_M_enum	1		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	2345		
k_OnStateDiag_Cnt_str.PStep	9211		
target_GenGateDriveFitAccPtr_Cnt_T_u16	0		
target_OnStateFitAccPtr_Cnt_T_u16	4577		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	78	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	*none*	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFitAccPtr_Cnt_T_u16	1	1	✔
target_OnStateFitAccPtr_Cnt_T_u16	0	0	✔



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFlt2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFlt2ToOutput	1	✓

Test Step 3.6 (Repeat Count = 1) ✓

Name	Input Value		
GenGateDriveFltAccPtr_Cnt_T_u16	target_GenGateDriveFltAccPtr_Cnt_T_u16		
OnStateFltAccPtr_Cnt_T_u16	target_OnStateFltAccPtr_Cnt_T_u16		
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed(NTCFailed_Ptr_T_lgc)	target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc		
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32(CurrentTime)	target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime		
SVDiag_FETFaultPhase_Cnt_M_enum	2		
SVDiag_FETFaultType_Cnt_M_enum	0		
SVDiag_ResetWaitLoop_Cnt_M_lgc	1		
k_OnStateDiag_Cnt_str.Threshold	455		
k_OnStateDiag_Cnt_str.PStep	376		
target_GenGateDriveFltAccPtr_Cnt_T_u16	200		
target_OnStateFltAccPtr_Cnt_T_u16	134		
target_Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_GetNTCFailed_NTCFailed_Ptr_T_lgc	0		
target_Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32_CurrentTime	4294967295		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	77	77	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2	2	✔
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✔
SVDiag_FETFaultPhase_Cnt_M_enum	0	0	✔
SVDiag_FETFaultType_Cnt_M_enum	0	0	✔
SVDiag_ResetWaitLoop_Cnt_M_lgc	1	1	✔
target_GenGateDriveFltAccPtr_Cnt_T_u16	100	100	✔
target_OnStateFltAccPtr_Cnt_T_u16	0	0	✔



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ProcBridgeFlt



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ProcBridgeFit

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	253	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	Rte_Call_Sa_MtrDrvDiag_FetFit1Data_OP_GET	1	✓
Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetFit2Clk_OP_SET	1	✓
GateDrvWaitTime	1	GateDrvWaitTime	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_GetSystemTime_uS_u32	1	✓
Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	Rte_Call_Sa_MtrDrvDiag_SystemTime_DtrmnElapsedTime_uS_u16	1	✓
Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	Rte_Call_Sa_MtrDrvDiag_IoHwAbPortConfig_SetFetFit2ToOutput	1	✓
Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Sa_MtrDrvDiag_NxtrDiagMgr_SetNTCStatus	1	✓

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MtrDrvDiag_Trns1

Project	MtrDrvDiag
Module	MtrDrvDiag
Test Object	MtrDrvDiag_Trns1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1 ✓
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\CBD_MtrDrvDiag
Configuration File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrDrvDiag\src\Sa_MtrDrvDiag.c
Compiler Options	-Dconst= -D_DATA_ACCESS= -DSKIP_MAGIC_NUMBER= -Dstatic= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract\Sa_MtrDrvDiag -I\$(PROJECTROOT)\MtrDrvDiag\utp\contract -I\$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrDrvDiag'	<p>*****UNIT TEST DESCRIPTION*****</p> <p>Name of Tester: Spoorti Mali Code File(s) Under Test: Sa_MtrDrvDiag.c Code File(s) Version: 22 Module Design Document:Motor_Driver_Diagnostics_MDD.doc Module Design Document Version: 11 Data Dictionary Version: 11 Unit Test Plan Version: 3 Optimization Level:Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/ EPS Library 1.31 Total FLASH Used (Bytes): 1334 Total RAM Used (Bytes): 17 Total CALS Used (Bytes): 50 Special Test Requirements: Test Date:01-29-2015 Comments:</p> <p>Note 1:Inline function defined in ""Gloabalmacro.h"" is not unit Tested.</p> <p>Note 2:In ""GateDrvWaitTime"" function extra code is added in VBA,to exit the while loop and to replicate the usercode functionality of TESSY.</p> <p>Note 3:In ""ReadMtrDrvFitData""function,""DataHigh_Cnt_T_lgc"" temporary variable and ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16"" stub are used as ""DataHigh_Cnt_T_lgc_Array[10]"" and ""Rte_Call_FetFit1Data_OP_GET_Array[11]"" to replicate the usercode functionality of TESSY.</p> <p>Note 4:In functions ""ProcGateDriveFit"" , ""ProcBridgeFit"" , ""MtrDrvDiag_Per1"",""ReadMtrDrvFitData"" functions, ""Rte_Call_SystemTime_DtrmnElapsedTime_uS_u16""is not tested with its Min value, to avoid the infinite loop.</p> <p>Note 5: ""CBD_Sandbox_dbg.map"" file is embedded for reference."</p> <p>*****</p>

Attributes

Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj

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MtrDrvDiag_Tms1



Attributes	
Name	Value
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CBD_MtrDrvDiag\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

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MtrDrvDiag_Trns1



Test Case 1: Path Test

Specification	Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TC1.1 610 Cycles
Description	Vector Description TS1.1 Check for Call Trace

Test Step 1.1 (Repeat Count = 1)

Name	Input Value		
Rte_Inst_Sa_MtrDrvDiag	tgt_Rte_Inst_Sa_MtrDrvDiag		
tgt_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Per1_GateDriveResetActive_Cnt_lgc	1		
tgt_Rte_Inst_Sa_MtrDrvDiag.MtrDrvDiag_Trns1_MtrDrvInitComplete_Cnt_lgc	tgt_MtrDrvDiag_Trns1_MtrDrvInitComplete_Cnt_lgc		
Name	Actual Value	Expected Value	Result
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET(signal)	1	1	✓
SVDiag_GateDriveFltAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_GenGateDriveFltAcc_Cnt_M_u16	0	0 ± 1	✓
SVDiag_MtrDrvInitComp_Cnt_M_lgc	0	0	✓
SVDiag_OnStateFltAcc_Cnt_M_u16	0	0 ± 1	✓
tgt_MtrDrvDiag_Trns1_MtrDrvInitComplete_Cnt_lgc.value	0	0	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	Rte_Call_Sa_MtrDrvDiag_FetDrvReset_OP_SET	1	✓