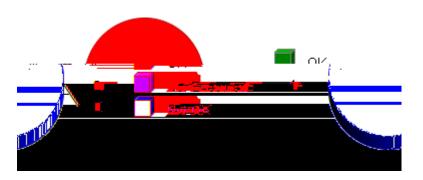


#### Summary

#### **Overall Test Object Results (including Coverage)**

Total Test Objects: 1
Successful: 0
Failed: 1
Not Executed: 0

**Date:** 2018-05-18 **Time:** 14:17:27+0530



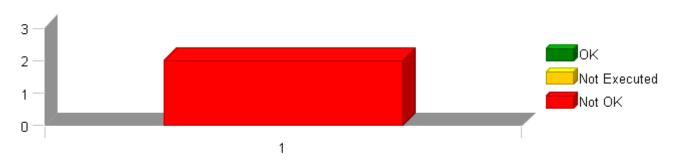
#### **Selected Project Items**

Test Object "CBD\_Unittest/LoaMgr/LoaMgr\_Per1"

#### **Used Test Environments**

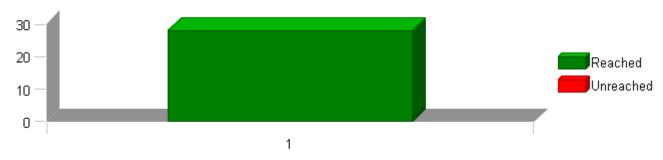
TI TMS 570 PLS UDE (Default)

#### **Test Case Results for Each Test Object (without Coverage)**



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

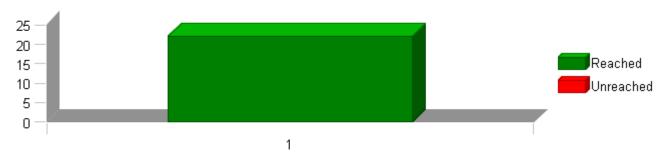
#### Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

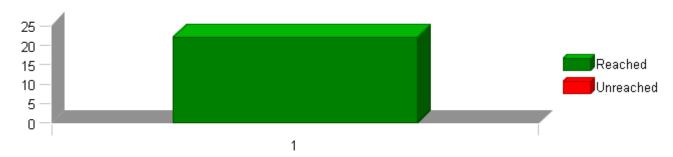


#### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

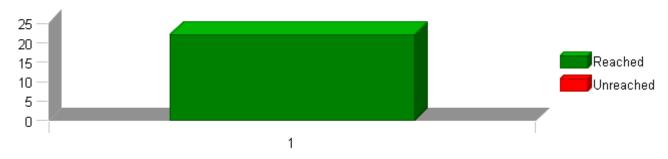
#### **Decision Coverage: Total Decision Outcomes for Each Test Object**



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

#### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

#### **TEST OVERVIEW REPORT**

2018-05-18, 14:17:27+0530



#### **Test Object List**

Project LoaMgr

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	Test Cases	Result
	LoaMgr	100 %	100 %	100 %	100 %	2 of 2 failed	×
	CBD_Unittest	100 %	100 %	100 %	100 %	2 of 2 failed	×
	LoaMgr	100 %	100 %	100 %	100 %	2 of 2 failed	×
1	LoaMgr_Per1	100 %	100 %	100 %	100 %	2 of 2 failed	×

© Report created by TESSY V3.1.12, report template V2.0

2018-05-18, 14:04:23+0530



LoaMgr\_Per1

Project	LoaMgr
Module	LoaMgr
Test Object	LoaMgr_Per1

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	2
Successful	0
Failed	2
Not Executed	0

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	

2018-05-18, 14:04:23+0530



LoaMgr\_Per1

2018-05-18, 14:04:23+0530



Test Case 1: PRM\_R Test

2018-05-18, 14:04:23+0530



#### LoaMgr\_Per1 Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: CPU Cycles:

TS4.1 5037.00 Cycles
TS4.2 4978.00 Cycles
TS4.3 5037.00 Cycles
TS4.3 5037.00 Cycles
TS4.4 5026.00 Cycles
TS4.5 5003.00 Cycles
TS4.6 5017.00 Cycles
TS4.7 5052.00 Cycles
TS4.8 4969.00 Cycles
TS4.9 5023.00 Cycles
TS4.10 5056.00 Cycles
TS4.11 5015.00 Cycles
TS4.12 5022.00 Cycles
TS4.13 4963.00 Cycles
TS4.14 4970.00 Cycles
TS4.15 4957.00 Cycles
TS4.16 5059.00 Cycles
TS4.17 4989.00 Cycles
TS4.17 4989.00 Cycles
TS4.19 4976.00 Cycles
TS4.19 4976.00 Cycles TS4.18 4976.00 Cycles TS4.19 4993.00 Cycles TS4.20 4987.00 Cycles TS4.21 4951.00 Cycles TS4.22 5066.00 Cycles TS4.22 4997.00 Cycles TS4.24 4997.00 Cycles TS4.25 4965.00 Cycles TS4.26 5000.00 Cycles TS4.27 4990.00 Cycles TS4.28 4961.00 Cycles TS4.29 5022.00 Cycles TS4.30 4966.00 Cycles TS4.30 4966.00 Cycles TS4.31 5011.00 Cycles TS4.33 4998.00 Cycles TS4.33 4998.00 Cycles TS4.33 4999.00 Cycles TS4.33 4999.00 Cycles TS4.34 4999.00 Cycles TS4.35 4975.00 Cycles TS4.36 5469.00 Cycles TS4.37 4975.00 Cycles TS4.37 4975.00 Cycles TS4.38 4954.00 Cycles TS4.38 4935.00 Cycles TS4.40 4996.00 Cycles TS4.41 4934.00 Cycles TS4.42 5044.00 Cycles TS4.43 5006.00 Cycles TS4.43 5006.00 Cycles TS4.44 5011.00 Cycles TS4.46 5008.00 Cycles TS4.47 5012.00 Cycles TS4.48 5022.00 Cycles TS4.49 4950.00 Cycles TS4.50 5022.00 Cycles TS4.51 5015.00 Cycles TS4.52 4959.00 Cycles TS4.52 4959.00 Cycles TS4.53 4956.00 Cycles TS4.54 4933.00 Cycles TS4.55 5008.00 Cycles TS4.56 5012.00 Cycles TS4.57 5039.00 Cycles TS4.56 5012.00 Cycles
TS4.57 5039.00 Cycles
TS4.58 4975.00 Cycles
TS4.59 4987.00 Cycles
TS4.60 4952.00 Cycles
TS4.61 4916.00 Cycles
TS4.63 5012.00 Cycles
TS4.63 5012.00 Cycles
TS4.64 5022.00 Cycles
TS4.66 4987.00 Cycles
TS4.66 4987.00 Cycles
TS4.66 4987.00 Cycles
TS4.68 4916.00 Cycles
TS4.69 4997.00 Cycles
TS4.71 5022.00 Cycles
TS4.71 5022.00 Cycles
TS4.73 4976.00 Cycles
TS4.74 4938.00 Cycles
TS4.75 4916.00 Cycles
TS4.76 4997.00 Cycles
TS4.76 4997.00 Cycles
TS4.77 4979.00 Cycles TS4.78 5022.00 Cycles TS4.79 4952.00 Cycles TS4.80 4976.00 Cycles TS4.81 4938.00 Cycles TS4.82 4916.00 Cycles TS4.83 4997.00 Cycles TS4.84 4979.00 Cycles TS4.83 4997.00 Cycles
TS4.84 4979.00 Cycles
TS4.85 5022.00 Cycles
TS4.86 4921.00 Cycles
TS4.86 4921.00 Cycles
TS4.87 4916.00 Cycles
TS4.89 4979.00 Cycles
TS4.90 5022.00 Cycles
TS4.90 5022.00 Cycles
TS4.91 4976.00 Cycles
TS4.92 4938.00 Cycles
TS4.93 4916.00 Cycles
TS4.94 4997.00 Cycles
TS4.95 4979.00 Cycles
TS4.96 5022.00 Cycles
TS4.96 5022.00 Cycles
TS4.97 5039.00 Cycles
TS4.97 5039.00 Cycles
TS4.98 4975.00 Cycles
TS4.101 4916.00 Cycles
TS4.102 5008.00 Cycles
TS4.103 5022.00 Cycles
TS4.104 4952.00 Cycles
TS4.105 4976.00 Cycles
TS4.106 4938.00 Cycles
TS4.107 4989.00 Cycles
TS4.108 4976.00 Cycles
TS4.108 4976.00 Cycles
TS4.108 4976.00 Cycles
TS4.109 4993.00 Cycles

TS4.109 4993.00 Cycles TS4.110 4987.00 Cycles TS4 109 4993 00

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



TS4.111 4938.00 Cycles





#### **Description** Vector Description:

TS4.1k\_LoaMgr\_HwTqldptSig1TloaAvailResp\_Cnt\_u08==>Min TS4.2k\_LoaMgr\_HwTqldptSig1TloaAvailResp\_Cnt\_u08==>Max TS4.3k\_LoaMgr\_HwTqldptSig1TloaAvailResp\_Cnt\_u08==>Pos/default TS4.4k\_LoaMgr\_HwTqldptSig1NoTloaResp\_Cnt\_u08==>Min TS4.5k\_LoaMgr\_HwTqldptSig1NoTloaResp\_Cnt\_u08==>Max TS4.6k\_LoaMgr\_HwTqldptSig1NoTloaResp\_Cnt\_u08==>Pos/default TS4.7k\_LoaMgr\_HwTqldptSig2TloaDiResp\_Cnt\_u08==>Min TS4.8k\_LoaMgr\_HwTqldptSig2TloaDiResp\_Cnt\_u08==>Max TS4.8k\_LoaMgr\_HwTqldptSig2TloaDiResp\_Cnt\_u08==>Max
TS4.9k\_LoaMgr\_HwTqldptSig2TloaDiResp\_Cnt\_u08==>Pos/default
TS4.10k\_LoaMgr\_HwTqldptSig2TloaEnaResp\_Cnt\_u08==>Min/default
TS4.11k\_LoaMgr\_HwTqldptSig2TloaEnaResp\_Cnt\_u08==>Max
TS4.12k\_LoaMgr\_HwTqldptSig2TloaEnaResp\_Cnt\_u08==>Pos
TS4.13k\_LoaMgr\_HwTqldptSig4Resp\_Cnt\_u08==>Min/default
TS4.14k\_LoaMgr\_HwTqldptSig4Resp\_Cnt\_u08==>Max
TS4.15k\_LoaMgr\_HwTqldptSig4Resp\_Cnt\_u08==>Pos
TS4.16k\_LoaMgr\_HwTqldptSig0TloaAvailResp\_Cnt\_u08==>Min
TS4.17k\_LoaMgr\_HwTqldptSig0TloaAvailResp\_Cnt\_u08==>Max
TS4.18k\_LoaMgr\_HwTqldptSig0TloaAvailResp\_Cnt\_u08==>Pos/default
TS4.19k\_LoaMgr\_RedcdStSca\_Uls\_f32==>Min TS4.19k\_LoaMgr\_RedcdStSca\_Uls\_f32==>Min TS4.20k\_LoaMgr\_RedcdStSca\_Uls\_f32==>Max/default TS4.21k\_LoaMgr\_RedcdStSca\_Uls\_f32==>Pos TS4.21k\_LoaMgr\_RedcdStRate\_UlspS\_f32==>Min TS4.22k\_LoaMgr\_RedcdStRate\_UlspS\_f32==>Max TS4.23k\_LoaMgr\_RedcdStRate\_UlspS\_f32==>Defaut TS4.25k\_LoaMgr\_RedcdStRate\_UlspS\_f32==>Pos TS4.25k\_LoaMgr\_FadeOutStRate\_UlspS\_f32==>Min TS4.27k\_LoaMgr\_FadeOutStRate\_UlspS\_f32==>Min TS4.27k\_LoaMgr\_FadeOutStRate\_UlspS\_f32==>Max IS4.27k\_LoaMgr\_FadeOutstRate\_UlspS\_f32==>Max
TS4.28k\_LoaMgr\_FadeOutstRate\_UlspS\_f32==>Default
TS4.29k\_LoaMgr\_FadeOutstRate\_UlspS\_f32==>Pos
TS4.30k\_LoaMgr\_HwTqldptSigFltTnd\_Cnt\_u08==>Min
TS4.31k\_LoaMgr\_HwTqldptSigFltTnd\_Cnt\_u08==>Max
TS4.32k\_LoaMgr\_HwTqldptSigFltTnd\_Cnt\_u08==>Pos/default
TS4.33k\_LoaMgr\_MotAgldptSigFltTnd\_Cnt\_u08==>Min
TS4.34k\_LoaMgr\_MotAgldptSigFltTnd\_Cnt\_u08==>Max
TS4.34k\_LoaMgr\_MotAgldptSigFltTnd\_Cnt\_u08==>Max TS4.35k\_LoaMgr\_MotAgldptSigFltThd\_Cnt\_u08==>Pos/default TS4.36k\_LoaMgr\_MotCurrldptSigFltThd\_Cnt\_u08==>Min/default TS4.37k\_LoaMgr\_MotCurrldptSigFltThd\_Cnt\_u08==>Max TS4.38k\_LoaMgr\_MotCurrldptSigFltThd\_Cnt\_u08==>Pos TS4.39k\_LoaMgr\_lvtrldptSigFltThd\_Cnt\_u08==>Min/default TS4.40k\_LoaMgr\_lvtrldptSigFltThd\_Cnt\_u08==>Max TS4.41k\_LoaMgr\_lvtrldptSigFitThd\_Cnt\_u08==>Pos TS4.41k\_LoaMgr\_lvtrldptSigFitThd\_Cnt\_u08==>Pos TS4.42k\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08==>Min TS4.43k\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08==>Max TS4.44k\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08==>Pos/default TS4.44K\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08==>Pos/default
TS4.45K\_LoaMgr\_MotAgldptSig1NoSmaResp\_Cnt\_u08==>Min
TS4.46K\_LoaMgr\_MotAgldptSig1NoSmaResp\_Cnt\_u08==>Max/default
TS4.47K\_LoaMgr\_MotAgldptSig1NoSmaResp\_Cnt\_u08==>Pos
TS4.48K\_LoaMgr\_MotAgldptSig2Resp\_Cnt\_u08==>Pos
TS4.49K\_LoaMgr\_MotAgldptSig2Resp\_Cnt\_u08==>Min
TS4.49K\_LoaMgr\_MotAgldptSig2Resp\_Cnt\_u08==>Pos/default
TS4.51K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos/default
TS4.52K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Max
TS4.53K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Pos
TS4.54K\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08==>Min/default TS4.54k\_LoaMgr\_MotCurrldptSig1Resp\_Cnt\_u08==>Min/default TS4.55k\_LoaMgr\_MotCurrldptSig1Resp\_Cnt\_u08==>Max TS4.56k\_LoaMgr\_MotCurrldptSig1Resp\_Cnt\_u08==>Pos TS4.57k\_LoaMgr\_MotCurrldptSig2Resp\_Cnt\_u08==>Min/default TS4.58k\_LoaMgr\_MotCurrldptSig2Resp\_Cnt\_u08==>Max TS4.59k\_LoaMgr\_MotCurrldptSig2Resp\_Cnt\_u08==>Pos TS4.60k\_LoaMgr\_MotCurrIdptSig0Resp\_Cnt\_u08==>Min TS4.61k\_LoaMgr\_MotCurrldptSig0Resp\_Cnt\_u08==>Max TS4.62k\_LoaMgr\_MotCurrldptSig0Resp\_Cnt\_u08==>Pos/default TS4.63k\_LoaMgr\_lvtrldptSig1Resp\_Cnt\_u08==>Min/default TS4.64k\_LoaMgr\_lvtrldptSig1Resp\_Cnt\_u08==>Max TS4.65k\_LoaMgr\_lvtrldptSig1Resp\_Cnt\_u08==>Pos TS4.66k\_LoaMgr\_lvtrldptSig2Resp\_Cnt\_u08==>Min/default TS4.67k\_LoaMgr\_lvtrldptSig2Resp\_Cnt\_u08==>Max TS4.68k\_LoaMgr\_lvtrldptSig2Resp\_Cnt\_u08==>Pos TS4.69k\_LoaMgr\_HwTqMtgtnSca\_Uls\_f32==>Min TS4.70k\_LoaMgr\_HwTqMtgtnSca\_Uls\_f32==>Max/default TS4.71k\_LoaMgr\_HwTqMtgtnSca\_Uls\_f32==>Pos TS4.72k\_LoaMgr\_HwTqMtgtnRate\_UlspS\_f32==>Min TS4.73k\_LoaMgr\_HwTqMtgtnRate\_UlspS\_f32==>Max TS4.74k\_LoaMgr\_HwTqMtgtnRate\_UlspS\_f32==>Default TS4.75k\_LoaMgr\_HwTqMtgtnRate\_UlspS\_f32==>Pos TS4.76k\_LoaMgr\_MotAgMtgtnScaZeroEn\_Cnt\_lgc==>Min TS4.77k\_LoaMgr\_MotAgMtgtnScaZeroEn\_Cnt\_lgc==>Max TS4.78k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32==>Min TS4.78k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32==>Min
TS4.79k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32==>Max/default
TS4.80k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32==>Pos
TS4.81k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32==>Min
TS4.82k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32==>Max
TS4.83k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32==>Default
TS4.84k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32==>Pos
TS4.85k\_LoaMgr\_MotCurrMtgtnScaZeroEn\_Cnt\_lgc==>Min
TS4.86k\_LoaMgr\_MotCurrMtgtnScaZeroEn\_Cnt\_lgc=>Max
TS4.87k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_f32==>Min
TS4.88k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_f32==>Max/default
TS4.88k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_f32==>Max/default
TS4.89k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_f32==>Pos TS4.80k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_132==>Max/defaul TS4.80k\_LoaMgr\_MotCurrMtgtnSca\_Uls\_132==>Pos TS4.90k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32==>Min TS4.91k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32==>Max TS4.92k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32==>Default TS4.93k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32==>Pos TS4.94k\_LoaMgr\_IvtrMtgtnScaZeroEn\_Cnt\_Igc==>Min



TS4.95k\_LoaMgr\_lvtrMtgtnScaZeroEn\_Cnt\_lgc==>Max
TS4.96k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>Min
TS4.97k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>PMax/default
TS4.97k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>Pos
TS4.99k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.100k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Max
TS4.101k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.102k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.103k\_LoaMgr\_CurrlvtrMtgtnScaZeroEn\_Cnt\_lgc==>Min
TS4.104k\_LoaMgr\_CurrlvtrMtgtnScaZeroEn\_Cnt\_lgc==>Min
TS4.105k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Max
TS4.105k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Max
TS4.105k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Max/Default
TS4.107k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Pos
TS4.108k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Min
TS4.109k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Max
TS4.110k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Max
TS4.111k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Default
TS4.111k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Default

est Step 1.1 (Repeat Count = 1)			
Name	Input Value		
LoaMgr_AgSV_Cnt_M_lgc	0		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1		
LoaMgr HwTqRespSV Cnt M u08	2		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1		
LoaMgr_lvtrRespSV_Cnt_M_u08	2		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1		
LoaMgr_MotAgRespSV_Cnt_M_u08	4		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2		
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	3		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr CurrlvtrMtgtnSca Uls f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		





Name	Input Value		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptS	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaM	//tgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_Sta	ate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoal	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldp	otSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLo	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdpt	tSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_	Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cn	t_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
a.gocagr_r or r_motourizodini.gr./in_oni_ige.value	•	· ·	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	•	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	•	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•	
AssignScale	1	AssignScale	1	•	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>	

		_
Test Step 1.2 (Repeat Count = 1)		×.
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr HwTqRespSV Cnt M u08	4	





Name	Inner Males		
Name	Input Value		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	5		
LoaMgr MotAgldptSigSV Cnt M u08	2		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotAgRespSV_Cnt_M_u08	1		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr MotCurrRespSV Cnt M u08	5		
LoaMgr SnsrlsMotAgAvail Cnt M lgc	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1		
Rte Inst Ap LoaMgr	target Rte Inst Ap LoaMgr		
k LoaMgr CurrlytrMtgtnRate UlspS f32	1		
k_LoaMgr_CurrivtrMtgtnRcaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
	4		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsl		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoal		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_St		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoa		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrld		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 175}	{170, 172, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2





Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
	0		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k LoaMgr IvtrldptSigFltThd Cnt u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr IvtrMtgtnSca Uls f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnSca_Us_f32	1		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	lak laa	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	yc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	00	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1_
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.4 (Repeat Count = 1)	<b>□</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1

2018-05-18, 14:04:23+0530



Loaingi_r ei i			
Name	Input Value		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target LoaMgr Per1 lvtrLoaMtgtnEn Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enui	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>-</b>
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 0	0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 0 0	0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 0 0 4	0 0 4	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1 0 0 4 0	0 0 4 0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1 0 0 4 0 0	0 0 4 0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 0 0 4 0 0 5	0 0 4 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum	1 0 0 4 0 0 5 5	0 0 4 0 0 5 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 0 4 0 0 5 5 5 5	0 0 4 0 0 5 5 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 0 4 0 0 5 5 5 500	0 0 4 0 0 5 5 5 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 0 0 4 0 0 5 5 5 5 500 1 5 5	0 0 4 0 0 0 5 5 5 500 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 0 0 4 0 0 5 5 5 500	0 0 4 0 0 5 5 5 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0	0 0 4 0 0 0 5 5 5 500 1 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 0	0 0 4 0 0 0 5 5 5 500 1 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 0 0 0 4 0 0 5 5 5 5 5 5 0 0 0 5 5 5 5 5	0 0 4 0 0 0 5 5 5 500 1 5 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 0 5 5 0 0	0 0 4 0 0 0 5 5 5 500 1 5 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 0 0 0 4 0 0 5 5 5 5 5 0 0 0 5 5 0 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgr_Cnt_MotSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 0 1 5 0	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 0 0 4 0 0 0 5 5 5 500 1 1 5 0 0 0 0 1 2 1 2 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 1 2 1 1 1 1 1 1 170, 172, 173, 175}	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 1 1 1 1 170, 172, 173, 175}	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 0 0 4 0 0 0 0 5 5 5 5 500 1 1 5 5 0 0 0 0 5 5 0 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0}	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 5 0 0 0 0 5 5 0 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 5 0 0 0 0 5 5 0 0 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaStateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtlgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 0 0 0 4 0 0 0 0 5 5 5 5 500 1 1 5 5 0 0 0 1 1 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_ateL_mit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlAoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlAoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0 0 0 4 0 0 0 5 5 5 5 500 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StltpSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0	0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StytgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 0 0 0 4 0 0 0 5 5 5 5 5 5 5 5 5 5 5 5 5	0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 0 0 0 5 5 0 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqloptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgltptSigSV_Cnt_M_u08  LoaMgr_MotAgltptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_DoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0	0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 1.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1.
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•
		· ·	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.7 (Repeat Count = 1)		X
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530

Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotAgLoaMtgtnEn C	int lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	w	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMar MotAal oaMtatnEn, Cot, M. Jac			•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr MotAgRespSV Cnt M u08	0	0	<b>V</b>

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.8 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

© Report created by TESSY V3.1.12, report template V2.1





N	Lucas Malaca		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	Cnt lac	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt	,	
•			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cn	— -	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enun	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	m	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr HwTqRespSV Cnt M u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr LOASt State M enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
	2	2	-
LoaMgr_MotAgloptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgBoogSV Cot M v03	4	4	
LoaMgr_MotAgRespSV_Cnt_M_u08			
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value		4	<b>✓</b>
target_reamgr_rerright	1	1	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
			· ·
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 5	0 5	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.9 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1li	nacty Cnt loc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2li		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgti	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	• • • • • • • • • • • • • • • • • • •
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	<b>•</b>
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>•</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<u> </u>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>•</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<u> </u>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<u> </u>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1} 500 + 0.04	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	_
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>-</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 1.10 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Dead_Res_Dead_Res_Rte_Inst_Ap_LoaMgr_Dead_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_A	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.11 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

Input Value





Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08		4		
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2			
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1			
	5			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08				
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k LoaMgr MotAgldptSig2Resp Cnt u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
	2			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08				
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
	0			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value				
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inact	v Cnt Igc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr2Inact			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTqLoaMtgtnEn			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgo			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C	_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn	t_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓	
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~	
· · · · · · ·	1	1		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 4	4		
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>~</b>	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓	
LoaMgr_LoaScaleFctr_Uls_M_f32	Ō	0	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓	
5_ · · 5 · · · · · · · · · · · · · · · ·				
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	3
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enun	1	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc		_	
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	ım	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name		Expected Value	Result
	Actual Value	Expected value	Result
LoaMgr_AgSV_Cnt_M_lgc	Actual Value	1	/ Nesure
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08			~
	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 0	1 0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 0 1 4	1 0 1 4	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 0 1 4 0	1 0 1 4 0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 0 1 4 0	1 0 1 4 0	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 0 1 4 0 0 5	1 0 1 4 0 0 5	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 0 1 4 0 0 5 5	1 0 1 4 0 0 5 5	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 1 4 0 0 5 5 5	1 0 1 4 0 0 5 5 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 1 4 0 0 0 5 5 5 5 500 1 1	1 0 1 4 0 0 5 5 5 5 500	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 1 4 0 0 0 0 5 5 5 5 500 1 5 5	1 0 1 4 0 0 0 5 5 5 500 1 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 1 4 0 0 0 5 5 5 5 500 1 1	1 0 1 4 0 0 0 5 5 5 5 500 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 1 4 0 0 0 0 5 5 5 5 500 1 5 5	1 0 1 4 0 0 0 5 5 5 500 1 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 0 1 4 0 0 0 0 5 5 5 5 500 1 5 0 0	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	1 0 1 4 0 0 0 0 5 5 5 5 500 1 5 5 0 0 0 0	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 0 1 4 0 0 0 0 5 5 5 5 5 5 5 0 0 0 0 5 5 5 5	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 5 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 0 1 4 0 0 0 0 5 5 5 5 5 5 0 0 0 5 5 0 0 0 5 5 0 0	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 5 5 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	1 0 1 4 0 0 0 0 5 5 5 5 5 5 0 0 0 0 5 5 0 1 1 5 5 0 0 1 1 5 5 0 0 1 1 5 5 0 0 1 1 5 5 0 0 1 1 5 5 0 0 1 1 5 5 0 0 1 1 1 1	1 0 1 4 0 0 0 0 5 5 5 5 500 1 1 5 0 0 0 5 5 0 1 1 3	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 0 1 4 0 0 0 0 5 5 5 5 5 5 0 0 0 0 5 5 0 0 1 1 3 3 1 1	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 5 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 5 0 0	1 0 1 4 0 0 0 5 5 5 5 500 1 1 5 5 0 0 1 1 3 1 1 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	1 0 1 4 0 0 0 5 5 5 5 500 1 5 5 0 0 0 1 3 1 3 1	1 0 1 4 0 0 0 0 5 5 5 5 500 1 1 5 5 0 0 1 1 3 1 1 1 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MatAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0 1 3 1 1 3 1 1 1 1	1 0 1 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 1 3 1 1 1 1 170, 173, 174, 175}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0 1 3 1 1 1 1 1 1,1,1,1}	1 0 1 4 0 0 0 5 5 5 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1}	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 0 1 4 0 0 0 0 5 5 5 5 5 500 1 1 5 5 0 0 0 1 1 3 3 1 1 1 1 1 1 1 1 1 1 1 1 1	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 0 1 4 0 0 0 0 5 5 5 5 5 500 1 1 5 5 0 0 0 1 1 3 3 1 1 1 1 1 1 1 1 1 1 1 1 1	1 0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 1 1	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 1 1	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170,173,174,175} {0,0,0,0,0} {1,1,1,1} 500±0.01 1±0.001 1 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 1 1	1 0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170,173,174,175} {0,0,0,0,0} {1,1,1,1} 500±0.01 1±0.001 1 0 5	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name	
LoaMgr_HWTqloptsigSV_Cnt_M_u08	
LoaMgr_HwTqltptSigSV_Cnt_M_u08	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	
LoaMgr_IntridptSigSV_Cnt_M_u08	
LoaMgr_IvtrLoaMtgnEn_Cnt_M_Igc	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	
LoaMgr_IvtrRespSy_Cnt_M_u08   3     LoaMgr_MotAgldptSigSv_Cnt_M_u08   1     LoaMgr_MotAglcoaMightEn_Cnt_M_lgc   1     LoaMgr_MotAglcoaMightEn_Cnt_M_u08   1     LoaMgr_MotCurrloptSigSv_Cnt_M_u08   2     LoaMgr_MotCurrloaMightEn_Cnt_M_lgc   0     LoaMgr_MotCurrLoaMightEn_Cnt_M_lgc   0     LoaMgr_MotCurrLoaMightEn_Cnt_M_lgc   0     LoaMgr_MotCurrSepSv_Cnt_M_u08   2     LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc   1     LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc   1     LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc   1     LoaMgr_CurrlvtMightScaSv_Cnt_M_u08   0     Rte_Inst_Ap_LoaMgr   target_Rte_Inst_Ap_LoaMgr     L_LoaMgr_CurrlvtMightRate_UlspS_f32   1     L_LoaMgr_CurrlvtMightSca_Uls_f32   1     L_LoaMgr_CurrlvtMightSca_Uls_f32   1     L_LoaMgr_FadeOutsRate_UlspS_f32   1     L_LoaMgr_HavTqldptSigOTloaAvailResp_Cnt_u08   2     L_LoaMgr_HavTqldptSigOtTloaAvailResp_Cnt_u08   4     L_LoaMgr_HavTqldptSig1TloaAvailResp_Cnt_u08   2     L_LoaMgr_HavTqldptSig2TloaEnaResp_Cnt_u08   1     L_LoaMgr_HavTqldptSig2TloaEnaResp_Cnt_u08   0     L_LoaMgr_HavTqldptSig2TloaEnaResp_Cnt_u08   0     L_LoaMgr_HavTqldptSigFitThd_Cnt_u08   1     L_LoaMgr_HavTqldptSigFitThd_Cnt_u08   0     L_LoaMgr_HavTqldptSigFitThd_Cnt_u08   1     L_LoaMgr_HavTqldptSigFitThd_Cnt_u08   0     L_LoaMgr_HavTqldptSigFitThd_Cnt_u08   1     L_LoaMgr_HavTq	
LoaMgr_MotAglcptSigSV_Cnt_M_u08	
LoaMgr_MotAgLoaNtgtnEn_Cn_M_lgc	
LoaMgr_MotAgRespSV_Cnt_M_u08	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtghEn_Cnt_M_lgc         0           LoaMgr_SnsrisMotAgAvail_Cnt_M_lgc         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrIvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrIvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_CurrIvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_FadeOutSiRate_UlspS_f32         1           k_LoaMgr_HavTqldptSig1NoTloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Find_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Find_Cnt_u08         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_HwTqMtgtnSc	
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc         0           LoaMgr_SnsrisMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrlvtMigtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtMigtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_GurrlvtMigtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaEvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         0           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnRate_UlspS_Cnt_u08         0           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0	
LoaMgr_MotCurrRespSV_Cnt_M_u08         2           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrlvtMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtMtgtnSca_Uls_f32         0           k_LoaMgr_CurrlvtMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitInd_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitInd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitInd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitInd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitInd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0	
LoaMgr_SnsrisMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0	
LoaMgr_VitgMdSrcSV_Cnt_M_u08         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigfItThd_Cnt_u08         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0	
Rte_Inst_Ap_LoaMgr       target_Rte_Inst_Ap_LoaMgr         k_LoaMgr_CurrIvtMtgtnRate_UlspS_f32       1         k_LoaMgr_CurrIvtMtgtnSca_Uls_f32       0         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_CurrIvtMtgtnRate_UlspS_f32       1         k_LoaMgr_CurrIvtMtgtnSca_Uls_f32       0         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         0       0	
k_LoaMgr_CurrIvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32       1         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_CurrIvtrMtgtnSca_UIs_f32       1         k_LoaMgr_FadeOutStRate_UIspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UIspS_f32       1         k_LoaMgr_HwTqMtgtnSca_UIs_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32       1         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       4         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFitThd_Cnt_u08 0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0  k_LoaMgr_MotAgMtgtnSca_Uls_f32 1	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target LoaMgr Per1 SteerMod Cnt enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1li	nacty Cnt loc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2li		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 lvtrLoaMtgtnEi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_	,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lg	_v	
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	Result
	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lytrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	Ž
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	Ž
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	2	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	2		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	<b>*</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	·	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1} 500 + 0.04	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>*</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>*</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Total Part   Total   Total   Total	Test Step 1.14 (Repeat Count = 1)	
Loading_Invitediant  Sept Cort Murble		
Leading		·
Londing_HardingsRepC_CM_HA_SB   1   Londing_HardingsRepC_CM_HA_SB   2   Londing_HardingsRepC_CM_HA_SB   3     Londing_HardingsRepC_CM_HA_SB   4   Londing_HardingsRepC_CM_HA_SB   5   Londing_HardingsRepC_CM_HA_SB   6   Londing_HardingsRepC_CM_HA		
Loading_Interfeleps(Cort_M_Ups)		
Loading		
Loading / Info@dispS / Cort M, US		2
Loading   Load	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
Leading JubiAngles (PM Mg)   9	LoaMgr_lvtrRespSV_Cnt_M_u08	5
Landing Makhaghesay Crit M, Ur38	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
Landery McCurrens(SSS) CM, ML-98  Landery Currens(SSS) CM, ML-98  Landery Currens(SSS) CM, ML-98  Landery Currens(SSSS) CM, ML-98  Landery Currens(SSSS) CM, ML-98  Landery Landery CM, ML-98  Landery Landery CM, ML-98  Landery Landery SSS CM, ML-98  Landery Landery SSS CM, ML-98  Landery Landery SSS (ML-98) CM, Landery Landers La	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
Landing IndOurnesWarsEn, Crit M, US		
Leading: ShrietSharkyanil.Crit. M_UB		
Leading   Shrifskotokykani   Chr. M.   ge		
Loading		
Bits_Inst_A_Loaklycr	· - · · ·	
		, · · - ·
K.LoMayl. Print(spiss) (1)		
K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08         2           K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08         2           K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08         1           K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08         5           K_Loading - HvTolptiSign Teacher Cnt_U08         5           K_Loading - HvTolptiSign Resp_Cnt_U08         1           K_Loading - HvTolptiSign Resp_Cnt_U08         1           K_Loading - HvTolptiSign Resp_Cnt_U08         1           K_Loading - HvTolptiSign Resp_Cnt_U08         0           K_Loading - HvTolptiSize Zeroein, Cnt_Ug         0           K_Loading - HvTolptiSize Zeroein, Cnt_Ug         0           K_Loading - MvTolptiSize Zeroein, Cnt_Ug         1           K_Loading - MvTolptiSize Zeroein, Cnt_Ug         1           K_Loading - MvTolptiSize Zeroein, Cnt_Ug         0		
K_LOMING / HVTGIPES/STERNA/RISE, COT. U/08         2           K_LOMING / HVTGIPES/STERNERS, COT. U/08         0           K_LOMING / HVTGIPES/STERNERS, COT. U/08         0           K_LOMING / HVTGIPES/STERNERS, COT. U/08         1           K_LOMING / HVTGIPES/STERNER, COT. U/08         1           K_LOMING / HVTGIPES/STERNER, COT. U/08         0           K_LOMING / HVTGIPES/STERNER, COT. U/08         1           K_LOMING / HVTGIPES/STERNER, COT. U/08         5           K_LOMING / HVTGIPES/STERNER, COT. U/08         1           K_LOMING / MVTGIPES/STERNER, COT. U/08         2           K_LOMING / MVTGIPES/STERNER, COT. U/08 <td></td> <td></td>		
		2
K_LoaMgr_HvTqlqpSig4Resp_Cnt_u08		1
k_Loakig_HwTqdqsispliftInd_Cnt_Uo8         1           k_Loakig_HwTqdqsingRate_UispS_132         1           k_Loakig_HwTqdsingRate_UispS_132         1           k_Loakig_HwTqdsisgResp_Cnt_Uo8         0           k_Loakig_HwTqdsisgResp_Cnt_Uo8         0           k_Loakig_HwTqdsisgResp_Cnt_Uo8         0           k_Loakig_HwTqdsisgFesp_Cnt_Uo8         0           k_Loakig_HwTqdsisgFesp_Cnt_Uo8         0           k_Loakig_HwTqdsiss_Dist_ITM_Cnt_Uo8         1           k_Loakig_HwTqdsiss_INS_Enc_Cnt_Uo8         5           k_Loakig_MwTqdsiss_INS_Enc_Cnt_Uo8         1           k_Loakig_MwTqdsiss_INS_Enc_Cnt_Uo8         1           k_Loakig_MwTqdsiss_INS_Enc_Cnt_Uo8         2           k_Loakig_MwTqdcurridgsisgTesp_Cnt_Uo8         2           k_Loakig_MwCouridgsisgTesp_Cnt_Uo8         2           k_Loakig_MwCouridgsisgTesp_Cnt_Uo8         0           k_Loakig_MwCouridgsisgTesp_Cnt_Uo8         0	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_Loakigr_HwTqMtginRate_UlspS_I32         1           k_Loakigr_HwTqMtginScz_Uls_I32         1           k_Loakigr_HwTqMtginScz_Uls_I32         0           k_Loakigr_HwTqMtginScz_Den_UlspS_I32         0           k_Loakigr_HwTqMtginScz_Den_Crt_UlspS_I32         1           k_Loakigr_IwTqMtginScz_Uls_I32         1           k_Loakigr_IwTqMtginScz_Uls_I32         1           k_Loakigr_IwTqqqtpScz_Uls_I32         1           k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         2           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         2           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls_I32         1           k_Loakigr_MotQiqtsigRis_Crt_Uls	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	5
k_LoaMgr_HvTqMtghtSq1Resp_Cnt_U08         0           k_LoaMgr_HvtqhtSg1Resp_Cnt_U08         0           k_LoaMgr_HvtqhtSg1Resp_Cnt_U08         0           k_LoaMgr_HvtqhtSg1Resp_Cnt_U08         0           k_LoaMgr_IvtMtghtSq2Resp_Cnt_U08         0           k_LoaMgr_IvtMtghtSq2Resp_Cnt_U19C         0           k_LoaMgr_IvtMtghtSq1Ds1Resp_Cnt_U08         5           k_LoaMgr_MotAglotSq1Sq1Resp_Cnt_U08         5           k_LoaMgr_MotAglotSq1Sq1Resp_Cnt_U08         1           k_LoaMgr_MotAglotSq1Sg1Resp_Cnt_U08         2           k_LoaMgr_MotAglotSq1Sg1Resp_Cnt_U08         2           k_LoaMgr_MotAglotSq1Resp_Cnt_U08         2           k_LoaMgr_MotAglotSq1Resp_Cnt_U08         2           k_LoaMgr_MotAglotSq1Resp_Cnt_U08         2           k_LoaMgr_MotAglotSq1Resp_Cnt_U08         2           k_LoaMgr_MotCq1Resp_Cnt_U08         2           k_LoaMgr_MotCq1Resp_Cnt_U08         2           k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08         0           k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08         0           k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08         0           k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08         0           k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08         0           k_LoaMgr_MotCuntdptSq2Resp_Cnt_U08         0 <tr< td=""><td>k_LoaMgr_HwTqldptSigFltThd_Cnt_u08</td><td></td></tr<>	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_Loaklgr_IntridptSig1Resp_Cnt_U08         0           k_Loaklgr_IntridptSig2Resp_Cnt_U08         0           k_Loaklgr_IntridptSig2Resp_Cnt_U08         0           k_Loaklgr_IntridptSig2Resp_Cnt_U08         0           k_Loaklgr_IntridptSig2Resp_Cnt_U08         1           k_Loaklgr_IntridptSig2Resp_Cnt_U08         5           k_Loaklgr_MotAjdiptSig2Resp_Cnt_U08         1           k_Loaklgr_MotAjdiptSig2Resp_Cnt_U08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_U08         0           k_Loaklgr_MotCurridptSig1Sig1Resp_Cnt_U08         1           k_Loakl		
k_LoaMgr_IvridptSig/Resp_Cnt_u08         0           k_LoaMgr_IvridptSig/Rithd_Cnt_u08         0           k_LoaMgr_IvriMgtReate_UlspS_G22         1           k_LoaMgr_IvriMgthSeaZeroEn_Cnt_lgc         0           k_LoaMgr_IvriMgthSeaZeroEn_Cnt_lgc         1           k_LoaMgr_MotAglotptSigrNoSmaResp_Cnt_u08         5           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         1           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         0           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         2           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         2           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         2           k_LoaMgr_MotAglotptSigrResp_Cnt_u08         2           k_LoaMgr_MotOglotptSigrResp_Cnt_u08         2           k_LoaMgr_MotOurridptSigrResp_Cnt_u08         2           k_LoaMgr_MotOurridptSigrResp_Cnt_u08         2           k_LoaMgr_MotOurridptSigrResp_Cnt_u08         0           k_LoaMgr_MotOurrid		
k_Loak/gr_Ivrit/pts/SpiFitThd_Cnt_U08         0           k_Loak/gr_Ivrit/ginScazeroEn_Cnt_U08         1           k_Loak/gr_Ivrit/ginScazeroEn_Cnt_U08         5           k_Loak/gr_Mot/gipSigiTNoSmaResp_Cnt_U08         5           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         1           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         1           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/gipSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/curridpSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08         2           k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08         0           k_Loak/gr		
k_Loak/gr_IvriNtginRate_UlspS_I32         1           k_Loak/gr_IvriNtginSca_Uls_I32         1           k_Loak/gr_IvriNtginSca_Uls_I32         1           k_Loak/gr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_Loak/gr_MotAgldptSig1Resp_Cnt_u08         1           k_Loak/gr_MotAgldptSig1Resp_Cnt_u08         0           k_Loak/gr_MotAgldptSig1Resp_Cnt_u08         2           k_Loak/gr_MotAgldptSig1Resp_Cnt_u08         2           k_Loak/gr_MotAgltginSca_Cere_Cnt_t]c         0           k_Loak/gr_MotAgltginSca_Cere_Cnt_t]c         0           k_Loak/gr_MotAgltginSca_Cere_Cnt_t]c         0           k_Loak/gr_MotOuridptSig1Resp_Cnt_u08         2           k_Loak/gr_MotOuridptSig1Resp_Cnt_u08         2           k_Loak/gr_MotOuridptSig1Resp_Cnt_u08         0           k_Loak/gr_MotOuridptSig1Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig1Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig1Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig2Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig2Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig2Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig2Resp_Cnt_u08         0           k_Loak/gr_MotOurindptSig2Resp_Cnt_u08         0           k_Loak/		
k_Loak/gr_Ivriht/ginSca_Uis_732         1           k_Loak/gr_Ivriht/ginSca_Uis_732         1           k_Loak/gr_MotApldpis/ginSignNosmaResp_Cnt_u08         5           k_Loak/gr_MotApldpis/ginSignResp_Cnt_u08         1           k_Loak/gr_MotApldpis/ginSignResp_Cnt_u08         0           k_Loak/gr_MotApldpis/ginSignResp_Cnt_u08         2           k_Loak/gr_MotApldysis/ginTind_Cnt_us         2           k_Loak/gr_MotApldysis/ginTind_Cnt_u08         2           k_Loak/gr_MotApldysis/ginSes_Cnt_u08         2           k_Loak/gr_MotApldysinSignResp_Cnt_u08         2           k_Loak/gr_MotCurridptSignResp_Cnt_u08         2           k_Loak/gr_MotCurridptSignResp_Cnt_u08         0           k_Loak/gr_MotCurridptSignResp_Cnt_u08         1           k_Loak/gr_MotCurridptSignResp_Cnt_u08         0     <		
k_Loak/gr_Mvth/tginSca_U/s_f32         1           k_Loak/gr_Mvth/tginSig1YnOsmaResp_Cnt_u08         5           k_Loak/gr_Mvth/tginSig3Resp_Cnt_u08         0           k_Loak/gr_Mvth/tginSig3Resp_Cnt_u08         0           k_Loak/gr_Mvth/tginSig3Resp_Cnt_u08         2           k_Loak/gr_Mvth/tginSig3FitTnd_Cnt_u08         2           k_Loak/gr_Mvth/tginSig3FitTnd_Cnt_u08         2           k_Loak/gr_Mvth/tginSig2FitTnd_Cnt_u08         0           k_Loak/gr_Mvth/tginSig2FitTnd_Cnt_u08         2           k_Loak/gr_Mvth/tginSig2Resp_Cnt_u08         2           k_Loak/gr_Mvth/tginSig2FitTnd_Cnt_u08         0           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Mvth/tginFate_UispS_f32         1           k_Loak/gr_Rvth/tginFate_UispS_f32         1           k_Loak/gr_Rvth/tginSig_cn_tu_08 value         1           target_Loak/gr_Per1_DiagsStsivtrInactv_Cnt_lgc-value         1           target_Loak/gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per1_Loak-gr_Per		
k_Loaklgr_MolAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MolAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MolAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MolAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MolAgldptSig4Tind_Cnt_u08         2           k_Loaklgr_MolAgMignScaZeroEn_Cnt_lgc         0           k_Loaklgr_MolAgMignScaZeroEn_Cnt_lgc         0           k_Loaklgr_MolAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MolCurridptSig0Resp_Cnt_u08         2           k_Loaklgr_MolCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MolCurridptSig1RTM_Cnt_u08         0           k_Loaklgr_MolCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MolCurridptSig4RTM_Cnt_u08         0           k_Loaklgr_MolCurridptSig4Resp_Cnt_u08         0           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         0           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         0           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           k_Loaklgr_MolCurridptSig5RTM_Cnt_u08         1           t		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Firth_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Firth_Cnt_u08         2           k_LoaMgr_MotAgMtgmRate_UlspS_132         1           k_LoaMgr_MotAgMtgmSca_Uls_132         1           k_LoaMgr_MotCurdptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurdptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurdptSig1Firthd_Cnt_u08         0           k_LoaMgr_MotCurdptSigFirthd_Cnt_u08         0           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         0           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         1           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         1           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         1           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         1           k_LoaMgr_MotCurdtgtsigFirthd_Cnt_u08         1           k_LoaMgr_RedcdSitea_UlspS_132         1           k_LoaMgr_RedcdSitea_UlspS_122         1           k_LoaMgr_RedcdSitea_UlspS_122         1           k_LoaMgr_Per1_DiagcSistvtr1lnactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcSistvtr1lnactv_Cnt_lgc_value         1           target_LoaMgr_Per		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtghtRate_UlspS_f32         1           k_LoaMgr_MotAgMtghtSca_Uls_f32         0           k_LoaMgr_MotAgMtghtSca_Uls_f32         1           k_LoaMgr_MotCurridptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_Redcultintsca_UlspS_f32		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFIThd_Cn_u08         0           k_LoaMgr_MotCurrldptSigFIThd_Cn_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSitRate_UlspS_f32         1           k_LoaMgr_RedcdSitRate_UlspS_f32         1           k_LoaMgr_PerI_DiagcStsivtrInactv_Cnt_lgc_value         0           target_LoaMgr_PerI_DegcStsivtrInactv_Cnt_lgc_value         1           target_LoaMgr_PerI_HwTqldptSig_Cnt_u08_value         2           target_LoaMgr_PerI_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_PerI_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_PerI_TicaAvail_Cnt_lgc_value         1           target_LoaMgr_PerI_TicaAvail_Cnt_lgc_value         1           target_LoaMgr_PerI_TicaAvail_Cnt_lgc_value         1 </td <td></td> <td>0</td>		0
k_LoaMgr_MotAgMtgtnSca_Uls_132         1           k_LoaMgr_MotAgMtghSca_Uls_132         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_RedcdSica_Uls_532         1           k_LoaMgr_RedcdSica_Uls_532         1           k_LoaMgr_RedcdSica_Uls_532         1           k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc value         0           larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaAvai_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvai_Cnt_lgc.value         1           <	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgmSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Trb_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnReat_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnReat_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc           tar	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrMgtnResp_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnResp_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnResp_Cnt_Ungc         1           k_LoaMgr_MotCurrMtgtnResp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           target_LoaMgr_Perl_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perl_LoaScaDi_Cnt_u08 value         2           target_LoaMgr_Perl_MtrOsdfptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MtrOsdfptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MtrOsdfptSig_Cnt_u08 value         1           target_LoaMgr_Perl_SteerMod_Cnt_enum.value         2           target_LoaMgr_Perl_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_ToaAvail_Cnt_lgc.valu	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1PtInd_Cnt_u08         0           k_LoaMgr_MotCurrMtghRate_UlspS_f32         1           k_LoaMgr_MotCurrMtghRsca_UlspS_f32         1           k_LoaMgr_MotCurrMtghRsca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStsivtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtr1Inact		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_u08         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc         target_LoaMgr_Per1		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrlMtgtnSca_UlspS_f32       1         k_LoaMgr_MotCurrlMtgtnSca_Uls f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_Perd_DiagcStslvtr1nactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       2         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       2         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       2         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc       target_LoaMgr_Per1_DiagcSt		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 t_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc t_LoaMgr_MotCurrMtgtnSca_Uls_f32 t_LoaMgr_RedcdSiRate_UlspS_f32 t_LoaMgr_RedcdSiRate_UlspS_f32 t_LoaMgr_RedcdSiRate_UlspS_f32 t_LoaMgr_RedcdSiRate_UlspS_f32 t_LoaMgr_RedcdSiRate_UlspS_f32 t_LoaMgr_Per1_DiagcStsivtr1inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPoldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
k_LoaMgr_MolCurrMtgtnSca_UIs_f32 k_LoaMgr_RedcdStRate_UIspS_f32 k_LoaMgr_RedcdStSca_UIs_f32 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc targe		0
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace   ✓				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.15 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	2			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	n		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	•	
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0		
LoaMqr MotAqRespSV Cnt M u08	5	5		

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 5	
LoaMgr_IvtrldptSigSV_Cnt_M_u08 2	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_MotAgRespSV_Cnt_M_u08 3	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	4 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u( target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uc target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uc target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	Expected Value 0 0	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0	Expected Value 0 0 0	<b>*</b>
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5	Expected Value 0 0 0 5	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0	Expected Value  0 0 0 5 1	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 1	Expected Value 0 0 0 5	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 1	Expected Value  0 0 0 5 1	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 1 1 1	Expected Value  0 0 0 1 1 1 1	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 5 5000	Expected Value  0 0 0 5 1 1 1 5 500 1	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 5 5000 1 5	Expected Value  0 0 0 5 1 1 1 5 5 00 1	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 5 5000 1 5 2	Expected Value  0 0 0 5 1 1 1 5 5 500 1 5 2	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 1 5 5000 1 5 2 1	Expected Value  0 0 0 5 1 1 1 5 5 0 0 1 5 1 1 1 1 1 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaState_imit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 1 5 500 1 5 2 1 3	Expected Value  0  0  0  5  1  1  1  5  500  1  5  2  1  3	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 1 5 5000 1 5 2 1	Expected Value  0 0 0 5 1 1 1 5 5 0 0 1 5 1 1 1 1 1 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaState_imit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  5  1  1  1  5  500  1  5  2  1  3  1	Expected Value  0  0  0  5  1  1  1  5  500  1  5  2  1  3  1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaAst_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 5 5 500 1 5 2 1 3 1 0	Expected Value  0  0  0  5  1  1  1  5  500  1  5  2  1  3  1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwtrQaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LvtrRespSV_Cnt_M_u08 LoaMgr_LvtrRespSV_Cnt_M_u08 LoaMgr_LoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 5 5 500 1 1 5 2 1 3 1 0 4 1 1 1	Expected Value  0  0  0  5  1  1  1  5  500  1  5  2  1  3  1  0  4	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 5 5 500 1 1 5 2 1 3 1 0 4 1 1 0	Expected Value  0 0 0 5 1 1 1 5 5 00 1 5 2 1 3 1 0 4 1 1 1 0	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaAst_Limit_UlspS_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 1 5 500 1 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173}	Expected Value  0 0 0 5 1 1 1 5 500 1 5 2 1 3 1 0 4 1 1 0 {170, 173}	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaAst_Etimit_Ulsps_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_Status(Param_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173}	Expected Value  0  0  0  5  1  1  1  5  500  1  5  2  1  3  1  0  4  1  1  0  {170, 173}	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaSateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1}	Expected Value  0 0 0 5 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StenDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 1 5 2 1 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1} 500	Expected Value  0 0 0 5 1 1 1 5 5 00 1 5 2 1 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 5 00 ± 0.01	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaSateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1}	Expected Value  0 0 0 5 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_Stent_CStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1} 500 1	Expected Value  0 0 0 5 1 1 1 5 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 5500 ± 0.01 1 1 ± 0.001	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaDi_Cnt_M_lgc LoaMgr_LoaDi_Cnt_M_lgc LoaDi_Cnt_M_lgc LoaDi_Cnt_M_lgc LoaDi_Cn	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 5 1 1 1 1 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1} 500 1	Expected Value  0 0 0 5 1 1 1 5 5 500 1 5 2 1 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TtoaDi_Cnt_M_lgc LoaMgr_DtoaMgr_DtoaDi_Cnt_M_lgc LoaMgr_DtoaDi_Cnt_M_lgc LoaMgr_DtoaDi_Cnt_	target_LoaMgr_Per1_MtrPosldptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  1  5  5  1  1  1  0  4  1  1  0  {170, 173}  {1, 1}  {1, 1}  500  1	Expected Value  0 0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 1 5 (0, 0, 0, 0, 0) {1, 1} 5 500 ± 0.01 1 1 ± 0.001 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

T (0) (47/D (0) (4)	
Test Step 1.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k LoaMgr MotAgSmaAvailResp Cnt u08	2
	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0 1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_UIs_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	- 1.00 u.i.
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	_
LoaMgr TloaAvail Cnt M lgc	0	0	<b>✓</b>
LoaMgr TloaDi Cnt M Igc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	_
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	_
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	<b>✓</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	-
gg, o	-	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.18 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LoaScaDi_Cnt_Igc	target_LoaMgr_Perl_LOASt_State_enum target_LoaMgr_Perl_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaD1_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1	, = = = = = = = = = = = = = = = = = = =





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.19 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





Namo	Input Value		
Name  k LeaMar CurristrMtatnScaZeroEn Cnt Ice	Input Value		
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	-
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>V</b>
	-	_	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

LoaMgr\_Per1

2018-05-18, 14:04:23+0530



**Actual Value Expected Value** LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08 2 LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08 LoaMgr\_TloaAvail\_Cnt\_M\_lgc LoaMgr\_TloaDi\_Cnt\_M\_lgc 1 LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08 {170, 172, 173} {170, 172, 173} Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08) {0, 0, 0, 0, 0} Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) {1, 1, 1} {1, 1, 1} 500 500 ± 0.01  $Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data)$ Rte\_Write\_Ap\_LoaMgr\_LoaScaleFctr\_Uls\_f32(data) 0 0 ± 0.001  $target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value$ 0 0 target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc.value target\_LoaMgr\_Per1\_LOASt\_State\_enum.value 5 5 target\_LoaMgr\_Per1\_MotAgLoaMtgtnEn\_Cnt\_lgc.value target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.20 (Repeat Count = 1)	<b>18</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

LoaMgr\_Per1





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc			
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target LoaMgr Per1 TloaDi Cnt lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr MotAgldptSigSV Cnt M u08	5	5	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>V</b>
Dt- Mate As Landau Land Line Community	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0	1 ± 0.001 0	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc.value	1 0 1	1 ± 0.001 0 1	, , , , , , , , , , , , , , , , , , ,
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0	1 ± 0.001 0	•



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	<b>~</b>
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.21 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	
LoaMgr MotCurrldptSigSV Cnt M u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1	
k_LoaMgr_RedcdStRate_UlspS_f32	1	
k_LoaMgr_RedcdStSca_Uls_f32	0.258599997	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr TloaAvail Cnt M lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•
0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 -			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 1.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	0.0099999978
	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

LoaMgr\_Per1

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	4 2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnRate_UispS_r32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum	yu Tanani Ta	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Paguit
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LoASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MaxWightNesp_Crit_W_u08	0	0	

0

0

1 5

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08 LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.24 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Y
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>*</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08			
	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 0	1 0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 0 5	1 0 5	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 0 5 5	1 0 5 5	•
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 5 5 5	1 0 5 5 5 5	•
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 5 5 5 500 0	1 0 5 5 5 500	• • • • • • • • • • • • • • • • • • •
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 5 5 5 500 0 5	1 0 5 5 5 500 0 5	•
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 0 5 5 5 500 0 5 0	1 0 5 5 5 500 0 5 0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 0 5 5 5 500 0 5 0 0	1 0 5 5 5 500 0 5 0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 0 5 5 5 500 0 5 5 0 0 5 5	1 0 5 5 5 500 0 5 0 0 5	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 0 5 5 5 500 0 5 0 0 5 1	1 0 5 5 5 500 0 0 5 0 0 5	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 0 5 5 5 500 0 0 5 5 0 0 0 5 5 1 0 0	1 0 5 5 5 500 0 0 5 0 0 5 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	1 0 5 5 5 500 0 0 5 0 0 5 1 0 0 3	1 0 5 5 5 5 500 0 0 5 0 0 5 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 0 5 5 5 500 0 0 5 6 1 0 0 3 1 1	1 0 5 5 5 500 0 0 5 0 0 5 1 0 0 3	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 0 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 1	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 3 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 0 5 5 5 5 500 0 0 5 5 0 0 0 5 5 1 0 0 3 1 1 1 0 0	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 3 1 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 0 5 5 5 500 0 0 5 0 0 0 5 1 0 0 0 3 1 1 1 0 0 {170}	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 3 1 1 1 0 4 170}	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 0 5 5 5 500 0 0 5 0 0 0 5 1 0 0 0 3 1 1 1 0 0 (170) (1)	1 0 5 5 5 5 5 0 0 0 5 1 0 3 1 1 1 0 4 170}	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgtloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 5 5 5 500 0 0 5 0 0 0 5 1 1 0 0 3 3 1 1 1 0 0 {170}	1 0 5 5 5 500 0 5 0 0 5 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0} {1}	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NatrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 0 0 5 5 5 500 0 0 5 0 0 0 5 1 1 0 0 3 3 1 1 1 0 0 {170} {1} 500	1 0 5 5 5 500 0 5 0 0 5 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01	
LoaMgr_IvtrLoaMigtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1 0 0 5 5 5 500 0 0 5 5 0 0 0 5 5 1 1 0 0 3 3 1 1 1 1 0 0 {170} {1} {1} {1} 500 0 0	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001	
LoaMgr_IvtrLoaMigtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStale_Migt_S_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 0 0 5 5 5 500 0 0 5 5 0 0 0 5 5 1 1 0 0 3 3 1 1 1 1 0 0 {170} {1} 500 0 0 1 1	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_IvtrLoaMigtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStale_Migt_S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMigtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMigtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0 5 5 5 500 0 0 5 5 0 0 0 0 5 5 1 1 0 0 3 3 1 1 1 1 0 0 {170} {11} {11} 500 0 0 1 1 0 0	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1 0	
LoaMgr_IvtrLoaMigtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStale_Migt_S_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 0 0 5 5 5 500 0 0 5 5 0 0 0 5 5 1 1 0 0 3 3 1 1 1 1 0 0 {170} {1} 500 0 0 1 1	1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	-
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.25 (Repeat Count = 1)	C C
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	251.257599
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0

LoaMgr\_Per1

2018-05-18, 14:04:23+0530



Loaivigi_r er i			TOIL
Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	.08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~

Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

1

0

{1}

500

n

1

n

5

0

0

{170}

1

0

{170}

{1}

n

5

0

0

500 ± 0.01 0 ± 0.001

LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08

Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum)

Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data)

target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value

target\_LoaMgr\_Per1\_MotAgLoaMtgtnEn\_Cnt\_lgc.value

target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value

 $Rte\_Write\_Ap\_LoaMgr\_LoaScaleFctr\_Uls\_f32(data)$ 

 $target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc.value$ 

target\_LoaMgr\_Per1\_LOASt\_State\_enum.value

Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)
Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)

LoaMgr\_TloaAvail\_Cnt\_M\_lgc LoaMgr\_TloaDi\_Cnt\_M\_lgc LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08





Test Step 1.26 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1mactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
3 2 3 2 3 2 3 2 3 2 3 2 3 2 3 2 3 2 3 2	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.27 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

Input Value





k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	500		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	0 4		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	4 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	4 0 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	4 0 1 2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	4 0 1 2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	4 0 1 2 1 1	nt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	4 0 1 2 1 1 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_igc } t_igc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_igc } t_igc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lq	nt_igc } t_igc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_loASt_State_enum	nt_lgc s t_lgc gc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc s t_lgc gc nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_Creater_Creater_CoaMgr_Per1_MotAgLoaMtgtnEn_Creater_Crea	nt_lgc s t_lgc gc nt_lgc 08	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt_	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_let target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAglLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	Result
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_let target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloaMtgtnEn_ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	Result
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc Actual Value	nt_lgc 8 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value	Result
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc Actual Value 0	nt_lgc 8 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0	~
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ligtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ligtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Creatingtet_LoaMgr_Per1_MotCurrLoaMtgtnEn_Creatingtet_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 1	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0	~
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_Able_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TolaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Dicnt_igc target_LoaMgr_Per1_MotCurridptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrit_oaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 1	<b>*</b>
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_igt target_LoaMgr_Per1_LoaScaDi_Cnt_igc target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_LoaMgr_Per1_MotCurridoaMtgtnEn_Crit target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_igc target_LoaMgr_Per1_TloaDi_Cnt_igc Actual Value 0 1 1 2	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Cnt_lgc 08  Expected Value 0 1 1	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_igt target_LoaMgr_Per1_LoASc_State_enum target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 1	nt_lgc 3 t_lgc gc  nt_lgc 08 Cnt_lgc 08 Cnt_lgc 08   Expected Value 0 1 1 2 1	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IncaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_u0& target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Dat_logc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosloptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosloptSig_Cnt_u0& target_LoaMgr_Per1_TloaAvail_Cnt_logc target_LoaMgr_Per1_TloaDi_Cnt_logc Actual Value 0 1 1 2 1	nt_lgc st_lgc gc st_lgc gc st_lgc lgc lgc lgc lgc lgc lgc lgc lgc lgc	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc activated LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 3	nt_lgc  st_lgc gc  st_lgc gc  st_lgc	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Ivtr_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Diagt_Cnt_u0& target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 1 3 5 5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Ivtr_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 3 5 5000	nt_lgc st_lgc st_lgc gc  nt_lgc os pt_lgc st_lgc st	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 0 1 2 1 1 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAQLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrlotptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 3 5 500 0	nt_lgc  st_lgc  st_lgc	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 0 1 2 1 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 3 5 500 0 5	nt_lgc st_lgc gc  nt_lgc gc  nt_lgc 08  Cnt_lgc 08  Expected Value 0 1 1 1 2 1 1 3 5 5000 0 5	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.28 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_v target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_loaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	<b>Y</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr LOASt State M enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1} 500	{1, 1} 500 + 0.01	· ·
Rte_Write_Ap_LoaMgr_LoaScaleEctr_UIs_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0 ± 0.001	Ž
target_LoaMgr_Per1_HW1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
0 0			
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.29 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_IvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	159.248901	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1	
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0 1	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1	
k_LoaMgr_RedcdStRate_UlspS_f32	1	
k_LoaMgr_RedcdStSca_Uls_f32	1	
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0	



Sander, Justificações (C. M. J. Lillion (C. M. J	Test Step 1.30 (Repeat Count = 1)	13
Landry   L		·
Leading   InterCalego Com   A   100	LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
Leading   John Septings   Cort   M.   100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Conting   Managed Professor   Conting   Cont	LoaMgr_HwTqRespSV_Cnt_M_u08	
Loading - Michaglosoft - Comp.   Loading - Loading - Michaglosoft - Comp.   Loading - Michaglosoft - - Micha	LoaMgr_lvtrldptSigSV_Cnt_M_u08	-
Loading   MacAgeneris   Core   M. 195		
Loading Junding contingents		
Looking MacControl (Styl) Core   M. 198		
Londing MoCorranges St. Crit. M. 108		
Loading - Michardes Cont. M. Job Loading - Similation April - May 100 Rite   Loading - Loading		
Loading   Loading   Loading   Loading	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
Loading / Windpriss   Loading / CurrierMitgh   Loading / Loading / CurrierMitgh   Loading / Loading	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Ris (mat. Ap. Loably)  **Loably CurrivrMignifisca Units 5:122  **Loably CurrivrMignifisca Units 5:122  **Loably Friedrichs Control (Control)  **Loably Friedrichs Control  **Loably Friedrichs Contro	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
L. Landry_Currient/ignoSaz_Visin_Crit ge	LoaMgr_VltgMdSrcSV_Cnt_M_u08	
L. Landing: John Ministrick Carlotter   12	Rte_Inst_Ap_LoaMgr	V
Labady_MorphispSyllawAnimes_Crit_USS   2		
Linding   Mediging   Mediging   Cort, 108		
Loading-Interfacings   Transcription   Loading-Interfacings   Loading-Interfacings   Transcription   Loading-Interfacings   Loading-Interfacings   Transcription   Loading-Interfacing   Loading-Interf		
L. Loading-Info@ingStg2TeachBrisegs_Crd_UBB		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
L. Loaking-HwTqdptSig4Resp_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
L. Loaking / HwTqitignSpiFTIMC Cnt, U68	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
Loading / HwTqNtginSca Uis / G2	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loakg_NrtdptSigNeap_Cnt_u08		
K_LosMgr_InvirlightSig2Resp_Cnt_U08		
L. LooMgr_InviriginSignFitTind_Cnt_U08		
L. Looking - Inviringinis accentage - Lipes - 122		
k_LosMgr_InvintignSca_Uis_152         0           k_LosMgr_InvintignSca_Uis_152         1           k_LosMgr_MoNapidpSignSignSesp_Cnt_U08         5           k_LosMgr_MoNapidpSignSignSesp_Cnt_U08         1           k_LosMgr_MoNapidpSignSignSesp_Cnt_U08         0           k_LosMgr_MoNapidpSignSignSesp_Cnt_U08         2           k_LosMgr_MoNapidpSignSignSesp_Cnt_U08         2           k_LosMgr_MoNapidpsScazenSer_Cnt_Uge         0           k_LosMgr_MoNapidpsScazenSer_Cnt_U08         2           k_LosMgr_MonapidpSignSesp_Cnt_U08         2           k_LosMgr_MolCurridptSignResp_Cnt_U08         2           k_LosMgr_MolCurridptSignResp_Cnt_U08         2           k_LosMgr_MolCurridptSignResp_Cnt_U08         0           k_LosMgr_MolCurridptSignResp_Cnt_U08         0           k_LosMgr_MolCurridptSignResp_Cnt_U08         0           k_LosMgr_MolCurridptSignResp_Cnt_U08         0           k_LosMgr_MolCurridptSignResp_Cnt_U08         0           k_LosMgr_MolCurridptSignResp_Cnt_U08         1           k_LosMgr_MolCurridptSignResp_Cnt_U08         1           k_LosMgr_MolCurridptSignResp_Cnt_U08         1           k_LosMgr_Pert_Disps_Sistritinactv_Cnt_Igc_value         1           target_LosMgr_Pert_Disps_Sistritinactv_Cnt_Igc_value         0 <tr< td=""><td></td><td></td></tr<>		
LoaMgr_MotAgloptSig2Resp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldplSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldplSig1Thd_Cnt_u08         2           k_LoaMgr_MotAgldplSig1Thd_Cnt_u08         1           k_LoaMgr_MotAgMignRate_UBsp_S_22         1           k_LoaMgr_MotAgMignSca_Uis_r32         1           k_LoaMgr_MotAgMignSca_Uis_r32         1           k_LoaMgr_MotAgMignSca_Uis_r32         1           k_LoaMgr_MotQuridpISig1Resp_Cnt_u08         2           k_LoaMgr_MotCuridpISig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridpISig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridpISig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridpISig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridpIsignSca_Uis_r32         1           k_LoaMgr_MotCuridpIsignSca_Uis_r32         1           k_LoaMgr_RedoSiSea_Uis_r32         1           k_LoaMgr_RedoSiSea_Uis_r32         1           k_LoaMgr_RedoSiSea_Uis_r32         1           k_LoaMgr_RedoSiSea_Uis_r32         1           LoaMgr_Perl_DiagcSistvrInacv_Cnt_lgc value         0           target_LoaMgr_Perl_DiagcSistvrInacv_Cnt_lgc value         1           target_LoaMgr_Perl_MotCuridpISig_Cnt_u08 value         2           target_LoaMgr_Perl_MotCuridpISig_Cnt_u08 value         3           target_LoaMgr_Perl_MotCuridpISig_Cnt_u08 v	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_ LoaMgr_MotAgldptSigFitThd_Cnt_U08         2           k_ LoaMgr_MotAgMtgtNacte_UlspS_132         1           k_ LoaMgr_MotAgMtgtNacae_Cnt_Ct_lee         0           k_ LoaMgr_MotAgMtgtNacae_Cnt_U08         2           k_ LoaMgr_MotCuridptSigNace_Cnt_U08         2           k_ LoaMgr_MotCuridptSigNace_Cnt_U08         2           k_ LoaMgr_MotCuridptSigNace_Cnt_U08         0           k_ LoaMgr_MotCuridptSigPace_Cnt_U08         0           k_ LoaMgr_MotCuridptSigPace_Cnt_U08         0           k_ LoaMgr_MotCuridptSigNace_Cnt_U08         0           k_ LoaMgr_MotCuridptSigNace_Cnt_U08         0           k_ LoaMgr_MotCuridptSigNace_Cace_Cnt_Igc         1           k_ LoaMgr_MotCuridptInscace_Cnt_Igc         1           k_ LoaMgr_RedcdSiSca_Uls_52         1           k_ LoaMgr_RedcdSiSca_Uls_52         1           k_ LoaMgr_RedcdSiSca_Uls_52         1           k_ LoaMgr_Pert_DiagcSistvr1lnactv_Cnt_Igc value         0           target_LoaMgr_Pert_DiagcSistvr2lnactv_Cnt_Igc value         1           target_LoaMgr_Pert_ImcCuridptSig_Cnt_U08 value         2           target_LoaMgr_Pert_ImcCuridptSig_Cnt_U08 value         3           target_LoaMgr_Pert_ImcCuridptSig_Cnt_U08 value         1           target_LoaMgr_Pert_Imc_DiagcsIstvr2lnactv_Cnt_Igc         1	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghrRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
K_LoaMgr_MorkAgMtginSca_ZeroEn_Cnt_lgc         0           K_LoaMgr_MorkAgMtginSca_Uis_732         1           K_LoaMgr_MorkAgMtginSca_Uis_732         1           K_LoaMgr_Mork_Grapma-VaiResp_Cnt_u08         2           K_LoaMgr_Mork_Grapma-VaiResp_Cnt_u08         0           K_LoaMgr_Mork_Grapma-VaiResp_Cnt_u08         0           K_LoaMgr_Mork_Mork_Grapma-VaiResp_Cnt_u08         0           K_LoaMgr_Mork_Mork_Grapma-VaiResp_Cnt_u08         0           K_LoaMgr_Mork_Mork_Grapma-VaiResp_Cnt_u08         1           K_LoaMgr_Mork_Mork_Mark_Grapma-VaiResp_Cnt_u08         1           K_LoaMgr_Mork_MignSca_Uis_5122         1           K_LoaMgr_Mork_MignSca_Uis_5122         1           K_LoaMgr_ReddSisca_Uis_5122         1           K_LoaMgr_Pert_DiagcStsivtr1nactv_Cnt_lgc_value         0           Larget_LoaMgr_Pert_DiagcStsivtr2nactv_Cnt_lgc_value         1           Larget_LoaMgr_Pert_Mork_Grapma-Vail_Cnt_lgc_value         1           Larget_LoaMgr_Pert_Mork_Grapma-Vail_Cnt_lgc_value         1           Larget_LoaMgr_Pert_Mork_Grapma-Vail_Cnt_lgc_value         3           Larget_LoaMgr_Pert_Mork_Grapma-Vail_Cnt_lgc_value         1           Larget_LoaMgr_Pert_Mork_Grapma-Vail_Cnt_lgc_value         1           Larget_LoaMgr_Pert_ToaAvail_Cnt_lgc_value         1	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgdmay_MotAgdmayawilResp_Cnt_u08         2           k_LoaMgr_MotCurridptSignResp_Cnt_u08         2           k_LoaMgr_MotCurridptSignResp_Cnt_u08         0           k_LoaMgr_MotCurridptSignResp_Cnt_u08         0           k_LoaMgr_MotCurridptSignResp_Cnt_u08         0           k_LoaMgr_MotCurridptSignResp_Cnt_u08         0           k_LoaMgr_MotCurridptSignResp_Cnt_u08         0           k_LoaMgr_MotCurridptSignResp_Cnt_u08         1           k_LoaMgr_MotCurridptSignSesp_Cnt_u08         1           k_LoaMgr_ReddSisac_u1sg_32         1           k_LoaMgr_ReddSisac_u1sg_32         1           k_LoaMgr_Per_DiagcStsivrtInactv_Cnt_gc value         1           target_LoaMgr_Per_LicaScsbi_Cnt_u08 value         2           target_LoaMgr_Per_IntPosidptSig_Cnt_u08 value         3           target_LoaMgr_Per_IntPosidptSig_Cnt_u08 value         1           target_LoaMgr_Per_Interp_Cnt_lgc_cnt_u08         1 <td></td> <td></td>		
K_LoaMgr_MotOurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           K_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           K_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2Sa_Uis_f32         1           K_LoaMgr_RedGSISca_Uis_f32         1           K_LoaMgr_Perd_DiagcStsivtrilnactv_Cnt_igc value         0           Larget_LoaMgr_Per1_DiagcStsivtrilnactv_Cnt_igc value         1           Larget_LoaMgr_Per1_BagcStsivtrilnactv_Cnt_igc-value         1           Larget_LoaMgr_Per1_LoaScaDi_Cnt_igc-value         2           Larget_LoaMgr_Per1_LoaScaDi_Cnt_igc-value         0           Larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Per1_ToaOl_Cnt_igc-value         1           Larget_LoaMgr_Per1_ToaOl_Cnt_igc-value         1           Larget_LoaMgr_Per1_ToaOl_Cnt_igc-value         1           Larget_LoaMgr_Per1_ToaOl_Cnt_igc-value         1           Larget_LoaMgr		-
K_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           K_LoaMgr_MotCurridptSig-CareCnt_Cnt_lgc         1           K_LoaMgr_RedcdSiRate_UlspS_32         1           K_LoaMgr_RedcdSiRate_UlspS_32         1           K_LoaMgr_Perd_DiagcSistvirInactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcSistvirZinactv_Cnt_lgc.value         1           target_LoaMgr_Perd_LoaScs_Di_Cnt_lgc.value         1           target_LoaMgr_Perd_LoaScs_Di_Cnt_lgc.value         0           target_LoaMgr_Perd_MirPosliptSig_Cnt_u08 value         1           target_LoaMgr_Perd_MirPosliptSig_Cnt_u08 value         3           target_LoaMgr_Perd_TioaDl_Cnt_lgc.value         0           target_LoaMgr_Perd_TioaDl_Cnt_lgc.value         1           target_LoaMgr_Perd_TioaDl_Cnt_lgc.value         1           target_Rel_Inst_Ap_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_l		
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRete_UlspS_f32         1           k_LoaMgr_MolCurrMtgtnSea_UlspS_f32         1           k_LoaMgr_MolCurrMtgtnSea_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Perf_MolGurridptSig_Cnt_u08.value         2           target_LoaMgr_Perf_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perf_MolCurridptSig_Cnt_u08.value         3           target_LoaMgr_Perf_SteerMod_Cnt_enum.value         1           target_LoaMgr_Perf_TioaAvail_Cnt_lgc.value         1           target_LoaMgr_Perf_TioaDi_Cnt_lgc.value         1           target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perf_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_ins		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           target_LoaMgr_Perd_DiagcStstvftrInactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStstvft2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perd_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Perd_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perd_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perd_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perd_TinaOavail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinaOavail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinaOavail_Cnt_lgc.value         1           target_Re_inst_Ap_LoaMgr_Perd_DiagcStslvtr1inactv_Cnt_lgc         target_LoaMgr_Perd_DiagcStslvtr2inactv_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perd_DiagcStslvtr2inactv_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perd_InvTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MolCurrIdptSigFiltThd_Cnt_u08         0           k_LoaMgr_MolCurrIdptRate_UlspS_f32         1           k_LoaMgr_MolCurrMtgtnScazeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_PedcdSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_L		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_ToaDi_Cnt_lgc.value target_LoaMgr_Per1_ToaDi_Cnt_lgc.value target_LoaMgr_Per1_ToaDi_Cnt_lgc.value target_LoaMgr_Per1_ToaDi_Cnt_lgc.value target_LoaMgr_Per1_ToaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IoaScaDi_Cnt_lgc target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_VotLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_VotLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SterMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UlspS_132 k_LoaMgr_RedcdStSca_Uls_132 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rel_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Uo	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  2  target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Ne_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Re_inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Re_inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_VoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_LoaSt_State_enum  1 target_LoaMgr_Per1_LoaSt_State_enum  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  0  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value  target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc  target_LoaMgr_Per1_LoaScaDi_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  1 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqlotpSig_Cnt_u08  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqlotpSig_Cnt_u08  1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqlotpSig_Cnt_u08  1 target_LoaMgr_Per1_HwTqlotpSig_Cnt_u08  1 target_LoaMgr_Per1_LoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_LoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  1 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqlothSig_Cnt_u08  1 target_LoaMgr_Per1_HwTqlothSig_Cnt_u08  1 target_LoaMgr_Per1_HwTqlothSig_Cnt_u08  1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  1 target_LoaMgr_Per1_HwTqlothSig_Cnt_u08  1 target_LoaMgr_Per1_HwTqlothSig_Cnt_u08  1 target_LoaMgr_Per1_LoaMgtInEn_Cnt_lgc  1 target_LoaMgr_Per1_LoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.31 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

Input Value



Name

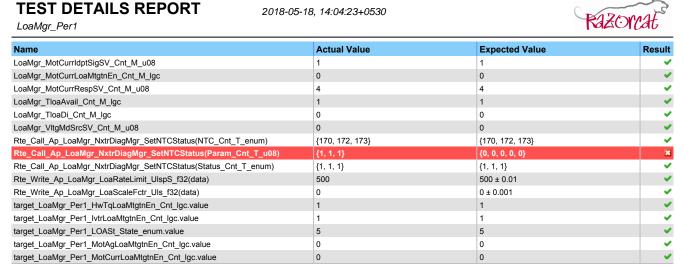


Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
	4		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0		
	1		
k_LoaMgr_MotAgSmgAyailRoap_Cat_u08			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
torigot bournight for a first graptiony ont aboutaine			
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 1 1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 1 1 1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 1 1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 1 1 1	Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 1 1 1 0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_(	Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_(	Cnt_lgc 8	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	Cnt_lgc 8 nt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	Cnt_lgc 8 nt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum	Cnt_lgc 8 nt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	Cnt_lgc 8 nt_lgc lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc 8 nt_lgc lgc int_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cd_ target_LoaMgr_Per1_MotCurrldptSig_Cnt_	Cnt_lgc 8 nt_lgc lgc int_lgc u08	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc	1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrldptSig_Cnt_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lotAst_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrldptSig_Cnt_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	Cnt_lgc 8 nt_lgc lgc int_lgc Cnt_lgc U08 Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ct_ target_LoaMgr_Per1_MotCurrldptSig_Cnt_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enum	Cnt_lgc 8 nt_lgc lgc int_lgc Cnt_lgc U08 Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgl_oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ct_ target_LoaMgr_Per1_MotCurrIdptSig_Cnt_uarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Cnt_lgc 8 nt_lgc lgc int_lgc Cnt_lgc U08 Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ct_ target_LoaMgr_Per1_MotCurrldptSig_Cnt_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enum	Cnt_lgc 8 nt_lgc lgc int_lgc Cnt_lgc U08 Cnt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ct_ target_LoaMgr_Per1_MotCurrIdptSig_Cnt_uarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Cnt_lgc 8 nt_lgc lgc int_lgc Cnt_lgc U08 Cnt_lgc	Result
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C1 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u1 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u1 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08	Result
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotGurrldptSig_Cnt_uarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_uarget_LoaMgr_Per1_MtrPosldptSig_Cnt_uarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08  Expected Value	Result
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotGurrldptSig_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosldptSig_Cnt_utarget_LoaMgr_Per1_MtrPosldptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08  Expected Value 0	Result
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_atste_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_ target_LoaMgr_Per1_VrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_VrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MotCurrloaMtgtnEn_LoaMgr_Per1_MotCurrLoaMtgtnEn_LoaMgr_Per1_MotCurrLoaMtgtnEn_LoaMgr_Per1_MotCurrLoaMtgtnEn_LoaMgr_Per1_MtrPosldptSig_Cnt_u target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08  Expected Value 0	Result
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqtoaMtgtnEn_Cnt_M_lo8 LoaMgr_HwTqtoaMtgtnEn_Cnt_M_lo8 LoaMgr_HwTqRespSV_Cnt_M_lo8	1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_target_LoaMgr_Per1_VrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_VrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_target_LoaMgr_Per1_MotCurrIdoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08  Expected Value 0 1 1 2	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_target_LoaMgr_Per1_LvtLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1	Cnt_lgc 8 nt_lgc lgc int_lgc u08 Cnt_lgc 08  Expected Value 0 1 1 2 1	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LotAst_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlaptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_TloaCont_IdptSig_Cnt_utarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1	Expected Value  0 1 1 2 1 1 1	***
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlaptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASc_Diagc_Indigc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 4	Expected Value  0 1 1 2 1 1 4	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_( target_LoaMgr_Per1_DiagcStsIvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 4 5	Ent_lgc 8 nt_lgc lgc  Int_lgc u08  Cnt_lgc 08  Expected Value 0 1 1 2 1 1 4 5	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 2 1 1 4 5 500	Ent_lgc 8 nt_lgc lgc  Int_lgc lgc  Expected Value 0 1 1 2 1 1 4 5 5 500	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTql.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_632 LoaMgr_LoaScaleFctr_Uls_M_632	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 4 5 500 0	Ent_lgc 8 nt_lgc lgc  Int_lgc lgc  Ent_lgc 08  Expected Value 0 1 1 2 1 2 1 1 4 5 5 500 0	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 2 1 1 4 5 500	Ent_lgc 8 nt_lgc lgc  Int_lgc lgc  Expected Value 0 1 1 2 1 1 4 5 5 500	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LotASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 4 5 500 0	Ent_lgc 8 nt_lgc lgc  Int_lgc lgc  Ent_lgc 08  Expected Value 0 1 1 2 1 2 1 1 4 5 5 500 0	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 1 1 1 1 1 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_DiagcStslvtr2Inactv_( target_LoaMgr_Per1_HwTqldptSig_Cnt_u0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 1 4 5 500 0 5	Expected Value  0 1 1 2 1 1 4 5 5 500 0 5	· · · · · · · · · · · · · · · · · · ·

3

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.32 (Repeat Count = 1)	<b>X</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

© Report created by TESSY V3.1.12, report template V2.1

62

2018-05-18, 14:04:23+0530



Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	C	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	I=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08			
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
·	2	2	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr LoaScaleFctr Uls M f32	0	0	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	_
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	-
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.33 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1l	nacty Cnt loc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgt	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_IvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr_Der1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cni		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Ig		
Name	Actual Value		Result
	0	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc		0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1		<b>•</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>*</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 1.34 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgloptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	3
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Perl_DiagcStsIvtr1mactv_Cnt_lgc target_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.35 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

Input Value



Name



Name	Input value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k LoaMgr HwTqMtgtnRate UlspS f32	1			
	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08				
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32				
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u			
target Rte Inst Ap LoaMgr.LoaMgr Perl SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Function Value	Des. II	
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	•	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	•			
	500	500	<b>✓</b>	
LoaMgr_LoaScaleFctr_Uls_M_f32		500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	500			
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 1 5	1 5	•	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	500	1	<b>V</b>	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 1 5 0	1 5 0	, ,	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace   ✓					
Actual Function	Count	Expected Function	Count	Result	
CalclytrState	2	CalcIvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	-	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	-	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 1.36 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	•	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(	ant lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	_	
		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	· ·	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0	0	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1	0 1 1	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2	0 1 1 2	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  1	0 1 1 2	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 1 0	0 1 1 2 1 0	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc  LoaMgr_HwTqldptSig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_lvtrldptSig\$V_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_lvtrResp\$V_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5	0 1 1 2 1 0 5	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc  LoaMgr_HwTqldptSig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_lvtrldptSig\$V_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_lvtrResp\$V_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 1 0 5 5	0 1 1 1 2 1 0 5 5 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500	0 1 1 2 1 0 5 5 5	· · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc  LoaMgr_HwTqldpt\$ig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_lvtrldpt\$ig\$V_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_LOA\$t_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0	0 1 1 2 1 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500	0 1 1 2 1 0 5 5 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc  LoaMgr_HwTqldpt\$ig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_lvtrldpt\$ig\$V_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_lvtrResp\$V_Cnt_M_u08  LoaMgr_LOA\$t_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0	0 1 1 2 1 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldpt\$ig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldpt\$ig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_LOA\$t_State_M_enum LoaMgr_LoaScaleFctr_Ulsp\$_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  5	0 1 1 2 1 0 5 5 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_atate_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  1	0 1 1 2 1 0 5 5 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaSt_atate_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  5  1  0  5	0 1 1 2 1 0 5 5 5 500 0 5 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaSt_ate_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  10  0  5  1	0 1 1 1 2 1 0 5 5 5 500 0 0 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  1  0  5  1	0 1 1 1 2 1 0 5 5 5 500 0 0 5 1 1 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_etate_Menum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  5  1  1  0  5  1  1  3	0 1 1 1 2 1 0 5 5 5 500 0 0 5 1 1 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  00  0  5  1  1  0  5  1  1  3  1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  5  1  1  0  5  1  0  6  6  7  8  8  8  8  8  8  8  8  8  8  8  8	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MoxAmtgtnResp_Cnt_M_u08 LoaMgr_MoxAmtgtnResp_Cnt_M_u08 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  500  0  5  1  1  0  5  1  0  5  1  0  5  1  0  5  1  0  5  0  5  0  0  5  0  0  5  0  0  5  0  0	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgUsigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  00  0  5  1  1  0  5  1  0  5  1  0  5  1  0  5  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 5 1 1 0 0 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  500  0  5  1  1  0  5  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgUsigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  00  0  5  1  1  0  5  1  0  5  1  0  5  1  0  5  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 5 1 1 0 0 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  500  0  5  1  1  0  5  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgHostgSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  500  0  5  1  1  0  5  1  1  0  5  1  1  1  1  2  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  500  0  5  1  1  0  5  1  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 0 0 (170, 173) {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 1 0 5 5 5 5 00 0 5 1 1 0 0 5 1 0 5 1 1 1 1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 0 6 5 1 1 1 3 1 0 0 0 {170, 173} {0,0,0,0,0} {1,1} 500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 1 0 5 5 5 5 00 0 0 5 1 1 0 0 5 1 1 1 2 1 1 0 0 5 1 1 1 1 1 3 1 1 1 3 1 1 0 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  1  0  5  5  5  500  0  5  1  1  0  5  1  1  1  1  1  1  1  1  1  1  1  1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 1 0 5 5 5 5 00 0 0 5 1 1 0 0 5 1 1 1 2 1 1 0 0 5 1 1 1 1 1 3 1 1 1 3 1 1 0 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

	_
Test Step 1.37 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig1Resp_Crit_u08 k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgtnEn C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 lvtrLoaMtgtnEn Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	. •	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ent lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	rtoouit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_IvtrRespSV_Cnt_M_u08	3	3	-
LoaMgr_LOASt_State_M_enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_totaMiginEn_cni_igc.value target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_LOASt_State_enum.value target LoaMgr_Per1_MotAgLoaMtgtnEn Cnt lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_igc.value		1	-
target_Loaivigt_Fer t_iviotouriLoaivitgtriEn_ont_igc.value	1		_

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalcivtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	•	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	



Test Step 1.38 (Repeat Count = 1)	<b></b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn	t_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lg	jc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	<b>✓</b>

Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	<b>✓</b>
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	<b>✓</b>
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	<b>V</b>

Test Step 1.39 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnRate_UispS_132 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.40 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	2
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k LoaMgr RedcdStRate UlspS f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inact		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_ target_LoaMgr_Per1_IvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	<b>Y</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	· ·
LoaMgr_HwTqRespSV_Cnt_M_u08	1		
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	
LoaMgr LOASt State M enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	Ō	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>V</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 173, 174, 175} {1, 1, 1, 1, 1}	{170, 172, 173, 174, 175} {0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	<u> </u>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Took Chan 4 44 (Damast Count = 4)	· · · · · · · · · · · · · · · · · · ·
Test Step 1.41 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0
	1
k_LoaMgr_MotAgSmaAvoilBoop_Cot_u08	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnRate_OlspS_132 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_UIs_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr HwTgRespSV Cnt M u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr TloaAvail Cnt M lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 1.42 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosioptSig_Cnt_uos.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrlopMitthEn_Cnt_loc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc

2018-05-18, 14:04:23+0530

Input Value





k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	2		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value	2 1 1 3		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 1 1 3 1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value	2 1 1 3 1	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	2 1 1 3 1 1 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Co	nt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	nt_lgc _lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ter_I	nt_lgc _lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum	nt_lgc _lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc lc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_target_LoaMgr_Per1	nt_lgc _lgc ic t_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	nt_lgc _lgc ic t_lgc 08	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_larget_LoaMgr	nt_lgc _lgc t_lgc 08 2nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	nt_lgc _lgc t_lgc 08 2nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_lgt target_LoaMgr_Per1_IvrtLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_lut target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgcc_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc _lgc t_lgc 08 2nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc	nt_lgc _lgc t_lgc 08 2nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	nt_igc _igc t_igc 08 cnt_igc 8	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_ugc_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_ugc_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	nt_igcigc t_igc 08 cnt_igc 8	Result
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_HvTLoaMtgtnEn_Cntarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_Loa	nt_igc _igc t_igc 08 cnt_igc 8  Expected Value	~
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotOurrloptSig_Cnt_u08target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_Loa	nt_igc _igc t_igc t_igc 08 cnt_igc 8  Expected Value 1	~
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 0	nt_igc _igc c t_igc 08 cnt_igc 8  Expected Value 1 2 0	<b>*</b>
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotOurrloptSig_Cnt_u08target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_Loa	nt_igc _igc t_igc t_igc 08 cnt_igc 8  Expected Value 1	~
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 0	nt_igc _igc c t_igc 08 cnt_igc 8  Expected Value 1 2 0	<b>*</b>
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 0 5	nt_igc _igc c t_igc 08 cnt_igc 8  Expected Value 1 2 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqldptSigSV_Cnt_M_u08	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u01 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_TloaAvail_Cnt_u01 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 0 5 0	ent_lgc L_lgc lgc lgc lgc lgc lgc lgc lgc lgc lgc	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u00 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u00 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 0 5 0 0	Expected Value  1 2 0 5 0 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlOaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HvTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc  Actual Value 1 2 0 5 0 0 5	Ligc   Ligc	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn_target_LoaMgr_Per1_TloaDid_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 1 2 0 5 0 0 5 5 5	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmint_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 1 1 3 1 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoAScaDi_Cnt_lgctarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotQurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cntarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_P	Ligc   Ligc	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRtelmint_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	2 1 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 1 2 0 5 5 5 500 1 5	Ligc   Ligc	****
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 1 1 3 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_loatarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loatarget_LoaMgr_Per1_LoaScaDi_Cnt_logtarget_LoaMgr_Per1_LoaScaDi_Cnt_logtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0atarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0atarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_TloaAvail_Cnt_logtarget_LoaMgr_Per1_TloaAvail_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_LoaMgr_Per1_TloaDi_Cnt_logcarget_DoaMgr_Per1_TloaDi_Cnt_logcarget_	Ligc   Ligc	***

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.44 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cl	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	J8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr MotCurrldptSigSV Cnt M u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	V
	0	0	-
target LoaMgr Per1 lytrLoaMtgtnEn Cnt lgc.value		1.5	_
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	5	5	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
	5 1 1	5 1 1	<b>V</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.45 (Repeat Count = 1)	C
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrNdptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	/ v
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAglaptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2	2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	(470, 470, 475)	· .	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 175}	{170, 172, 175}	<b>✓</b>
	{1, 1, 1}	{0, 0, 0, 0, 0}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>V</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.46 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2 2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_igc.value target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
5	00





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.47 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





Input Value	
k_LoaMgr_CurrivrtMtgInSca_UIs_f32         1           k_LoaMgr_HadeOuts/Rate_UIspS_f32         1           k_LoaMgr_HvTqldptSigTloaAvailResp_Cnt_u08         2           k_LoaMgr_HvTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HvTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HvTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HvTqldptSig4TloaEnaResp_Cnt_u08         0           k_LoaMgr_HvTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HvTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HvTqMtgInRac_UIspS_f32         1           k_LoaMgr_HvTqMtgInRac_UIspS_f32         1           k_LoaMgr_IvtfdptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtfdptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtfdptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtfdptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtfMtgInRac_UIspS_f32         1           k_LoaMgr_IvtfMtgInRac_UIspS_f32         1           k_LoaMgr_IvtfMtgInRac_UIspS_f32         1           k_LoaMgr_IvtfMtgInRac_UIspS_f32         1           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         3           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgidptSig4Resp_Cnt_u08         2           k_LoaMgr_MotAgMtgInSca_UIs_f32	
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutSffate_UlspS_f32         1           k_LoaMgr_HvTqldptSigTtoaAvailResp_Cnt_u08         2           k_LoaMgr_HvTqldptSig1ToaAvailResp_Cnt_u08         4           k_LoaMgr_HvTqldptSig1ToaAvailResp_Cnt_u08         2           k_LoaMgr_HvTqldptSig1ToaEnaResp_Cnt_u08         1           k_LoaMgr_HvTqldptSig4ToaEnaResp_Cnt_u08         0           k_LoaMgr_HvTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HvTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HvTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HvTqMtgtnSca_Uls_f32         1           k_LoaMgr_HvTqtpSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldpSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldpSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldpSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldpSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrldgnScaz_Uls_f32         1           k_LoaMgr_MotAgldptSigFitSigNosmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSigSResp_Cnt_u08         1           k_LoaMgr_MotAgldptSigSrResp_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtghtSca_Uls_f32         1           k_LoaMgr_MotAgMtghtSca_Uls_f32<	
k_LoaMg_HwTqldptSig1NoToaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoToaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaDiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtglnSca_Uls_f32         1           k_LoaMgr_HwTqMtglnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1FltThd_Cnt_u08         0           k_LoaMgr_IvtrldptSig1FltThd_Cnt_u08         0           k_LoaMgr_IvtrldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Sesp_Cnt_u08         2           k_LoaMgr_MotAglMgtmSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrld	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqMtglnRate_UlspS_f32         1           k_LoaMgr_HwTqMtglnSca_Uls_f32         1           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrMtglnRate_UlspS_f32         1           k_LoaMgr_IvtrMtglnScaZeroEn_Cnt_u08         0           k_LoaMgr_IvtrMtglnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1RoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig1RoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSi	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaEnResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaEnResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqldptRate_UlspS_f32         1           k_LoaMgr_HwTqldptRate_UlspS_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSca_Uls_f32         1           k_LoaMgr_IvtrldptSca_Cls_f32         1           k_LoaMgr_NotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAglMgtnSca_Uls_f32         1           k_LoaMgr_MotAglMgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFltThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3FitTnd_Cnt_u08         2           k_LoaMgr_MotAglMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAglMtgtnSca_Uls_f32         1           k_LoaMgr_MotAglmAglmSca_Uls_f32         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0 </td <td></td>	
k_LoaMgr_HWTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HWTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HWTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HWTqldptSig1Resp_LlispS_f32         1           k_LoaMgr_HWTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_IWtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IWtrldptSig1FitThd_Cnt_u08         0           k_LoaMgr_IWtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IWtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IWtrldptSig2Resp_Cnt_u08         1           k_LoaMgr_IWtrldptSig2Resp_Cnt_u08         3           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgmRate_UlspS_f32         1           k_LoaMgr_MotAgMtgmRate_UlspS_f32         1           k_LoaMgr_MotAgMtgmRate_UlspS_f32         1           k_LoaMgr_MotAgMtgmRate_UlspS_f32         1           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFltThd_Cnt_u08         1           k_LoaMgr_HwTqMtgmRca_UlspS_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFltThd_Cnt_u08         0           k_LoaMgr_IvtrlMtgtnScaZeroEn_Cnt_lge         0           k_LoaMgr_IvtrlMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Tnd_cnt_u08         2           k_LoaMgr_MotAgldptSigFltTnd_cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 </td <td></td>	
k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRete_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0 </td <td></td>	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1RtThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         3           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         2           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0     <	
k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_Ulsp5_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1RiThd_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1RiThd_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1RiThd_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1RiThd_Cnt_u08       0         k_LoaMgr_Mo	
k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Res	
k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnSca_Uls_f32       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_Lo	
k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       3         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgNttgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0   k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0   k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_Rte_Inst_Ap_LoaMgr.Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_LoaMgr.Per1_DiagcStsIvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.Per1_ivtrLoaMtgtnEn_Cnt_igc target_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_igc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	
target_Rte_inst_Ap_LoaMgr.LoaMgr_Peri_MotCurrLoaMigtnEn_Cnt_igc target_LoaMigr_Peri_MotCurrLoaMigtnEn_Cnt_igc target_LoaMigtnEn_Cnt_igc target_LoaMigtnE	
$target\_Rte\_Inst\_Ap\_LoaMgr\_Per1\_MtrPosldptSig\_Cnt\_u08 \\ target\_LoaMgr\_Per1\_MtrPosldptSig\_Cnt\_u08 \\$	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Resu
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Expected Value	Resu
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  1  target_LoaMgr_Per1_TloaDi_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc  Expected Value  1	Resu
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value  Expected Value  LoaMgr_AgSV_Cnt_M_lgc 1 LoaMgr_HwTqldptSigSV_Cnt_M_u08 2  Expected Value 2	Resu
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value  Expected Value  LoaMgr_AgSV_Cnt_M_lgc 1 LoaMgr_HwTqldptSigSV_Cnt_M_u08  Expected Value 2	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value  Expected Value  LoaMgr_AgSV_Cnt_M_lgc 1 LoaMgr_HwTqldptSigSV_Cnt_M_u08 2 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value Expected Value  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 2 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_HwTqRespSV_Cnt_M_u08 2 LoaMgr_IntrldptSigSV_Cnt_M_u08 2 LoaMgr_IntrldptSigSV_Cnt_M_u08 2 LoaMgr_IntrldptSigSV_Cnt_M_u08 2 LoaMgr_IntrldptSigSV_Cnt_M_u08 2 LoaMgr_IntrldptSigSV_Cnt_M_u08 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           target_LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_AgSV_Cnt_M_lgc         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0         0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaAvail_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_AgSV_Cnt_M_lgc         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_AgSV_Cnt_M_lgc         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_LoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_LoaMtgtnEn_Cnt_M_lgc         5         5           LoaMgr_LoaMtgtnEn_Cnt_M_lgc         5         5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_AgSV_Cnt_M_lgc         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_LoASt_State_M_enum         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_LOASt_State_M_enum         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_AgSV_Cnt_M_lgc         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_IwtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_LOASt_State_M_enum         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_HwTqldptSigSV_Cnt_M_u08         2         2           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5           LoaMgr_LOASt_State_M_enum         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1	

2018-05-18, 14:04:23+0530



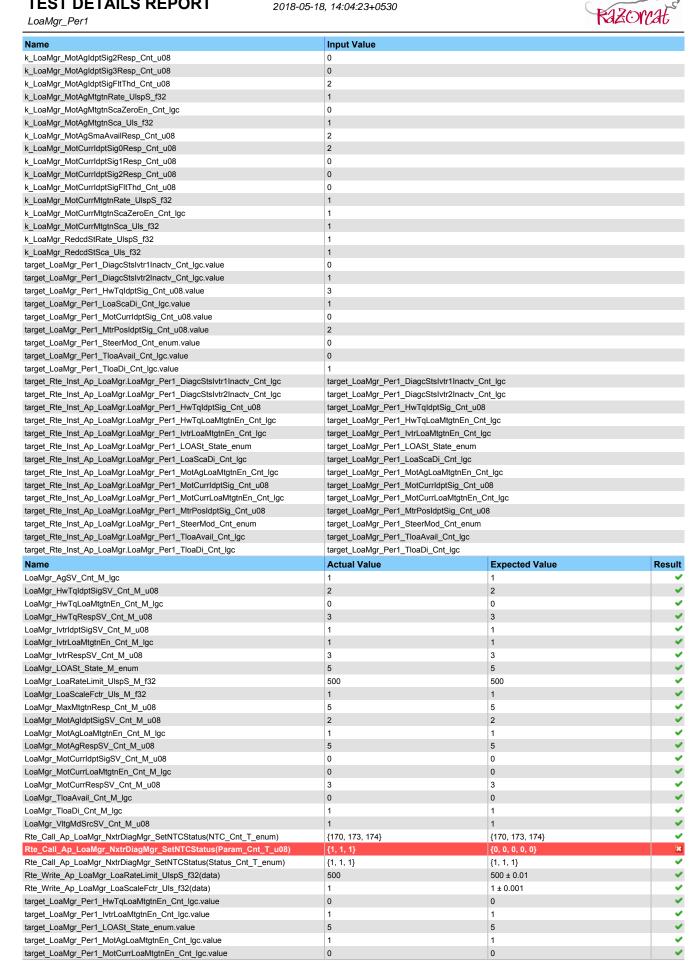
Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalcIvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.48 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

2018-05-18, 14:04:23+0530





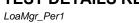
© Report created by TESSY V3.1.12, report template V2.1

91



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.49 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	/ (Count
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	·
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	-
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173, 174}	{170, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	{1, 1, 1} 500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	
· - ·	1	1	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value		1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.50 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2 2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
	2





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.51 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cnt_loc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrIdptSig Cnt u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_MotAgloptSigSV_Cnt_M_u08	1	1	· ·
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	· ·
LoaMgr MotAgRespSV Cnt M u08	2	2	<b>~</b>





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.52 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





	l		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	~
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>*</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	5	5	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaScaleEntr_UlgsS_f32(data)	0	500 ± 0.01	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)		$0 \pm 0.001$	<b>✓</b>
target LeaMar Per1 HwTal caMtathEn Cat Jac value			
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
	1	1	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 5	1 1 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.53 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	3
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	2	2	<b>V</b>
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Section   Sect	Test Step 1.54 (Repeat Count = 1)	
anding Junifronting Street, Control Jupic  anding Junifronting Street, Control Jupic  anding Junifronting Street, Control Jupic  anding Interfaces Street, Child Jupic  and Junifronting Street, Child Jupic  Loady Junifronting Street, Child Jupic  and Junifronting Street, Child Jupic  Loady Junifronting Street, Child Jupic  and Junifronting Street, Child Jupic  Loady Junifronting Street, Child Jupic  and Junifronting Street, Child Jupic  and Junifronting Street, Child Jupic  and Junifronting Street, Child Julies J		
Loading Invitroplation Cost Mgs  analog Service (Cost Mgs)  analog Service		·
andley, Partispessory, CM, Mullis  andley, Partispessory, CM, Mullis  4  andley, Partispessory, CM, Mullis  5  andley, Partispessory, CM, Mullis  6  andley, Partispessory, CM, Mullis  6  andley, Partispessory, CM, Mullis  7  andley, Partispessory, CM, Mullis  8  andley, Partispessory, CM, Mullis  9  andley, Partispessory, CM, Mullis  9  andley, Partispessory, CM, Mullis  10  andley, Partispessory, CM, Mullis  11  andley, Partispessory, CM, Mullis  11  andley, Partispessory, CM, Mullis  12  Lander, Cardinality, CM, Mullis  13  Lander, Partispessory, CM, Mullis  14  Lander, Lander, CM, Mullis  15  Lander, Lander, CM, Mullis  16  Lander, Lander, CM, Mullis  17  Lander, Lander, CM, Mullis  18  Lander, Lander, CM, Mullis  19  Lander, Lander, CM, Mullis  19  Lander, Lander, CM, Mullis  10  Lander, Lander, CM,		
analog   Michaelphory Che M, villa   1   1   1   1   1   1   1   1   1	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
analog Junice Sattegrini, Crit M. Ugs  aboling Manylopidisplay Crit M. Ug8  1  aboling Manylopidisplay Crit M. Ug8  1  aboling Manylopidisplay Crit M. Ug8  3  aboling Manylopidisplay Crit M. Ug9  0  aboling Manylopidisplay Crit M. Ug9  0  aboling Manylopidisplay Crit M. Ug9  0  aboling Manylopidisplay Crit M. Ug9  1  aboling Manylopidisplay Crit M. Ug9  2  aboling Manylopidisplay Crit M. Ug9  3  aboling Manylopidisplay Crit M. Ug9  3  aboling Manylopidisplay Crit M. Ug9  4  aboling Manylopidisplay Crit M. Ug9  5  aboling Manylopidisplay Crit M. Ug9  6  aboling	LoaMgr_HwTqRespSV_Cnt_M_u08	4
analog _	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
2. 2048/J. MANQUASHER C. M. M. DO 2. 2048/J. MANQUASHER C. M. D	LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	
. coadeg. Modeg. coadeg. Co. 19. 1909	LoaMgr_lvtrRespSV_Cnt_M_u08	
audity Justicality (Justicality Cont.) (List) audity Justicality C		
coaling / Michard Angles (Cont. M. Ligo.  2		
Coading - Company Coading - Coadin		·
Coadery Contribution   Coadery Coade		
Loading _ Limited   Limi		
Landarg_Low/MinigesSac_Use_S122	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
CLandary_Interfigence_Comp.Com_Loc   CLandary_Interfigence_Com_Loc   CLandary_Interfigence_C	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
Clandary Febro California Using S. 102	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
Laadig / Hoffspitspit Tolavairillesp. Ont. 198	k_LoaMgr_CurrIvtrMtgtnScaZeroEn_Cnt_lgc	
Leading / Hirofleptisp() Toolanderson Chrt. USB   2	k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	
Loading Interfoliation   Change   Child	k_LoaMgr_FadeOutStRate_UlspS_f32	
Loading   M-17gdgSigIThouReaps Cut UB    -Loading   M-17gdgSigIThouRea		
Loading   Mind policy Siz Triban Resp. Crit. UB8		
Loading: https://dispise/teasp.com/Loading: https		
CLoaking_HwTqlpptSigNtRep_Cnt_UBS		
Loaking   MrVToMignishate   Usips   132		·
Loading: In-Virtightispace; Usp S, 122		
Leading   Intriducing   International   Inte	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Cloaking_NintingSig2Resp_Cnt_u08	k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
Loaking   NytrightSigiFiThd C, CH, USB   0	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
Cloakkgr   WrthkighRate_UlspS_R2	k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
Cloaking	k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
Cloaking	k_LoaMgr_IvtrMtgtnRate_UlspS_f32	
Loaking	k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	
LoaMgr_MotAgloptSig2Resp_Cnt_u08		
LoaMgr_MotAgldptSig3Resp_Cnt_u08		
CLoaMgr_MotAglidpiSigFitThd_Cnt_u08		
LoaMgr_MotAgMtghrRate_UlspS_132		
CLoaMgr_MotAgMiginScaZeroEn_Cnt_lgc		
LoaMgr_MotAgMtgthSca_Uis_132		
LoaMgr_MotCurridptSig0Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_value 0 arget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_value 1 arget_LoaMgr_Per1_DiagcSitslvtr2Inactv_Cnt_lgc_value 2 arget_LoaMgr_Per1_LoaScaD_Cnt_lgc_value 1 arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value 2 arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value 3 arget_LoaMgr_Per1_SteenMod_Cnt_enum_value 2 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 0 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgInEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgInEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgInEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgInEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr	k LoaMgr MotAgMtgtnSca Uls f32	1
C_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         0           C_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           C_LoaMgr_MotCurridptSig2Sig2         1           C_LoaMgr_MotCurridptSig2Car_Uis_32         1           C_LoaMgr_MotCurridptSig2Car_Uis_32         1           C_LoaMgr_RedcdSitRate_Uisp5_fi22         1           C_LoaMgr_RedcdSitRate_Uisp5_fi22         1           C_LoaMgr_Per1_DiagcStsivtr1nactv_Cnt_lgc_value         0           arget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_value         1           arget_LoaMgr_Per1_LoaScaDi_Cnt_log.value         2           arget_LoaMgr_Per1_LoaScaDi_Cnt_log.value         1           arget_LoaMgr_Per1_MirPosidptSig_Cnt_uo8.value         2           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         3           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         1           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2inactv_Cnt_lgc           arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_MidcLoaMignEn_Cnt_lgc	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
LoaMgr_MotCurridptSig2Resp_Cnt_u08 0  LoaMgr_MotCurridptSigFitThd_Cnt_u08 0  LoaMgr_MotCurridptSigFitThd_Cnt_u08 1  LoaMgr_MotCurridptSigFitThd_Cnt_lgc 1  LoaMgr_MotCurridptSigFitThd_Cnt_lgc 1  LoaMgr_MotCurridptSigSaZeroEn_Cnt_lgc 1  LoaMgr_MotCurridptSigSaZeroEn_Cnt_lgc 1  LoaMgr_MotCurridptSigSaZeroEn_Cnt_lgc 1  LoaMgr_RedcdSiRate_UlspS_f32 1  LoaMgr_RedcdSiRate_UlspS_f32 1  arget_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc value 0  arget_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc value 1  arget_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc value 1  arget_LoaMgr_Perd_MotCurridptSig_Cnt_u08_value 2  arget_LoaMgr_Perd_MotCurridptSig_Cnt_u08_value 2  arget_LoaMgr_Perd_MotCurridptSig_Cnt_u08_value 3  arget_LoaMgr_Perd_MotCurridptSig_Cnt_u08_value 2  arget_LoaMgr_Perd_TinoaAvail_Cnt_lgc_value 1  arget_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc_value 1  arget_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc_value_Va	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
\LoaMgr_MotCurrIdptSigFitThd_Cnl_u08	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
\LoaMgr_MotCurrMtgthRate_UlspS_f32 \ \LoaMgr_MotCurrMtgthSca_Uls_f32 \ \LoaMgr_MotCurrMtgthSca_Uls_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStSca_Uls_f32 \ \LoaMgr_RedcdStSca_Uls_f32 \ \langet_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value \ \langet_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value \ \langet_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value \ \langet_LoaMgr_Per1_LoaScaD_Cnt_lgc.value \ \langet_LoaMgr_Per1_LoaScaD_Cnt_lgc.value \ \langet_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value \ \langet_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value \ \langet_loaMgr_Per1_MtrPosldptSig_Cnt_u08.value \ \langet_loaMgr_Per1_TloaDidptSig_Cnt_u08.value \ \langet_loaMgr_Per1_TloaAvail_Cnt_lgc.value \ \langet_loaMgr_Per1_TloaAvail_Cnt_lgc.value \\ \langet_loaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc \\ \langet_loaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc \\ \langet_loaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc \\ \langet_loaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc \\ \langet_loaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc \\ \langet_loaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc \\ \langet_loaMgr_Per1_HwTqldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_HwTqldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_HwTqldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_HwTqldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_LoaMgr_LoaMgr_Per1_VltLoaMtgtnEn_Cnt_lgc \\ \langet_loaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaScaD_LoaMgr_Per1_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_MotCurrldptSig_Cnt_u08 \\ \langet_loaMgr_Per1_MotCurrldptSig_Cnt_u08 \\ \lange	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	
\LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc \\ \LoaMgr_MotCurrMtgtnSca_Uls_f32 \\ \LoaMgr_RedcdStRate_UlspS_f32 \\ \LoaMgr_RedcdStRate_UlspS_f32 \\ \LoaMgr_Perd_DiagcStstvtr1Inactv_Cnt_lgc.value \\ \text{arget_LoaMgr_Perf_DiagcStstvtr2Inactv_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_DiagcStstvtr2Inactv_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_DiagcStstvtr2Inactv_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_LoaScaDl_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_MotCurrldptSig_Cnt_uo8.value} \\ \text{arget_LoaMgr_Perf_SteerMod_Cnt_enum.value} \\ \text{arget_LoaMgr_Perf_TioaDi_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_TioaDi_Cnt_lgc.value} \\ \text{arget_LoaMgr_Perf_TioaMgr_LoaMgr_Perf_DiagcStstvtr1Inactv_Cnt_lgc} \\ \text{arget_LoaMgr_Perf_DiagcStstvtr2Inactv_Cnt_lgc} \\ \text{arget_LoaMgr_Perf_DiagcMgr_DiagcStstvtr2Inactv_Cnt_lgc} \\ \text{arget_LoaMgr_Perf_NotAgLoaMgr_DiagcStstvtr2Inactv_Cnt_lgc} \\ arget_LoaMgr_P		
\LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStState_UlspS_f32 \ \LoaMgr_RedcdStState_UlspS_f32 \ \LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value \ \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value}} \ \text{arget_LoaMgr_Per1_IoaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_IoaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_IoaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_IoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc} \ \text{arget_LoaMgr_Per1_DiagcMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \ \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \text{arget_LoaMgr_Per1_LOASLState_enum} \text{arget_LoaMgr_Per1_LOASLState_enum} arget_LoaMgr_Per1_MotQL		
\LoaMgr_RedcdStRate_UispS_f32		
LoaMgr_RedcdStSca_Uls_f32  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  1  arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  2  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  2  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  3  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  3  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  2  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	_ 0_ 0	
arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1 arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1 arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 3 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Neamgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Neamgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8  target_LoaMgr_Per1_NotCurrldptSig_Cnt_	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_NoaMgr_Per1_LoaMgr_Per1_MotQLoaMgr_Per1_MotQLoaMgr_Per1_MotQLoaMgr_Per1_MotQLoaMgr_Per1_MotQLoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_loaMgr_Per1_LoaScaDi_Cnt_lgc  target_loaMgr_Per1_LoaScaDi_Cnt_lgc  target_loaMgr_Per1_LoaScaDi_Cnt_lgc  target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_loaMgr_Per1_LoaScaDi_Cnt_lgc  target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_loaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1}</b>	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Evit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	<b>V</b>

Test Step 1.55 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cnt_lge		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt. lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAglopMattEp_Cot_M_u08	2	2	<b>*</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
CHANGE MICHAURESDAY CHEN IVEHIO			V

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalcIvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.56 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	ent lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		From a start Walter	D14
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	~
LoaMgr MaxMtgtnResp Cnt M u08	0	0	~
Loawigi_waxivitgtirkesp_Crit_ivi_uoo			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	_
	0 5	0 5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 5 2	0 5 2	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08	0 5 2 0	0 5 2 0	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 5 2 0 1	0 5 2 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc	0 5 2 0 1 0	0 5 2 0 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 5 2 0 1 0 1 3	0 5 2 0 1 0 1 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc	0 5 2 0 1 0 1 3	0 5 2 0 1 0 1 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc	0 5 2 0 1 0 1 3 1	0 5 2 0 1 0 1 3 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 5 2 0 1 0 1 3 1 1	0 5 2 0 1 0 1 3 1 1	***************************************
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 5 2 0 1 0 1 3 1 1 1 1 1 1(170, 173, 175)	0 5 2 0 1 0 1 3 1 1 1 1 1 1(70, 173, 175)	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 5 2 0 1 0 1 3 1 1 1 1 1(170, 173, 175) {1, 1, 1}	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0}	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 5 2 0 1 0 1 3 1 1 1 1 (170, 173, 175) {1, 1, 1}	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0	0 5 2 0 1 0 1 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1	0 5 2 0 1 0 1 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1 1 5	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 1 5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.57 (Repeat Count = 1)	C.
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.58 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1 5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0 1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtqtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.59 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target Rie Inst An LoaMgr LoaMgr Peri DiagoStsivtrilnacty Chi Igo	target LoaMgr Per1 DiagcStslytr1Inacty Cn	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cn target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsivtr1Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsivtr2Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cn target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_loaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_loaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_P	nt_lgc c t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0	nt_lgc c t_lgc 8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc c t_lgc b8 nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0	nt_lgc c t_lgc b8 nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldp	nt_lgc c t_lgc b8 nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc c t_lgc b8 nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc c t_lgc b8 nt_lgc	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	nt_lgc c t_lgc 08 nt_lgc 3	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	tt_lgc cc tt_lgc 08 nt_lgc 3  Expected Value 1	<b>V</b>
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	t_lgc c t_lgc 08 nt_lgc 3  Expected Value 1 0 1	<b>*</b>
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	t_lgc c t_lgc b8 nt_lgc 3  Expected Value 1 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_StreerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igctarget_LoaMgr_Per1_LoaScaDi_Cnt_Igctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08_target_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igctarget_LoaMgr_Per1_TloADi_Cnt_Igctarget_LoaMgr_Per1_TloADi	t_lgc c t_lgc c t_lgc 88 nt_lgc 3  Expected Value 1 0 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	t_lgc c t_lgc b8 nt_lgc 3  Expected Value 1 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrrlqtpSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	et_lgc c t_lgc c t_lgc 88 nt_lgc 3  Expected Value 1 0 1 0 0 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u02 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u02 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u02 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u02 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 0 0 1	t_lgc c t_lgc s t_lgc	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ItvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_ItvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 0 1 5 5	t_lgc c t_lgc s8 nt_lgc 8  t_lgc 8  t_lgc 1 0 1 0 0 1 5 5	*****
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlAcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotQurrldptSig_Cnt_u0ttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  **Actual Value**  1	L   Igc	***************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotQurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igctarget_LoaMgr_Per1_LoaScaDi_Cnt_Igctarget_LoaMgr_Per1_MotQurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value 1 0 1 5 5 5 500 0	L   Igc   C   C   C   C   C   C   C   C   C	*****

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.60 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca UIs f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		)-4 l	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr LOASt State M enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5		-
LoaMgr_MotAgldptSigSV_Cnt_M_u08			
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	2	2	~
7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1 2	1 2	· ·
7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	1	1	* * * * * * * * * * * * * * * * * * *
LoaMgr_MotAgRespSV_Cnt_M_u08	1 2	1 2	~
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 2 0	1 2 0	~
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 2 0 1	1 2 0 1	~
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 2 0 1	1 2 0 1	~
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 2 0 1 1 1 0	1 2 0 1 1 1 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 2 0 1 1 1 0	1 2 0 1 1 1 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 2 0 1 1 1 0 0 0 {170, 173, 174}	1 2 0 1 1 1 0 0 0 {170, 173, 174}	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 2 0 1 1 1 1 0 0 0 {170, 173, 174}	1 2 0 1 1 1 1 0 0 0 {170, 173, 174} {0, 0, 0, 0, 0}	
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 2 0 1 1 1 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	1 2 0 1 1 1 1 0 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1}	**************************************
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 2 0 1 1 1 1 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 2 0 0 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	**************************************
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 0 1 1 1 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0 1 1	1 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	**************************************
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 2 0 0 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	**************************************
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 0 1 1 1 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0 1 1	1 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	**************************************
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 2 0 0 1 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0 1 1 1 1	1 2 0 1 1 1 1 1 1 1 0 0 0 {\{170, 173, 174\}} {\{0, 0, 0, 0, 0, 0\}} {\{1, 1, 1\}} 500 \(\pmu 0.001\) 1 1 1	**************************************



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.61 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k LoaMgr MotCurrldptSig0Resp Cnt u08	5
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
<u> </u>	

2018-05-18, 14:04:23+0530



Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_LoaScabi_Crit_igc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		Almosti, Ont Iso	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_Stat		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdpt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptS		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_		1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqIdptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	✓
	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	000		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
		1 ± 0.001	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0	0	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0	0	· · · · · · · · · · · · · · · · · · ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 1.62 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaD1_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cht_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1 2 2 2 2 3 32 2 34 34 24 28	, <u> </u>





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.63 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





<u> </u>			
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(	Ont Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Perl SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
	Actual Value	Expected Value	Popult
Name		Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08			
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	· · · · · ·
LoaMgr_LoaNst_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr MotAgRespSV Cnt M u08	4	4	<b>✓</b>

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.64 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k LoaMgr MotAgMtgtnSca Uls f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08				
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value				
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1		
LoaMgr HwTqRespSV Cnt M u08	4	4	<b>~</b>	
LoaMgr lvtrldptSigSV Cnt M u08	0	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1		
1 - 1 - 1				
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5		
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-	
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>	
LoaMgr MotCurrldptSigSV Cnt M u08	0	0		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2		
	1	1		
LoaMgr_TloaAvail_Cnt_M_lgc				
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	-	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5		
target LoaMar Peri MotAal caMtathEn Cot lacyalus	0			
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.65 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	3
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr1l	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_	Cnt u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgt	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_IvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cni		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	Ž
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>•</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	· ·
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>•</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<u> </u>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 1.66 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTgldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LoaScaDi_Cnt_Igc	target_LoaMgr_Perl_LOASt_State_enum target_LoaMgr_Perl_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDI_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1	, 5 = 5 = 5 = 5 = 5 = 5





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.67 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





	k_LoaMgr_CurrivtrMgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaLort_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 1 2 4 2 1 0 0 0 1 1 1 1 0 5 0 1 1 0 2 1 1 0 1 2 2 1 0 0 1		
	k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1FtThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 2 4 4 2 1 1 0 0 0 0 1 1 1 1 0 0 1 1 2 2 2 0 0 0 0		
	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1RThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 4 2 1 0 0 0 1 1 1 1 0 5 0 1 0 1 1 0 1 2 2 1 0 0 0		
Loading - Hiroflockisk   Thorward   Loading - Loading - Loading - Hiroflockisk   Thorward   Loading - Hiroflockisk   Thorward   Loading - Hiroflockisk   Thorward   Loading - Hiroflockisk   Loadi	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	4 2 1 0 0 0 1 1 1 1 0 5 0 1 1 0 1 5 1 0 2 1 1 0 1 2 2 1		
K_LOAMGY - HVTGLPSIGN TDAAVAIRRED, CML U08         2           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         1           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         1           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         5           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         0           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         1           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         5           K_LOAMGY - HVTGLPSIGN ZTROBERS CML U08         5           K_LOAMGY - MVTGLPSIGN ZTROBERS CML U08         1           K_LOAMGY - MVTGLPSIGN ZTROBERS CML U08         2           K_LOAMGY - MVTGLPSIGN ZTROBERS CML U08	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 1 0 0 0 1 1 1 1 0 5 0 1 0 1 0 1 1 0 1 2 2 1 0 0 0		
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1RtThd_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 0 0 0 1 1 1 0 0 1 1 2 2 2 0 0 0 0		
K_Loakig_ HVTqlpdSsgPftPnd_Cnt_U08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1RtThd_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MottAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MottAgldptSig2Resp_Cnt_u08 k_LoaMgr_MottAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0 0 1 1 1 1 0 5 0 1 0 1 5 1 0 2 1 1 0 1 2 2 1		
	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 1 1 0 5 0 1 0 1 5 1 0 2 1 1 0 1 2 2 2 0		
Loakig_HvToMigniRea_UispS_132	k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltTnd_Cnt_u08 k_LoaMgr_IvtrldptSigFltTnd_Cnt_u08 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 1 0 5 0 1 0 1 5 1 0 2 1 1 0 1 2 2 2 0 0		
Loakig_NrtrightSig1Resp_Crt_U08	k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnSca_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 0 5 0 1 1 0 1 1 2 2 2 0 0 0		
	k_LoaMgr_vtrldptSig1Resp_Cnt_u08 k_LoaMgr_vtrldptSig2Resp_Cnt_u08 k_LoaMgr_vtrldptSig2Resp_Cnt_u08 k_LoaMgr_vtrldptSigFltThd_Cnt_u08 k_LoaMgr_vtrMtgtnRate_UlspS_f32 k_LoaMgr_vtrMtgtnSca_Uls_f32 k_LoaMgr_vtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig5ltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0 5 0 1 0 1 5 1 0 2 1 0 1 2 2 1 0 1 2 2		
K_Loaklgr_IvridptSigParsp_Cnt_U08	k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	5 0 1 0 1 5 1 0 2 1 0 1 2 2 1 0 0		
	k_LoaMgr_IvtrIdptSigFItThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFItThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0 1 0 1 5 1 0 2 1 0 1 2 2 1 0 0		
k_Loak/gr_IvriMtginRate_UlspS_132         1           k_Loak/gr_IvriMtginSca_Uls 1732         1           k_Loak/gr_MtkinSca_Uls 1732         1           k_Loak/gr_MokajdiptSig17koSmaResp_Cnt_u08         5           k_Loak/gr_MokajdiptSig17ksp_Cnt_u08         0           k_Loak/gr_MokajdiptSig17ksp_Cnt_u08         0           k_Loak/gr_MokajdiptSig17ksp_Cnt_u08         2           k_Loak/gr_MokajdiptSig18ksp_Cnt_u08         2           k_Loak/gr_MokajdiptSig28msp_Cnt_u08         1           k_Loak/gr_MokajdiptSig28msp_Cnt_u08         2           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         2           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         2           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         0           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         0           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         0           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         0           k_Loak/gr_MotCurridptSig18ksp_Cnt_u08         0           k_Loak/gr_MotCurridptSig28cy_Cnt_u08         1           k_Loak/gr_MotCurridptSig28cy_Cnt_u08         0           k_Loak/gr_MotCurridptSig20cy_Us_Sig2         1           k_Loak/gr_MotCurridptSig20cy_Us_Sig2         1           k_Loak/gr_MotCurridptSig20cy_Us_Sig2         1           k_Loak/gr_	k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 0 1 5 1 0 2 1 0 1 2 2 2 0 0		
	k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 5 1 0 2 2 1 0 0 0 0 0		
k_Loaklgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3FitTnd_Cnt_u08         0           k_Loaklgr_MotAgldptSig3FitTnd_Cnt_u08         2           k_Loaklgr_MotAgldptSig3FitTnd_Cnt_u08         2           k_Loaklgr_MotAgldptSig3FitTnd_Cnt_u08         0           k_Loaklgr_MotAgldptSigaCesp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	5 1 0 2 1 0 1 2 2 2 0 0		
k_Loaklgr_MotAgloptSig2Resp_Cnt_u08         1           k_Loaklgr_MotAgloptSig1Resp_Cnt_u08         0           k_Loaklgr_MotAgloptSig1Resp_Cnt_u08         2           k_Loaklgr_MotAgMtgmRate_UispS_r32         1           k_Loaklgr_MotAgMtgmSac_UispS_r32         1           k_Loaklgr_MotAgMtgmSac_Uis_r32         1           k_Loaklgr_MotAgMtgmSac_Uis_r32         1           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Sca_Uisp_S12         1           k_Loaklgr_ReddSiRse_UispS_f32         1           k_Loaklgr_ReddSiRse_UispS_f32         1           k_Loaklgr_ReddSiRse_UispS_f32         1           k_Loaklgr_Pert_DetaigScaltivitinact_Cnt_lgc value         1           target_Loaklgr_Pert_DetaigScaltivitinact_Cnt_lgc value         1           t	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 0 2 1 0 1 2 2 0 0		
k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSigiFilThd_Cnt_u08         2           k_LoaMgr_MotAgMightRac UsbS_132         1           k_LoaMgr_MotAgMightRac UsbS_132         1           k_LoaMgr_MotAgMightRac UsbS_132         1           k_LoaMgr_MotAgMightRac UsbS_132         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigSeroen_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSeroen_Cnt_lgc         1           k_LoaMgr_RedcdSitRet_Ulsp5_72         1           k_LoaMgr_RedcdSitRet_Ulsp5_72         1           k_LoaMgr_Pert_DiagCstsivtrInactv_Cnt_lgc_value         1           target_LoaMgr_Pert_DiagCstsivtrInactv_Cnt_lgc_value         1           target_LoaMgr_Pert_LoaScaD_Cnt_lgc_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1 <td>k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08</td> <td>0 2 1 0 1 2 2 2 0</td> <td></td> <td></td>	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0 2 1 0 1 2 2 2 0		
k_LoaMgr_MotAgidptSigFiltThd_Cnt_u08         2           k_LoaMgr_MotAgMitghRate_UispS_132         1           k_LoaMgr_MotAgMitghRate_UispS_132         1           k_LoaMgr_MotAgMitghRate_UispS_132         1           k_LoaMgr_MotAgMitghRate_UispS_132         1           k_LoaMgr_MotAgMitghRate_UispS_132         1           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_RedodSiRate_UispS_132         1           k_LoaMgr_RedodSiRate_UispS_132         1           k_LoaMgr_RedodSiRate_UispS_132         1           k_LoaMgr_Rent_DiagcSistvirtInactv_Cnt_lgc_value         0           target_LoaMgr_Pent_DiagcSistvirtInactv_Cnt_lgc_value         1           target_LoaMgr_Pent_MircpSig_Cnt_u08_value         1           target_LoaMgr_Pent_MircpSigD_Cnt_u08_value         1           target_LoaMgr_Pent_MircpSigD_Cnt_u08_value         1 <tr< td=""><td>k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08</td><td>2 1 0 1 2 2 0 0</td><td></td><td></td></tr<>	k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 1 0 1 2 2 0 0		
k_LoaMgr_MotAgMitginRate_UispS_f32         1           k_LoaMgr_MotAgMitginScazeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMitginSca_Uis_f32         1           k_LoaMgr_MotAgMitginSca_Uis_f32         1           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigTResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigScate_Uisp_f32         1           k_LoaMgr_RedcdSitSca_Uis_f32         1           k_LoaMgr_RedcdSitSca_Uis_f32         1           target_LoaMgr_Per1_DiagcStsivtTinactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtZinactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MitPostdptSig_Cnt_u08 value         1           target_LoaMgr_Per1_TioaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDi_Cnt_lgc.value	k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 0 1 2 2 0 0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmat/Misspa_Cnt_u08         2           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigIResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigIResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigIResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigIResp_Sf32         1           k_LoaMgr_MotCurridptSigIResp_Sf32         1           k_LoaMgr_MotCurridptSig_Cnt_u5_f32         1           k_LoaMgr_MotCurridptSig_Uls_f32         1           k_LoaMgr_MotCurridptSig_Cnt_u5_f32         1           k_LoaMgr_MotCurridptSig_Uls_f32         1           k_LoaMgr_ReddSiRate_UlspS_f32         1           k_LoaMgr_ReddSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagCststvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagCststvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagCststvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_Thr_ThoaVvail_Cnt_lgc_value         1           target_LoaMgr_Per1_Thr_ToaAvvail_Cnt_lgc_value	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0 1 2 2 0 0		
k_LoaMgr_MotAgNtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u1gc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u1gc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u1gc         1           k_LoaMgr_RedcdSitSca_U1s_f32         1           k_LoaMgr_RedcdSitSca_U1s_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStsIvtr1nactv_Cnt_lgc.value         1           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MitPositghSig_Cnt_u08 value         1           target_LoaMgr_Perl_MitPositghSig_Cnt_u08 value         1           target_LoaMgr_Perl_TioaDc_nt_lgc.value         1           target_LoaMgr_Perl_TioaDc_nt_lgc.value         1           target_LoaMgr_Perl_TioaDc_nt_lgc.value         1           target_LoaMgr_Perl_TioaDc_nt_lgc.value	k_LoaMgr_MotAgMtgtnSca_UIs_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	1 2 2 0 0		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig0Lesp_Gnt_u08         1           k_LoaMgr_MotCurridptSig0Lesp_Gnt_u08         1           k_LoaMgr_MotCurridptSig0Lesp_Gnt_u08         1           k_LoaMgr_RedcdSiSca_uls_182         1           k_LoaMgr_RedcdSiSca_uls_182         1           k_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TloaAvall_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaAvall_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 2 0 0		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1rtThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc_value         0           target_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc_value         1           target_LoaMgr_Perl_Unactv_Cnt_lgc_value         1           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MrDotSig_Cnt_u08 value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_TloaDc_Cnt_lgc_value         1           target_LoaMgr_Perl_DaigcSts	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0 0		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 k_LoaMgr_MotCurridptSigCa_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 lt_LoaMgr_RedcdStRate_UlspS_f32 lt_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value ltarget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value ltarget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value ltarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value ltarget_LoaMgr_Per1_SteerMod_Cnt_lgnum.value ltarget_LoaMgr_Per1_SteerMod_Cnt_lgnum.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08 ltarget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08 ltarget_LoaMgr_Per1_HvTqldptSig_Cnt_u08 ltarget_LoaMgr_Per1_HvTqloaMtgr_Per1_HvTqloaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_UrtLoaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_LvTLoaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_LvTLoaMtgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_LvTLoaMgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_LoaMgr_LoaMgr_Per1_LvTLoaMgrnEn_Cnt_lgc ltarget_Rle_Inst_Ap_L	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrMgtnStagFitTnd_Cnt_u08 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 larget_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStstvtr2lnactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStstvtr2lnactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStstvtr2lnactv_Cnt_lgc.value larget_LoaMgr_Per1_LoaScaDi_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_SteerMod_Cnt_enum.value larget_LoaMgr_Per1_SteerMod_Cnt_enum.value larget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value larget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value larget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2lnactv_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2lnactv_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UispS_132 k_LoaMgr_MotCurrMtgtnSca_Uis_132 k_LoaMgr_RedcdSiRate_UispS_132 k_LoaMgr_RedcdSiRate_UispS_132 k_LoaMgr_RedcdSiRate_UispS_132 k_LoaMgr_RedcdSiSca_Uis_132 target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc		0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08			
k_LoaMgr_RedcdStRate_UlspS_f32				
k_LoaMgr_RedcdStRate_UlspS_132 1  k_LoaMgr_RedcdStSca_Uls_132 1  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_McTorldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_IvtrLoaMtgtn				
k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       1         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08       target_LoaMgr_Per1_HwTqldptSig_Cnt_u08         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc				
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum				
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc				
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum  target_LoaMgr_Per1_LoASt_State_enum		1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc		1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_Indextor_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_Indextor_Cnt_lgc  target_Lo	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc				
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_Intr_LoaMtgtnEn_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_				
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum			at lac	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum				
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum				
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum				
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	c	
1				
	target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Mitrosidpi3ig_Crit_uoo target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum			<u></u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc target_LoaMgr.Per1_TloaAvail_Cnt_Igc				
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	· · · · · ·	_ , _ ,		
			Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc         1         1	LoaMgr_AgSV_Cnt_M_lgc	1	1	~
02 144.00 20 20 2 200	LoaMgr_HwTqldptSigSV_Cnt_M_u08		0	~
zoungm.qzoung.nzn_on_m_go				•
				~
3 3 4 4 4 5 4 5 5 5 6	LoaMgr_HwTqRespSV_Cnt_M_u08			<b>✓</b>
32 4 44 54 24 2 254	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08			~
<u> </u>	LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc			~
1	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08			
	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum			~
	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32			~
2.200.00.00.00.00.00.00.00.00.00.00.00.0	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	0		~
	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5 0		1
LoaMgr_MotAgRespSV_Cnt_M_u08 5	LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 5 0 0		<b>✓</b>

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.68 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	2
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	1		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnl	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtn	•	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	3	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1.	!	
	0	0	
	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 4	1 4	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 4 1	1 4 1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 4 1 0	1 4 1 0	, , , , , , , , , , , , , , , , , , ,
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 4 1 0 4	1 4 1 0 4	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 4 1 0 4 5	1 4 1 0 4 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 4 1 0 4 5 5 500	1 4 1 0 4 5 500	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 4 1 0 4 5 5 500 0	1 4 1 0 4 5 500	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 4 1 0 4 5 5 500 0 5 5	1 4 1 0 4 5 500 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 4 1 0 4 5 5 500 0 5 0 0	1 4 1 0 4 5 5 500 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	1 4 1 0 4 5 5 500 0 5 5 0 0 0	1 4 1 0 4 5 5 500 0 5 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 4 1 0 4 1 0 4 4 5 5 500 0 5 5 0 0 5 5	1 4 1 0 4 5 5 500 0 5 0 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 4 1 0 4 1 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0	1 4 1 0 4 5 5 500 0 5 0 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0 1 1 3	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0 1 3	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0 1 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0 1 3 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	1 4 1 0 4 5 5 500 0 0 5 0 0 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 4 1 0 4 5 500 0 5 0 0 5 0 1 3 1 1 1 {170, 172, 173}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1 11 170, 172, 173}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 4 1 0 4 5 5 500 0 5 0 0 1 3 1 1 1 1 {170, 172, 173} {1, 1, 1}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOARst_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 3 1 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170, 172, 173} {1, 1, 1} 500	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyD_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} 500 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} 500 0 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} {1,1,1} 500 0 1 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlotSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 0 5	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} {1,1,1} 500 0 1 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1 0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.69 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtqtaSco_Ulo_632	0
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
· ·	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	-
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	,
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	-
Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value			

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 1.70 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1 5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_NotCurrintgthSca_UIs_t32 k_LoaMgr_RedcdStRate_UIspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LeaMar Perf DiagoStelytr/Inacty Cnt Inc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
· · · · · · · · · · · · · · · ·	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.71 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0.589399993		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSigSResp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08			
k_LoaMgr_MotAgMtotoSoaZoroEn_Cot_loa	1 0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1 2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	3		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 lvtrLoaMtgtnEn Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEi	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	,	
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MaxwigtnResp_Cnt_M_u08	2	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.72 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_oispo_ioz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1 2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	tv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrloaMgr_D	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	· ·
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 4	4	
LoaMgr LOASt State M enum	4	4	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
		{171, 172, 173}	
Rie Call Ab Loawig Nxirdiagwig Seint Cstatus(NTC Chi i enum)	{171, 172, 173}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{171, 172, 173} {1, 1, 1}	{0, 0, 0, 0, 0}	
			×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1} {1, 1, 1} 500	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1, 1, 1) {1, 1, 1} 500 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	(1, 1, 1) {1, 1, 1} 500 0 0 1	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Imput Value		
Londing	Test Step 1.73 (Repeat Count = 1)	
Londing   Invitation   Londing	Name	· ·
Londing   Invitor   Londing   Lond		
Londing   Interfige Servy   Cort   M.   Londing   Lond		
Londings   Introduction   Londings   Londi		
Loadlegr_Invitable(prific_Ort_M_lge   O	LoaMgr_HwTqRespSV_Cnt_M_u08	
Londing_ Invitriges Sty. Crit. M_ L08	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
LoaMagr   ModAquipothSigsV   Cmt   M   LoaMagr   ModCurrRepsisV   Cmt   M   LoaMagr   LoaMag	LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
Loakingr MokAgit RespSV Cnr M, U88  Loakingr Spsishkohajkvali Cnr M, Uge  Loakingr MokAgit RespSV Cnr M, U88  Loakingr Spsishkohajkvali Cnr M, Uge  Loakingr Loakingr Resp Cnr M, U88  Rite Inst Ap Loakingr  k, Loakingr Landridgrificate, Ulips J, 122  k, Loakingr Landridgrificate, Ulips J, 122  k, Loakingr Landridgrificate, Ulips J, 123  k, Loakingr Landridgrificate, Ulips J, 124  k, Loakingr Landridgrificate, Ulips J, 125  k, Loakingr Landridgrificate, Ulips J, 124  k, Loakingr Landridgrificate, Ulips J, 125  k, Loakingr Landridgrificate, Ulips J, 125  k, Loakingr Landridgrificate, Ulips J, 125  k, Loakingr Mokagiterisgrificate, Cnt, U08  k, Loakingr Mokagiterisgrificate	LoaMgr_IvtrRespSV_Cnt_M_u08	4
Londing MortCarrisonNignis Sey Vor IM, Lu08 Londing Sey MortCarrisonNignis Sey Vor IM, Lu08 Londing CarrisonNignis Sey Vor IM, Lu08 Londing Landing Lu08 Londing Landing Sey Vor IM, Lu08 Londi	LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MoCurricaptSigSPC_Crit_M_U08	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MoCurrLoaMtghrEn_Cnt_M_ge	LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_NorteRepSV_Cnt_M_U08	LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr SharisMoNagAvair Cnt_M Ligs	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_VitgMdSrcSV_Cnt_M_u08	LoaMgr_MotCurrRespSV_Cnt_M_u08	3
Ric_Inst_Ap_LoaMgr	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. LosMgr_CurrivrMtglnScaZeroEn_Cnt_lgc	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
L. LosMign CurrivrMitghtScaZeroEn. Cnt.lgc	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_Currivtn/tglnScazeroEn_Cnt_lgc	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
LoaMgr_CurrivtnMgtnSca_Uls_f32	k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_FadeOutSiRate_UlspS_132         1           k_LoaMgr_HwTqlqbSig0TiOsAvalResp_Cnt_u08         2           k_LoaMgr_HwTqlqbSig1TiOsAvalResp_Cnt_u08         4           k_LoaMgr_HwTqlqbSig1TiOsAvalResp_Cnt_u08         2           k_LoaMgr_HwTqlqbSig2TiosEnaResp_Cnt_u08         1           k_LoaMgr_HwTqlqbSig2TiosEnaResp_Cnt_u08         0           k_LoaMgr_HwTqlqbSig4TResp_Cnt_u08         0           k_LoaMgr_HwTqlqbSig4TResp_Cnt_u08         1           k_LoaMgr_HwTqldsigFiResp_Cnt_u08         1           k_LoaMgr_HwTqldsigFiResp_Cnt_u08         0           k_LoaMgr_HwTqldsigFiResp_Cnt_u08         0           k_LoaMgr_IwtrldpSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldpSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldpSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldpSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldpiSig2Resp_Cnt_u08         1           k_LoaMgr_IwtrldpiSig2Resp_Cnt_u08         1           k_LoaMgr_IwtrldpiSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_Motqlqsisig1NoSmaResp_Cnt_u08         1           k_LoaMgr_Motqlqsisig3Resp_Cnt_u08         2           k_LoaMgr_Motqlqsisig3Resp_Cnt_u08         2           k_LoaMgr_Motqlqsisig3Resp_Cnt_u08         2           k_LoaMgr_Motqlqsig1Sig2Resp_Cn		1
LoaMgr_HWTqldptSig0TloaAvailResp_Cnt_u08		1
k_LoaMgr_HwTqldplSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldplSig1TroaAvaiResp_Cnt_u08         2           k_LoaMgr_HwTqldplSig2TroaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldplSig2TroaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldplSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldplSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         0           k_LoaMgr_IvridplSig2Resp_Cnt_u08         0           k_LoaMgr_IvridplSig2Resp_Cnt_u08         0           k_LoaMgr_IvridplSig4Rest_UlspS_132         1           k_LoaMgr_IvridplSig4Resp_Cnt_u08         0           k_LoaMgr_IvridplSig2Resp_Cnt_u08         0           k_LoaMgr_IvridplSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldplSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldpSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldpSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldpSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldpSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldpSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridpsig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridpsig1Resp_Cnt_u08 <td></td> <td></td>		
k_LoaMgr_HwTqldptSig1TioaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtghtRate_UlspS_132         500           k_LoaMgr_HwTqMtghtSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Rithd_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Rithd_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Rithd_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Rithd_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Rithd_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotC		
LoaMgr_HvTqlqtplsig2TloaDiResp_Cnt_u08		
LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08		
LoaMgr_HwTqldptSig4Resp_Cnt_u08		
LoaMgr_HwTqldptSigFitThd_Cnt_u08		
K_LoaMgr_HwrqMtgnRate_UlspS_f32         500           K_LoaMgr_HwrqMtgnRsca_Uls_f32         1           K_LoaMgr_HwrlptSig1Resp_Cnt_u08         0           K_LoaMgr_IwrldptSig2Resp_Cnt_u08         0           K_LoaMgr_IwrldptSigFitThd_Cnt_u08         0           K_LoaMgr_IwrlMtgnRate_UlspS_f32         1           K_LoaMgr_IwrlMtgnRsca_UlspS_f32         1           K_LoaMgr_IwrlMtgnSca_Uls_f32         1           K_LoaMgr_IwrlMgnSca_Uls_f32         1           K_LoaMgr_IwrlMglorSca_Uls_f32         1           K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           K_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           K_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           K_LoaMgr_MotAgMtgnRate_UlspS_f32         1           K_LoaMgr_MotAgMtgmRate_UlspS_cnt_u08         2           K_LoaMgr_MotAgMtgmRate_UlspS_f32         1           K_LoaMgr_MotCurrldptSigResp_Cnt_u08         2           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0		
k_LoaMgr_HwTqMtghrSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2FltTnd_Cnt_u08         0           k_LoaMgr_IvtrMtghrRate_UlspS_f32         1           k_LoaMgr_IvtrMtghrSca_Uls_f32         1           k_LoaMgr_IvtrMtghrSca_Uls_f32         1           k_LoaMgr_IvtrMtghrSca_Uls_f32         1           k_LoaMgr_IvtrMtghrSca_Uls_f32         1           k_LoaMgr_IvtrMtghrSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgMtghrSca_Uls_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0 <t< td=""><td></td><td></td></t<>		
k_LoaMgr_IvtridptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtriMgtnRate_UlspS_f32         1           k_LoaMgr_IvtriMgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_IvtriMgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08		
k_LoaMgr_lvtridptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtridptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_lvtrMtgtnSca_Uis_532       1         k_LoaMgr_lvtrMtgtnSca_Uis_532       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotAgldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotAgldptSig1Rete_UispS_132       1         k_LoaMgr_MotAgMtgtnSca_Uis_732       1         k_LoaMgr_MotAgMtgtnSca_Uis_732       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Uisp_5132       1         k_LoaMgr_MotCurrMtgtnSca_Uis_182       1         k_LoaMgr_RedcdSiRate_UispS_132       1         k_LoaMgr_RedcdSiSca_Uis_132       1		
k_LoaMgr_lvtrldptSigFitTnd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_lvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgldtptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       2         k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1Resp_LuspS_f32       0         k_LoaMgr_MotCurrIdptSig1Resp_LuspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdSiRate_UlspS_f32       1         k_LoaMgr_RedcdSiRate_UlspS_f32       1         k_LoaMgr_RedcdSiSca_Uls_f32       1         k_LoaMgr_RedcdSiSca_Uls_f32       1		
k_LoaMgr_IvtrMtgtnRate_UispS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UispS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgMsmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1TiTnd_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1TiTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UispS_f32       1         k_LoaMgr_MotCurrMtgtnRate_UispS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uis_f32       1         k_LoaMgr_RedcdSiRate_UispS_f32       1         k_LoaMgr_RedcdSiSca_Uis_f32       1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig4ITnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig5ItThd_Cnt_u08       0         k_LoaMgr_MotCurridptSig5ItThd_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Ae_u1spS_f32       1         k_LoaMgr_RedcdStRate_U1spS_f32       1         k_LoaMgr_RedcdStSae_U1spS_f32       1         k_LoaMgr_RedcdStSae_U1spS_f32       1          k_LoaMgr_RedcdStS		
k_LoaMgr_NvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig5Resp_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Trhd_Cnt_u08       0         k_LoaMgr_MotCurrlMgtNgtRate_UlspS_f32       1         k_LoaMgr_MotCurrMgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtmScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtmSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1FlTnd_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1FlTnd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrlMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Vis_f32       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSigOResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSig 1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMigtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMigtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_RedcdStSca_Uls_f32 1	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	k_LoaMgr_RedcdStSca_Uls_f32	
	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u			
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr AgSV Cnt M lgc	0	0	✓ ×	
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	_	
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1		
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•	
LoaMgr lvtrldptSigSV Cnt M u08	1	1		
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4		
LoaMgr_LOASt_State_M_enum	5	5	·	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1		
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3		
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	-	
LoaMgr_TloaDi_Cnt_M_lgc	1	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_CITE_1_endIII)	{1, 1, 1}	{0, 0, 0, 0, 0}	W	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1}	{1, 1, 1}		
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001		
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1		
	0	0		
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	5	5		
target_LoaMgr_Per1_LOASt_State_enum.value	1			
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.75 (Repeat Count = 1)	E Company of the Comp
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	o Input value		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	250.478897		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAnMgrbPeto_UlasS_623	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_ui08	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at Igo	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIoaMtgtpEp_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target Rte Inst Ap LoaMgr.LoaMgr Perl SteerMod Cnt enum	target_LoaMgr_Per1_MttPostaptSig_Crit_ut		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	i ve suit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
	500	500	•
LoaMgr_LoaRateLimit_UlspS_M_f32			
	0	0	<b>₩</b>
LoaMgr_LoaRateLimit_UispS_M_f32 LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5	5	-
LoaMgr_LoaScaleFctr_Uls_M_f32			
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	<b>✓</b>
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.76 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	)-4 l	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0	В	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr_Der1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	-
LoaMgr LOASt State M enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	-
	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08			-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08			
zoamgmotoamapto.gov_om_m_aoo	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc		2 0	<b>*</b>
	2		-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2 0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	2 0 3	0 3 1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	2 0 3 1	0 3	· ·
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08	2 0 3 1 1 0	0 3 1 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 0 3 1 1 0 {170, 172, 173}	0 3 1 1 0 {170, 172, 173}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1}	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0}	\rightarrow \frac{1}{\sigma} \rightarrow \fra
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1}	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1}	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	\rightarrow \frac{1}{\sigma} \rightarrow \fra
LoaMgr_MotCurrLoaMgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.77 (Repeat Count = 1)           Name         Input Value           LoaMgr_AgsV_Cnt_M_lgc         1           LoaMgr_HwTqldptSigsV_Cnt_M_u08         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1           LoaMgr_HwTqRespSV_Cnt_M_u08         1	<b>33</b>
LoaMgr_AgSV_Cnt_M_lgc         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1           LoaMgr_HwTqRespSV_Cnt_M_u08         1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08       0         LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       1         LoaMgr_HwTqRespSV_Cnt_M_u08       1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_HwTqRespSV_Cnt_M_u08 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 1	
VI	
LoaMgr_IvtrldptSigSV_Cnt_M_u08 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotAgRespSV_Cnt_M_u08 5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_MotCurrRespSV_Cnt_M_u08 3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 1	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
_ 0_ 0 0 0	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurridptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 1	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	✓ ×
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Sealing   Justification   Ju	Test Step 1.78 (Repeat Count = 1)	13
Leading Fart Capability Cor M Loss  Leading Fart Capability Cor M Loss  1  Leading Fart Capability Cor M Loss  2  Leading Fart Capability Cor M Loss  1  Leading Fart Capability Cor M Loss  2  Leading Carbon Market Links 2  Leading Links 3  Leading Links 4  Leadin		
Leading - Indiquisation Co. P. Mgs  Leading - Indiquisation Use Co.  Leading - Indiquisation Use		·
Landary Juniferpies Cort M, 100  Landary Juniferpies Cort Landary Cort M, 100  Landary Juniferpies Cort Landary Cort M, 100  Landary Juniferpies Cort Landary Cort M, 100  Landary Juniferpies Cort Cort M, 100  Landary Juni	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Landary Juniferson (Co. M. 1, 100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Looking Johnson, Ch. M. Julio  Looking Johnson, Ch. Julio  Looking Jo	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Landary Mandylapathyperis, Cert. My per  Landary Mandylapathyperis, Landary My per  Landary My periodic My per  Landary My periodic	LoaMgr_lvtrldptSigSV_Cnt_M_u08	-
Loading   MonApples (1907, V. O.)	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading   Manufage in Stage   Manufage   Loading   Loading   Manufage in Stage   Loading   Manufage   Loading   Manufage   Loading   Manufage   Loading   Manufage   Loading   Loading   Manufage   Loading   Loading   Manufage   Loading   Loading   Manufage   Loading   Loading   Loading   Manufage   Loading   Loadi		
Landary, MacCardina Staglistic Cont. Jul. 198  Landary, Landary, Cont. Jul. 198  Landary, La		
Loading McCorridor(Sign) (Loading Control Marge   1   1   1   1   1   1   1   1   1		1
Loading   John Company   Loading		
Landary Carthyridespread Urings 522  1	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Contribiting Price   Lipid Str.	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Internation	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landary Controlotignosa: Prof. 10   1	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Luading / CarrierMingrisca/Lucinia (Lucinia) (Luading / CarrierMingrisca/Lucinia) (Luading / CarrierMingrisca/Lucinia) (Luading / ArrierMingrisca/Lucinia) (Luading / ArrierMi	Rte_Inst_Ap_LoaMgr	V
Loading Failed Shiften Users   1		
Leading / Hirofest Spi Towakowillase, Ort. 108		
Landary Hartfeldrist (Introduction Charles)		
Loading   Mr Options   The Tourism   Loading   Loading   Loading   Mr Options   The Tourism   The Touris		
Loading   Mindigos (2011		
Loading   Mr Option (SQ Total Resp. Cmt   UB	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7qlqptiSig4ffeeg_Cnt_UB	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
Loading-HwTqMgginFiltriD_Cri_U0S	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loaking   MrVTqMigginSaip LispS .122	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
Loaking   Introduction   Loaking	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking   Indirigisig   Reap Crit   Loaking   Loaking   Loaking   Indirigisig   Reap Crit   Loaking   Indirigisig   Reap Crit   Loaking   Indirigisig   Reap Crit   Loaking   Indirigisig   Reap Reap Crit   Loaking   Indirigisig   Indirigisig   Reap Reap Crit   Loaking   Indirigisig   Indirigisi	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Loaklgr_IntriptSig2Resp_Crt_u08		
Loaking   NrtiopisSigFiTTINd   CPI   USB   12		
L Loaking / Intrinsipasa (Lips)S		
Loading		
Loading		
LoaMgr_MorLagidpsigarResp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
LoaMgr_MotAgloptSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MotAgliqhisigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghtRate_UlspS_132	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
C_LoaMgr_MotAgMignScaZeroEn_Cnt_lgc         0           C_LoaMgr_MotAgMignSca_Uls_132         0           C_LoaMgr_MotAgMignSca_Uls_132         0           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         2           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         0           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         0           C_LoaMgr_MotCurlidptSignThd_Cnt_u08         0           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         0           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         1           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         1           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         1           C_LoaMgr_MotCurlidptSignResp_Cnt_u08         1           C_LoaMgr_RedcuSiRcalle_ing_2         1           C_LoaMgr_RedcuSiRcalle_ing_2         1           C_LoaMgr_RedcusiRcalle_uisp_S_132         1           C_LoaMgr_RedcusiRcalle_uisp_S_132         1           C_LoaMgr_Perl_DiagcStsivtrilnactv_Cnt_lgc.value         0           Larget_LoaMgr_Perl_DiagcStsivtrilnactv_Cnt_lgc.value         3           Larget_LoaMgr_Perl_MotCurlidptSig_Cnt_u08 value         0           Larget_LoaMgr_Perl_MtPosightSign_Cnt_u08 value         2           Larget_LoaMgr_Perl_MtPosightSign_Cnt_u08 value         2           Larget_LoaMgr_Perl_MtPosightSign_Cnt_u08 value         2	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgSmaAvailResp_Ont_u08         2           k_LoaMgr_MotAgSmaAvailResp_Ont_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_Per_DiagcStsivtrilnactv_Cnt_lgc.value         1           larget_LoaMgr_Per_HotQsig1Sig_Cnt_u08 value         3           larget_LoaMgr_Per_MotQuridptSig_Cnt_u08 value         0           larget_LoaMgr_Per_MotQuridptSig_Cnt_u08 value         1           larget_LoaMgr_Per_MotQuridptSig_Cnt_u08 value         <		
LoaMgr_MotOurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2a_Uls_32         1           k_LoaMgr_RedcdSiRea_Uls_Sp_32         1           k_LoaMgr_RedcdSiRea_Uls_Sp_32         1           k_LoaMgr_Per1_DiagcSistvtr1lnactv_Cnt_lgc.value         0           larget_LoaMgr_Per1_DiagcSistvtr2lnactv_Cnt_lgc.value         1           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         3           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           larget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           larget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           larget_LoaMgr_LoaMgr_LoaMgr_Pe		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_Ugc         1           k_LoaMgr_MotCurridptSig2SaZeroEn_Cnt_Ugc         1           k_LoaMgr_MotCurridptSig.SaZeroEn_Cnt_Ugc         1           k_LoaMgr_RedcdSiStade_Uls_732         1           k_LoaMgr_RedcdSiStade_Uls_732         1           k_LoaMgr_Perd_DiagcStstvfr1Inactv_Cnt_Ugc.value         0           target_LoaMgr_Perd_DiagcStstvfr2Inactv_Cnt_Ugc.value         1           target_LoaMgr_Per1_LoaScaD_Cnt_Ugc.value         3           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MirPosidptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MirPosidptSig_Cnt_u08.value         2           target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value         1           target_LoaMgr_Per1_ToaDi_		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Unstrate_Ulsp5_f32         1           k_LoaMgr_MotCurridptSigaCa_Uls_f32         1           k_LoaMgr_RedcdSitRate_Ulsp5_f32         1           k_LoaMgr_RedcdSitRate_Ulsp5_f32         1           k_LoaMgr_Per1_DiagcStstvfrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvfrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_MrPosidptSig_Cnt_u08 value         2           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigFitThd_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSa_Uls_f32         1           k_LoaMgr_MotCurridptSigSca_Uls_f32         1           k_LoaMgr_RedcdSiKate_UlspS_f32         1           k_LoaMgr_Ped_DiagcStslvtrInactv_Cnt_lgc value         0           larget_LoaMgr_Ped_DiagcStslvtr2inactv_Cnt_lgc value         1           larget_LoaMgr_Ped_LoaMgr_Ped_JourdptSig_Cnt_u08 value         3           larget_LoaMgr_Ped_MotCurridptSig_Cnt_u08 value         0           larget_LoaMgr_Ped_MotCurridptSig_Cnt_u08 value         0           larget_LoaMgr_Ped_SteerMod_Cnt_enum value         0           larget_LoaMgr_Ped_TloaAvail_Cnt_lgc value         1           larget_LoaMgr_Ped_TloaAvail_Cnt_lgc, value         0           larget_LoaMgr_Ped_TloaAvail_Cnt_lgc, value         0           larget_Re_Inst_Ap_LoaMgr_Ped_DiagcStslvtr1Inactv_Cnt_lgc         larget_LoaMgr_Ped_DiagcStslvtr2Inactv_Cnt_lgc           larget_Re_Inst_Ap_LoaMgr_Ped_Ded_Ped_Ped_DiagcStslvtr2Inactv_Cnt_lgc         larget_LoaMgr_Ped_DiagcStslvtr2Inactv_Cnt_lgc           larget_Re_Inst_Ap_LoaMgr_LoaMgr_Ped_HvTqLoaMtgthEn_Cnt_lgc         larget_LoaMgr_Ped_MotQloaMtgtnEn_Cnt_lgc <td< td=""><td></td><td></td></td<>		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         3           target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value         0           target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value         0           target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value         2           target_LoaMgr_Perd_SteerMod_Cnt_enum.value         0           target_LoaMgr_Perd_Tioal_Cnt_lgc.value         1           target_Nell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perd_Tioal_Cnt_lgc.value           target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc           target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc           target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc           target_Rell_First_Ap_LoaM	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	
LoaMgr_MotCurrMigtnScaZercEn_Cnt_lgc k_LoaMgr_MotCurrMigtnSca_Uls_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 larget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value larget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_SteerMod_Cnt_enum.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLo	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
LoaMgr_MotCurrMigtnSca_Uls_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
K_LoaMgr_RedcdStRate_UispS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_L	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  2 target_LoaMgr_Per1_MrtCurrldptSig_Cnt_u08.value  2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value  3 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc  4 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc  5 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc  6 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  6 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc  6 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_Igc  6 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc  6 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc  6 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc  6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc  6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc  6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc  6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc  6 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc  6 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_Int_LoaMtgtnEn_Cnt_lgc target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Nel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	-

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.79 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k LoaMgr_MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnRate_UispS_t32 k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
	U		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_igc } t_igc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loamgr_Per1_IvtrLoaMtgtnEn_IvtrLoamgr_Per1_IvtrLoamgr_Per1_IvtrLoamgr_Per1_IvtrLoamgr_Per1_IvtrLoamgr_Per1_IvtrLoamgr_Per1_IvtrLoamgr_Per	nt_igc } t_igc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LOASt_State_enum	nt_igc } t_igc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc s t_lgc gc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc s t_lgc gc nt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0	nt_lgc s t_lgc gc nt_lgc 08	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u02 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u02 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	Result
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value	Result
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0	~
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0	Result
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 0	<b>*</b>
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgd.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 0 4	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Cnt_lgc 08 0 0 0 0	<b>*</b>
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqlogMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloptSig_Cnt_uctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value 0 0 0 4 0	nt_lgc st_lgc gc st_lgc	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 0 4	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Cnt_lgc 08 0 0 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotCurrloptSig_Cnt_uctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uctarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 4 0 0	nt_lgc st_lgc gc st_lgc gc st_lgc st_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_lenum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	1 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_Intr_LoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 4 0 0 5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cn	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500	nt_lgc st_lgc st_lgc gc  nt_lgc 08 Cnt_lgc 08 Expected Value 0 0 0 4 0 0 5 5 5	***
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlpdtSigSV_Cnt_M_u08 LoaMgr_HwTqleaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500 1	nt_lgc st_lgc st_lgc gc  nt_lgc os st_lgc st	******
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 5 5 500 1 5	nt_lgc st_lgc gc  nt_lgc gc  nt_lgc 08  Cnt_lgc 08  Expected Value 0 0 0 0 4 0 0 5 5 5 5000 1 5	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.80 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
Name	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0.697300017		
	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	target LoaMgr Per1 DiagcStslvtr1Inactv C	nt lao	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaMgr_Per1_IvtrLoaM	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	st Inc.	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	06	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓ ✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	- 4
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	¥
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	¥
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	4 0	<b>4</b> 0	· ·
	4 0 1	4 0 1	<i>y</i>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4 0 1 2	4 0 1 2	\rightarrow \frac{1}{\rightarrow \frac{1}{\rightarr
LoaMgr_TloaAvail_Cnt_M_lgc	4 0 1 2 1	4 0 1 2 1	· ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 0 1 2 1	4 0 1 2 1 1	\rightarrow \frac{1}{2}
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	4 0 1 2 1 1 0	4 0 1 2 1 1 0	\rightarrow \right
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	4 0 1 2 1 1 0 {170, 172, 173, 174}	4 0 1 2 1 1 0 {170, 172, 173, 174}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1}	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 5	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 5	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.81 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	-
LoaMgr_LOASt_State_M_enum	4	4	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	Ö	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 1.82 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	500
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0
target LoaMgr Per1 DiagcStsIvtr1Inactv_Crit_igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_igc	target_LoaMgr_Perl_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



LoaMgr\_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 1.83 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530





	k_LoaMgr_CurrIvtrMigtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1Tlnd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 1 2 4 2 1 0 0 0 1 1 1 1 0 0 0 1 1 0 0 0 1 1 0 1 5 1 0 0 2 1 0 1 2		
Loakig	k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1FiThd_Cnt_u08 k_LoaMgr_HwTqldptSig1FiThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1FitThd_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 2 4 4 2 1 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
K_Loaking - HvTqidpSignThoRavkailResp_Cnt_u08         4           K_Loaking - HvTqidpSignThoRavkailResp_Cnt_u08         2           K_Loaking - HvTqidpSignThoRavkailResp_Cnt_u08         1           K_Loaking - HvTqidpSignThoRavkailResp_Cnt_u08         0           K_Loaking - HvTqidpSignTesp_Cnt_u08         0           K_Loaking - HvTqidpSignTesp_Cnt_u08         1           K_Loaking - HvTqidpSignTesp_Cnt_u08         1           K_Loaking - HvTqidpSignTesp_Cnt_u08         0           K_Loaking - HvTqidpSignSignTesp_Cnt_u08         0           K_Loaking - HvTqidpSignSignTesp_Cnt_u08         1           K_Loaking - HvTqidpSignSignTesp_Cnt_u08         5           K_Loaking - MvAlqidpSignSignTesp_Cnt_u08         5           K_Loaking - MvAlqidpSignSignTesp_Cnt_u08         2           K_Loaking - MvAlqidpSignSignTesp_Cnt_u08         2           K_Loaking - MvAlqidpSignSignTesp_Cnt_u08         2           K_Loaking - MvAlqidpSignTesp_Cnt_u08         2           K_Loaking - MvCaridpSignTesp_Cnt_u08 <td>k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFitThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFitThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFitThd_Cnt_u08 k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08</td> <td>2 4 2 1 0 0 0 1 1 1 1 0 0 0 1 1 0 0 0 1 1 0 0 1 1 5 1 0 0 2 1 0 1 2</td> <td></td> <td></td>	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFitThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFitThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFitThd_Cnt_u08 k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 4 2 1 0 0 0 1 1 1 1 0 0 0 1 1 0 0 0 1 1 0 0 1 1 5 1 0 0 2 1 0 1 2		
	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_IwtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	4 2 1 0 0 0 1 1 1 1 0 0 0 0 1 1 0 1 5 1 1 0 2 1		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSig1tThd_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Thd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 1 0 0 1 1 1 1 0 0 0 0 1 1 5 1 1 0 2 1 1 0 2		
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Thd_Cnt_u08 k_LoaMgr_MotAgldptSig1FltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 0 0 0 1 1 0 0 0 0 1 1 5 5 1 1 0 0 2 2 1 1 0 0 1 1 2		
	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IwtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig1ltThd_Cnt_u08 k_LoaMgr_IvtrldptSig1ltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1Resp_Cnt_u08 k_LoaMgr_MotAgldptSig1FltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0 0 1 1 1 1 0 0 0 0 1 0 1 5 1 1 0 2 1		
K_Loak/gr_HwTqldptSigFitThd_Cnt_u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltTnd_Cnt_u08 k_LoaMgr_IvtrldptSigFltTnd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMatgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	1 1 1 0 0 0 1 0 1 5 1 0 2 1 1 0 2		
K_Loakig_HwTqMighRate_UlspS_f32	k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrldptSig1Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnSca_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 1 0 0 0 1 0 1 5 1 0 2 1 1 0 2		
K_Loak/gr_HvTqMtghtScq_Uls_132	k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08 k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgIdptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgIdptSig2Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig5Resp_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	1 0 0 0 1 0 1 5 1 0 2 1 0 2		
k_Loaklgr_IvridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvridptSig1Resp_Cnt_u08         1           k_Loaklgr_IvridptSig1Resp_Cnt_u08         5           k_Loaklgr_ModxlqdptSig1Resp_Cnt_u08         1           k_Loaklgr_ModxlqdptSig2Resp_Cnt_u08         0           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxlqdptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         2           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3Resp_Cnt_u08         0           k_Loaklgr_ModxuridptSig3R	k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08 k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgIdptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgIdptSig2Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig5ltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0 0 0 1 0 1 5 1 0 2 1 0 1		
k_Loaklgr_IvrtiqptSigPetind_cnt_u08         0           k_Loaklgr_IvrtiqptSigPitTnd_cnt_u08         0           k_Loaklgr_IvrtidptSigPitTnd_cnt_u08         1           k_Loaklgr_IvrtidptSigPitTnd_cnt_u08         0           k_Loaklgr_IvrtidptSigAca_Uls_f32         1           k_Loaklgr_MotAglidptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAglidptSig1Resp_Cnt_u08         1           k_Loaklgr_MotAglidptSig1Resp_Cnt_u08         0           k_Loaklgr_MotAglidptSig1Resp_Cnt_u08         0           k_Loaklgr_MotAglidptSigResp_Cnt_u08         2           k_Loaklgr_MotAglidptSigResp_Cnt_u08         2           k_Loaklgr_MotAglidptSigAca_Uls_f32         1           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loak	k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08 k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgIdptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgIdptSig2Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig5ltThd_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0 0 1 0 1 5 1 0 2 1 0 1		
k_Loaklgr_IvtridptSigFitThd_Cnt_u08         0           k_Loaklgr_IvtriktginScazeroEn_Cnt_lgc         0           k_Loaklgr_IvtriktginScazeroEn_Cnt_lgc         0           k_Loaklgr_IvtriktginScazeroEn_Cnt_lgc         1           k_Loaklgr_MotAglighSig3rNoSmaResp_Cnt_u08         5           k_Loaklgr_MotAglighSig3rResp_Cnt_u08         1           k_Loaklgr_MotAglighSig3rResp_Cnt_u08         2           k_Loaklgr_MotAglighSigFitThd_Cnt_u08         2           k_Loaklgr_MotAglighSigFitThd_Cnt_u08         2           k_Loaklgr_MotAglighSigAsesp_Cnt_u08         1           k_Loaklgr_MotAglighSigAsesp_Cnt_u08         2           k_Loaklgr_MotAglighSigAsesp_Cnt_u08         2           k_Loaklgr_MotAgsinsAvailResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig-Cnt_u08         0           k_Loaklgr_MotCurridptSig-Cnt_u08         0           k_Loaklgr_MotCurridptSig-Cnt_u08         0           k_Loaklgr_MotCurridptSig-Cnt_u08         0           k_Loaklgr_MotCurridptSig-Cnt_u08         0           k_Loaklgr_M	k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSig5ltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0 1 0 1 5 1 0 2 1 0 1		
k_Loaklgr_lvrivltginRate_UispS_R32         1           k_Loaklgr_lvrivltginSca_Use_To_Cnt_Igo         0           k_Loaklgr_lvrivltginSca_Use_132         1           k_Loaklgr_MotAglotpSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAglotpSig3Resp_Cnt_u08         1           k_Loaklgr_MotAglotpSig3Resp_Cnt_u08         0           k_Loaklgr_MotAglotpSig3Resp_Cnt_u08         2           k_Loaklgr_MotAglotpSig3Resp_Cnt_u08         2           k_Loaklgr_MotAgMignSca_Cere_Cn_Cn_Igc         0           k_Loaklgr_MotAgMignSca_Uis_f32         1           k_Loaklgr_MotAgMignSca_Uis_f32         1           k_Loaklgr_MotAgMignSca_Uis_f32         1           k_Loaklgr_MotAgMignSca_Uis_f32         1           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurri	k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 0 1 5 1 0 2 1 0 1		
k_LoaMgr_IvriMtginSca_Uls_f32         1           k_LoaMgr_MthtQigltSigNSmaResp_Cnt_u08         5           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgMigtnRea_UlspS_f32         1           k_LoaMgr_MotAgMigtnSea_Pcn_u08         2           k_LoaMgr_MotAgMigtnSea_Pcn_u08         2           k_LoaMgr_MotCurridptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         1           k_LoaMgr_MotCurridptSigResp_Cnt_u08         1           k_LoaMgr_NotCurridptSig_Cnt_u08         1           k_LoaMgr_NotCurridptSig_Cnt_u08         0           k_LoaMgr_NotCurridptSig_Cnt_u08         1           k_LoaMgr_Pert_DiagcStsivtrilnact_Cnt_lgc value	k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0 1 5 1 0 2 1 0 1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Fithd_Cnt_u08         2           k_LoaMgr_MotAglMtgnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAglMtgnScaZeroEn_Cnt_u08         2           k_LoaMgr_MotAglMtgnScaZeroEn_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurriMtgnScaZeroEn_Cnt_u08         1           k_LoaMgr_MotCurriMtgnScaZeroEn_Cnt_u08         1           k_LoaMgr_MotCurriMtgnSca_Uis_f32         1           k_LoaMgr_RotCurridptSig.Sig.2         1           k_LoaMgr_RotCurridptSig.Sig.32         1           k_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	5 1 0 2 1 0 1 2		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurdptSigResp_Cnt_u08         2           k_LoaMgr_MotCurdptSigResp_Cnt_u08         2           k_LoaMgr_MotCurdptSigResp_Cnt_u08         0           k_LoaMgr_MotCurdptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurdptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurdltgtnScaZereEn_Cnt_lgc         1           k_LoaMgr_MotCurdltgtnScaZereEn_Cnt_lgc         1           k_LoaMgr_MotCurdltgtnScaZereEn_Cnt_lgc         1           k_LoaMgr_MotCurdltgtnScaZereEn_Cnt_lgc         1           k_LoaMgr_MotCurdltgtnScaZereEn_Cnt_lgc         1           k_LoaMgr_MotCurdltgtnSca_Uls_f32         1           k_LoaMgr_ReddSiSca_Uls_f32         1           k_LoaMgr_ReddSiSca_Uls_f32         1           target_LoaMgr_Per1_DiagcSistvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HrtqlqbtSig_Cnt_u08 value         1           target_LoaMgr_Per1_MtPosd	k_LoaMgr_MotAgIdptSig2Resp_Cnt_u08 k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	1 0 2 1 0 1 2		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotOurdptSigPResp_Cnt_u08         2           k_LoaMgr_MotCurdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurdptSigSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurdptSigSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurdptSigSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurdptSigSigSig Sig Sig Sig Sig Sig Sig Sig Si	k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0 2 1 0 1 2		
k_LoaMgr_MotAglidptSigFItThd_Cnt_u08         2           k_LoaMgr_MotAgMtgmRate_UlspS_j32         1           k_LoaMgr_MotAgMtgmRate_UlspS_j32         0           k_LoaMgr_MotAgMtgmSca_Uls_j52         1           k_LoaMgr_MotAgMtgmSca_Uls_j52         1           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigOresp_Cnt_u08         1           k_LoaMgr_MotCurridptSigOresp_Cnt_u08         1           k_LoaMgr_RedcdSiRate_UlspS_j32         1           k_LoaMgr_RedcdSiSca_Uls_j32         1           k_LoaMgr_Per1_DiagcStsIvIrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvIrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TloaCnt_lgc.value         1	k_LoaMgr_MotAgIdptSigFitThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 1 0 1 2		
k_LoaMgr_MotAgMightRate_UispS_f32         1           k_LoaMgr_MotAgMightSca_Uis_f32         0           k_LoaMgr_MotAgMightSca_Uis_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1PitThd_Cnt_u08         0           k_LoaMgr_MotCurrMigtnRate_UispS_f32         1           k_LoaMgr_MotCurrMigtnRate_UispS_f32         1           k_LoaMgr_RedcdStrate_UispS_f32         1           k_LoaMgr_RedcdStSca_Uis_f32         1           k_LoaMgr_RedcdStSca_Uis_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value         1           target_LoaMgr_Per1_brayT_loaScaDi_Cnt_U08_value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_U08_value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_ToaAbrail_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         <	k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1 0 1 2		
k_LoaMgr_MotAgMtgInSca_Uls_f32         1           k_LoaMgr_MotAgAmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitTnd_Cnt_u08         0           k_LoaMgr_MotCurrMtghSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrMtghReat_UlspS_f32         1           k_LoaMgr_MotCurrMtghReat_UlspS_f32         1           k_LoaMgr_MotCurrMtgthSca_Uls_f32         1           k_LoaMgr_MotCurrMtgthSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_PedcdStSrate_Uls_f32         1           k_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc_value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0 1 2		
k_LoaMgr_MotAgMigtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrMgthSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_RedcdStRat_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_StertMod_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         <	k_LoaMgr_MotAgMtgtnSca_UIs_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	1 2		
k_LoaMgr_MotQsmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnSeaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrZlnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaAvail_Cnd_gc.value         1           target_LoaMgr_Per1_ToaAyail_CoaMgr_Per	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFiTh_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_ForEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStsIvtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc l_k_LoaMgr_MotCurrMtgtnSca_UlspS_f32 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 l_k_LoaMgr_MotCurrMtgtnSca_Uls_f32 l_k_LoaMgr_RedcdSiRate_UlspS_f32 l_k_LoaMgr_RedcdSiRate_UlspS_f32 l_k_LoaMgr_RedcdSiSca_Uls_f32 larget_LoaMgr_Per1_DiagcSistvtr1Inactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcSistvtr2Inactv_Cnt_lgc.value larget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value larget_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value larget_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value larget_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value larget_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value larget_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_ulse 1 target_LoaMgr_Per1_LoaScaDi_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_Perd_DiagcStsivtr1nactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_SteerMod_Cnt_enum.value       1         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc	k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       1         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_SteerMod_Cnt_enum.value       1         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       1         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_SteerMod_Cnt_enum.value       1         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_LoaMgr_Per1_TloaDi_Cnt_lgc.value       1         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
k_LoaMgr_RedcdStSca_Uls_f32  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc			at lac	
target interinst Ap Loanigi Loanigi Ferr riwrqiuptoig Ont doo target Loanigi Ferr riwrqiuptoig Ont doo	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc			<del></del>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	· · · · · · · · · · · · · · · · ·			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08				
target_Rte_Inst_Ap_LoaMgr_Per1_Mitrosidpi3ig_Cnt_dood target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum			<u> </u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc				
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	· · - · ·			
			Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc         1         1	Name	1	1	~
0.00 0.		1	1	~
zoamg	LoaMgr_AgSV_Cnt_M_lgc			~
	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			~
1 32 1 4 4 3 2 2 2 3 4	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	10		<b>Y</b>
32 7 7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08			~
<u> </u>	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1		-
	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 5		
	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08	1 5 5	1500	
	LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaRateLimit_UlspS_M_f32	1 5 5 500		•
	LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	1 5 5 5 500 0	0	~
	LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 5 5 500 0 5 1	0 5 1	~
LoaMgr_MotAgRespSV_Cnt_M_u08 5	LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 5 5 500 0 5 1	0 5 1 0	~

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.84 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	300.489197		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	· ·		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	<b>S</b>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	gc .	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	at lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	98	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr HwTqRespSV Cnt M u08	4	4	<b>~</b>
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
1 - 1 1			
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1	1	V
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-
		5	
target LoaMgr Per1 LOASt State enum value	15		
target_LoaMgr_Per1_LOASt_State_enum.value	5		
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.85 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurridptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_Loanigt_Ferr_Diagooloivti illidetv_Offt_ige.value	





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.86 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.87 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr2Inactv C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_Der1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum	, <del>-</del>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScabi_Crit_igc target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtpEp_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	00	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.88 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Loading: ModaglestigicResp. Crt. URB	Name	Invest Value		
	Name	Input Value		
L. Loading / MoAphgrisheit L. Using St. 22   1				
Loadigr_MolCurriquisignResp_Cnt_U08		1		
	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
		2		
K_Loaklgr_MolCurridetSigFITTed_Cnt_U08	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
Loadigr_RedcdSiRate_Ulps_Fl2   1	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
Loading-RedoSiSica_Uis_122				
target_LoaMgr_Pert_DiagcStstvtr1nactv_Cnt_jc.value				
target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value		· ·		
target_LoaMgr_Per1_HwTqidptSig_Cnt_u08 value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc value 1 target_LoaMgr_Per1_MctroPosidptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MctroPosidptSig_Cnt_u08 value 2 target_LoaMgr_Per1_MctroPosidptSig_Cnt_u08 value 2 target_LoaMgr_Per1_MctroPosidptSig_Cnt_u08 value 1 target_LoaMgr_Per1_SteemMod_Cnt_enum_value 0 target_LoaMgr_Per1_ToaDi_Cnt_Igc_value 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc_value 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc_value 1 target_LoaMgr_Per1_DoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_Per1_HwTqidptSig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_HwTqidptSig_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_HwTqidptSig_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_LoAScaDi_Cnt_Igc 1 target_LoaMgr_Per1_HwTqidptSig_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_LoAScaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_LoAScaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_LoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_DoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_LoaMgr_Per1_MctourtopMisig_Cnt_u08 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc 1 target_LoaMgr_Per1_				
target_LoaMgr_Perl_LoaScaDi_Cnt_Igc_value		· ·		
target_LoaMgr_Perl_MitrPositiptSig_Cnt_u08 value 2 target_LoaMgr_Perl_MitrPositiptSig_Cnt_u08 value 2 target_LoaMgr_Perl_Tloadvail_Cnt_enum value 1 target_LoaMgr_Perl_Tloadvail_Cnt_lgc value 1 target_LoaMgr_Perl_DiagcStslvfr2inactv_Cnt_lgc 1 target_LoaMgr_Perl_DiagcDiagc_Perl_DiagcStslvfr2inactv_Cnt_lgc 1 target_LoaMgr_Perl_MitrDosidgsig_Cnt_u08 1 target_LoaMgr_Perl_MitrDosidgsig				
target_LoaMgr_Per1_SiterMod_Cnt_enum_value				
target_LoaMgr_Per1_SteerMod_Cnt_enum.value   1				
target_LoaMgr_Per1_TloaAvail_Cnt_lgc_value				
target_LoaMgr_Perr_TloaDi_Cnt_lgc_value target_LoaMgr_Perr_DloagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_DloagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_DloagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_Intr_OaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_Intr_OaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotAp_LoaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotAp_LoaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotCurrloaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotCurrloaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotCurrloaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotCurrloaMtghtEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotPadptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotPadptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_MotPadptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_SteerMod_Cnt_enum target_LoaMgr_Perr_SteerMod_Cnt_enum target_LoaMgr_Perr_SteerMod_Cnt_enum target_LoaMgr_Perr_TloaDi_Cnt_lgc target_LoaMgr_Perr_Tl				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Virt_OaMtgnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosidptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosidptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosidptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaAvail_Cnt_Igc target_LoaMgr_Per1_ToaDi_Cnt_Igc  Name  Actual Value  Actual Value  Expected Value  Res LoaMgr_HwTqLoaMgthEn_Cnt_M_Igc  LoaMgr_HwTqLoaMgthEn_Cnt_M_Igc  LoaMgr_HwTqLoaMgthEn_Cnt_M_Igc  LoaMgr_HwTqLoaMgthEn_Cnt_M_Igc  LoaMgr_IntropSigSV_Cnt_M_Iu08  LoaMgr_LoaScaleFotr_UB_Iu08  LoaMgr_LoaScaleFotr_UB_Iu08  LoaMgr_LoaScaleFotr_UB_Iu08  Expected Value  LoaMgr_MaxMtgnResp_Cnt_M_Iu08				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pert_HwTqLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_IvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pert_IvtrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaSt_State_enum         target_LoaMgr_Pert_IvtrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc         target_LoaMgr_Pert_LoaScaDi_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_MotGurdptSig_Cnt_u08         target_LoaMgr_Pert_MotGurdptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_MotGurtdptSig_Cnt_u08         target_LoaMgr_Pert_MotOurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOurrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pert_MotOurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOurrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pert_MotOurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc         target_LoaMgr_Pert_ToaAvail_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc         target_LoaMgr_Pert_ToaAvail_Cnt_lgc           target_LoaMgr_Pert_ToaDi_Cnt_lgc         target_LoaMgr_Pert_ToaDi_Cnt_lgc           Name         Actual Value         Expected Value           LoaMgr_HwTqRespSv_Cnt_M_u08         0           LoaMgr_HvtrLoaMtgtnEn_Cn			Ont Igo	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MrTqLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MrTqLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOAScabl_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScabl_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MrPostdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MrPostdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MrPostdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.RoaMgr.Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.RoaMgr.RoaMgr.Per1_TloaDi_Cnt_Igc  Name  Actual Value  Expected Value  Res  LoaMgr_HwTqRespSV_Cnt_M_u08  0  0  0  LoaMgr_HwTqRespSV_Cnt_M_u08  0  0  0  LoaMgr_HwTqRespSV_Cnt_M_u08  5  5  5  LoaMgr_IvtLoaMgtnEn_Cnt_M_u08  5  LoaMgr_VtrtLoaMgtnEn_Cnt_M_u08  5  LoaMgr_LoaScaleFotr_Uis_M_f32  1  LoaMgr_LoaScaleFotr_Uis_M_f32  500  LoaMgr_LoaScaleFotr_Uis_M_f32  1  LoaMgr_MaxMtgtnResp_Cnt_M_u08  5  LoaMgr_LoaScaleFotr_Uis_M_f32  1  LoaMgr_MaxMtgtnResp_Cnt_M_u08				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASs_State_enum         target_LoaMgr_Per1_LoASs_Di_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogtsig_Cnt_u08         target_LoaMgr_Per1_MotCurrlogtSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value         Res           LoaMgr_AgsV_Cnt_M_lgc         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         0         0           LoaMgr_IvrloaMtgtnEn_Cnt_M_lgc         0         0		target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtghtEn_Cnt_Igc         target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtghtEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08         target_LoaMgr_Per1_MotCurrloaMtghtEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtghtEn_Cnt_Igc         target_LoaMgr_Per1_MotCurrloaMtghtEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc         target_LoaMgr_Per1_TloaAvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc         target_LoaMgr_Per1_TloaDi_Cnt_Igc           Name         Actual Value         Expected Value         Res           LoaMgr_AgsV_Cnt_M_Igc         0         0         0           LoaMgr_HwTqLoaMtghtEn_Cnt_M_Igc         0         0         0           LoaMgr_HwTqLoaMtghtEn_Cnt_M_Igc         0         0         0           LoaMgr_IvtrIoaMtghtEn_Cnt_M_Igc         0         0         0           LoaMgr_LoaSt_State_M_enum         5         5         5           LoaMgr_LoaScaleFctr_Uls_M_f32	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc         target_LoaMgr_Per1_LoaScaDi_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrl_caMtgtnEn_Cnt_Igc         target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_TloaAvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc         target_LoaMgr_Per1_TloaDi_Cnt_Igc           Name         Actual Value         Expected Value         Res           LoaMgr_AgSV_Cnt_M_Igc         0         0         0           LoaMgr_HwTqlpdsIgSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqlcaMtgtnEn_Cnt_M_Igc         0         0         0           LoaMgr_IvtrIdoaMtgtnEn_Cnt_M_u08         4         4         4           LoaMgr_IvtrIdoaMtgtnEn_Cnt_M_igc         0         0         0           LoaMgr_IvtrIdoaMtgtnEn_Cnt_M_igc         0         0         0           LoaMgr_LoaRste_Imi_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_D_M_igs_	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_f	gc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosl	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08         target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_TloaAvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc         target_LoaMgr_Per1_TloaDi_Cnt_Igc           Name         Actual Value         Expected Value         Res           LoaMgr_AgSV_Cnt_M_Igc         0         0         0           LoaMgr_HwTqldptSigsV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_u08         4         4         4           LoaMgr_IvtrlopKigSV_Cnt_M_u08         0         0         0           LoaMgr_IvtrlopKigSV_Cnt_M_u08         5         5         5           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LoASt_State_M_enum         5         5         5           LoaMgr_LoASt_Etler_Uls_M_f32         1         1         1           LoaMgr_LoaScaleFctr_Uls_M_f32         5         5         5	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc    Name	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value Expected Value Res  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  5  LoaMgr_lvtrRespSV_Cnt_M_u08  5  5  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_loaScaleFctr_Uls_M_f32  LoaMgr_loaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  5  5	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_i	.08 	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc         target_LoaMgr_Per1_TloaDi_Cnt_Igc           Name         Actual Value         Expected Value         Res           LoaMgr_AgSV_Cnt_M_Igc         0         0         0           LoaMgr_HwTqIdptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         4         4         4           LoaMgr_IvtrIdptSigSV_Cnt_M_u08         0         0         0           LoaMgr_IvtrIdptSigSV_Cnt_M_u08         0         0         0           LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc         0         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LOASt_State_M_enum         5         5         5           LoaMgr_LoaRateLimit_UIspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_UIs_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5         5				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc         target_LoaMgr_Per1_TloaDi_Cnt_Igc           Name         Actual Value         Expected Value         Res           LoaMgr_AgSV_Cnt_M_Igc         0         0         0         0           LoaMgr_HwTqIdptSigSV_Cnt_M_u08         0         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc         0         0         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         4         4         4         4           LoaMgr_IvtrIdptSigSV_Cnt_M_u08         0         0         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc         0         0         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LOASt_State_M_enum         5         5         5           LoaMgr_LoaRateLimit_UIspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_UIs_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5         5				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value         Res           LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         4         4         4           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0         0         0           LoaMgr_IvtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LOASt_State_M_enum         5         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5         5				
Name         Actual Value         Expected Value         Res           LoaMgr_AgsV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigsV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtmEn_Cnt_M_lgc         0         0         0           LoaMgr_lvtrldptSigsV_Cnt_M_u08         4         4         4           LoaMgr_lvtrLoaMtgtmEn_Cnt_M_lgc         0         0         0           LoaMgr_lvtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LOASt_State_M_enum         5         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5         5				
LoaMgr_AgSV_Cnt_M_lgc       0       0         LoaMgr_HwTqldptSigSV_Cnt_M_u08       0       0         LoaMgr_HwTqLoaMtgtmEn_Cnt_M_lgc       0       0         LoaMgr_HwTqRespSV_Cnt_M_u08       4       4         LoaMgr_lvtrldptSigSV_Cnt_M_u08       0       0         LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_lvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5			le	
LoaMgr_HwTqldptSigSV_Cnt_M_u08       0       0         LoaMgr_HwTqLoaMtgtmEn_Cnt_M_lgc       0       0         LoaMgr_HwTqRespSV_Cnt_M_u08       4       4         LoaMgr_lvtrldptSigSV_Cnt_M_u08       0       0         LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_lvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5			•	Result
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_HwTqRespSV_Cnt_M_u08       4       4         LoaMgr_lvtrldptSigSV_Cnt_M_u08       0       0         LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_lvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5				-
LoaMgr_HwTqRespSV_Cnt_M_u08       4       4         LoaMgr_IvtrldptSigSV_Cnt_M_u08       0       0         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_IvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5				
LoaMgr_IvtrIdptSigSV_Cnt_M_u08       0       0         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_IvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5				·
LoaMgr_IvtrLoaMigtnEn_Cnt_M_lgc       0       0         LoaMgr_IvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5			· .	-
LoaMgr_IvtrRespSV_Cnt_M_u08       5       5         LoaMgr_LOASt_State_M_enum       5       5         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5				<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5	1		5	-
LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       5       5	LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 5	LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
	LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08 2	LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08 4				~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0				~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1	V =			~
LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2				<b>Y</b>
LoaMgr_TloaAvail_Cnt_M_lgc 1 1				•
LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltdMdSrcSV Cnt M u08         0         0				•
32 3 4 4 2 2 2 4		-	1	,
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173, 174}       {170, 172, 173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1, 1}       {0, 0, 0, 0, 0}		Ti-	Ţ	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1, 1} {1, 1, 1, 1}				~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 500 500 ± 0.01				•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  1 1±0.001				
target LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0				~
target_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc.value 0				•
target_LoaMgr_Per1_LOASt_State_enum.value 5		5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1	target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc value		1	_
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_coarrigi_i or i_mor igcoarriginen_ont_igc.value	1	1	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~	
AssignScale	1	AssignScale	1	<b>✓</b>	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

T4 04 4 00 (D4 04 1)	
Test Step 1.89 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0.671599984
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In	actv Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2In		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig 0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgti		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target LoaMgr Per1 MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Igo	•	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	4	4	_
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>*</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	_
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr MotCurrRespSV Cnt M u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	_
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr VltgMdSrcSV Cnt M u08	0	0	_
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
			199
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1} {1, 1, 1, 1}	{0, 0, 0, 0, 0} {1, 1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	
· - ·		1	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1		· ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 1.90 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Invr_QLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc

LoaMgr\_Per1





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.91 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

Input Value



Name



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
	0		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	· ·		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target LoaMgr Per1 TloaAvail Cnt lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr1Inact	ty Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact	*	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu	ım	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgd	0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl		
	target LoaMgr Per1 MtrPosldptSig Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
		0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>*</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
	1	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	. 4

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.92 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e	enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	:	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
	'	·	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			•
	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 1 4	0 1 4	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 1 4 0 1 5	0 1 4 0 1 5	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 1 4 0	0 1 4 0 1 5 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 1 4 0 1 5	0 1 4 0 1 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 1 4 0 1 5 5	0 1 4 0 1 5 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 4 0 1 5 5 5 500 0 5	0 1 4 0 1 5 5 5 500 0 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 4 0 1 5 5 5 500 0 5	0 1 4 0 1 5 5 5 500 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	0 1 4 0 1 5 5 5 500 0 5 1	0 1 4 0 1 5 5 5 500 0 5 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 4 0 1 5 5 5 500 0 5 1 0 5	0 1 4 0 1 5 5 5 500 0 5 1 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0	0 1 4 0 1 5 5 5 500 0 5 1 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 1 4 0 1 5 5 5 500 0 5 1 1 0 5 0 1 2 1	0 1 4 0 1 5 5 5 5 500 0 5 1 0 5 0 1 2 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	0 1 4 0 1 5 5 5 500 0 5 1 1 0 5 0 1 2 1 1	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 {170, 172, 173}	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 2 1 1 1 1 1 1 1 1,70,172,173}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 1 4 0 1 5 5 5 500 0 5 1 0 1 2 1 1 1 1 {170, 172, 173} {1, 1, 1}	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOARst_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1}	0 1 4 0 1 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlotSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StypMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {1, 1, 1} 500	0 1 4 0 1 1 5 5 5 5 500 0 0 5 1 0 5 0 1 2 1 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrlotSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotPsigSV_Cnt_M_u08 LoaMgr_MotCurrlotPsigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyP_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {1, 1, 1} 500 0	0 1 4 0 1 1 4 0 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {1, 1, 1} 500 0 1	0 1 4 0 1 4 0 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 1 1	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlotSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 5 1 1 2 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 1 5	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 1 1	0 1 4 0 1 1 5 5 5 5 500 0 5 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.93 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
	4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	350.459595
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
a.gocogi_r or ibiagootorea imaote_oni_igo.ealac	<u>'</u>





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	_ <del>-</del>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	n	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr MotCurrRespSV Cnt M u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr TloaDi Cnt M lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.94 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 torrest Dto Inst An Lockfor
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	f.
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	f.
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
5 2 2 2 12 5 52 7 2 1111 11 11 11 11 11 11	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.95 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Nama	Innut Value		
Name	Input Value		
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
target_LoaMgr_Per1_MolCurridptSig_Cnt_uo8.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target LoaMgr Per1 LOASt State enum	<del>,</del>	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
Lockfor Mother confictors Cost M. Inc.			

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.96 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca UIs f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		)	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	_	
LoaMgr_LOASt_State_M_enum		5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	5	5	<b>V</b>
		5	· ·
	500	5 500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32	500	5 500 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 0 5	5 500 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	500 0 5 2	5 500 0 5 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	500 0 5 2	5 500 0 5 2	-
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	500 0 5 2 1 2	5 500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	500 0 5 2 1 2 0	5 500 0 5 2 1 2	-
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	500 0 5 2 1 2 0	5 500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	500 0 5 2 1 2 0	5 500 0 5 2 1 2 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2 0 1 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	500 0 5 2 1 2 0 1 2 1 0	5 500 0 5 2 1 2 0 1 2 1 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174}	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1) 500	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500 0 5 2 1 2 0 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 0	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	500 0 5 2 1 2 0 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 0 1	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1, 1) 500 0 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 5	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 1.97 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	<b>√</b>
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr TloaAvail Cnt M lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cht_1_endin)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	D2
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	_
	1	1	.,
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value		1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.98 (Repeat Count = 1)   Name	<b>⊠</b>
·	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08         2           LoaMgr_MotAgLoaMtdtnEn_Cnt_M_loc         1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_MotAgRespSV_Cnt_M_u08 4	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k LoaMgr MotCurrMtgtnSca Uls f32	
k_LoaMgr_RedcdStRate_UlspS_f32	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0	
target LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target LoaMgr Per1 TloaDi Cnt lgc.value 1	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStslvtr1lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_Bhagcsistvtziniactv_Crit_igc target_LoaMgr_Per1_Bhagcsistvtziniactv_Crit_igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.Per1_ivtrLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_inst_Ap_LoaMgr_Peri_LOASt_State_enum target_LoaMgr_Peri_LOASt_State_enum target_LoaMgr_Peri_LoASt_State_enum target_LoaMgr_Peri_LoaScaDi_Cnt_lgc target_LoaMgr_Peri_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.99 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



	1		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
		at lan	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_IvtrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	-
		4	
LoaMgr_lvtrRespSV_Cnt_M_u08	4		~
LoaMgr_LOASt_State_M_enum	4	4	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>V</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.100 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	)-4 l	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08		0	~
LooMar lytrl coMtataEn Cot M Iss	1	1	<b>*</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 0		<b>*</b>
		1	· ·
LoaMgr_IvtrRespSV_Cnt_M_u08	0 4	1 0 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 4 5	1 0 4 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 4 5 500	1 0 4 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 4 5 500 1	1 0 4 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 4 5 500 1 5	1 0 4 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 4 5 500 1 5	1 0 4 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	0 4 5 500 1 5 1	1 0 4 5 500 1 5 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 4 5 500 1 5 1 1	1 0 4 5 500 1 5 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	0 4 5 500 1 5 1 1 4	1 0 4 5 5 500 1 5 1 1 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 4 5 500 1 5 1 1	1 0 4 5 500 1 5 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 4 5 500 1 5 1 1 4	1 0 4 5 5 500 1 5 1 1 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_NtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 4 5 500 1 5 1 1 4 1	1 0 4 5 5 500 1 5 1 1 4 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 4 5 500 1 5 1 1 4 1 0 3	1 0 4 5 5 500 1 5 1 1 4 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 4 5 500 1 5 1 1 4 1 0 3	1 0 4 5 5 500 1 5 1 1 4 1 0 3	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08	0 4 5 5 500 1 1 5 1 1 1 4 1 1 0 0 3 1 1 1 0 0	1 0 4 5 5 500 1 5 1 1 1 4 1 0 3 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 0 {170, 172, 173}	1 0 4 5 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 3 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 0 {170, 172, 173}	1 0 4 5 5 500 1 5 1 1 1 4 4 1 1 0 0 3 1 1 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0, 0}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 4 5 500 1 5 1 1 1 1 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1}	1 0 4 5 5 500 1 5 1 1 4 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgrespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} 500	1 0 4 5 5 500 1 5 1 1 4 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 0 {170,172,173} {1,1,1} {1,1,1} 500	1 0 4 5 5000 1 5 1 1 4 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170,172,173} {1,1,1} {1,1,1} 500 1	1 0 4 5 5000 1 5 1 1 4 1 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 4 5 500 1 5 1 1 1 4 1 0 3 3 1 1 1 0 {1,1,1} {1,1,1} 500 1	1 0 4 5 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 1±0.001 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_igc LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 4 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170,172,173} {1,1,1} {1,1,1} 500 1	1 0 4 5 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 1±0.001 1 0 5	
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc.value	0 4 5 500 1 5 1 1 1 4 1 0 3 3 1 1 1 0 {1,1,1} {1,1,1} 500 1	1 0 4 5 5 500 1 5 1 1 1 4 1 0 3 1 1 1 0 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 1±0.001 1	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.101 (Repeat Count = 1)	<u> </u>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	





Test Step 1.102 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	367.258606
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMigr_Per1_Loascabi_Crit_igc target_LoaMigr_Per1_MotAgLoaMigtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.103 (Repeat Count = 1)		E3
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530





-			
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1 2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1 0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k LoaMgr MotCurrMtgtnSca Uls f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	Cat lea	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	m	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	E
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaScaleEctr_Uls_M_f32	1	1	•
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtotnResp_Cnt_M_u08	1	1 2	
EUGINGI MGAMIGUINGSU CHI IVI UUO			

1

0

LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2

1

0

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.104 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1





Name	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAsSmaAuailBean_Cat.u09	1 0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_loa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_0 target_LoaMgr_Per1_MotCurrLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
m.2.5	.eg		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target LoaMgr Per1 TloaAvail Cnt Ig	С	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg target_LoaMgr_Per1_TloaDi_Cnt_lgc	С	
		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	Expected Value	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1	Expected Value  1 1	<b>≅</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1	Expected Value  1 1 0 1	<b>≅</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1	Expected Value  1 1 0 1 1	<b>≅</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 0	Expected Value	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqltptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 0 1 1 1 2	Expected Value	<b>≅</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqltptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 0 1 1 1 2 500	Expected Value  1 1 0 1 1 1 0 2 500	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 0 1 1 1 2 500 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 2 500 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 0 1 1 1 2 500 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTrlqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 2 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 1	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 2 1 0 1 2 1 0	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 1 0	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 0	Expected Value  1 1 0 1 1 1 0 2 500 1 2 1 0 0 0	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaStateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  1  1  2  500  1  2  1  0  0  0  0  0  0  0	Expected Value  1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaState_Limit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  1  1  2  500  1  2  10  0  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 0 0 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  1  1  2  500  1  2  1  0  0  1  2  1  0  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 1 1 2 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaCscaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCyrrldptSigSV_Cnt_M_u08  LoaMgr_MotCyrrlDaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  1  1  2  500  1  2  1  0  0  1  2  1  0  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MoxAMtgtnResp_Cnt_M_u08  LoaMgr_MoxAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotCarldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 2 500 1 2 1 0 0 1 2 1 1 0 0 1 2 1 1 0 0 0 1 2 1 1 0 0 0 1 2 1 1 0 0 0 0	Expected Value  1 1 0 1 1 0 1 1 1 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 2 500 1 2 1 0 0 1 2 1 1 0 0 1 2 1 1 0 1 0 1	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 2 500 1 2 1 0 0 1 2 1 0 1 2 1 0 1 1 2 1 0 1 2 1 1 1 1	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 0 1 2 1 2 0 1 3 {173,174} {0,0,0,0,0,0} {1,1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 2 500 1 2 1 0 1 2 1 0 1 2 1 1 0 1 2 1 1 1 1	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 0 1 2 1 2 0 1 3 {173,174} {0,0,0,0,0} {1,1} 500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1 1 1 2 1 0 0 1 2 1 1 0 1 2 1 1 0 1 2 1 1 1 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1 0 0	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqloptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrlQaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaStateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotOurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_StltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1 1 1 2 1 0 0 1 2 1 1 0 1 2 1 1 0 1 2 1 1 1 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1 0 1 0 1	Expected Value  1 1 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_Ag\$V_Cnt_M_lgc  LoaMgr_HwTqloptSig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSig\$V_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrResp\$V_Cnt_M_u08  LoaMgr_IvtrResp\$V_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_Ulsp\$_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgltoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgAcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSig\$V_Cnt_M_u08  LoaMgr_MotCurrldptSig\$V_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_NxtrDlagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDlagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDlagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1 0 1 2 1 2	Expected Value  1 1 0 1 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1 2	

2018-05-18, 14:04:23+0530



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalcivtrState	2	~	
LatchInputs	1	LatchInputs	1	•	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	•	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	•	
SwBasMtgtn	1	SwBasMtgtn	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	rtoodit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	-
LoaMgr_LOASt_State_M_enum	4	4	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr MotCurrldptSigSV Cnt M u08	2	2	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VitgMdSrcSV_Cnt_M_u08	· ·		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{0, 0, 0, 0, 0}	~
	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0	500 ± 0.01	<b>*</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)		0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	<b>V</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	· ·
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	<b>V</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.106 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgNtratsPate_UlanS_f33	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_LoaScabl_Crit_igc.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc
5	2. 72.7





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.107 (Repeat Count = 1)	Test Step 1.107 (Repeat Count = 1)	
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



	I		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0.987299979		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32			
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
		9	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	.08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target LoaMgr Per1 TloaAvail Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_Der1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Form a set of Malana	D
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>V</b>
1 - 1 - 1			
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32			
	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08		5	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1		
	1 5	5	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.108 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	





Landary Developation Computer	Name	Input Value		
Landary Monigensignation Company	Name			
Landary Mandagolishing Lings   12				
Landay Morighterise Use   12				
Licably_Moderates Use   1		1		
Looking MoCardistigNResp. CH LIGS   2	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
Loading / McCorrispisign Resp. Crit L08   0	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
Landary_MonComprisor(1978) PRIOR_COLLUBS	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landing John-Carristop/Prince Cort Linds	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landary McCardingSepTime Cort Lands Landary McCardingSepTime Cort Lands Landary McCardingSea Lands Cort Lands Landary McCardingSea Lands Cort Lands Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Pert Disposition Front Cort Lipt continue Landary Pert Landary Pert Landary Cort Landary Cort Landary Pert				
Lacety McCurringhreSazzerien   Leg   1				
Looking / Redough Re				
Loading-Red States   Use   D2		· ·		
Landary   Rescolations   Landary				
Image_Loady_Der_Loagestein/Tenetry_Cort_govalue   1				
Image   Loady Per   Mortargosis, Cet   USA value		0		
Langet_Loady_ Perf_LosSeaD_Cnt_go:auto   Langet_Loady_ Perf_Micropicing_Cnt_u08 value   Langet_Loady_ Perf_Micropicing_Cnt_u08 value   Langet_Loady_ Perf_Nocorruphicing_Cnt_u08 value   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Doady_Cnt_goady_Perf_Doady_Cnt_go:autopet_Roady_Perf_Doady_Cnt_goady_Perf_Doady_Cnt_goady_Cnt_goady_Perf_D		1		
Image   Landing Pert   Medicineriplisis   Cert   Landing   Landing Pert   Medicineriplisis   Landing Pert   Landing   Landing Pert   Medicineriplisis   Landing Pert   Landing   Landing	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
Integred_Loading_PerT_TORDI_COL_EQUE value	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
Image   Canadar   Canada	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
	V - V	1		
target, Rein, Rad, Loadlyr, Loadlyr, Perf. DisposSisht/Planck, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Hw7qt, Coadlyr, Perf. Lync, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Lync, Loadlyr, Perf. Lync, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Lync, Coadlyr, Coadlyr, Lync, Coadlyr, Lync, Coadl	0 = 0 = = = = = = = = = = = = = = = = =			
single_Rinet_Ap_LoaMy_LoaMy_Per1_Hr7qipoRisig_Cnt_U8				
target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   Nri*ToLoadkginEn_Cnt_lgc   target_Rel_inst_Ap_Loadkgr_Per1   Nri*ToLoadkginEn_Cnt_lgc   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_Dtate_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_Dtate_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Doadkgr_Doadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Doadkgr_Doadkgr_Per1   MoRourdostigs_Cnt_uo8   target_Loadkgr_Doadk				
large_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_unit_caltytinEn_Cnt_Upc			_	
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASL_State_enum arget_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaDi_Cnt_lgc target_Rite_Inst_Ap_LoaMgr				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc   target_LoaMgr_Pert_MotOgLoaMignen_Cnt_lgc   target_Rite_Inst_Ap_LoaMigr_LoaMgr_Pert_SteemMod_Cnt_enum   target_LoaMgr_Pert_Nort-MotOgN_Pert_SteemMod_Cnt_enum   target_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_LoaMgr_Pert_ToaAvail_Cnt_lgc   ta				
target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklyr_MicNQ				
Integret   Ref.   net   Ap   Loakingr   Loakingr   Pert   Microciantiquisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Cnt   Pert   Microciantiquisting   Pert   Thereologisting   Pert   Per				
larget_Rie_Inst_Ap_Loaklgr_Loaklgr_Perf_MirPosidptSig_Cnt_u08   target_Loaklgr_Perf_Neakrad_Cnt_enum   target_Loaklgr_Perf_Neakrad_Cnt_lege   target_Loaklgr_Perf_Toaklaval_Cnt_lge   target_Loaklgr_Perf_Toaklgr_Perf_Toaklge   target_Loaklgr_Perf_Toaklgr_Perf_Toaklge   target_Loaklgr_Perf_Toaklgr_Perf_Toaklge   target_Loaklgr_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Toaklget_Perf_Perf_Loaklgr_Perf_Toaklget_				
target_Rie_Inst_Ap_Loak/gr_Loak/gr_Pert_TioaAvail_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	nEn_Cnt_lgc	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaNc_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C	nt_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_Igc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_Igc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_Igc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_u08         4         4         4           LoaMgr_LwtrLoaMignEn_Cnt_M_Igc         1         1         1           LoaMgr_LwtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LwtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LwtrRespSV_Cnt_M_u08         5         5         5           LoaMgr_LoaRscale-furr_UIS_M_f32         500         500         500           LoaMgr_LoaRscale-furr_UIS_M_f32         0         0         0           LoaMgr_MotAglotSigSV_Cnt_M_u08         1         1         1           LoaMgr_MotAglotSigSV_Cnt_M_u08         1         1         1           LoaMgr_MotAglotSigSV_Cnt_M_u08         0         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         1         1         1           LoaMgr_TioaAvai_Cnt_M_lgc         <			С	
LoaMgr_HvTqlptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loaklyr_HwTqlcpKisjqSV_Cnt_M_u08			•	Result
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc				
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_IvitridptSigSV_Cnt_M_u08		· ·		
LoaMgr_IvtrLoaMtghEn_Cnt_M_log				
LoaMgr_IvtRespSV_Cnt_M_u08				
LoaMgr_LoaSt_State_Menum				
LoaMgr_LoaRateLimit_UispS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uis_M_f32         0         0           LoaMgr_MaxMighrResp_Cnt_M_u08         5         5           LoaMgr_MotAgLoaMtighten_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtighten_Cnt_M_lgc         1         1           LoaMgr_MotCurrloaMtighten_Cnt_M_lgc         1         1           LoaMgr_MotQurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMSrcSV_Cnt_M_u08         1         1           LoaMgr_VitgMSrcSV_Cnt_M_u08         1         1           LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_Se				<b>*</b>
LoaMgr_LoaScaleFct_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotQrudtptSigSV_Cnt_M_u08         5         5           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotOut_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum)         (170, 172, 173)         (170, 172, 173)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_A	·			•
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMdSrcsV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         500         500 ± 0.01         **           At arget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value				<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VttgMotScSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cont_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LoASt_State_enum.value         5         5	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_NtloaMgr_Sct_Cnt_M_u08         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LoASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.v	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         4           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         4           rarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         X           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         X	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_get_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc       1       1       1         LoaMgr_TloaDi_Cnt_M_lgc       1       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				~
LoaMgr_TloaDi_Cnt_M_lgc       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				~
LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_renum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0		Ti i		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)       0       0 ± 0.001       1         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value       1       1       1         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value       1       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       3         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc.value       0       0       4				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
0 = 0 = 0 = 0 = 0		5	5	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.109 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	/ toouli
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.110 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32 k_LoaMgr_RedcdStRate_UIspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LeaMar Per1 DiageStelutrIllacty Cnt Ige
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_blagcStstvt/2inactv_cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.111 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	477.259705	

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgNtotaPata_UlapS_f23	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgSmaAvoiiPesp_Crt_u08	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Crit_igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1Inactv C	Int lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Crt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum	9-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_igc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target LoaMgr Per1 MotCurrldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ul		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Nesult
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr MotAgldptSigSV Cnt M u08	0	0	- U
Louing I mound to be a continued to		0	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



```
Test Case 2: Path Test
 Specification
                                                                                                          Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                                                      CPU Cycles:

TS 5.1 5012.00 Cycles
TS 5.2 5012.00 Cycles
TS 5.3 4974.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.12 4930.00 Cycles
TS 5.15 4994.00 Cycles
TS 5.16 4969.00 Cycles
TS 5.17 4934.00 Cycles
TS 5.18 4934.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.20 4941.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.22 4908.00 Cycles
TS 5.23 4951.00 Cycles
TS 5.24 4961.00 Cycles
TS 5.24 4961.00 Cycles
                                                                                                          TS 5.24 4961.00 Cycles
                                                                                                       Vector Description:
Description
                                                                                                      TS5.1(HwTqldptSig_Cnt_T_u08 > D_MAXHWTQIDPTSIGALLWD_CNT_U08)==>True
TS5.2(MtrPosldptSig_Cnt_T_u08 > D_MAXMOTPOSNIDPTSIGALLWD_CNT_U08)==>True
TS5.3(MotCurrldptSig_Cnt_T_u08 > D_MAXMOTCURRIDPTSIGALLWD_CNT_U08)==>True
TS5.4(LoaMgr_LOASt_State_M_enum == LOAST_FADEOUT)==>True
TS5.5(LoaMgr_LOASt_State_M_enum == LOAST_SWBASDMTGTN)==>True
TS5.6(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)==>True
TS5.7(LoaMgr_LOASt_State_M_enum == LOAST_NORM)==>True
                                                                                                        TS5.8(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
                                                                                                   TS5.9/default:

LoaMgr_LOASt_State_M_enum = LOAST_FADEOUT==>True"

TS5.10(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
TS5.11((NewValFlag_Cnt_T_lgc == TRUE) ==>True && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>True
TS5.12((NewValFlag_Cnt_T_lgc == TRUE) ==>False && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.13((NewValFlag_Cnt_T_lgc == TRUE) ==>F && (k_LoaMgr_MotCurrlvtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.14((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.15((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>T
TS5.16((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.17((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>T
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>F && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.19(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.20(LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.21((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.22((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.23 (LoaMgr_LoaRateLimit_UlspS_M_f32<=0.01)=>T
TS5.24(LoaMgr_LoaScaleFctr_Uls_M_f32<=0.01)=>T
TS5.24(LoaMgr_LoaScaleFctr_Uls_M_f32<=0.01)=>F
                                                                                                        TS5.9"default:
```

Test Step 2.1 (Repeat Count = 1)	18
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978

© Report created by TESSY V3.1.12, report template V2.1

TS5.24(LoaMgr\_LoaScaleFctr\_Uls\_M\_f32<=0)=>F

2018-05-18, 14:04:23+0530



Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.00999999978		
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	0.00999999978		
k LoaMgr RedcdStSca Uls f32	0.00999999978		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	5		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	ent lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Citarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAglopMgtbEp_Cot_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr MotAgRespSV Cnt M u08	5	5	
Loamgr_motAgRespsv_Cnt_m_uu8 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	•
	0	0	•
LoaMgr_TloaAvail_Cnt_M_lgc		_	٠,
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 {170, 173, 174}	0 {170, 173, 174}	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 {170, 173, 174} {1, 1, 1}	0 {170, 173, 174} {0, 0, 0, 0, 0, 0}	2
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1}	- E
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 {170, 173, 174} {1, 1, 1}	0 {170, 173, 174} {0, 0, 0, 0, 0, 0}	N N





Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	0	0	<b>✓</b>

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.2 (Repeat Count = 1) Name	Input Value
	o input value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k LoaMgr lvtrldptSig1Resp Cnt u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k LoaMgr lvtrldptSigFltThd Cnt u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr IvtrMtgtnSca Uls f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k LoaMgr MotAgldptSig2Resp Cnt u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k LoaMgr MotAgldptSigFltThd Cnt u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
	0.0099999976
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0





Name	Input Value		
k LoaMgr MotCurrMtgtnRate UlspS f32	0.00999999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotCurrMtgtnSca Uls f32	0		
k LoaMgr RedcdStRate UlspS f32	0.00999999978		
k LoaMgr RedcdStSca Uls f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	4		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	0	0	Nesuit
LoaMgr_AgSV_Cnt_M_lgc	0	0	-
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08			-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum			-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>*</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	· ·
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Time to the second seco		×
	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1} {1, 1, 1} 500	{1, 1, 1} 500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1, 1, 1) {1, 1, 1} 500 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001	<b>*</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) (1, 1, 1) 500 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001 0	· · · · · · · · · · · · · · · · · · ·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) (1, 1, 1) 500 0 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001 0	· · · · · · · · · · · · · · · · · · ·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	(1, 1, 1) (1, 1, 1) 500 0 0 0 5	{1, 1, 1} 500 ± 0.01 0 ± 0.001 0 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) (1, 1, 1) 500 0 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001 0	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.3 (Repeat Count = 1)	X
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	0
LoaMgr MotAgRespSV Cnt M u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	- Ttoouit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	-
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	-
LoaMgr_MotAglaptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	5	-
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08			-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>Y</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>Y</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	3
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDirkesp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32 k_LoaMgr_RedcdStRate_UIspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LasMar Bord DiagoStelltrd Insety Cet Ign
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	3	3	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	3	3	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	3	3	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.5 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgSmgAygilRoan_Cat_u08	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k LoaMgr MotCurrMtgtnSca Uls f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_IvtrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr Peri_LoASt State_enum	target_LoaMgr_Per1_IVtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum	go	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cl	nt lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0		×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LUAIVIUL MOLAGIODISIOSV CIII M. 1108			_

1

0

1

0

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
CalclvtrState	2	CalclvtrState	2	~			
LatchInputs	1	LatchInputs	1	~			
RqstRespConds	1	RqstRespConds	1	-			
RqstHwTqResp	1	RqstHwTqResp	1	•			
ArbResp	1	ArbResp	1	~			
ChooseFinalResp	1	ChooseFinalResp	1	~			
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~			
AssignScale	1	AssignScale	1	~			
SwBasMtgtn	1	SwBasMtgtn	1	~			
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~			
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~			
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~			
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~			

Test Step 2.6 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1





	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	le	I
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
Landara Uha Talahat Oia OM Cart. M. 1900	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 1 1	1 1 1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 1 1 0	1 1 1 0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 1 0	1 1 1 0 1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 0 1	1 1 1 0 1	~
LoaMgr_HwTqLoaMtgtnEn_cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 0 1 1	1 1 1 0 1 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 1 0 1	1 1 1 0 1 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 1 1 0 1 1 1	1 1 1 0 1 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 1 0 1 1 1 1	1 1 1 0 1 1 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc	1 1 1 0 1 1 1 1 1 1	1 1 1 0 1 1 1 1 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08	1 1 1 0 1 1 1 1 1 1 0 0	1 1 1 0 1 1 1 1 1 1 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 1 1 0 1 1 1 1 1 1 0 0	1 1 1 0 1 1 1 1 1 1 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 1 1 0 1 1 1 1 1 1 0 0 0	1 1 1 0 1 1 1 1 1 1 0 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	1 1 1 0 1 1 1 1 1 1 0 0 0	1 1 1 0 1 1 1 1 1 1 0 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 1 1 0 1 1 1 1 1 1 0 0 0 0	1 1 1 0 1 1 1 1 1 1 0 0 0 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 1 1 0 1 1 1 1 1 1 0 0 0 0 0	1 1 1 0 1 1 1 1 1 1 0 0 0 0 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 1 1 0 1 1 1 1 1 1 1 0 0 0 0 0 0 1 1 0 1 1 1 1 {173, 174}	1 1 1 0 1 1 1 1 1 0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 1 1 0 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173, 174} {1, 1} {1, 1}	1 1 1 0 1 1 1 1 1 0 0 0 0 0 0 1 1 1 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleEtimit_UlspS_M_f32 LoaMgr_LoaScaleEctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 1 1 0 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173, 174} {1, 1} 1	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1 1 1 1 0 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173, 174} {1, 1} {1, 1} 1	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 1 1 1 0 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173, 174} {1, 1} 1 1 0 0	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1 1 1 1 0 1 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173,174} {1,1} {1,1} 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAglcoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 1 1 0 1 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173,174} {1,1} 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1 1 1 1 0 1 1 1 1 1 1 1 1 1 0 0 0 0 0 1 1 1 1 {173,174} {1,1} {1,1} 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1.
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1l	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2li		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cni		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_lytrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	Ž
LoaMgr_LOASt_State_M_enum		i i	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0		
LoaMgr_MotAgldptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	<b>•</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>*</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>•</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>*</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	0	0	<b>*</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Injust   Injus	Test Step 2.8 (Repeat Count = 1)	13
Looking   American   American		
Looking   Information   Dr. III   Jpc		
Loading - Intelligency Cord, July 80		1
Landing - Mendenghage Cort M. 108  Looking - Menden	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landing Medical May 100 M 100	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Loading   Junking George   Care   June	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking   Markelpring   Corp. M. pts	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Coadags   MacAge analoge   Corp.   Ug   Sec.		
Loading MacChard (1985) Core M. 108  Loading MacChard (1985) Core M. 108  Loading MacChard (1985) Core M. 108  Loading Serial MacChard (1985) Core M. 108  Loading Serial MacChard (1985) Core M. 108  Loading Serial MacChard (1985) Core M. 108  Till Loading Loadin (1985) Core M. 108  Loading Loadin (1985) Core M. 108		
Loading / McContradigners Cott / M. Dis		1
Leading Morbins and Paris County County (1996)  Leading Seminisheria Aprell CM, Muge  Leading Veryindrocky CM, Muge  1  Reg Inst. Ap. Leading  Leading Veryindrocky CM, Muge  1		
Loadley_LoydesSec_Con_Lo_Uol   Res Ireat_Ac_Loadley_Con_Lo_Uol   Res Ireat_Ac_Loadley_Con_Loadley_Con_Loadley    Loadley_ContrivitingSec_Con_Loadley    Loadley_ContrivitingSec_Con_Loadley    Loadley_ContrivitingSec_Con_Loadley    Loadley_ContrivitingSec_Con_Loadley    Loadley_ContrivitingSec_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadley_Loadley_Con_Loadley    Loadley_Loadle	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loadley_Carly Agriculture   Loadley_Carly Loadley   Loadley_Carly Loadley   Loadley_Carly Loadley   Loadley_Carly Loadley   Loadley_Carly Loadley   Loadley_Carly Loadley   Loadley_Carly Loadley_Carly Loadley   Loadley_Carly	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Restands   Lander	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Linable_DriftonthingTisca_Use_Driftonthing	Rte_Inst_Ap_LoaMgr	V =
K. Loadey_HwTopicsSg2TranDirects_CPU_UBS		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
L. LosAgr_MridginSea Dis_18_22	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loadig_NvtightSigResp_Crt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_Intringtsig2Resp_Cnt_U08		
L. Loukings / Intriducising Fittind C. Cnt. U08		
k_Looklogr_IvrinfignRate_UispS_132         1           k_Looklogr_IvrinfignScac_UispS_2         0           k_Looklogr_IvrinfignScac_UispS_2         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         5           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         1           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0 <t< td=""><td></td><td></td></t<>		
K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         5           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         0           K, LouMgr_MoNajdipSigNSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         0           K, LouMgr_MoNajdipSissa_Vis_IS2         1           K, LouMgr_MoNajdipSissa_Vis_IS2         0           K, LouMgr_MoCurridptSigResp_Cnt_U08         1           K, LouMgr_MoCurridptSigResp_Cnt_U08         0           K, LouMgr_MoCurridptSigResp_Cnt_U08         0           K, LouMgr_MoCurridptSigResp_Cnt_U08         1           K, LouMgr_MoCurridptSigResp_Cnt_U08         1           K, LouMgr_MoCurridptSig_Cnt_U08         1           K, LouMgr_MoCurridptSig_Cnt_U08         1           K, LouMgr_MoCurridptSig_Cnt_U08 <td< td=""><td></td><td></td></td<>		
LoaMgr MorAgidptSig3Resp_Cnt_U08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         2           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         1           k_LoaMgr_MorAgMignRate_UBsp5_f22         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Sf122         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig2Resp_Sf122         1           k_LoaMgr_MorCurridptSig2Resp_Sf22         1           k_LoaMgr_RedodSf3Rea_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Rea_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Rea_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Rea_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Rea_Ulsp_Sf22         1           k_LoaMgr_Ref_Disp_Sf22         1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldplSigFitTnd_Cnt_u08         2           K_LoaMgr_MotAghtghtRate_UlspS_132         1           K_LoaMgr_MotAghtghtScace_Cnt_crt_ge         0           K_LoaMgr_MotAghtghtScace_Uls_132         1           K_LoaMgr_MotAghtghtScace_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         1           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_lgc         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_lgc         1           K_LoaMgr_RedcdSiRat_UlspS_132         1           K_LoaMgr_RedcdSiRat_UlspS_132         1           K_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         0           Larget_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         0           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         0           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         0           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         0           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         0	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
K_LoaMgr_MotAyMightRate_UlspS_f32         1           K_LoaMgr_MotAyMightSac2renEn_Cnt_igc         0           K_LoaMgr_MotAyMightSac2renEn_Cnt_igc         1           K_LoaMgr_MotAyMightSac2lists_igc         1           K_LoaMgr_MotAyMightSac2lists_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Cnt_iu08         0           K_LoaMgr_MotCurridptSigPasp_Sig2         1           K_LoaMgr_MotCurridptSigPasp_Sig2         1           K_LoaMgr_MotCurridptSigPasp_Sig2         1           K_LoaMgr_RedcdSiRate_ulsp_Sig2         1           K_LoaMgr_Pert_indpasp_Sig2         1           K_LoaMgr_Pert_indpasp_Sig2         1           K_LoaMgr_Pert_indpasp_Sig2         1           Larget_LoaMgr_Pert_indpasp_Sig2_Cnt_uo8 value         0           Larget_LoaMgr_Pert_indpasp_Sig2_Cnt_uo8 value         1           Larget_LoaMgr_Pert_indpisp_Cnt_uo8 value         1           Larget_LoaMgr_Pert_indpasp_Cnt_indpusp_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_indps_sig_cnt_	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtgthSca_ZeroEn_Cn_Igc         0           K_LoaMgr_MotAgMtgthSca_Uls_132         1           K_LoaMgr_MotAgMtgthSca_Uls_132         1           K_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFilThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFilThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFilThd_Cnt_u08         1           K_LoaMgr_MotCurridptSigGreep_Cnt_u08         1           K_LoaMgr_MotCurridptSigGreep_Cnt_u08         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_Perl_DiagcSistivtrInact_Cnt_lgc_value         0           Larget_LoaMgr_Perl_DiagcSistivtrInact_Cnt_lgc_value         0           Larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1<	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMiginSca_Ulis_f32         1           k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value         0           target_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_Tinactile_Cnt_gc value         1           target_LoaMgr_Pert_Tinactile_Cnt_gc value         1 <t< td=""><td>k_LoaMgr_MotAgMtgtnRate_UlspS_f32</td><td></td></t<>	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurdptSig0Resp_Cnt_u08         1           k_LoaMgr_MotCurrdptSig0Resp_Cnt_u08         1           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1FIThd_Cnt_u08         0           k_LoaMgr_MotCurrMgtnSigFIThd_Cnt_u08         0           k_LoaMgr_MotCurrMgtnSca_Uls_52         1           k_LoaMgr_MotCurrMgtnSca_Uls_52         1           k_LoaMgr_MotCurrMgtnSca_Uls_52         1           k_LoaMgr_ReddSiSca_Uls_52         1           k_LoaMgr_ReddSiSca_Uls_52         1           k_LoaMgr_Pert_DiagcSistvtr1inactv_Cnt_igc_value         0           target_LoaMgr_Pert_DiagcSistvtr2inactv_Cnt_igc_value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         2           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_TioaOvail_Cnt_igc_value         1           target_LoaMgr_Pert_TioaOvail_Cnt_igc_value         1           target_LoaMgr_Pert_TioaOvail_Cnt_igc_value         1           target_LoaMgr_Pert_TioaOvail_Cnt_igc		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRate_UlspS_f32         1           k_LoaMgr_MotCurridptSig_Cnt_u52         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Pert_Desc_Stslvtr_Uds_value         0           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         1           target_Re_Inst_Ap_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Pert_HvTqldptSig_Cnt_u08           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HvTqLoaMtgrEn_Cnt_lgc <td< td=""><td></td><td></td></td<>		
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRste_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSca_Uls_152         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_132         1           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrIdptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum_value         2           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Per1_DaigcStsivtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTq		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSlSca_Uls_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TinaOavail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOavail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOavail_Cnt_lgc.value         1           target_Rel_inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMignEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMignEn_Cnt_lgc         target_LoaMgr_Per1_IvtrLoaMignEn_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc         target_LoaMg		
k_Loaklgr_MotCurridptSigFitThd_Cn_u08         0           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRisca_Uls_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_Loaklgr_Per1_SteerMod_Cnt_enum.value         2           target_Loaklgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08         target_Loaklgr_Per1_HvTqldptSig_Cnt_u08           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_lgc         target_Loaklgr_Per1_HvTqLoakltgtnEn_Cnt_lgc           t		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_ToaDvall_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDvall_Cnt_lgc.value         1           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc         ta		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 2 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SterMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMgr_Per1_	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtEn_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSudptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSudptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_u08 target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_u08 target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgt_Enc_nt_lgc target_LoaMgr_Per1_LoaSc_State_enum target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgEnc_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgEnc_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgEnc_Cnt_Uo8		
target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_Inactv_Dert_Inactv_		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.9 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 14:04:23+0530

LoaMgr\_Per1



Nama	Immut Value		
Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k LoaMgr lvtrldptSig1Resp Cnt u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt Igc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Ent Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>~</b>

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>~</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





		•	
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnSca_Ula_623	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target LoaMgr Per1 TloaAvail Cnt lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	.80u	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	From a set of Malara	Daniel
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	×
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	· ·
LoaMgr_TloaAvail_Cnt_M_lgc	1	0	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	
Louisign_vitgividoroov_ont_ivi_uoo		•	
Rte Call An LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)		{173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{173, 174}	{173, 174} {0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{173, 174} {1, 1}	{0, 0, 0, 0, 0}	**************************************
	{173, 174}		×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{173, 174} <b>{1, 1}</b> <b>{1, 1</b> }	{0, 0, 0, 0, 0} {1, 1}	¥ <b>∀</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{173, 174} <b>{1, 1}</b> <b>{1, 1</b> } 500	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	**************************************
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	{173, 174} <b>(1, 1)</b> {1, 1} 500 1	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	**************************************
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	{173, 174} (1, 1) {1, 1} 500 1 0	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	{173, 174}  (1, 1)  {1, 1}  500  1  0  1	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	¥ ×



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	<b>~</b>
AssignScale	1	AssignScale	1	<b>✓</b>
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt Igc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnRate_Olsp3_132	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_IvtrMtgtnSca_UIs_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	·
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrNdptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	





LoaMgr_AgsV_Cnt_M_lgc         0         1         1           LoaMgr_HwTqldptSigsV_Cnt_M_u08         1         1         1           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         1         1         1           LoaMgr_IvtrloptSigSV_Cnt_M_u08         1         1         1           LoaMgr_IvtrRespSV_Cnt_M_u08         0         0         0           LoaMgr_LoASt_State_M_enum         2         2         2           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         1	Loalvigi_r er i			( CILC   COID
target_Loaking_Pert_DiagoStstvt/Zinactv_Cnt_igc value   2	Name	Input Value		
target_LoaMgr_Per1_HwTqlqptSig_Cnt_u08 value	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_MrtPostiptSig_Ont_u08 value	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
Target_LoaMgr_Per1   MirPoslotpiSig_Cnt_u08.value   1   1   1   1   1   1   1   1   1	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Perf_SteerMod_Cnt_enum_value	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Peri_TloaD_Cnt_Igc.value	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc_value	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagoStstvtr1Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagoStstvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagoStstvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqloaMtghEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_Int_CoaMtghEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_LoASt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_LoASt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_LoASt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MotSc_Cnt_U08 target_Re_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_St_Inst_Ap_LoaMgr_Pert_MotSc_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Pert_St_Inst_Ap_LoaMgr_Pert_St_Inst_Ap_LoaMgr_Pert_Inst_Ap_Inst_	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_Re_Inst_Ap_LoaMgr_Pert_DiagcStstvtr2Inactv_Cnt_Igc   target_LoaMgr_Pert_DiagcStstvtr2Inactv_Cnt_Igc   target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqLoaMtghEn_Cnt_Igc   target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqLoaMtghEn_Cnt_Igc   target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_IvtrLoaMtghEn_Cnt_Igc   target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMigr_Pert_HwTqldptSig_Cnt_u08         target_LoaMigr_Pert_HwTqldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMigr_Pert_HwTqLoaMitghEn_Cnt_Igc         target_LoaMigr_Pert_HwTqLoaMitghEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMigr_Dert_HwTqLoaMitghEn_Cnt_Igc         target_LoaMigr_Pert_LOASt_State_enum           target_Rte_Inst_Ap_LoaMigr_LoaMigr_Pert_LoASt_State_enum         target_LoaMigr_Pert_LoaScaDi_Cnt_Igc           target_Rte_Inst_Ap_LoaMigr_Dert_MotAgLoaMigr_Dert_Cnt_Igc         target_LoaMigr_Pert_MotAgLoaMigr_Dert_MotAgLoaMigr_Dert_MotCurrIdptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMigr_Dert_MotCurrIdptSig_Cnt_u08         target_LoaMigr_Pert_MotCurrIdptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMigr_Dert_MotCurrIdptSig_Cnt_u08         target_LoaMigr_Pert_MotCurrIdptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMigr_Dert_SteerMod_Cnt_enum         target_LoaMigr_Pert_TitoAdvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMigr_Dert_TitoAdvail_Cnt_Igc         target_LoaMigr_Pert_TitoAdvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMigr_Dert_TitoAdvail_Cnt_Igc         target_LoaMigr_Pert_TitoAdvail_Cnt_Igc           target_LoaMigr_Pert_TitoAdvail_Cnt_Igc         target_LoaMigr_Pert_TitoAdvail_Cnt_Igc           Name         Actual Value         Expected Value         Result_Advail_Cnt_Igc           LoaMigr_HwTiqRespSV_Cnt_M_Igc         0         1         1           LoaMigr_HwTiqRespSV_Cnt_M_Igc         1         1         1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr	1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perr_IntrLoaMtgnEn_Cnt_Igc   target_LoaMgr_Perr_IntrLoaMtgnEn_Cnt_Igc   target_Rte_Inst_Ap_LoaMgr_Perr_IntrLoaMtgnEn_Cnt_Igc   target_LoaMgr_Perr_LoASt_State_enum   target_Rte_Inst_Ap_LoaMgr_Perr_LoAScabi_Cnt_Igc   target_LoaMgr_Perr_LoAScabi_Cnt_Igc   target_LoaMgr_Perr_LoAScabi_Cnt_Igc   target_LoaMgr_Perr_LoaScabi_Cnt_Igc   target_LoaMgr_Perr_MotQLoaMgr_Perr_IndaAvail_Cnt_Igc   target_LoaMgr_Perr_IndaAvail_Cnt_Igc   target_LoaMgr_Perr_Ind	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2	2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASt_State_enum         target_LoaMgr_Pert_LOASt_State_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_LOAScaDi_Cnt_Igc         target_LoaMgr_Pert_LoASt_State_enum           target_Rte_Inst_Ap_LoaMgr_Pert_Moral_CoaMtgn=Pert_Moral_CoaMtgn	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtghtEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosidptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDi_Cnt_Igc target_LoaMgr_Per1_ToaDi_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtghtEn_Cnt_Igc target_LoaMgr_Per1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtq	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc         target_LoaMgr_Per1_LoaScaDi_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdpSig_Cnt_u08         target_LoaMgr_Per1_MotCurrIdpatMtgtnEn_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPostlqtSig_Cnt_u08         target_LoaMgr_Per1_MtrPostlqtSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPostlqtSig_Cnt_u08         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_ToaAvail_Cnt_Igc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc         target_LoaMgr_Per1_ToaDi_Cnt_Igc           Name         Actual Value         Expected Value         Resul           LoaMgr_AgsV_Cnt_M_Igc         0         1         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         1         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         1         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08         1         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08         1         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08         0         0         0           LoaMgr_LoaRstel_init_Ulsps_M_f32         500         500         0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtginEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value  Expected Value  Resul LoaMgr_HwTqtlopHSigSV_Cnt_M_u08  1  LoaMgr_HwTqtoAMtginEn_Cnt_M_Igc  0  0  1  LoaMgr_HwTqtoAMtginEn_Cnt_M_Igc  1  LoaMgr_HwTqtdpHSigSV_Cnt_M_u08  1  LoaMgr_IvtrloaMtginEn_Cnt_M_Igc  1  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstel_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_LoaRstet_Imit_UlspS_M_f32  LoaMgr_MotAgldptSigSV_Cnt_M_u08  1  LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State	e_enum	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_MtrPoslotptSigc	target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Mame  Actual Value  Expected Value  Resul LoaMgr_AgSV_Cnt_M_Igc  0  1  LoaMgr_HwTqloptSigsV_Cnt_M_u08  1  LoaMgr_HwTqloaMtgtnEn_Cnt_M_Igc  0  0  0  0  0  0  0  0  0  0  0  0  0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_MtrPosldptSig_Cnt_u08         target_LoaMgr_Perl_MtrPosldptSig_Cnt_u08           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_SteerMod_Cnt_enum         target_LoaMgr_Perl_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_TloaAvail_Cnt_Igc         target_LoaMgr_Perl_TloaAvail_Cnt_Igc           Name         Actual Value         Expected Value         Resul           LoaMgr_AgsV_Cnt_M_Igc         0         1         2           LoaMgr_HwTqLoaMtghEn_Cnt_M_lgc         0         4         2           LoaMgr_HwTqRespSV_Cnt_M_u08         1         1         4         4           LoaMgr_lwTlOaMtghSigSV_Cnt_M_u08         1         1         4         4           LoaMgr_lwTlOaMtghEn_Cnt_M_u08         1         1         4         4           LoaMgr_lwTrRespSV_Cnt_M_u08         1         1         4         4           LoaMgr_lvTrRespSV_Cnt_M_u08         0         0         4         4           LoaMgr_lvTrRespSV_Cnt_M_u08         0         0         4         4           LoaMgr_loASt_State_M_enum         2         2         2         4           LoaMgr_LoaScaleFctr_Uls_M_f32         500         500         4           LoaMgr_MaxMtgtnResp_Cnt_M_u08         1         1         4 <td>target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08</td> <td>target_LoaMgr_Per1_MotCurrIdptS</td> <td>Sig_Cnt_u08</td> <td></td>	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptS	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value         Resul           LoaMgr_AgSV_Cnt_M_lgc         0         1         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         1         1         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0         0         0         0           LoaMgr_InvTqDespSV_Cnt_M_u08         1         1         0         0           LoaMgr_IvrtLoaMtgtnEn_Cnt_M_u08         1         1         0         0           LoaMgr_IvrtLoaMtgtnEn_Cnt_M_lgc         1         1         0         0           LoaMgr_IvrtRespSV_Cnt_M_u08         0         0         0         0         0           LoaMgr_LOASt_State_M_enum         2         2         0         0         0         0           LoaMgr_LoaScaleFctr_Uls_M_f32         500         500         0         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2         2         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         1         1         1         0         0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoal	//dtgtnEn_Cnt_lgc	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaAvail_Cnt_lgc           Name         Actual Value         Expected Value         Result           LoaMgr_AgsV_Cnt_M_lgc         0         1         3           LoaMgr_HwTqldptSigsV_Cnt_M_u08         1         1         4           LoaMgr_HwTqLoaMtgnEn_Cnt_M_lgc         0         0         4           LoaMgr_HwTqLoaMtgnEn_Cnt_M_u08         1         1         4           LoaMgr_IvtrloptSigsV_Cnt_M_u08         1         1         4           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         1         1         4           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         1         1         4           LoaMgr_LoaSt_State_M_enum         2         2         2           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptS	g_Cnt_u08	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value         Result           LoaMgr_AgsV_Cnt_M_lgc         0         1         0           LoaMgr_HwTqldptSigsV_Cnt_M_u08         1         1         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0         0         0           LoaMgr_IvtrloptSigsV_Cnt_M_u08         1         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_IvtrRespSV_Cnt_M_u08         0         0         0           LoaMgr_LoASt_State_M_enum         2         2         2           LoaMgr_LoaScaleFctr_Uls_M_f32         500         500         500           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_C	nt_enum	
Name         Actual Value         Expected Value         Result           LoaMgr_AgsV_Cnt_M_Igc         0         1         1           LoaMgr_HwTqldptSigsV_Cnt_M_u08         1         1         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc         0         0         0           LoaMgr_IvtrldptSigsV_Cnt_M_u08         1         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc         1         1         1           LoaMgr_IvtrRespSV_Cnt_M_u08         0         0         0           LoaMgr_LOASt_State_M_enum         2         2         2           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Ci	nt_lgc	
LoaMgr_AgsV_Cnt_M_Igc         0         1           LoaMgr_HwTqldptSigSV_Cnt_M_u08         1         1           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         1         1           LoaMgr_IvtrloptSigSV_Cnt_M_u08         1         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc         1         1           LoaMgr_IvtrRespSV_Cnt_M_u08         0         0           LoaMgr_LOASt_State_M_enum         2         2           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_	lgc	
LoaMgr_HwTqldptSigSV_Cnt_M_u08       1       1         LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_HwTqRespSV_Cnt_M_u08       1       1         LoaMgr_IvtrloptSigSV_Cnt_M_u08       1       1         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1       1         LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	Name	Actual Value	Expected Value	Result
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_HwTqRespSV_Cnt_M_u08       1       1         LoaMgr_IvtrloptSigSV_Cnt_M_u08       1       1         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1       1         LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqRespSV_Cnt_M_u08       1       1         LoaMgr_IvtrIdptSigSV_Cnt_M_u08       1       1         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1       1         LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_IvtrIdptSigSV_Cnt_M_u08       1       1         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1       1         LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1       1         LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08       0       0         LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_LOASt_State_M_enum       2       2         LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32       500       500         LoaMgr_LoaScaleFctr_Uls_M_f32       1       1         LoaMgr_MaxMtgtnResp_Cnt_M_u08       2       2         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1	LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32         1         1           LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1	LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08         2         2           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1	LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08 1	LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
	LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	<b>✓</b>
LoaMar MotAglicaMtotnEn Cot Milac 0	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓

LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	-
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	_





Section   Sect	Test Step 2.12 (Repeat Count = 1)	8
andag _ Amprignations		
andler printingstraging Co. M. Ju 30 andler prin		·
analog Juniforgiosign Cruft Music  analog Juniforgi	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
analog _ Michaelphony_Cort_M_ value  analog_ Michaelphony_Cort_M_	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
analog Junice Sattegrini, Crit M. Ug  ability Individuality Critical Individuality Crit M. Ug  ability Indiv	LoaMgr_HwTqRespSV_Cnt_M_u08	1
analog _	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
analog Muchagnesips Cort Mu 106 analog Muchagnesips Cort Muchagnesips Cort Mu 106 analog Muchagnesips Cort Muchagnesips Cort Mu 106 analog Muchagnesips Cort Muchagnesips Cort Mu 106 analog M	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
. analogy . Managle gendingeries . Come M My Los 6		-
auditeg MacAlignesia C nil. M y y y y y y y y y y y y y y y y y y		
		1
Conding Modification (Conting Marian)   Conting Marian (Conting Marian)		
Coading Contribinity (Co. 1)   Coading Contribinity (Co. 1)   Coading Contribinity (Co. 1)   Coading Contribinity (Co. 1)   Coading Contribinity (Coading Contribinity (Coading Contribinity (Coading Contribinity (Coading Contribinity (Coading Coading Co	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Canaday   Christophics   Canaday	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Image   Tell   Junity   Juni	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landarg_cutvirialingsissa_bean_cot_upe	LoaMgr_VltgMdSrcSV_Cnt_M_u08	3
Clasking	Rte_Inst_Ap_LoaMgr	V =
Clandary Febro Carlos Beau Usas S. 192		
Laadig   Michightspit Total-waitesp. Cnt   USB		
Landary Hartipatispin Tomokanego, Cht. U08		
Loading Interfoliation Styl Transcription Cut Lugar   Loading Interfoliation Cut Lugar   Loading		
Loading   M-17 offst Sig2Thank Parks   Cut_U08		
Loading   Mind project SQCT beath Resp. Cort. URS	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loading: Mind publish piles p. Cnt. UB8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
Loaking   MarToMorginate   UspS   132	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loading	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
CLoaking   WirtightSig   Resp. Cnt. U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Clasking   Invitigisting   Resp. Cnt_U8	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Cloaking_Ministry_Crit_Un8		
Loaking   WritightSigiFiFTIM   Crit U89		
Cloaking   WritingtonRate_UlspS_R2		
Cloaking		
Cloaking		
Loaklgr_MotAgldpSigtNoSmaResp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
CloaMgr_MotAgloptSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
CLoaMgr_MotAgldpiSigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghrRate_UlspS_132	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
CLoaMgr_MotAgMigtnScaZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
CLoaMgr_MotAgStmaAvailResp_Cnt_u08		
CloaMgr   MotCurridptSigOResp   Cnt_U08   2   2   2   2   2   2   2   2   2		·
LoaMgr_MotCurridptSig0Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_MotCurridptSig2SaZeroEn_Cnt_gc LoaMgr_RedcdSiRate_UlspS_f32 LoaMgr_RedcdSiRate_UlspS_f32 LoaMgr_RedcdSiRate_UlspS_f32 LoaMgr_RedcdSiSaz_Uls_f32 LoaMgr_RedcdSiSaz_Uls_f32 LoaMgr_Per1_DiagcStstvfr1Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStstvfr2Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStstvfr2Inactv_Cnt_lgc.value 1 arget_LoaMgr_Per1_DiagcD_Cnt_lgc.value 1 arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 2 arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_ToaDi_Cnt_lgc.value 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMtghEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_LoaMgr_Per1_HvTqLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMtghEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurridptSig_Cn		
C_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           C_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         1           C_LoaMgr_MotCurridptSig2Resp_Cnt_u05g         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_MotCurridptSig2SezoreIn_Cnt_lgc         1           C_LoaMgr_RedodSitRet_u1bg5_g2         1           C_LoaMgr_RedodSitRet_u1bg5_g2         1           C_LoaMgr_RedodSitRet_u1bg5_g2         1           C_LoaMgr_Perl_DiagcStsivtr1lnactv_Cnt_lgc_value         0           arget_LoaMgr_Perl_DiagcStsivtr2loactv_Cnt_lgc_value         1           arget_LoaMgr_Perl_SteerMod_Cnt_enum_value         0           arget_LoaMgr_Perl_ToaAvail_Cnt_lgc_value         0           arget_LoaMgr_Perl_ToaAvail_Cnt_lgc_value         1           arget_LoaMgr_Perl_ToaAvail_Cnt_lgc_value         1           arget_LoaMgr_Perl_ToaAvail_Cnt_lgc_value         1           arget_LoaMgr_Perl_ToaAvail_Cnt_lgc_val		-
LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           C_LoaMgr_MotCurriMgtnSeaPulspS_f32         1           C_LoaMgr_MotCurriMgtnSeaZeroEn_Cnt_lgc         1           C_LoaMgr_MotCurriMgtnSea_Uls_f32         1           C_LoaMgr_RedcdSiRate_UlspS_f32         1           C_LoaMgr_RedcdSiRate_UlspS_f32         1           arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc value         0           arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc value         0           arget_LoaMgr_Per1_MrqlqbtSig_Cnt_u08 value         2           arget_LoaMgr_Per1_MrdlcurldptSig_Cnt_u08 value         1           arget_LoaMgr_Per1_MrtPosldptSig_Cnt_u08 value         0           arget_LoaMgr_Per1_MrtPosldptSig_Cnt_u08 value         1           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         0           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         0           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         1           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         0           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         1           arget_LoaMgr_Per1_ToaAvail_Cnt_lgc value         1           arget_Rie_Inst_Ap_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc         1           arget_Rie_Inst_Ap_LoaMgr_Per1_HurqlqbIdfSig_Cnt_u08         1 <tr< td=""><td></td><td></td></tr<>		
C_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08         0           C_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           C_LoaMgr_MotCurrMtgtnSca_Uls f32         1           C_LoaMgr_RedcdStRate_UlspS_f32         1           C_LoaMgr_RedcdStRate_UlspS_f32         1           C_LoaMgr_RedcdStRate_UlspS_f32         1           C_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc.value         0           arget_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc.value         0           arget_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc.value         0           arget_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         1           arget_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         1           arget_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         0           arget_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         1           arget_LoaMgr_Perl_SteerMod_Cnt_enum.value         0           arget_LoaMgr_Perl_ToaPid_Clg.cvalue         0           arget_Rel_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc           arget_Rel_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc           arget_Rel_Inst_Ap_LoaMgr_LoaMgr_Perl_HvTQLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Perl_HvTQLoaMtgtnEn_Cnt_lgc           arget_Rel_Inst_Ap_LoaMgr_LoaMgr_Perl_LOASt_State_enum         target_LoaMgr_Perl_LoaSt_State_	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	
LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
\LoaMgr_MotCurrMigtnSca_Uls_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_RedcdStRate_UlspS_f32 \ \LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc.value \ \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_HoaScaDi_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value} \ \text{arget_LoaMgr_Per1_InaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_TinaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_TinaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_TinaAvail_Cnt_lgc.value} \ \text{arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc} \ \text{arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_MotGurrldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_MotGurrldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_MotGurrldptSig_Cnt_u08} \ \text{arget_LoaMgr_Per1_MotGurrldptSig_Cnt_u08}	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
\( \LoaMgr_RedcdSiRate_UispS_f32 \) \( \LoaMgr_RedcdSiSca_Uis_f32 \) \( \LoaMgr_RedcdSiSca_Uis_f32 \) \( \taget_LoaMgr_Perl_DiagcStsitvfr1lnactv_Cnt_igc.value \) \( \taget_LoaMgr_Perl_DiagcStsitvfr2lnactv_Cnt_igc.value \) \( \taget_LoaMgr_Perl_DiagcStsitvfr2lnactv_Cnt_igc.value \) \( \taget_LoaMgr_Perl_HwTqldptSig_Cnt_u08.value \) \( \taget_LoaMgr_Perl_MctCurridptSig_Cnt_u08.value \) \( \taget_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value \) \( \taget_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value \) \( \taget_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value \) \( \taget_LoaMgr_Perl_TioaAvail_Cnt_igc.value \) \( \taget_LoaMgr_Perl_TioaAvail_Cnt_igc.value \) \( \taget_LoaMgr_Perl_TioaAvail_Cnt_igc.value \) \( \taget_LoaMgr_Perl_TioaAvail_Cnt_igc.value \) \( \taget_LoaMgr_Perl_TioaDigc.value \) \( \taget_LoaMgr_Perl_TioaDigc.value \) \( \taget_LoaMgr_LoaMgr_LoaMgr_Perl_DiagcStsivtr1Inactv_Cnt_igc \) \( \taget_LoaMgr_Perl_DiagcStsivtr2Inactv_Cnt_igc \) \( \taget_LoaMgr_Perl_DiagcStsivtr2Inactv_Cn	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
LoaMgr_RedcdStSca_Uls_f32  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  arget_LoaMgr_Per1_HwTqldptSig_Cnt_uo8 value  2  arget_LoaMgr_Per1_MctCurrldptSig_Cnt_uo8.value  2  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_uo8.value  3  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_uo8.value  4  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_uo8.value  5  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  6  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  7  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  8  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  9  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_uo8  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_uo8  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_uo8  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  ar	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 2 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_NoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_NoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_LoaMgr_Per1_HwTqlotSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCur		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Net_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Net_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Net_Inst_Ap_LoaMgr_LoaMgr_Per1_Int_LoaMtgtnEn_Cnt_lgc target_Net_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Net_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LOASt_State_enum 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqlotPisig_Cnt_u08  target_LoaMgr_Per1_HwTqlotPisig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		1 - 1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1, 1}</b>	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





			_
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k LoaMgr MotAgSmaAvailResp Cnt u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Ont lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0i		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	90	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScab1_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Crit_ige target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurridptSig Cnt u08	target LoaMgr Per1 MotCurrldptSig Cnt i		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08			
	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ul		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	l=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>~</b>

target_inte_inst_Ap_Edaingr.Edaingr_Ferr_ndabt_Crit_igc	target_Loawigi_Feri_floaDi	target_Loamgi_Ferr_Tioadi_Crit_ige		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc			*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>	
LoaMgr_LOASt_State_M_enum	2	2	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	✓	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	

2018-05-18, 14:04:23+0530



LoaMgr\_Per1

Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓

Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus





Edaingi_i ei i			(-4
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvt	r1Inactv_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSi		
rarget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_Sta		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdpt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt		
Name	Actual Value	Expected Value	Res
LoaMgr_AgSV_Cnt_M_lgc	0	1	1100
LoaMgr HwTqldptSigSV Cnt M u08	1	1	
· · · · · ·	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr HwTqRespSV Cnt M u08	1	1	
	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32		1500	
_oaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2	1 2	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 2 1	1 2 1	
LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 2 1 0	1 2 1 0	
LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 2 1 0 0	1 2 1 0	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 2 1 0 0 0 0 0 0	1 2 1 0 0	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 2 1 0 0 0 0 1 1	1 2 1 0 0 0	
LoaMgr_LoaScaleFct_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	1 2 1 0 0 0 0 1 1 2	1 2 1 0 0 0 0 1 1 2	
LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	1 2 1 0 0 0 0 1 1 2 0 0	1 2 1 0 0 0 0 1 1 2 0 0	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	1 2 1 0 0 0 0 1 1 2 0 0 1	1 2 1 0 0 0 0 1 1 2 0 0 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1 {173, 174}	1 2 1 0 0 0 1 2 0 1 1 1 1 {173, 174}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174}	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174} {1, 1}	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1}	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1 1	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1 1	1 2 1 0 0 0 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1 1	1 2 1 0 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_lwtrLoaMtgtnEn_Cnt_lgc.value	1 2 1 0 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1	1 2 1 0 0 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 2 1 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1 1	1 2 1 0 0 0 0 0 1 1 2 0 0 1 1 1 1 1 1 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Imput Value	Test Step 2.15 (Repeat Count = 1)	<b>□</b>
Loadyr_New Total Magnetics   Magnetics		
Loader_ JavriguesgaSV_Crit_M_U08		·
Loaking_ Invitages(Sey, Cent, M_ UB)		
Loading   Participage SV, Crit. M, USB		
Loaking_ Invited Selecy Cort_M_ USB		
Loaking_thritionMignific_Cit_M_Lips		
LoaMagr_MoRajgloStyCort_M_U08		
LoaMagr MAAgdeSigSV_CRI_M_UBS	1	
Loaking_MAPA_DaskingInEn_Crit_M_Ugs		*
Loaking		
LoaMgr McCurrispisigsV _ Crit_M _ u08		
LoaMgr MoCurrLoaMtgnEnE_Cnt_M_LoB   LoaMgr MoCurrLoaMtgnEnE_UM_LoB   LoaMgr MocurrLoaMtgnEnE_UM_LoB   LoaMgr MocurrLoaMtgnEnE_UM_LoB   LoaMgr CurrivrMtgnEnE_UM_SS_12		
Loading		
LoaMigr_SnsfsMoAgAvail_Cnt_M_logc	1	
Loading - VirighdSrcSV_Cnt_M_ U08         2           Rie_Inst_Ap_Loading*         target_Rie_Inst_Ap_Loading*           K_Loading**CurrivriktightRate_UispS_f32         1           K_Loading**CurrivriktightSacazencEn_Cnt_Igc         0           K_Loading**CurrivriktightSacazencEn_Cnt_Igc         1           K_Loading**LivrivriktightSacazencEn_Cnt_U08         1           K_Loading**HirdightSightNtoTacesp.Cnt_U08         1           K_Loading**HirdightSightNtoTacesp.Cnt_U08         1           K_Loading**HirdightSightTodavialResp.Cnt_U08         1           K_Loading**HirdightSightTodavialResp.Cnt_U08         1           K_Loading**HirdightSightTodavialResp.Cnt_U08         1           K_Loading**HirdightSightTodavialResp.Cnt_U08         0           K_Loading**HirdightSightTodavialResp.Cnt_U08         1           K_Loading**HirdightSightTodavialResp.Cnt_U08         0           K_Loading**HirdightSightTodavialResp.Cnt_U08         0           K_Loading**HirdightSightTodavialResp.Cnt_U08         0           K_Loading**IrdightSightTodavialResp.Cnt_U08         0           K_Loading**IrdightSightTodavialResp.Cnt_U08         1           K_Loading**IrdightSightTodavialResp.Cnt_U08         1           K_Loading**IrdightSightTodavialResp.Cnt_U08         2           K_Loading**MoralightSightTodavialResp.Cnt_U08		
Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrivriftginSacZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivriftginSacZeroEn_Cnt_lgc         1           k_LoaMgr_FadeOulSiRate UlspS_132         1           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         2           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         1           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         1           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         1           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         1           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         0           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         0           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         0           k_LoaMgr_Hordpilsgb(TroakvaliResp_Cnt_u08         1           k_LoaMgr_Hordpilsgb(Trib, Cnt_u08         1           k_LoaMgr_Hordpilsgb(Trib, Cnt_u08         0           k_LoaMgr_Ivritiplsig(Trib, Cnt_u08         0           k_LoaMgr_Ivritiplsig(Trib, Cnt_u08         0           k_LoaMgr_IvritiplsracZeroEn_Cnt_u08         0           k_LoaMgr_IvritiplsracZeroEn_Cnt_u08         0           k_LoaMgr_IvritiplsracZeroEn_Cnt_u08         0           k_LoaMgr_MotAgldplsig)Filth_Cnt_u08         0           k_LoaMgr_MotAgldplsig)Filth_Cnt_u08         0           k_LoaMgr_MotAgldplsig)Filth_Cnt_u08         0		
k_LoaMgr_CurrivtrMtginSca_Uis_f32         1           k_LoaMgr_FadeCutriStrate_UispS_f32         1           k_LoaMgr_HortgichsginToaNarlesp_Cnt_u08         2           k_LoaMgr_HwTqichsiSginToaNarlesp_Cnt_u08         1           k_LoaMgr_HwTqichsiSginToaNarlesp_Cnt_u08         2           k_LoaMgr_HwTqichsiSginToaNarlesp_Cnt_u08         1           k_LoaMgr_HwTqichsiSginToaNarlesp_Cnt_u08         0           k_LoaMgr_HwTqichsiSginToaNarlesp_Cnt_u08         0           k_LoaMgr_HwTqichsiSginTind_Cnt_u08         1           k_LoaMgr_HwTqichsiSginTind_Cnt_u08         1           k_LoaMgr_HwTqidpsiSginTind_Cnt_u08         1           k_LoaMgr_HwTqidpsiSqinTind_Cnt_u08         0           k_LoaMgr_IvridpsiSqinTind_Cnt_u08         0           k_LoaMgr_IvridpsiSqinTind_Cnt_u08         0           k_LoaMgr_IvridpsiSqinTind_Cnt_u08         0           k_LoaMgr_IvridpsiSqinTind_Sca_Uis_f32         1           k_LoaMgr_IvridpsiSqinTind_Sca_Uis_f32         1           k_LoaMgr_IvridpsiSqinTind_Sca_Uis_f32         1           k_LoaMgr_MorajdpsiSqinSqinSqinSqinSqinSqinSqinSqinSqinSqi		
K_LoaMgr_Hort citrsisgoTroaAvailResp_Cnt_u08		
K_LoaMgr_HwTqldptSig0TioaAvailResp_Cnt_u08         2           K_LoaMgr_HwTqldptSig1NoTioaResp_Cnt_u08         1           K_LoaMgr_HwTqldptSig1NoTioaResp_Cnt_u08         2           K_LoaMgr_HwTqldptSig2TioaDiResp_Cnt_u08         1           K_LoaMgr_HwTqldptSig2TioaEnaResp_Cnt_u08         0           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           K_LoaMgr_IwTrldptSig4Resp_Cnt_u08         0           K_LoaMgr_IwTrldptSig4Resp_Cnt_u08         0           K_LoaMgr_IwTrldptSig4Resp_Cnt_u08         0           K_LoaMgr_IwTrldptSig4Resp_Cnt_u08         0           K_LoaMgr_IwTrldptSig4Resp_Cnt_u08         1           K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         1           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         2           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         2           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         0           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         0           K_LoaMgr_MotAgldptSig4Resp_Cnt_u08         0           K_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurr		
k_LoaMgr_HwTqldptSig11NoTloaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig11ToaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig21ToaBresp_Cnt_u08         1           k_LoaMgr_HwTqldptSig21ToaBraResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1ToaLnCnt_u08         0           k_LoaMgr_HwTqldptSig1ToaLnCnt_u08         1           k_LoaMgr_HwTqldptSig1ToaLnCnt_u08         1           k_LoaMgr_HwTqldptSig1ToaLnCnt_u08         1           k_LoaMgr_HwTqldptSig1ToaLnCnt_u08         0           k_LoaMgr_IwTrdptSig1ToaLnCnt_u08         0           k_LoaMgr_IwTrdptSig1Frep_Cnt_u08         0           k_LoaMgr_IwTrdptSig1Frthd_Cnt_u08         0           k_LoaMgr_IwTrdptSig2ToaLnCnt_u08         1           k_LoaMgr_IwTrdptSig2ToaLnCnt_u08         1           k_LoaMgr_IwTrdptSig2ToaLnCnt_u08         1           k_LoaMgr_MotAgldptSig1Sig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Sig2Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Sig2Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig2Sesp_Cnt_u08         2           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1Sig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrdp		
k_LoaMgr_HwTqloptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqloptSig1TloaDResp_Cnt_u08         1           k_LoaMgr_HwTqloptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqloptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqloptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqloptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrloptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrloptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrloptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrloptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrloptSig1Resp_Cnt_u08         1           k_LoaMgr_IwtrloptSig1PtSig2Resp_Cnt_u08         5           k_LoaMgr_MotAgloptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgloptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgloptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgloptSig4Resp_Cnt_u08         0           k_LoaMgr_MotAgloptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgloptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgloptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrloptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrloptSig1Resp_Cnt_u0		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtgtnStac_Uls_f32         1           k_LoaMgr_HwTqMtgtnStac_Uls_f32         1           k_LoaMgr_IwtrdptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrdptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrdptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrdptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrdptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrdptRate_UlspS_f32         1           k_LoaMgr_IwtrdptRate_UlspS_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig4Resp_Cnt_u08         0           k_LoaMgr_MotAgmanavaiiResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         <		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Rthd_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_f32         1           k_LoaMgr_HwTqMtghrRate_UlspS_f32         1           k_LoaMgr_HwTqMtghSig1Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtridptSig1Sig1NoSmaResp_Cnt_u08         0           k_LoaMgr_IvtridptSig1Sig1Sig1Sig1Sig1Sig1Sig1Sig1Sig1Sig1		
k_LoaMgr_HwTqldptSigHResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigHThd_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_f32         1           k_LoaMgr_HwTqMtghrSac_Uls_f32         1           k_LoaMgr_IvtrldptSigHResp_Cnt_u08         0           k_LoaMgr_IvtrldptSigHResp_Cnt_u08         0           k_LoaMgr_IvtrldptSigHThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigHThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigHThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigHSpS_G2Resp_Cnt_u08         1           k_LoaMgr_IvtrldptSigSResp_Cnt_u08         5           k_LoaMgr_MotAgldptSigTRoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSigHTnd_Cnt_u08         0           k_LoaMgr_MotAgldptSigHTnd_Cnt_u08         2           k_LoaMgr_MotAgMgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMgtnRate_UlspS_f32         1           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         0		
k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_132         1           k_LoaMgr_HwTqMtghrSac_Uls_132         1           k_LoaMgr_IvtrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrMtghrSac_UlspS_132         1           k_LoaMgr_IvtrMtghrSac_ZeroEn_Cnt_Ugc         0           k_LoaMgr_IvtrMtghrSac_ZeroEn_Cnt_Ugc         0           k_LoaMgr_MotAgldptSig1NbSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglMtghrSate_UlspS_132         1           k_LoaMgr_MotAgMtghrSate_UlspS_132         1           k_LoaMgr_MotAgMtghrSac_Uls_132         1           k_LoaMgr_MotAgMtghrSac_Uls_132         1           k_LoaMgr_MotAgMtghrSac_Uls_132         1           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0 <td></td> <td></td>		
k_Loaklgr_HwTqMtgtnRate_UlspS_f32         1           k_Loaklgr_HwTqMtgtnSca_Uls_f32         1           k_Loaklgr_LwTdqtSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdqtSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdqtSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdtgtnSca_Uls_f32         1           k_Loaklgr_LwTdtgtnSca_Jus_f32         1           k_Loaklgr_LwTdtgtnSca_Uls_f32         1           k_Loaklgr_MwtAgldptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MotAgldptSig5Ca_Uls_f32         1           k_Loaklgr_MotAgldptSig6Ca_Uls_f32         1           k_Loaklgr_MotAgldptSig6Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig0Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig0Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1FtThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig1FtThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig1FtThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig1FtThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig1FtThd_Cnt_u08		
k_LoaMgr_HvTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrIdptSig1FitTh_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgltgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_cnt_u08         0           k_LoaMgr_MotAgRayAailResp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_lvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_lvtrldptSig2FitThd_Cnt_u08         0           k_LoaMgr_lvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig4Rete_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMmavailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMsmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_NvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Res		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_lvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_igc         0           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_u08         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSi	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc         0           k_LoaMgr_WtfMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Flthd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnSteate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrMgtSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrMgtSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrMgtRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RotCurrMtgtnSca_Uls_f32         1	k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMsmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_ForEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 1	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
	k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
1	k_LoaMgr_RedcdStRate_UlspS_f32	1
K_Loamgr_Reacastsca_Uis_t32 1	k_LoaMgr_RedcdStSca_Uls_f32	1





zoamgi_i ori			(
Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr	1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr	2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig	g_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtr	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State	e_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaM	tgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldpt	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptS	ig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_C	Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_	lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>

		1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	-
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	_





Test Step 2.16 (Repeat Count = 1)	
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	_

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_IvtrMtgtnSca_UIs_f32			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MotCurrLoaMiginEn_Cnt_gc target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	result
LoaMar HwTaldotSiaSV Cnt M u08	1	1	-

target_rtte_mat_Ap_coamgr.coamgr_r en_noabi_ont_ige	target_Loalvigi_i cri_rloabi	_ont_igc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_LOASt_State_M_enum	2	2	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0





Loanigi_i ei i			1-4-1-16-16
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1I	nactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2I	nactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_	Cnt_u08	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgt	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_	enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi	g_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMi	tgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn	t_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lg	c	
Name	Actual Value	Expected Value	Res
LoaMgr_AgSV_Cnt_M_lgc			
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	2	2	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	
		{1, 1}	
	{1, 1}	1 1	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1 ± 0.01	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)		1 ± 0.01 1 ± 0.001	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1 ± 0.001	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	1 1 0	1 ± 0.001	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1 1 0 1	1 ± 0.001 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	<b>~</b>
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32	1	<b>✓</b>
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Imput Value	Test Step 2.19 (Repeat Count = 1)	<b>□</b>
Loadley_ Fast Control   Load		
Loading_InvitagesSeyCott_M_L08		
Loading_ Invarigations		
Loadkg_ IntroDespSV_CM_M_USS		
Loaking_ hartingsteps(SV_ Ont_M_ 108   0		
Loaking_Mintopathglist_Cott_M_Ligs		
LoaMgr_WinRepsSV_Crit_M_U08		
LoaMay_MoAglosNigsV_CH_M_U08	1 - 1 - 1	
Loaking MAAQRespSV_Cnt_M_U08		·
LoaMgr MoCurridpiSgSV_Cni_M_u08   0		
LoaMgr McCurricpSisGPV_Crd_M_U08		
Loaking	· · · · · · · · · · · · · · · · · · ·	
LoaMgr   McCurrRespSV_Cnt_M_U08		
LoaMgr_YngMSrcSv_Cnt_M_U08		
LoaMgr_VilgMdSrcSV_Cnt_M_u08		
Rie_Inst_Ap_LoaMgr		
k_LoaMgr_Currivn/figinSac_Vis_S122		
LoaMgr_CurrivrtMigtnSca_Uis_132		
LoaMgr   CurrivtrMtginSea_Uis_132		
K. LoaMgr_HVTqlqtPisgrTtoaAvailResp_Cnt_u08		
K_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           K_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         2           K_LoaMgr_HwTqldptSig1TloaClareResp_Cnt_u08         1           K_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           K_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           K_LoaMgr_HwTqldptSig2Resp_Cnt_u08         0           K_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           K_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           K_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           K_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           K_LoaMgr_IwtrldptSea_Uls F_132         1           K_LoaMgr_IwtrldptSea_Uls F_132         1           K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           K_LoaMgr_MotAgldptSig1Sig1NoSmaResp_Cnt_u08         1           K_LoaMgr_MotAgldptSig1Sig1NoSmaResp_Cnt_u08         0           K_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           K_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           K_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           K_Loa		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaRoxalResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqMtgnRate_UispS_f32         1           k_LoaMgr_HwTqMtgnSac_Uis_f32         0           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         1           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 </td <td></td> <td></td>		
k_LoaMgr_HwTqldptSig1TioaAvailResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TioaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig4Sig5SezCereEn_Cnt_Ugc         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         1           k_LoaMgr_MoldpldptSig3Resp_Cnt_u08         5           k_LoaMgr_MoldpldptSig3Resp_Cnt_u08         1           k_LoaMgr_MoldpldptSig3Resp_Cnt_u08         1           k_LoaMgr_MoldpldptSig3Resp_Cnt_u08         2           k_LoaMgr_Moldpldtsig4Sig3Resp_Cnt_u08         0           k_LoaMgr_Moldpldtsig5ig4Resp_Cnt_u08         0           k_LoaMgr_Moldpldtsig5ig5itTnd_Cnt_u08         2           k_LoaMgr_Moldpldtsig5ig7Resp_Cnt_u08         0           k_LoaMgr_Moldpldtsig7Mgrate_Ulsp_S d2         1           k_LoaMgr_Moldpldtsi		
K_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1FiThd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1FiThd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1FiThd_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_132         1           k_LoaMgr_HwTqMtghSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1Sig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdptSig2Resp_Cnt_u08		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqMtglnRate_UlspS_f32         1           k_LoaMgr_HwTqMtglnRate_UlspS_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         1           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgMtghrRate_UlspS_f32         1           k_LoaMgr_MotAgMtghrSca_ZeroEn_Cnt_u08         0           k_LoaMgr_MotAgMtgmSca_Uls_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08		
k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_132         1           k_LoaMgr_HwTqMtghrSac_Uls 132         1           k_LoaMgr_IvrtdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvrtdptSig2Resp_Cnt_u08         0           k_LoaMgr_IvrtMtghrSac_UlspS_132         1           k_LoaMgr_IvrtMtghrSac_ZeroEn_Cnt_Ugc         0           k_LoaMgr_IvrtMtghrSac_ZeroEn_Cnt_Ugc         0           k_LoaMgr_MotAgidptSig1NosmaResp_Cnt_u08         5           k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgMtghrSac_Uis_132         1           k_LoaMgr_MotAgMtghrSac_Uis_232         1           k_LoaMgr_MotAgMtghrSac_Uis_132         1           k_LoaMgr_MotAgMtghrSac_Uis_132         1           k_LoaMgr_MotAgMtghrSac_Uis_132         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0		
k_Loaklgr_HwTqMtgtnRate_UlspS_f32         1           k_Loaklgr_HwTqMtgtnSca_Uls_f32         1           k_Loaklgr_LwTdptSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdptSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdptSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdtgtnSig1Resp_Cnt_u08         0           k_Loaklgr_LwTdtgtnSca_Uls_f32         1           k_Loaklgr_LwTdtgtnSca_Uls_f32         1           k_Loaklgr_LwTdtgtnSca_Uls_f32         1           k_Loaklgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MotAgldptSigAresp_Cnt_u08         2           k_Loaklgr_MotAgldptSigSca_Uls_f32         1           k_Loaklgr_MotAgldptSigResp_Cnt_u08         0           k_Loaklgr_MotAgldptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08		
k_Loaklgr_HwTqMtgtnSca_Uls_f32         1           k_Loaklgr_IvtridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvtridptSig2Resp_Cnt_u08         0           k_Loaklgr_IvtridptSig1FitThd_Cnt_u08         0           k_Loaklgr_IvtrMtgtnSca_Ulsp_f32         1           k_Loaklgr_IvtrMtgtnSca_Uls_f32         1           k_Loaklgr_IvtrMtgtnSca_Uls_f32         1           k_Loaklgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig1Resp_Cnt_u08         0           k_Loaklgr_MotAgltgtnRete_UlspS_f32         1           k_Loaklgr_MotAgltgtnRete_UlspS_f32         1           k_Loaklgr_MotAgltgtnSca_Uls_f32         1           k_Loaklgr_MotAgltgtnSca_Uls_f32         1           k_Loaklgr_MotAgltgtnSca_Uls_f32         1           k_Loaklgr_MotAgltgtSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_lvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_lvtrldptSig2FitThd_Cnt_u08         0           k_LoaMgr_lvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_tJgc         0           k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Rete_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMsmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Usp_f32         1           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Usp_f32	k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_Ivtrl0ptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurrldptS	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1SeaTen_Cnt_lgc         1           k_LoaMgr_MotCurridptSig1SeaTen_Cnt_lgc         1           k_LoaMgr_MotCurridptSig1SeaTen_Cnt_lgc         1           k_LoaMgr_MotCurridptSig1SeaTen_Cnt_lgc         1           k_LoaMgr_MotCurr	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig5ItThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnStgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurriMgtSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurriMgtSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurriMgtSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurriMgtnRate_UlspS_f32         1           k_LoaMgr_MotCurriMgtnRate_UlspS_f32         1           k_LoaMgr_RotCurriMgtnSca_Uls_f32         1           k_LoaMgr_RotCurriMgtnSca_Uls_f32         1           k_LoaMgr_RotCurriMgtnSca_	k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMigtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1FltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_IvtrMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltTnd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltTnd_Cnt_u08         0           k_LoaMgr_MotCurrlMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrlMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
k_LoaMgr_MotAgNttgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMttgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgNtdgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1	k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 1	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LooMar PododStSon IIIn 92	k_LoaMgr_RedcdStRate_UlspS_f32	1
r_Luaivigi_neucusisca_uis_is2	k_LoaMgr_RedcdStSca_Uls_f32	1





Eddingi_i or i			(
Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr	1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr	2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State	e_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaM	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldpt	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoal	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptS	target LoaMgr Per1 MtrPosldptSig Cnt u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_C	nt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_	lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~

LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	•

Fest Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	-
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	_



Test Step 2.20 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LoaScaDi_Cnt_Igc	target_LoaMgr_Perl_LOASt_State_enum target_LoaMgr_Perl_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDI_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1	, 5 = 5 = 5 = 5 = 5 = 5



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>~</b>

Test Step 2.21 (Repeat Count = 1)	<u> </u>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr

2018-05-18, 14:04:23+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
	2		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1		
· - ·	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Der1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	go	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
	500	300	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08			~

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

1

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0

2018-05-18, 14:04:23+0530



Nome	Innut Value		
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VitgMdSrcSV_Cnt_M_u08	1 (472, 474)	1 (472, 474)	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	(1, 1)	{0, 0, 0, 0, 0}	×
	111 11	{1, 1}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}		<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500		·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 1 0	1 ± 0.001 0	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 0 0	1 ± 0.001 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
g (0000010000_010_102	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Ii	nactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Ii	nactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_	enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgt	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig	g_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMt	tgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt	t_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lg	c	
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	3	3	~
LoaMgr_LoaRateLimit_UlspS_M_f32	0.00999999978	0.0099999978	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>→</b>
		0.00999999978 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0.00999999978		
	0.0099999978	1 ± 0.001	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value		1 ± 0.001	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1		<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 0	0	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0 0	0	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>



Injust Value   Injus Value	Test Step 2.24 (Repeat Count = 1)	12
Coating   Forticostage   Cost   Mode   Cost   Cos		
Leading Instructions Cot III jug 0  Leading Instructions Leading Leading Cot III jug 0  Leading Instructions Leading Leading Cot III jug 0  Leading Instructions Leading Leading Cot III jug 1  Leading Instructions Leading Leading Cot III jug 1  Leading Instructions Leading Leadi		·
Loading - Info-General Cord   Muse		1
Leading   Medicipality   Cort   M.   108	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Leading Fundangsing Core M. 198  Leading Mangadistics Or Core M. 199  Leading Mangadistics Or Core M. 198  Leading Leading Mangadistics Or M. 199  Leading Mangadistics Or Core M. 198  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Upus 192  Leading Mangadi	LoaMgr_HwTqRespSV_Cnt_M_u08	0
Loading_Ambidges/Sept Cost M_Loss	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading   Managlaphing   Cort   Mg   10		
Contagn_Managenesing_Cont_M_Size   Contagn_Manage		•
Louding MacCurt (1985) (**O. IM. 198   0   1   1   1   1   1   1   1   1   1		
Loading   MacCardengling Cort   Mile		
Looking - Michael Personnel Cont. Mulge		
Lookings	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Lindhoffseta, Using 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   L	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. Landing/Lamfordingnissazzenich (mil. go.)		1
Landing-Landing-Resolution   Landing-Resolution	Rte_Inst_Ap_LoaMgr	V
Landary Information Style Transcripting Cott (Jul 8)		
L.Dodrigs   Marciago   Not Total Resp. Cert. USB		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-HwiTqldsSigNRtsp_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
K.Loshig / Hv/TqhtighRate_UspS_122	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K. Loosing-/ InvirightignResp_Cnt_u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loadig_NvfdptSigPResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_Loshig_HvirighSig2Pes_Cnt_U08		
k_LoaMgr_IvridqsSigPRTRd_Cnt_u08         0           k_LoaMgr_IvridqsSigPRTRd_Cnt_u08         1           k_LoaMgr_IvridqsSig2RecEn_Cnt_u08         5           k_LoaMgr_MoNdqdptSig1NsSamResp_Cnt_u08         5           k_LoaMgr_MoNdqdptSig2Resp_Cnt_u08         1           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         1           k_LoaMgr_MoNdqdptsSig2Resp_Cnt_u08         0           k_LoaMgr_MoNdqdtgmsAcareCnt_u08         0           k_LoaMgr_MoNdqdtgmsCareCnt_u08         0           k_LoaMgr_MoNdcuridqtSig1Resp_Cnt_u08         0           k_LoaMgr_MoCuridqtSig1Resp_Cnt_u08         0 </td <td></td> <td></td>		
K_Loakly_InvitMignRate_UlspS_132		
k_LoaMgr_IvriMiginSca_Uis_IS2         1           k_LoaMgr_Morty (brighs)ca_Uis_IS2         1           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         5           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         1           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         2           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         2           k_LoaMgr_Morty(bris)ca_Uis_IS2         1           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         1		
k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_Pert_DiagosSistr/Tinactv_Cnt_Igc value         0           target_LoaMgr_Pert_Inscapable_Cnt_U08 value         0           <		
K_LoaMgr_MorAgidptSigSResp_Cnt_u08         0           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         2           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         1           K_LoaMgr_MorAgMighrRate_Uibs_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgSmaAvailresp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_RedodSisca_Uis_32         1           K_LoaMgr_RedodSisca_Uis_32         1           K_LoaMgr_RedodSisca_Uis_32         0.123400003           Larget_LoaMgr_Pert_DiagcSistvrTianctv_Cnt_lgc value         0           Larget_LoaMgr_Pert_LoaSca_Dic_Dic_Uig_value </td <td>k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08</td> <td>5</td>	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           K_LoaMgr_MotAghtglmScate_UlspS_f32         1           K_LoaMgr_MotAghtglmScate_Cnt_u0g         1           K_LoaMgr_MotAghtglmScate_Uls_f32         1           K_LoaMgr_MotAghtgmScate_Uls_f32         1           K_LoaMgr_MotCurridptSigGeRsp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRsp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFersp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_log         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_log         1           K_LoaMgr_RodoSiRaL_ulspS_f32         1           K_LoaMgr_RodoSiRaL_ulspS_f32         1           K_LoaMgr_RodoSiRaL_ulspS_f32         1           K_LoaMgr_Port_DiagcSistvirInactv_Cnt_lgc value         0           Larget_LoaMgr_Port_DiagcSistvirInactv_Cnt_lgc value         0           Larget_LoaMgr_Port_LoaScaDi_Cnt_lgc value         0           Larget_LoaMgr_Port_Sig_Cnt_u08 value         1           Larget_LoaMgr_Port_ToaQvalu_Cnt_lgc value         0	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnSacZeroEn_Cn_Ltgc         1           k_LoaMgr_MotAgMtgtnSacZeroEn_Cn_Uo8         0           k_LoaMgr_MotAgMtgtnSac_Us_Us_f32         1           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigt_UspS_f32         1           k_LoaMgr_MotCurridptSigt_UspS_f32         1           k_LoaMgr_RedcdSiRate_UispS_f32         1           k_LoaMgr_RedcdSiRate_UispS_f32         1           k_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_Bridger_Cnt_uo8 value         0           target_LoaMgr_Pert_MotCrotisig_Cnt_uo8 value         0           target_LoaMgr_Pert_MotCrotisig_Cnt_uo8 value         0           target_LoaMgr_Pert_MotGr_Cnt_lgc value         0           target_LoaMgr_Pert_MotGr_Cnt_lgc_value         0           target_LoaMgr_Pert_MotGr_Cnt_lgc_value         0	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         1           k_LoaMgr_MotAgMtgtnSca_Uis_132         1           k_LoaMgr_MotAgmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_Pert_DiagcSistivt1riact_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistivt2lnact_Cnt_lgc value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         2           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMtginSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2AcroEn_Cnt_lgc         1           k_LoaMgr_ReddSiSca_Uls_f32         1           k_LoaMgr_ReddSiSca_Uls_f32         1           k_LoaMgr_Per_DiagcStsivtrInactv_Cnt_lgc value         0           target_LoaMgr_Per_LioaScaDi_Cnt_lgc value         0           target_LoaMgr_Per_TioaDi_Cnt_lgc value         0           target_LoaMgr_Per_TioaDi_Cnt_lgc value         0	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_Mgr_MotCurridptSig2FitThd_Cnt_u08         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         1           k_LoaMgr_MotCurridptSig_Cnt_u1g         1           k_LoaMgr_RedcdStRate_UlspS_32         1           k_LoaMgr_RedcdStRate_UlspS_32         1           k_LoaMgr_Pert_DiagcStstwtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Pert_DiagcStstwtr2nactv_Cnt_lgc.value         0           target_LoaMgr_Pert_HvTqlqbtSig_Cnt_u08 value         2           target_LoaMgr_Pert_Mrtp-ladptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MtrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Pert_TicaScalic_Cnt_lgc.value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HvTqLoaMgtpEn_Cnt_lgc         target_LoaMgr_Pert_HvTqLoaMgr_Pert_HvTqLoaMgtpEn_Cnt_lgc <tr< td=""><td>_ 0_ 0</td><td></td></tr<>	_ 0_ 0	
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRate_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSca_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_1532         1           k_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_SteerMod_Cnt_enun_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           targ		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MoldptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaAvaii_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvaii_Cnt_lgc_value         1           target_Rel_inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaRgcD		
k_LoaMgr_MotCurridptSigFltTh_Cn_u08         0           k_LoaMgr_MotCurridptMgthRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgthScaz_Uls_f32         1           k_LoaMgr_MotCurrMtgthSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value         0           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMg		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_L		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 2 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SterMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_L	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UispS_32 1 k_LoaMgr_RedcdStSca_Uis_f32 0.123400003 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MctCurridptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rel_inst_Ap_LoaMgr_Per1_MotAg	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSudptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDegr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_Indegr_Per1_Ind		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosidptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	· · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		0
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	1	1	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0.123400003	0.123400003	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>✓</b>	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0.123400003	0.123400003 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalcivtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	<b>✓</b>	
RqstRespConds	1	RqstRespConds	1	<b>✓</b>	
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>	
ArbResp	1	ArbResp	1	•	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	•	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>	