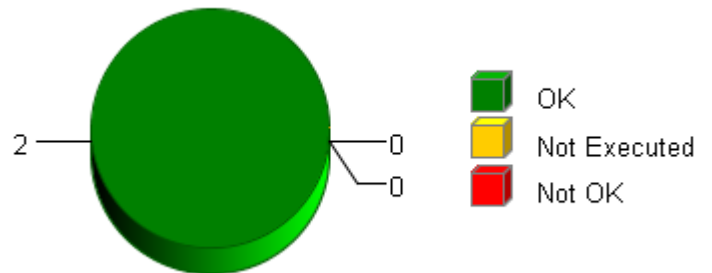


## Summary

**Total Test Objects:** 2  
**Successful:** 2  
**Failed:** 0  
**Not Executed:** 0  
**Date:** 2018-03-02  
**Time:** 15:56:49+0530

## Overall Test Object Results (including Coverage)



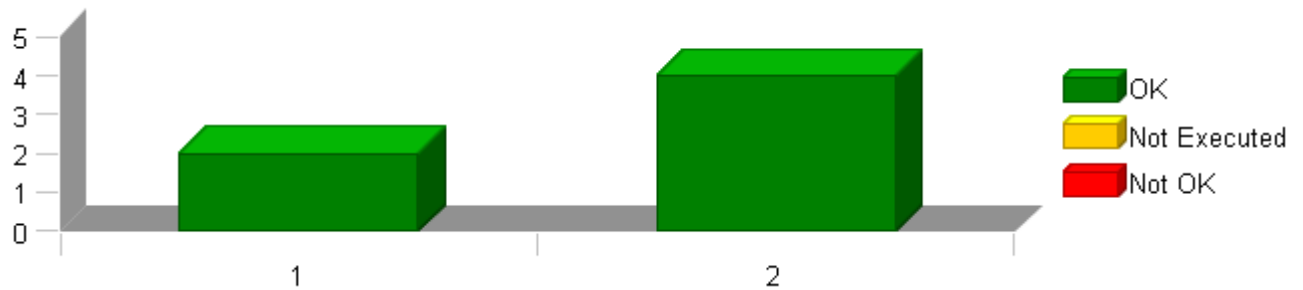
## Selected Project Items

Test Object "UnitTest/PSATA\_CF14/PSATA\_Init1"  
 Test Object "UnitTest/PSATA\_CF14/PSATA\_Per1"

## Used Test Environments

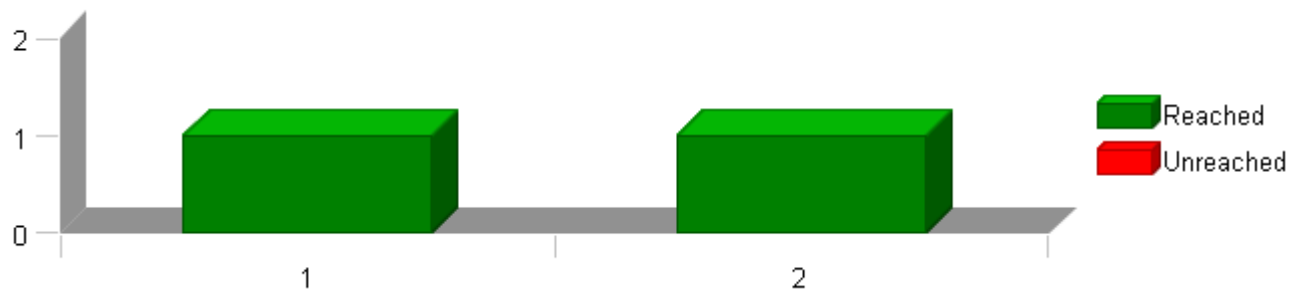
TI TMS 570 PLS UDE (Default)

## Test Case Results for Each Test Object (without Coverage)



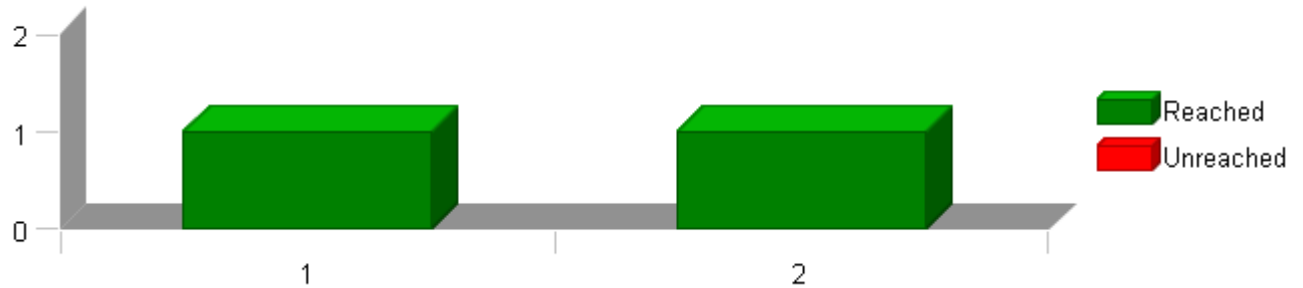
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

## Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

## Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

## TEST OVERVIEW REPORT

2018-03-02, 15:56:49+0530

Project PSATA



### Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	Test Cases	Result
	PSATA	100 %	100 %	6 of 6 passed	✓
	UnitTest	100 %	100 %	6 of 6 passed	✓
	PSATA_CF14	100 %	100 %	6 of 6 passed	✓
1	<a href="#">PSATA_Init1</a>	100 %	100 %	2 of 2 passed	✓
2	<a href="#">PSATA_Per1</a>	100 %	100 %	4 of 4 passed	✓

# TEST DETAILS REPORT

2018-03-02, 15:40:39+0530

PSATA\_Init1



Project	PSATA
Module	PSATA_CF14
Test Object	PSATA_Init1

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	2
Successful	2 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CF14_5.0.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\CF14_5.0.0_0_NoUTP\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\PSATA\src\Ap_PSATA.c
Compiler Options	-D_DATA_ACCESS=-D__inline=-D_extern=-Dconst=-I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include -I\$(PROJECTROOT)\PSATA\contract
File	\$(PROJECTROOT)\PSATA\src\interpolation.c
Compiler Options	-D_DATA_ACCESS=-D__inline=-D_extern=-Dconst=-I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include -I\$(PROJECTROOT)\PSATA\contract

## Comments/Description/Specification

Name	Text
Module 'PSATA_CF14'	*****Unit Test Information***** Name of Tester:Ameya Shirodkar Code File(s) Under Test:Ap_PSATA.c Code File(s) Version:8 Module Design Document:PSATA_MDD.docx Module Design Version:9 Data Dictionary Version:8 Unit Test Plan Version:5 Optimization Level:Ogeneral Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:None Model Version:Nexteer EPS Unit Test Tool 1.0 Total FLASH Used (Bytes):3088 Total RAM Used (Bytes):112 Total CALS Used (Bytes):342 Special Test Requirements:FDD describes functionality and structural breakdown of this component. Data dictionary contains all attributes of variables and calibrations used. Test Date:3/2/2017 Comments: Note 1: The Expected output calculated from MATLAB model. Note 2: Local functions "APASupervision_Fn", "Cal_ApaNTC", "Cal_LxaOpTrqOv", "Cal_LxaPosSrvoCmdLimit", "Cal_LxaPosSrvoSftySmotngFactor", "Cal_LxaPosSrvoSmotng", "Cal_LxaPosSrvoSmotngFactor", "Cal_OpTrqOv", "Cal_PosServoSmoothingFactor", "LxaNTC", "LxaSupervision_Fn", "NTCDiagc_Fn", "TrqArbn_Fn" are not unit tested separately as those are tested in "PSATA_Per1" function. Please refer the snapshot "PSATA_PSATAPer1_coverage". Note 3: Inline functions defined in "GlobalMacro.h" are not unit tested. Note 4: "CBD_Sandbox_dbg.map" map file is embedded in UTP for reference. *****

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(TESSY_SYSPATH)\compiler\ti\tms470\sys_link.cmd

# TEST DETAILS REPORT

2018-03-02, 15:40:39+0530

PSATA\_Init1



Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CF14_5.0.0_NoUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

## Test Case 1: PIM Range Test

<b>Specification</b>	Performance Metrics: (With "None" Instrumentation and "WithPS" Environment) CPU Cycles:  TS 1.1 2698.00 Cycles TS 1.2 2726.00 Cycles TS 1.3 2726.00 Cycles TS 1.4 2726.00 Cycles TS 1.5 2726.00 Cycles TS 1.6 2726.00 Cycles TS 1.7 2726.00 Cycles TS 1.8 2726.00 Cycles TS 1.9 2726.00 Cycles TS 1.10 2726.00 Cycles
<b>Description</b>	Vector Description:  TS1.1All Min TS1.2All Max TS1.3PSATA_FilterdTrqSV_HwNm_M_Str.K==>Min TS1.4PSATA_FilterdTrqSV_HwNm_M_Str.K==>Max TS1.5PSATA_FilterdTrqSV_HwNm_M_Str.K==>Pos/Default TS1.6PSATA_FilterdTrqSV_HwNm_M_Str.SV==>Min TS1.7PSATA_FilterdTrqSV_HwNm_M_Str.SV==>Max TS1.8PSATA_FilterdTrqSV_HwNm_M_Str.SV==>Pos TS1.9PSATA_FilterdTrqSV_HwNm_M_Str.SV==>Zero/Default TS1.10PSATA_FilterdTrqSV_HwNm_M_Str.SV==>Neg

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0			
k_PsaTA_FilterCutOff_Hz_f32	0			
k_PsaTA_LxaDiTranTi_Sec_f32	0.100000001			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	10	10	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 1.2 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195436			
k_PsaTA_FilterCutOff_Hz_f32	2			
k_PsaTA_LxaDiTranTi_Sec_f32	2			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	0.5	0.5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 1.3 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.352620006			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

# TEST DETAILS REPORT

PSATA\_Init1

2018-03-02, 15:40:39+0530



Test Step 1.4 (Repeat Count = 1)				
Name		Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		6.75656986		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32		0.0248195436		
k_PsaTA_FilterCutOff_Hz_f32		0.200000003		
k_PsaTA_LxaDiTranTi_Sec_f32		0.200000003		
Name		Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32		5	5	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

Test Step 1.5 (Repeat Count = 1)				
Name		Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		0.352620006		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32		0.00251011807		
k_PsaTA_FilterCutOff_Hz_f32		0.200000003		
k_PsaTA_LxaDiTranTi_Sec_f32		0.200000003		
Name		Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32		5	5	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

Test Step 1.6 (Repeat Count = 1)				
Name		Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		-10		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32		0		
k_PsaTA_FilterCutOff_Hz_f32		0.200000003		
k_PsaTA_LxaDiTranTi_Sec_f32		0.200000003		
Name		Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32		5	5	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

Test Step 1.7 (Repeat Count = 1)				
Name		Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		10		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32		0.0246283002		
k_PsaTA_FilterCutOff_Hz_f32		0.200000003		
k_PsaTA_LxaDiTranTi_Sec_f32		0.200000003		
Name		Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32		5	5	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32		0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

# TEST DETAILS REPORT

2018-03-02, 15:40:39+0530

PSATA\_Init1



Test Step 1.8 (Repeat Count = 1)				✓
Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	8.93562984			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0153339999			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_Ulpsp_S_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓	

Test Step 1.9 (Repeat Count = 1)				✓
Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0124399997			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_Ulpsp_S_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓	

Test Step 1.10 (Repeat Count = 1)				✓
Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-2.5473001			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0211110003			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_Ulpsp_S_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

Test Step Call Trace					✓
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓	

Test Case 2: PRM Range Test		✓
Specification	Performance Metrics: (With "None" Instrumentation and "WithPS" Environment) CPU Cycles:  TS 2.1 2726.00 Cycles TS 2.2 2726.00 Cycles TS 2.3 2726.00 Cycles TS 2.4 2726.00 Cycles TS 2.5 2726.00 Cycles TS 2.6 2726.00 Cycles	
Description	Vector Description:  TS2.1k_PSATA_LxaDiTranTi_Sec_f32==>Min TS2.2k_PSATA_LxaDiTranTi_Sec_f32==>Max TS2.3k_PSATA_LxaDiTranTi_Sec_f32==>Pos/Default TS2.4k_PSATA_FilterCutOff_Hz_f32==>Min TS2.5k_PSATA_FilterCutOff_Hz_f32==>Max TS2.6k_PSATA_FilterCutOff_Hz_f32==>Pos/Default	

Test Step 2.1 (Repeat Count = 1)		✓
Name	Input Value	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	



# TEST DETAILS REPORT

2018-03-02, 15:40:39+0530



PSATA\_Init1

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2.56456995			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.100000001			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	10	10	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 2.2 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.012339999			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	-10			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	2			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	0.5	0.5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 2.3 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0248195436			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3.76839995			
k_PsaTA_FilterCutOff_Hz_f32	0.200000003			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 2.4 (Repeat Count = 1)

Name	Input Value			
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0			
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	7.5478301			
k_PsaTA_FilterCutOff_Hz_f32	0			
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003			
Name	Actual Value	Expected Value	Result	
LxaDiTranRate_UlpspS_M_f32	5	5	✓	
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓	

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

# TEST DETAILS REPORT

2018-03-02, 15:40:39+0530

PSATA\_Init1



## Test Step 2.5 (Repeat Count = 1)

Name	Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	10		
k_PsaTA_FilterCutOff_Hz_f32	2		
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003		
Name	Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32	5	5	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

## Test Step 2.6 (Repeat Count = 1)

Name	Input Value		
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0		
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1.36372995		
k_PsaTA_FilterCutOff_Hz_f32	0.200000003		
k_PsaTA_LxaDiTranTi_Sec_f32	0.200000003		
Name	Actual Value	Expected Value	Result
LxaDiTranRate_UlpspS_M_f32	5	5	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	5	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Project	PSATA
Module	PSATA_CF14
Test Object	PSATA_Per1

## Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

## Statistics

Total Testcases	4
Successful	4 ✓
Failed	0
Not Executed	0

## Module Properties

Project Root Directory	D:\Synergy_Work_Area\CF14_5.0.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\CF14_5.0.0_0_NoUTP\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\PSATA\src\Ap_PSATA.c
Compiler Options	-D_DATA_ACCESS= -D__inline= -D_extern= -Dconst= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include -I\$(PROJECTROOT)\PSATA\contract
File	\$(PROJECTROOT)\PSATA\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -D__inline= -D_extern= -Dconst= -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include -I\$(PROJECTROOT)\PSATA\contract

## Comments/Description/Specification

Name	Text
Module 'PSATA_CF14'	*****Unit Test Information***** Name of Tester:Ameya Shirodkar Code File(s) Under Test:Ap_PSATA.c Code File(s) Version:8 Module Design Document:PSATA_MDD.docx Module Design Version:9 Data Dictionary Version:8 Unit Test Plan Version:5 Optimization Level:Ogeneral Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:None Model Version:Nexteer EPS Unit Test Tool 1.0 Total FLASH Used (Bytes):3088 Total RAM Used (Bytes):112 Total CALS Used (Bytes):342 Special Test Requirements:FDD describes functionality and structural breakdown of this component. Data dictionary contains all attributes of variables and calibrations used. Test Date:3/2/2017 Comments: Note 1: The Expected output calculated from MATLAB model.

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Attributes	
Name	Value
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\CF14_5.0.0_NoUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Case 1: Range Test

### Specification

Performance Metrics:  
(With "None" Instrumentation and  
"WithPS" Environment)  
CPU Cycles:

TS 1.1 4662.00 Cycles  
TS 1.2 2723.00 Cycles  
TS 1.3 2097.00 Cycles  
TS 1.4 2092.00 Cycles  
TS 1.5 2206.00 Cycles  
TS 1.6 2166.00 Cycles  
TS 1.7 2147.00 Cycles  
TS 1.8 2147.00 Cycles  
TS 1.9 2147.00 Cycles  
TS 1.10 2147.00 Cycles  
TS 1.11 2147.00 Cycles  
TS 1.12 2147.00 Cycles  
TS 1.13 2147.00 Cycles  
TS 1.14 2147.00 Cycles  
TS 1.15 2147.00 Cycles  
TS 1.16 2147.00 Cycles  
TS 1.17 2148.00 Cycles  
TS 1.18 2148.00 Cycles  
TS 1.19 2148.00 Cycles  
TS 1.20 2148.00 Cycles  
TS 1.21 2148.00 Cycles  
TS 1.22 2148.00 Cycles  
TS 1.23 2148.00 Cycles  
TS 1.24 2148.00 Cycles  
TS 1.25 2148.00 Cycles  
TS 1.26 2148.00 Cycles  
TS 1.27 2148.00 Cycles  
TS 1.28 2148.00 Cycles  
TS 1.29 2148.00 Cycles  
TS 1.30 2148.00 Cycles  
TS 1.31 2148.00 Cycles  
TS 1.32 2148.00 Cycles  
TS 1.33 2148.00 Cycles  
TS 1.34 2148.00 Cycles  
TS 1.35 2148.00 Cycles  
TS 1.36 2148.00 Cycles  
TS 1.37 2148.00 Cycles

### Description

Vector Description:

TS1.1All Min  
TS1.2All Max  
TS1.3HwTorque\_HwNm\_f32==>Min  
TS1.4HwTorque\_HwNm\_f32==>Max  
TS1.5HwTorque\_HwNm\_f32==>Pos  
TS1.6HwTorque\_HwNm\_f32==>Zero/Default  
TS1.7HwTorque\_HwNm\_f32==>Neg  
TS1.8PosSrvoCmd\_MtrNm\_f32==>Min  
TS1.9PosSrvoCmd\_MtrNm\_f32==>Max  
TS1.10PosSrvoCmd\_MtrNm\_f32==>Pos  
TS1.11PosSrvoCmd\_MtrNm\_f32==>Zero/Default  
TS1.12PosSrvoCmd\_MtrNm\_f32==>Neg  
TS1.13PosSrvoEnable\_Cnt\_lgc==>Min  
TS1.14PosSrvoEnable\_Cnt\_lgc==>Max  
TS1.15VehicleSpeed\_Kph\_f32==>Min/Default  
TS1.16VehicleSpeed\_Kph\_f32==>Max  
TS1.17VehicleSpeed\_Kph\_f32==>Pos  
TS1.18EPSSStateForLxa\_Cnt\_enum==>Min  
TS1.19EPSSStateForLxa\_Cnt\_enum==>Max  
TS1.20EPSSStateForLxa\_Cnt\_enum==>Pos  
TS1.21ApaState\_Cnt\_enum==>Min  
TS1.22ApaState\_Cnt\_enum==>Max  
TS1.23ApaState\_Cnt\_enum==>Pos  
TS1.24HandwheelVelocity\_HwRadpS\_f32==>Min  
TS1.25HandwheelVelocity\_HwRadpS\_f32==>Max  
TS1.26HandwheelVelocity\_HwRadpS\_f32==>Pos  
TS1.27HandwheelVelocity\_HwRadpS\_f32==>Zero/Default  
TS1.28HandwheelVelocity\_HwRadpS\_f32==>Neg  
TS1.29LxaDrvrAbsntHwVelDetd\_Cnt\_lgc==>Min  
TS1.30LxaDrvrAbsntHwVelDetd\_Cnt\_lgc==>Max  
TS1.31LxaHwAgDetd\_Cnt\_lgc==>Min  
TS1.32LxaHwAgDetd\_Cnt\_lgc==>Max  
TS1.33HwTrqRate\_HwNmpS\_f32==>Min/Default  
TS1.34HwTrqRate\_HwNmpS\_f32==>Max  
TS1.35HwTrqRate\_HwNmpS\_f32==>Pos  
TS1.36 LxaCorrnFacReqLimd\_Uls\_f32==>Min  
TS1.37 LxaCorrnFacReqLimd\_Uls\_f32==>Max

## Test Step 1.1 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.5
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	1
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwVelPNAccum_Cnt_M_u16	1
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	1
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	0
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	0		
t_PsaTA_SmoothingX_Uls_u6p10[2]	0		
t_PsaTA_SmoothingX_Uls_u6p10[3]	0		
t_PsaTA_SmoothingX_Uls_u6p10[4]	0		
t_PsaTA_SmoothingX_Uls_u6p10[5]	0		
t_PsaTA_SmoothingX_Uls_u6p10[6]	0		
t_PsaTA_SmoothingX_Uls_u6p10[7]	0		
t_PsaTA_SmoothingX_Uls_u6p10[8]	0		
t_PsaTA_SmoothingX_Uls_u6p10[9]	0		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	0		
t_PsaTA_SmoothingY_Uls_u6p10[2]	0		
t_PsaTA_SmoothingY_Uls_u6p10[3]	0		
t_PsaTA_SmoothingY_Uls_u6p10[4]	0		
t_PsaTA_SmoothingY_Uls_u6p10[5]	0		
t_PsaTA_SmoothingY_Uls_u6p10[6]	0		
t_PsaTA_SmoothingY_Uls_u6p10[7]	0		
t_PsaTA_SmoothingY_Uls_u6p10[8]	0		
t_PsaTA_SmoothingY_Uls_u6p10[9]	0		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

	Actual Value	Expected Value	Result
_PrevSlewRateLmt_Uls_M_f32	0	0	✓
PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	-0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	-0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	300
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaMaxRate_UlspS_f32	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	100
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	8
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	224
t_PsaTA_DisableRateX_HwNm_u11p5[1]	224
t_PsaTA_DisableRateX_HwNm_u11p5[2]	224
t_PsaTA_DisableRateX_HwNm_u11p5[3]	224
t_PsaTA_DisableRateX_HwNm_u11p5[4]	224
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	160
t_PsaTA_DisableRateY_UlspS_u11p5[1]	160
t_PsaTA_DisableRateY_UlspS_u11p5[2]	160
t_PsaTA_DisableRateY_UlspS_u11p5[3]	160
t_PsaTA_DisableRateY_UlspS_u11p5[4]	160
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	32640
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	51200
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	65408
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	36044

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	64000
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	1024
t_PsaTA_SmoothingX_Uls_u6p10[0]	1024
t_PsaTA_SmoothingX_Uls_u6p10[1]	1024
t_PsaTA_SmoothingX_Uls_u6p10[2]	1024
t_PsaTA_SmoothingX_Uls_u6p10[3]	1024
t_PsaTA_SmoothingX_Uls_u6p10[4]	1024
t_PsaTA_SmoothingX_Uls_u6p10[5]	1024
t_PsaTA_SmoothingX_Uls_u6p10[6]	1024
t_PsaTA_SmoothingX_Uls_u6p10[7]	1024
t_PsaTA_SmoothingX_Uls_u6p10[8]	1024
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024
t_PsaTA_SmoothingY_Uls_u6p10[1]	1024
t_PsaTA_SmoothingY_Uls_u6p10[2]	1024
t_PsaTA_SmoothingY_Uls_u6p10[3]	1024
t_PsaTA_SmoothingY_Uls_u6p10[4]	1024
t_PsaTA_SmoothingY_Uls_u6p10[5]	1024
t_PsaTA_SmoothingY_Uls_u6p10[6]	1024

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[7]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[8]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	200		
target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10	10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	1	1	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	1	1	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.3 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251012
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	1
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	1
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	1
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-24.6191
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	110.336
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.5678
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	167.415
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0251011997	-0.0251011997	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0244140625	-0.0244140625	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.4 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSstateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	19.4928		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	25.3763		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.905		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	331.621		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSstateForLxa_Cnt_enum	target_PSATA_Per1_EPSSstateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-7.90500021	-7.90500021	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	6.30076975e-005	6.30076975e-005	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.5 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.002
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.4394		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.3568		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	74.2862		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.4956		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	186.095		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0184036009	-0.0184036028	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00400000019	0.00400000019	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.6 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	28.6907
target_PSATA_Per1_HwTorque_HwNm_f32.value	0
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	80.6497
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.1142
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	189.392
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0183574054	-0.0183574073	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00337500009	0.00337500009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.7 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.003375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-2.1488		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.9978		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	192.717		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.6348		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	167.344		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0408968851	-0.0408968851	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00600000005	0.00600000005	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00537500018	0.00537500018	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.8 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.005375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	27.0636		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.2695		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	103.729		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	109.957		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0250570327	-0.0250570327	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00600000005	0.00600000005	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475000031	0.00475000031	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.9 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-20.9682
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.7056
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	118.99
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	108.713
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0493563563	-0.0493563563	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00800000038	0.00800000038	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00674999971	0.00674999971	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.10 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	0.2486		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.9752		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	162.507		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.9976		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	216.663		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0342339948	-0.0342339985	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	7.99760008	7.99760008	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.0100000007	0.0100000007	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.11 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-7.5782		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.1043		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	153.495		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	316.159		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0163154174	-0.0163154211	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.90234375	0.90234375	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0100000007	0.0100000007	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.90234375	0.90234375	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0081249997	0.0081249997	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.12 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.008125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	6.8687
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.8898
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	54.5985
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.1528
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	123.638
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.00400047936	-0.00400048262	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-7.15280008	-7.15280008	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.877929688	0.877929688	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0100000007	0.0100000007	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.877929688	0.877929688	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00749999983	0.00749999983	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.13 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0075
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-29.6839		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.0476		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	13.0203		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.9976		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	351.295		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.0267009977	-0.0267010015	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.853515625	0.853515625	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0100000007	0.0100000007	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.853515625	0.853515625	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00687499996	0.00687499996	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.14 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.006875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-9.4528		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.3053		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	84.7446		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.0757		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	302.044		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	5.07569981	5.07569981	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.029910434	-0.0299104378	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.829101563	0.829101563	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.012000001	0.012000001	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.829101563	0.829101563	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00887500029	0.00887500029	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.15 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFit_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFit_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFit_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.008875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.3211
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.115
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	16.0401
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.1528
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.00946573354	-0.00946573634	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-7.15280008	-7.15280008	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.829101563	0.829101563	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0029296875	0.0029296875	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0140000014	0.0140000014	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.829101563	0.829101563	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0108750006	0.0108750006	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.16 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.010875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-26.9646		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.6001		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	21.4359		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.003		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.00712506846	0.00712506659	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8046875	0.8046875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0029296875	0.0029296875	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0029296875	0.0029296875	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0160000008	0.0160000008	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8046875	0.8046875	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.012875	0.012875	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.17 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.012875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-16.5277		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.1557		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	92.3395		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.4623		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	315.226		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.00169611815	0.00169611815	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0029296875	0.0029296875	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0180000011	0.0180000011	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0148750003	0.0148750003	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.18 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.014875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-27.6409
target_PSATA_Per1_HwTorque_HwNm_f32.value	10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	158.543
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.5083
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	78.443
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-8.50829983	-8.50829983	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.026793059	0.026793059	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0200000014	0.0200000014	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0168750007	0.0168750007	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.19 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.016875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	22.2939		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.3004		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	115.627		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.7807		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	126.067		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-0.780700028	-0.780700028	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0475608036	0.0475608036	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0029296875	0.0029296875	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0200000014	0.0200000014	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0162500013	0.0162500013	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.20 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.01625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-18.8584		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.819		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	15.9814		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.3231		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	204.163		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-1.32309997	-1.32309997	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0253046714	0.0253046714	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0220000017	0.0220000017	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0182499997	0.0182499997	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.21 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.01825
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.3298
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.9407
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	83.0381
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.6795
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	298.971
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0501935035	0.0501935035	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0240000021	0.0240000021	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0202500001	0.0202500001	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.22 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.02025
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.5085		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.3556		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	112.692		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	147.266		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0441546738	0.0441546738	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0260000024	0.0260000024	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0222500004	0.0222500004	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.23 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.02225
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	19.93		
target_PSATA_Per1_HwTorque_HwNm_f32.value	1.0222		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	122.697		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.1281		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	428.224		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	6.12809992	6.12809992	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0466096848	0.0466096848	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0260000024	0.0260000024	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.021625001	0.021625001	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.24 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.021625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.1093
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	34.4689
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.1475
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	459.819
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0618277639	0.0618277639	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0280000027	0.0280000027	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0236249994	0.0236249994	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.25 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.023625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.8764		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	159.727		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.0327		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	269.628		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0393917412	0.0393917412	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0280000027	0.0280000027	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.023	0.023	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.26 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.023
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	2		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.4416		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	139.419		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.8615		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	198.419		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0454215705	0.0454215705	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0280000027	0.0280000027	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0223750006	0.0223750006	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.27 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.022375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	0
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.1573
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	126.38
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.6447
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	1.39
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0449127145	0.0449127145	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.005859375	0.005859375	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.0280000027	0.0280000027	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0217500012	0.0217500012	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.28 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.02175
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-12.8097		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.7385		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	23.0782		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.1675		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	128.445		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0354158953	0.0354158953	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0068359375	0.0068359375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0300000031	0.0300000031	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0237499997	0.0237499997	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.29 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.02375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-16.3178		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.0766		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	44.1798		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.7377		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	83.3732		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0430496335	0.0430496335	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0068359375	0.0068359375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0300000031	0.0300000031	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0231250003	0.0231250003	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.30 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFit_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFit_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFit_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.023125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-3.5305
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.6042
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	181.47
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.7765
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	330.308
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0519885495	0.0519885495	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	8.77649975	8.77649975	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0068359375	0.0068359375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0048828125	0.0048828125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0300000031	0.0300000031	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0225000009	0.0225000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.31 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0225
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-10.5072		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.4603		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	160.251		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.5392		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	502.347		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.040662162	0.0406621657	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0068359375	0.0068359375	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0320000015	0.0320000015	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0245000012	0.0245000012	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.32 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0245
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	9.7046		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.1338		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	94.6388		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.2147		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	159.699		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0301837623	0.0301837642	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0078125	0.0078125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.005859375	0.005859375	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0340000018	0.0340000018	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0264999997	0.0264999997	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.33 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0265
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-4.0889
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.4258
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.3303
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	123.238
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-2.33030009	-2.33030009	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0487476438	0.0487476476	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.90234375	0.90234375	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0078125	0.0078125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0068359375	0.0068359375	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0360000022	0.0360000022	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.90234375	0.90234375	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0285	0.0285	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.34 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0285
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	22.704		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.3166		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	200		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.8576		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	449.84		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0669908226	0.06699083	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0078125	0.0078125	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0068359375	0.0068359375	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.0380000025	0.0380000025	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0305000003	0.0305000003	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.35 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0305
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-1.7242		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.3724		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	71.7081		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.3515		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	66.3766		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0727776736	0.0727776811	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0087890625	0.0087890625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0078125	0.0078125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0400000028	0.0400000028	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0324999988	0.0324999988	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 1.36 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	*none*
PSATA_ApaPosSrvoFit_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFit_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFit_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFit_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0305
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-1.7242
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.3724
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	71.7081
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.3515
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	66.3766
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.0785500035	0.0785500109	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.009765625	0.009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0078125	0.0078125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0420000032	0.0420000032	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.951171875	0.951171875	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0324999988	0.0324999988	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 1.37 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	*none*
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	*none*

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	*none*
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	*none*
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	*none*
PSATA_LxaTrqOvFlt_Cnt_M_lgc	*none*
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	*none*
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoLmdCmd_MtrNm_M_f32	*none*
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	*none*
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	*none*
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0305
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-1.7242		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.3724		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	71.7081		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.3515		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	66.3766		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.084307842	0.0843078494	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.00251011993	0.00251011993	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.009765625	0.009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0078125	0.0078125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0440000035	0.0440000035	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.926757813	0.926757813	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0324999988	0.0324999988	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓



Test Case 2: PIM Range Test





# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

## Specification

Performance Metrics:  
(With "None" Instrumentation and  
"WithPS" Environment)  
CPU Cycles:

TS 2.1 2089.00 Cycles  
TS 2.2 2057.00 Cycles  
TS 2.3 2083.00 Cycles  
TS 2.4 2213.00 Cycles  
TS 2.5 2213.00 Cycles  
TS 2.6 2295.00 Cycles  
TS 2.7 2207.00 Cycles  
TS 2.8 2057.00 Cycles  
TS 2.9 2294.00 Cycles  
TS 2.10 2278.00 Cycles  
TS 2.11 2357.00 Cycles  
TS 2.12 2057.00 Cycles  
TS 2.13 2057.00 Cycles  
TS 2.14 2317.00 Cycles  
TS 2.15 2162.00 Cycles  
TS 2.16 2238.00 Cycles  
TS 2.17 2057.00 Cycles  
TS 2.18 2025.00 Cycles  
TS 2.19 2162.00 Cycles  
TS 2.20 2060.00 Cycles  
TS 2.21 2042.00 Cycles  
TS 2.22 2162.00 Cycles  
TS 2.23 2162.00 Cycles  
TS 2.24 2162.00 Cycles  
TS 2.25 2162.00 Cycles  
TS 2.26 2162.00 Cycles  
TS 2.27 2162.00 Cycles  
TS 2.28 2167.00 Cycles  
TS 2.29 2162.00 Cycles  
TS 2.30 2162.00 Cycles  
TS 2.31 2162.00 Cycles  
TS 2.32 2162.00 Cycles  
TS 2.33 2162.00 Cycles  
TS 2.34 2162.00 Cycles  
TS 2.35 2162.00 Cycles  
TS 2.36 2178.00 Cycles  
TS 2.37 2178.00 Cycles  
TS 2.38 2178.00 Cycles  
TS 2.39 2162.00 Cycles  
TS 2.40 2162.00 Cycles  
TS 2.41 2178.00 Cycles  
TS 2.42 2178.00 Cycles  
TS 2.43 2162.00 Cycles  
TS 2.44 2162.00 Cycles  
TS 2.45 2162.00 Cycles  
TS 2.46 2178.00 Cycles  
TS 2.47 2178.00 Cycles  
TS 2.48 2165.00 Cycles  
TS 2.49 2105.00 Cycles  
TS 2.50 2207.00 Cycles  
TS 2.51 2059.00 Cycles  
TS 2.52 2040.00 Cycles  
TS 2.53 2009.00 Cycles  
TS 2.54 2038.00 Cycles  
TS 2.55 2095.00 Cycles  
TS 2.56 2191.00 Cycles  
TS 2.57 2314.00 Cycles  
TS 2.58 2304.00 Cycles  
TS 2.59 2243.00 Cycles

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

## Description

Vector Description:

TS2.1PSATA\_PrevIntermPosSrvoCmd\_MtrNm\_M\_f32==>Min  
 TS2.2PSATA\_PrevIntermPosSrvoCmd\_MtrNm\_M\_f32==>Max  
 TS2.3PSATA\_PrevIntermPosSrvoCmd\_MtrNm\_M\_f32==>Pos  
 TS2.4PSATA\_PrevIntermPosSrvoCmd\_MtrNm\_M\_f32==>Zero/Default  
 TS2.5PSATA\_PrevIntermPosSrvoCmd\_MtrNm\_M\_f32==>Neg  
 TS2.6PSATA\_PosServoSmoothing\_Uls\_M\_f32==>Min/Default  
 TS2.7PSATA\_PosServoSmoothing\_Uls\_M\_f32==>Max  
 TS2.8PSATA\_PosServoSmoothing\_Uls\_M\_f32==>Pos  
 TS2.9PSATA\_FilterdTrqSV\_HwNm\_M\_Str.K==>Min  
 TS2.10PSATA\_FilterdTrqSV\_HwNm\_M\_Str.K==>Max  
 TS2.11PSATA\_FilterdTrqSV\_HwNm\_M\_Str.K==>Pos/Default  
 TS2.12PSATA\_FilterdTrqSV\_HwNm\_M\_Str.SV==>Min  
 TS2.13PSATA\_FilterdTrqSV\_HwNm\_M\_Str.SV==>Max  
 TS2.14PSATA\_FilterdTrqSV\_HwNm\_M\_Str.SV==>Pos  
 TS2.15PSATA\_FilterdTrqSV\_HwNm\_M\_Str.SV==>Zero/Default  
 TS2.16PSATA\_FilterdTrqSV\_HwNm\_M\_Str.SV==>Neg  
 TS2.17LxaDiTranRate\_UlpspS\_M\_f32==>Min  
 TS2.18LxaDiTranRate\_UlpspS\_M\_f32==>Max  
 TS2.19LxaDiTranRate\_UlpspS\_M\_f32==>Pos  
 TS2.20PSATA\_LxaPosSrvoSmotngFactor\_Uls\_M\_f32==>Min/Default  
 TS2.21PSATA\_LxaPosSrvoSmotngFactor\_Uls\_M\_f32==>Max  
 TS2.22PSATA\_LxaPosSrvoSmotngFactor\_Uls\_M\_f32==>Pos  
 TS2.23PSATA\_PrevLxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Min  
 TS2.24PSATA\_PrevLxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Max  
 TS2.25PSATA\_PrevLxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Pos  
 TS2.26PSATA\_PrevLxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Zero/Default  
 TS2.27PSATA\_PrevLxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Neg  
 TS2.28PSATA\_PrevLxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Min  
 TS2.29PSATA\_PrevLxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Max  
 TS2.30PSATA\_PrevLxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Pos  
 TS2.31PSATA\_PrevLxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Zero/Default  
 TS2.32PSATA\_PrevLxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Neg  
 TS2.33PSATA\_LxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Min/Default  
 TS2.34PSATA\_LxaPosSrvoSftySmotngFactor\_Uls\_M\_f32==>Max  
 TS2.35PSATA\_LxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Min  
 TS2.36PSATA\_LxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Max  
 TS2.37PSATA\_LxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Pos  
 TS2.38PSATA\_LxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Zero/Default  
 TS2.39PSATA\_LxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Neg  
 TS2.40PSATA\_PrevLxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Min  
 TS2.41PSATA\_PrevLxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Max  
 TS2.42PSATA\_PrevLxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Pos  
 TS2.43PSATA\_PrevLxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Zero/Default  
 TS2.44PSATA\_PrevLxaPosSrvoSmotdCmd\_MtrNm\_M\_f32==>Neg  
 TS2.45PSATA\_LxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Min  
 TS2.46PSATA\_LxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Max  
 TS2.47PSATA\_LxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Pos  
 TS2.48PSATA\_LxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Zero/Default  
 TS2.49PSATA\_LxaPosSrvoLimdCmd\_MtrNm\_M\_f32==>Neg  
 TS2.50PSATA\_ApaPosSrvoFit\_Cnt\_M\_lgc==>Min/Default  
 TS2.51PSATA\_ApaPosSrvoFit\_Cnt\_M\_lgc==>Max  
 TS2.52PSATA\_LxaTrqOvFit\_Cnt\_M\_lgc==>Min/Default  
 TS2.53PSATA\_LxaTrqOvFit\_Cnt\_M\_lgc==>Max  
 TS2.54PSATA\_LxaHwVelFit\_Cnt\_M\_lgc==>Min/Default  
 TS2.55PSATA\_LxaHwVelFit\_Cnt\_M\_lgc==>Max  
 TS2.56PSATA\_LxaHwTrqFit\_Cnt\_M\_lgc==>Min/Default  
 TS2.57PSATA\_LxaHwTrqFit\_Cnt\_M\_lgc==>Max  
 TS2.58PSATA\_LxaHwTrqRateFit\_Cnt\_M\_lgc==>Min/Default  
 TS2.59PSATA\_LxaHwTrqRateFit\_Cnt\_M\_lgc==>Max

## Test Step 2.1 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	5.5763
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFit_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFit_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFit_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2575
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.2063
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFit_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0708
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.6948
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.3827
PSATA_PrevSlewRateLmt_Uls_M_f32	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-2.7099		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.4412		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	194.751		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.443		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	453.459		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-4.44299984	-4.44299984	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.69480038	-8.69480038	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.2 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	3.888
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.2922
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3965
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.4123
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8081
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.2624
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.0237
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.7468
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.8311
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	151.004
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.1465
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	165.768
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.000136621995	0.000136621995	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1.99999995e-005	1.99999995e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.8522234	6.8522234	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.26239991	-2.26239991	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.02370024	7.02370024	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.8522234	6.85222 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.8522234	6.85222 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.3 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	3.4625
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-7.5541
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.1262
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.5689
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.2704
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3803
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.305
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	16.733		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.1473		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	5.4663		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.2093		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	430.81		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-7.55380917	-7.55380917	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2.99999992e-005	2.99999992e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.17620301	-1.17620301	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.355885923	0.355885923	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.56890011	6.56890011	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.27040005	2.27040005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.355885923	0.355885923	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.30500007	-3.30500007	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.17620301	-1.1762 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.17620301	-1.1762 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	6.6467
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-1.0753
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	4e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.9718
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.436
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.046
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.5528
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-6.3573		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.5717		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	53.8566		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.5568		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	373.779		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-1.07543981	-1.07543981	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3.9999999e-005	3.9999999e-005	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.184620216	-0.184620216	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0215859376	0.0215859376	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0215859376	0.0215859376	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.55280018	-8.55280018	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.184620216	-0.18462 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.184620216	-0.18462 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_M_f32	2.5456
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-1.1683
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	5e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.599
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.8895
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.266
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.6661
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.619
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9416
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.7797
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	1.496
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.1656
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	106.042
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.6753
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	474.591
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-1.16834986	-1.16834986	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	4.99999987e-005	4.99999987e-005	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.66610003	-4.66610003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.61899996	5.61899996	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.6 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsM_f32	0.5535
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-4.2826
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	6e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.4045
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2729
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.2978
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6988
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.3118
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.9811
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1113
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.3522
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800





# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-4.3736		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.944		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	48.3541		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.9318		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	94.999		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-4.28293943	-4.28293943	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	5.99999985e-005	5.99999985e-005	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.3118	7.3118	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.98110008	-7.98110008	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.135714054	-0.135714054	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.35220003	2.35220003	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	3.446
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.1277
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	7e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	1.6609
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4658
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3856
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.7545
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.4084
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2084
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.2876
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.8522		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.5091		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	63.0003		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.0612		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	281.243		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.128286704	0.128286704	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	7.00000019e-005	7.00000019e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.15682995	-1.15682995	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.183985934	0.183985934	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.75450039	8.75450039	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.40840006	5.40840006	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.183985934	0.183985934	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.28760004	-6.28760004	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.15682995	-1.15683 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.15682995	-1.15683 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.8 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_M_f32	0.0446
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-8.7677
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	8e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.4446
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.5975
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3225
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.1974
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.2359
PSATA_PrevSlewRateLmt_Uls_M_f32	0.002
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-1.739
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.4837
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	79.7518
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.9503
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	500.104
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-8.76632023	-8.76632023	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	7.9999998e-005	7.9999998e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.230140716	0.230140716	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-6.95030022	-6.95030022	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.197400004	0.197400004	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.2359	0.2359	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00400000019	0.00400000019	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.230140716	0.230141 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.230140716	0.230141 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.9 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	8.6729
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.9006
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.7133
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.8625
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.837
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.8427
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.0846
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0408
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.1405
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.9636		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.5045		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	86.1143		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.7314		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	166.74		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.900600016	-0.900600016	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.0842319205	0.0842319205	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.016385939	0.016385939	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.8427	4.8427	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.0846000016	0.0846000016	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.016385939	0.016385939	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.14050007	5.14050007	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00325000007	0.00325000007	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.0842319205	0.0842319 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.0842319205	0.0842319 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.10 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	2.7624
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-6.4071
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.5861
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8383
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.5024
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9798
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.3223
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.1389
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.4605
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00325
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_S_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_S_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-17.4939		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.8405		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.113		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.8041		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	421.177		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00617412105	0.00617412105	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-6.0286622	-6.0286622	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.449257314	-0.449257314	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.32229996	6.32229996	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.1388998	-8.1388998	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.460500002	-0.460500002	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00525000039	0.00525000039	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00617412105	0.00617412 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.449257314	-0.449257 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.449257314	-0.449257 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.11 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_M_f32	5.3861
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-3.4844
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.796
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0906
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.666
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.6243
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.0147
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9149
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.7919
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00525
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	20.6156
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.1021
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	69.046
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.0496
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	198.835
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	1.04960001	1.04960001	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-3.48426819	-3.48426819	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1.99999995e-005	1.99999995e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.6243	5.6243	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.01469994	5.01469994	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0023124998	0.0023124998	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.12 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	4.8216
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.3204
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.797
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.4372
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4712
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	0.6721
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.6661
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.0572
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8635
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.3238
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0023125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-26.9871		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.1152		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	59.8972		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.0195		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	234.955		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.271696031	0.271696031	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.839085937	0.839085937	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.66610003	-4.66610003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.05719995	-1.05719995	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.839085937	0.839085937	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.323799998	0.323799998	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00431250036	0.00431250036	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.271696031	0.271696 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.271696031	0.271696 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.7675
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.4238
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5396
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.5541
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.121
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.6518
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0043125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-7.1687		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.3801		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0.0809		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.8049		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	67.3128		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00737705082	-0.00737705082	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10	10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-7.46498871	-7.46498871	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.55410004	-7.55410004	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.12099981	-5.12099981	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.65180016	-7.65180016	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00631249975	0.00631249975	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00737705082	-0.00737705 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-7.46498871	-7.46499 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-7.46498871	-7.46499 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	3.0548
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	2.6645
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.5817
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9842
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.4604
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.6273
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.0753
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.6459
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.7524
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0063125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.783
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.6611
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	127.256
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.4996
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	460.083
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	2.6645	2.6645	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.07529998	-1.07529998	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.64589977	7.64589977	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00431250036	0.00431250036	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.15 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	4.0117
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.4238
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5396
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.5541
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.121
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.6518
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0043125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-7.1687		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.3801		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0.0809		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.8049		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	67.3128		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00737705082	-0.00737705082	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-7.46498871	-7.46498871	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.55410004	-7.55410004	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.12099981	-5.12099981	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.65180016	-7.65180016	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00631249975	0.00631249975	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00737705082	-0.00737705 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-7.46498871	-7.46499 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-7.46498871	-7.46499 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.16 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	2.6403
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-6.4071
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.5817
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9842
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.4604
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.6273
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.0753
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.6459
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.7524
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0063125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_S_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_S_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.783		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.6611		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	127.256		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.4996		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	460.083		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-6.4071002	-6.4071002	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.07529998	-1.07529998	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.64589977	7.64589977	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0.5
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.8087
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6632
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.8247
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.4071
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.4984
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7815
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.1083
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	16.8246
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.0008
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	86.5389
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.3979
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	300.858
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	9.70214653	9.70214653	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	6.3979001	6.3979001	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.4071002	-6.4071002	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.49839997	-1.49839997	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.805914044	-0.805914044	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.10830021	-6.10830021	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.18 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	10
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.5986
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8935
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.0168
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.857
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.4844
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.5117
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0053
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.9294
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.295		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.8298		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	74.1445		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.7515		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	429.855		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	1.75150001	1.75150001	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.48440003	-3.48440003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.51170015	5.51170015	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0191140622	-0.0191140622	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.92939997	-5.92939997	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.19 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	7.6558
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.4579
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.3183
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.7478
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.5242
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.6792
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9545
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.8062
PSATA_PrevSlewRateLmt_Uls_M_f32	0.002
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.1952		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.0542		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	146.598		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.401		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	507.707		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-1.40100002	-1.40100002	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.524200022	-0.524200022	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.67920017	-5.67920017	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00137499999	0.00137499999	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.20 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.8469
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.0034
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.049
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.3977
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.4369
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7595
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.63
PSATA_PrevSlewRateLmt_Uls_M_f32	0.001375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.2485
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.2119
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	162.612
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.8273
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	255.621
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	5.82730007	5.82730007	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.39769983	5.39769983	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.43690014	-7.43690014	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.783914089	-0.783914089	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.63000011	-2.63000011	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.000749999948	0.000749999948	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.21 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	1.1748
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.6186
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3215
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.011
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.096
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.1381
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7941
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.9961
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00075
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	24.7646		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.7409		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	4.8831		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.8273		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	11.472		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-4.61511374	-4.61511374	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.769685924	0.769685924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.13809991	-2.13809991	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.769685924	0.769685924	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.99609995	-5.99609995	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00274999999	0.00274999999	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-4.61511374	-4.61511 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-4.61511374	-4.61511 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	1.8277
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.6186
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3215
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.011
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.3002
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.096
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.1381
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7941
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.9961
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00275
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_S_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_S_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	24.7646		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.7409		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	4.8831		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.8273		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	11.472		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	5.82730007	5.82730007	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-4.61511374	-4.61511374	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.769685924	0.769685924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.13809991	-2.13809991	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.769685924	0.769685924	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.99609995	-5.99609995	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475000031	0.00475000031	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-4.61511374	-4.61511 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-4.61511374	-4.61511 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.23 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	5.9016
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.649
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1105
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.825
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	0.7114
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.7326
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	8.3759
target_PSATA_Per1_HwTorque_HwNm_f32.value	0.5181
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	40.8231
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.0353
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	257.137
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.58515644	8.58515644	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	6.03529978	6.03529978	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.73260021	-6.73260021	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00412499998	0.00412499998	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.24 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	8.0045
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.6209
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0788
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.0043
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.3002
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5059
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.794
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2089
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.4134
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.5006
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.7269
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	126.679
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.0023
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	180.486
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo=	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.79399991	3.79399991	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.233314067	-0.233314067	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.4133997	-8.4133997	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00349999988	0.00349999988	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.25 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.8481
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.1774
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.2075
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.2273
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.5117
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.128
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0035
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-30.0622		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.5807		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	29.2708		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.261		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	317.407		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.22730017	-5.22730017	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.51170015	5.51170015	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.152414069	-0.152414069	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00287500001	0.00287500001	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.26 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	1.7161
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.6545
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9185
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.1133
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4368
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8744
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.7981
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7744
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.5609
PSATA_PrevSlewRateLmt_Uls_M_f32	0.002875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.8707
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.7338
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	29.4542
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.0671
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	153.737
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	5.06710005	5.06710005	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.79810047	8.79810047	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.798814058	-0.798814058	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.56089973	8.56089973	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0022499999	0.0022499999	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.27 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	1.1594
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.1053
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.0848
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5199
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.4369
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0616
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.0174
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00225
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	16.2332		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.4062		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	138.716		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.2854		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-4.28539991	-4.28539991	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	2	2	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.43690014	-7.43690014	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0860140622	-0.0860140622	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.01739979	6.01739979	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0016249998	0.0016249998	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.28 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	2.6024
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.9367
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1029
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.9462
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.1462
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	2
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.0703
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.7986
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.1557
PSATA_PrevSlewRateLmt_Uls_M_f32	0.001625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.6938		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.493		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	1.0184		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.0419		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	183.842		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.07029998	1.07029998	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.7986002	6.7986002	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1.02441406	-1.02441406	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.15570021	-6.15570021	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00362500013	0.00362500013	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.29 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	2.6129
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.5819
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.876
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	0.2853
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.589
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	1.2731
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.9496
PSATA_PrevSlewRateLmt_Uls_M_f32	0.003625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	9.0076
target_PSATA_Per1_HwTorque_HwNm_f32.value	0.119
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	158.168
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.485
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	421.4
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	2.87758827	2.87758827	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.58900023	-7.58900023	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	1.27310002	1.27310002	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.94959998	2.94959998	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00299999979	0.00299999979	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	2.87758827	2.87759 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	2.87758827	2.87759 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.30 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.7322
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5008
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.7346
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0718
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.9931
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.8788
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2422
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0.003
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.5855		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.7667		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	10.1425		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.5279		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	75.5703		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00585263688	-0.00585263688	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.9165163	1.9165163	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.21778594	0.21778594	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.99310017	-5.99310017	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.878799975	0.878799975	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.21778594	0.21778594	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00499999989	0.00499999989	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00585263688	-0.00585264 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.9165163	1.91652 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.9165163	1.91652 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.31 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	3.801
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.1165
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.7116
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.7523
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.6379
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.2541
PSATA_PrevSlewRateLmt_Uls_M_f32	0.005
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-6.5075		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.3164		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	24.137		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.0051		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	364.529		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.752300024	0.752300024	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.63790035	-8.63790035	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0244140625	-0.0244140625	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.25409985	-7.25409985	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00437500002	0.00437500002	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.32 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	5.1749
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.3219
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2237
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.4575
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.6631
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.5545
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9511
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.679
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.8205
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	20.7874
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.9596
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	53.8359
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.9864
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	175.246
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-5.98640013	-5.98640013	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.554499984	0.554499984	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.95109987	-4.95109987	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.703414083	-0.703414083	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.8204999	-4.8204999	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00374999992	0.00374999992	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.33 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0.0969
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.4886
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.8138
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.3204
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.2654
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6704
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9772
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.9574		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.4146		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	189.38		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.6948		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	342.315		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-2.41459984e-005	-2.41459984e-005	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	9.99999975e-006	9.99999975e-006	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	4.50717306	4.50717306	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.645985961	0.645985961	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.3204	0.3204	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.26539993	6.26539993	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.645985961	0.645985961	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.97720003	6.97720003	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00312499981	0.00312499981	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	4.50717306	4.50717 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	4.50717306	4.50717 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.34 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	4.031
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.6656
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.8894
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.6589
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.957
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.9222
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6533
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.1735
PSATA_PrevSlewRateLmt_Uls_M_f32	0.003125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-22.7058		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.2915		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0.7644		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.6165		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	243.451		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.0067939451	-0.0067939451	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.9957695	-1.9957695	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.628885925	0.628885925	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.95699978	-6.95699978	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.9222002	6.9222002	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.628885925	0.628885925	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.17350006	-3.17350006	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00512500014	0.00512500014	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.0067939451	-0.00679395 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.9957695	-1.99577 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.9957695	-1.99577 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.35 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_M_f32	3.581
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-2.1157
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5422
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.1983
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.8206
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.5389
PSATA_PrevSlewRateLmt_Uls_M_f32	0.005125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	23.1752
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.587
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	50.7501
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.9864
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	197.011
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00409990223	-0.00409990223	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-5.98640013	-5.98640013	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.50132918	1.50132918	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.19829988	-4.19829988	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.82060003	7.82060003	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.53890002	1.53890002	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00712500047	0.00712500047	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00409990223	-0.0040999 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.50132918	1.50133 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.50132918	1.50133 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.36 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.9486
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.0152
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5914
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.3834
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.6434
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8274
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.6581
PSATA_PrevSlewRateLmt_Uls_M_f32	0.007125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.5628		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.1467		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	28.8103		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.4957		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	309.425		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00623378903	0.00623378903	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.000102799851	0.000102799895	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.38339996	6.38339996	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.64340019	-4.64340019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00650000013	0.00650000013	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00623378903	0.00623379 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.37 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.1775
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	5.9681
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5699
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.8894
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.1431
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.0474
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1297
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.419
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0065
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-24.5041		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.9336		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	150.265		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.006		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	148.904		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00395253906	-0.00395253906	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.0474	-4.0474	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.154114068	-0.154114068	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.41900015	6.41900015	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00587500026	0.00587500026	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00395253906	-0.00395254 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.00395253906	-0.00395254 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.38 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_M_f32	0.2973
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.9906
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0018
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4643
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.6394
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.5394
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7839
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.1185
PSATA_PrevSlewRateLmt_Uls_M_f32	0.005875
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	20.8459
target_PSATA_Per1_HwTorque_HwNm_f32.value	10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	108.93
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.4806
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	324.311
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.0084369136	0.0084369136	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.63939953	8.63939953	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.5394001	6.5394001	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00524999993	0.00524999993	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.0084369136	0.00843691 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.0084369136	0.00843691 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.39 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	6.9478
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.0269
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3701
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.7937
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7198
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.9834
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.0714
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0207
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.6613
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00525
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-9.9639		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.9336		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	5.0315		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.9361		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	44.558		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.98339999	1.98339999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.07139993	2.07139993	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0451140627	-0.0451140627	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.66130018	4.66130018	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00462500006	0.00462500006	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.40 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.6536
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.657
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3676
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.9754
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.7476
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6035
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	22.6823		
target_PSATA_Per1_HwTorque_HwNm_f32.value	0.8573		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	183.878		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.1783		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	112.41		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.000952539034	0.000952539034	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.975399971	0.975399971	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.747600019	0.747600019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00662500039	0.00662500039	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.000952539034	0.000952539 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.000952539034	0.000952539 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.41 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_M_f32	1.0969
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.1682
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.9055
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4057
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.9848
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.4107
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0.006625
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-13.7197
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.2614
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	83.5795
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4877
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	271.072
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00389140635	-0.00389140635	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-0.487699986	-0.487699986	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-6.52279996e-005	-6.52279996e-005	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1.99999995e-005	1.99999995e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.9848001	-3.9848001	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.41070008	-3.41070008	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00600000005	0.00600000005	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00389140635	-0.00389141 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.4362
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.5165
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.5864
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8005
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.786
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.3904
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4516
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.2199
PSATA_PrevSlewRateLmt_Uls_M_f32	0.006
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.3142		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.5198		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	168.691		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.7136		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	468.43		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00565039041	0.00565039041	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.000165594	0.000165594	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2.99999992e-005	2.99999992e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.78599977	5.78599977	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.39039993	4.39039993	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00537500018	0.00537500018	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00565039041	0.00565039 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.00565039041	0.00565039 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.43 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.5135
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	4e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.022
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.6887
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8582
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.5903
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.7963
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0
PSATA_PrevSlewRateLmt_Uls_M_f32	0.005375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	8.4165		
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.3381		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	95.894		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.3867		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	256.325		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.000173523993	0.000173523993	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3.9999999e-005	3.9999999e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.59030008	4.59030008	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.79629993	7.79629993	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475000031	0.00475000031	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0.0999
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	5e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.1893
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.39
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5311
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5418
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.9355
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.3438
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00475
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	12.435
target_PSATA_Per1_HwTorque_HwNm_f32.value	-5.9146
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	156.111
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.6776
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	232.734
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-0.000295729988	-0.000295729988	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	4.99999987e-005	4.99999987e-005	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.93549991	-2.93549991	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00412499998	0.00412499998	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.45 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.058
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6746
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.1642
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7072
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.4557
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.8464
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7577
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3727
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-17.8991		
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.7107		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	75.7164		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.2614		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	384.659		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00825751945	0.00825751945	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.45569992	8.45569992	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.84639978	-7.84639978	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00612500031	0.00612500031	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00825751945	0.00825752 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.00825751945	0.00825752 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.46 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	4.8744
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6819
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.8816
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3058
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.9797
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.5498
PSATA_PrevSlewRateLmt_Uls_M_f32	0.006125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.6547		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	53.0496		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.6193		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	452.013		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	3.61929989	3.61929989	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	7.36547852	7.36547852	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.97969997	-1.97969997	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.54979992	7.54979992	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0081249997	0.0081249997	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	7.36547852	7.36548 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	7.36547852	7.36548 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.47 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_M_f32	4.1039
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.8394
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.8582
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.078
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.726
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.3277
PSATA_PrevSlewRateLmt_Uls_M_f32	0.008125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.6751
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.2973
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.538
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	196.544
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00210546865	-0.00210546865	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-7.53800011	-7.53800011	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	4.59460025e-005	4.59460025e-005	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	1.99999995e-005	1.99999995e-005	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.29528546	1.29528546	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.00559179671	0.00559179671	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	2	2	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00600000005	0.00600000005	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.07799995	-1.07799995	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.72599983	5.72599983	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.32770002	1.32770002	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.010125	0.010125	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00210546865	-0.00210547 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.29528546	1.29529 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.29528546	1.29529 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.48 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	3.9859
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	3e-005
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	1
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2863
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	2
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.7747
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.1786
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1191
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.0462
PSATA_PrevSlewRateLmt_Uls_M_f32	0.010125
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.756		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.9234		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	189.511		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.4719		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	195.172		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00541933579	0.00541933579	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	4.47189999	4.47189999	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0.000117701995	0.000117701995	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	2.99999992e-005	2.99999992e-005	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	3	3	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.001953125	0.001953125	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.77469993	2.77469993	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.17860007	3.17860007	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.143514067	-0.143514067	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.0461998	-5.0461998	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00949999969	0.00949999969	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00541933579	0.00541934 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.49 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.3941
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.1277
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	3
PSATA_PosServoSmoothing_Uls_M_f32	0.6915
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.3092
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.5506
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5774
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3338
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0095
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.5248		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.549		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	38.7459		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.7657		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	166.63		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00451015634	0.00451015634	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	2	2	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.30920005	2.30920005	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.55060005	-5.55060005	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.601814091	-0.601814091	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.33379984	4.33379984	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0115	0.0115	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00451015634	0.00451016 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.00451015634	0.00451016 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.50 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	7.281
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.1039
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.7929
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	2
PSATA_PosServoSmoothing_Uls_M_f32	0.5035
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.1827
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.9186
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0398
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.2082
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0115
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	11.8736
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.1906
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	9.2087
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.5445
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	156.481
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.18269992	3.18269992	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.91860008	-6.91860008	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0642140657	-0.0642140657	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.20820045	8.20820045	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00149999931	0.00149999931	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.51 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	1.1833
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.4394
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.6194
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.388
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.1962
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.4675
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.2866
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0015
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	6.5428
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.7546
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	133.818
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.5445
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	306.048
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.38800001	-4.38800001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.19620037	-8.19620037	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00350000011	0.00350000011	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.52 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.1393
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.4967
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6909
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.6345
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0843
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.3259
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2086
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.5974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0035
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-3.6755		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.4157		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	172.434		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.9864		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	62.1189		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-5.98640013	-5.98640013	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.325899988	-0.325899988	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.233014062	-0.233014062	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.59739995	-1.59739995	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.53 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	2.4296
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.7103
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5975
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.0493
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	1
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.7749
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.1352
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.679
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.3705
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	30.5289
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.6932
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	129.956
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.9864
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	402.634
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	9.86828804	9.86828804	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.77490044	-8.77490044	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.13519955	-8.13519955	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.703414083	-0.703414083	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.37050009	5.37050009	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.54 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	1.31
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.3516
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.6762
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3278
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.5874
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.5047
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2788
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.2661
PSATA_PrevSlewRateLmt_Uls_M_f32	0.002
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	0.4179		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.1243		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	68.7937		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.8688		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	323.683		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.59400773	-1.59400773	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.254385948	0.254385948	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.58739996	-2.58739996	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.50469995	2.50469995	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.254385948	0.254385948	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.26609993	-6.26609993	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00400000019	0.00400000019	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.59400773	-1.59401 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.59400773	-1.59401 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.55 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	4.1629
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	1
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8947
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.6313
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.7684
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8922
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.6164
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.5062
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6716
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3299
PSATA_PrevSlewRateLmt_Uls_M_f32	0.004
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.4321		
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.4951		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	51.7598		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.3845		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	388.753		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00255507813	0.00255507813	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-3.38450003	-3.38450003	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	9.8633709	9.8633709	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.00342402351	0.00342402351	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00600000005	0.00600000005	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.6164	2.6164	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.50620008	3.50620008	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.696014047	-0.696014047	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.32989979	4.32989979	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00600000005	0.00600000005	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00255507813	0.00255508 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.56 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	8.8622
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	1
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	1
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.5411
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.6534
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	1
PSATA_PosServoSmoothing_Uls_M_f32	0.3356
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.757
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.8755
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7227
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.1795
PSATA_PrevSlewRateLmt_Uls_M_f32	0.006
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.7836
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.5775
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	189.364
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.6814
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	420.887
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00171582028	-0.00171582028	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.52191424	-1.52191424	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.698285937	0.698285937	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.00280810543	0.00280810543	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00800000038	0.00800000038	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.75699997	-1.75699997	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.87549996	2.87549996	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.698285937	0.698285937	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.1795001	-2.1795001	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00800000038	0.00800000038	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00171582028	-0.00171582 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.52191424	-1.52191 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.52191424	-1.52191 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.57 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0.0892
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	1
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	2
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	2
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.1729
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.741
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.577
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.1664
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0269
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.4998
PSATA_PrevSlewRateLmt_Uls_M_f32	0.008
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-16.7661		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.1837		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	57.2333		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.8851		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	506.456		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.00797499996	-0.00797499996	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00782159995	0.00782159995	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.16639996	-8.16639996	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0513140634	-0.0513140634	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.49979973	-8.49979973	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00737500051	0.00737500051	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 2.58 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.1146
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	2
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	3
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	3
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.0408
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.6558
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.2502
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.5995
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.1056
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6281
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.0747
PSATA_PrevSlewRateLmt_Uls_M_f32	0.007375
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	6.252		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.2808		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	36.4307		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.2752		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	169.335		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0.00156201166	-0.00156201166	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.85615313	-1.85615313	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.603685915	0.603685915	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.00498593738	0.00498593738	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00759240007	0.00759240007	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.59949994	-1.59949994	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.10559988	5.10559988	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.603685915	0.603685915	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.07470012	-3.07470012	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675000018	0.00675000018	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0.00156201166	-0.00156201 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.85615313	-1.85615 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.85615313	-1.85615 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 2.59 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	2.2154
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	3
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	4
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	2
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.653
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7643
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3406
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.4535
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9816
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.0165107418	0.0165107418	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00959239993	0.00959239993	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.45349979	8.45349979	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓



Test Case 3: PRM Range Test



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

## Specification

Performance Metrics:  
(With "None" Instrumentation and  
"WithPS" Environment)  
CPU Cycles:

TS 3.1 2098.00 Cycles  
TS 3.2 2085.00 Cycles  
TS 3.3 2034.00 Cycles  
TS 3.4 2034.00 Cycles  
TS 3.5 2034.00 Cycles  
TS 3.6 2034.00 Cycles  
TS 3.7 2034.00 Cycles  
TS 3.8 2034.00 Cycles  
TS 3.9 2034.00 Cycles  
TS 3.10 2618.00 Cycles  
TS 3.11 2034.00 Cycles  
TS 3.12 2034.00 Cycles  
TS 3.13 2034.00 Cycles  
TS 3.14 2034.00 Cycles  
TS 3.15 2034.00 Cycles  
TS 3.16 2034.00 Cycles  
TS 3.17 2034.00 Cycles  
TS 3.18 2034.00 Cycles  
TS 3.19 2628.00 Cycles  
TS 3.20 2034.00 Cycles  
TS 3.21 2034.00 Cycles  
TS 3.22 2034.00 Cycles  
TS 3.23 2034.00 Cycles  
TS 3.24 2034.00 Cycles  
TS 3.25 2034.00 Cycles  
TS 3.26 2034.00 Cycles  
TS 3.27 2034.00 Cycles  
TS 3.28 2657.00 Cycles  
TS 3.29 2034.00 Cycles  
TS 3.30 2034.00 Cycles  
TS 3.31 2034.00 Cycles  
TS 3.32 2034.00 Cycles  
TS 3.33 2034.00 Cycles  
TS 3.34 2034.00 Cycles  
TS 3.35 2034.00 Cycles  
TS 3.36 2034.00 Cycles  
TS 3.37 2034.00 Cycles  
TS 3.38 2034.00 Cycles  
TS 3.39 2034.00 Cycles  
TS 3.40 2034.00 Cycles  
TS 3.41 2070.00 Cycles  
TS 3.42 2034.00 Cycles  
TS 3.43 2034.00 Cycles  
TS 3.44 2034.00 Cycles  
TS 3.45 2034.00 Cycles  
TS 3.46 2582.00 Cycles  
TS 3.47 2034.00 Cycles  
TS 3.48 2034.00 Cycles  
TS 3.49 2034.00 Cycles  
TS 3.50 2034.00 Cycles  
TS 3.51 2034.00 Cycles  
TS 3.52 2034.00 Cycles  
TS 3.53 2034.00 Cycles  
TS 3.54 2034.00 Cycles  
TS 3.55 2034.00 Cycles  
TS 3.56 2034.00 Cycles  
TS 3.57 2034.00 Cycles  
TS 3.58 2034.00 Cycles  
TS 3.59 2034.00 Cycles  
TS 3.60 2034.00 Cycles  
TS 3.61 2034.00 Cycles  
TS 3.62 2034.00 Cycles  
TS 3.63 2034.00 Cycles  
TS 3.64 2034.00 Cycles  
TS 3.65 2034.00 Cycles  
TS 3.66 2034.00 Cycles  
TS 3.67 2671.00 Cycles  
TS 3.68 2034.00 Cycles  
TS 3.69 2034.00 Cycles  
TS 3.70 2034.00 Cycles  
TS 3.71 2034.00 Cycles  
TS 3.72 2034.00 Cycles  
TS 3.73 2035.00 Cycles  
TS 3.74 2034.00 Cycles  
TS 3.75 2034.00 Cycles  
TS 3.76 2034.00 Cycles  
TS 3.77 2035.00 Cycles  
TS 3.78 2034.00 Cycles  
TS 3.79 2034.00 Cycles  
TS 3.80 2034.00 Cycles  
TS 3.81 2034.00 Cycles  
TS 3.82 2034.00 Cycles  
TS 3.83 2034.00 Cycles  
TS 3.84 2034.00 Cycles  
TS 3.85 2034.00 Cycles  
TS 3.86 2050.00 Cycles  
TS 3.87 2034.00 Cycles  
TS 3.88 2034.00 Cycles  
TS 3.89 2034.00 Cycles  
TS 3.90 2034.00 Cycles  
TS 3.91 2034.00 Cycles  
TS 3.92 2034.00 Cycles  
TS 3.93 2034.00 Cycles  
TS 3.94 2034.00 Cycles  
TS 3.95 2034.00 Cycles  
TS 3.96 2034.00 Cycles  
TS 3.97 2067.00 Cycles  
TS 3.98 2034.00 Cycles  
TS 3.99 2034.00 Cycles  
TS 3.100 2034.00 Cycles  
TS 3.101 2034.00 Cycles  
TS 3.102 2034.00 Cycles  
TS 3.103 2034.00 Cycles  
TS 3.104 2034.00 Cycles  
TS 3.105 2034.00 Cycles  
TS 3.106 2034.00 Cycles  
TS 3.107 2178.00 Cycles  
TS 3.108 2178.00 Cycles  
TS 3.109 2100.00 Cycles

TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

TS 3.110 2100.00 Cycles  
TS 3.111 2100.00 Cycles

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Description Vector Description:

TS3.1k\_PsaTA\_LxaMaxRate\_UlspS\_f32==>Min  
TS3.2k\_PsaTA\_LxaMaxRate\_UlspS\_f32==>Max  
TS3.3k\_PsaTA\_LxaMaxRate\_UlspS\_f32==>Pos/Default  
TS3.4t\_PsaTA\_LxaHwVelSftyThdY\_HwRadpS\_u7p9[15]==>Min  
TS3.5t\_PsaTA\_LxaHwVelSftyThdY\_HwRadpS\_u7p9[15]==>Max  
TS3.6t\_PsaTA\_LxaHwVelSftyThdY\_HwRadpS\_u7p9[15]==>Pos/Default  
TS3.7k\_PsaTA\_LxaSftyMaxHwTorque\_HwNm\_f32==>Min  
TS3.8k\_PsaTA\_LxaSftyMaxHwTorque\_HwNm\_f32==>Max  
TS3.9k\_PsaTA\_LxaSftyMaxHwTorque\_HwNm\_f32==>Pos/Default  
TS3.10k\_PsaTA\_LxaOpTrqOvFltMode\_Cnt\_str.Threshold==>Min  
TS3.11k\_PsaTA\_LxaOpTrqOvFltMode\_Cnt\_str.Threshold==>Max  
TS3.12k\_PsaTA\_LxaOpTrqOvFltMode\_Cnt\_str.Threshold==>Pos/Default  
TS3.13k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Nstep==>Min  
TS3.14k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Nstep==>Max  
TS3.15k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Nstep==>Pos/Default  
TS3.16k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Pstep==>Min  
TS3.17k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Pstep==>Max  
TS3.18k\_PSATA\_LxaOpTrqOvFltMode\_Cnt\_str.Pstep==>Pos/Default  
TS3.19k\_PsaTA\_LxaHwTrqFltMode\_Cnt\_str.Threshold==>Min  
TS3.20k\_PsaTA\_LxaHwTrqFltMode\_Cnt\_str.Threshold==>Max  
TS3.21k\_PsaTA\_LxaHwTrqFltMode\_Cnt\_str.Threshold==>Pos/Default  
TS3.22k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Nstep==>Min  
TS3.23k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Nstep==>Max  
TS3.24k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Nstep==>Pos/Default  
TS3.25k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Pstep==>Min  
TS3.26k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Pstep==>Max  
TS3.27k\_PSATA\_LxaHwTrqFltMode\_Cnt\_str.Pstep==>Pos/Default  
TS3.28k\_PsaTA\_LxaHwVelFltMode\_Cnt\_str.Threshold==>Min  
TS3.29k\_PsaTA\_LxaHwVelFltMode\_Cnt\_str.Threshold==>Max  
TS3.30k\_PsaTA\_LxaHwVelFltMode\_Cnt\_str.Threshold==>Pos/Default  
TS3.31k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Nstep==>Min  
TS3.32k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Nstep==>Max  
TS3.33k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Nstep==>Pos/Default  
TS3.34k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Pstep==>Min  
TS3.35k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Pstep==>Max  
TS3.36k\_PSATA\_LxaHwVelFltMode\_Cnt\_str.Pstep==>Pos/Default  
TS3.37t\_PsaTA\_LxaSmotngFactorX\_Uls\_u6p10[10]==>Min  
TS3.38t\_PsaTA\_LxaSmotngFactorX\_Uls\_u6p10[10]==>Max  
TS3.39t\_PsaTA\_LxaSmotngFactorX\_Uls\_u6p10[10]==>Pos/Default  
TS3.40t\_PsaTA\_LxaSmotngFactorY\_Uls\_u6p10[10]==>Min  
TS3.41t\_PsaTA\_LxaSmotngFactorY\_Uls\_u6p10[10]==>Max  
TS3.42t\_PsaTA\_LxaSmotngFactorY\_Uls\_u6p10[10]==>Pos/Default  
TS3.43k\_PsaTA\_LxaHwTrqRateSftyThd\_HwNmpS\_f32==>Min  
TS3.44k\_PsaTA\_LxaHwTrqRateSftyThd\_HwNmpS\_f32==>Max  
TS3.45k\_PsaTA\_LxaHwTrqRateSftyThd\_HwNmpS\_f32==>Pos/Default  
TS3.46k\_PsaTA\_LxaHwTrqRateFltMode\_Cnt\_str.Threshold==>Min  
TS3.47k\_PsaTA\_LxaHwTrqRateFltMode\_Cnt\_str.Threshold==>Max  
TS3.48k\_PsaTA\_LxaHwTrqRateFltMode\_Cnt\_str.Threshold==>Pos/Default  
TS3.49k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Nstep==>Min  
TS3.50k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Nstep==>Max  
TS3.51k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Nstep==>Pos/Default  
TS3.52k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Pstep==>Min  
TS3.53k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Pstep==>Max  
TS3.54k\_PSATA\_LxaHwTrqRateFltMode\_Cnt\_str.Pstep==>Pos/Default  
TS3.55k\_PsaTA\_MaxPosServoCmdSafety\_MtrNm\_f32==>Min  
TS3.56k\_PsaTA\_MaxPosServoCmdSafety\_MtrNm\_f32==>Max  
TS3.57k\_PsaTA\_MaxPosServoCmdSafety\_MtrNm\_f32==>Pos/Default  
TS3.58k\_PsaTA\_MaxVehicleSpeedSafety\_Kph\_f32==>Min  
TS3.59k\_PsaTA\_MaxVehicleSpeedSafety\_Kph\_f32==>Max  
TS3.60k\_PsaTA\_MaxVehicleSpeedSafety\_Kph\_f32==>Pos/Default  
TS3.61k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Pstep==>Min  
TS3.62k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Pstep==>Max  
TS3.63k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Pstep==>Pos/Default  
TS3.64k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Nstep==>Min  
TS3.65k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Nstep==>Max  
TS3.66k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Nstep==>Pos/Default  
TS3.67k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Threshold==>Min  
TS3.68k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Threshold==>Max  
TS3.69k\_PsaTA\_ApaPosServFltMode\_Cnt\_str.Threshold==>Pos/Default  
TS3.70t\_PSATA\_DisableRateX\_HwNm\_u11p5[6]==>Min  
TS3.71t\_PSATA\_DisableRateX\_HwNm\_u11p5[6]==>Max  
TS3.72t\_PSATA\_DisableRateX\_HwNm\_u11p5[6]==>Pos/Default  
TS3.73t\_PSATA\_DisableRateY\_UlspS\_u11p5[6]==>Min  
TS3.74t\_PSATA\_DisableRateY\_UlspS\_u11p5[6]==>Max  
TS3.75t\_PSATA\_DisableRateY\_UlspS\_u11p5[6]==>Pos/Default  
TS3.76k\_PsaTA\_DisableRate\_UlspS\_f32==>Min  
TS3.77k\_PsaTA\_DisableRate\_UlspS\_f32==>Max  
TS3.78k\_PsaTA\_DisableRate\_UlspS\_f32==>Pos/Default  
TS3.79k\_PsaTA\_APAEnableRate\_UlspS\_f32==>Min  
TS3.80k\_PsaTA\_APAEnableRate\_UlspS\_f32==>Max  
TS3.81k\_PsaTA\_APAEnableRate\_UlspS\_f32==>Pos/Default  
TS3.82t\_PSATA\_SmoothingX\_Uls\_u6p10[10]==>Min  
TS3.83t\_PSATA\_SmoothingX\_Uls\_u6p10[10]==>Max  
TS3.84t\_PSATA\_SmoothingX\_Uls\_u6p10[10]==>Pos/Default  
TS3.85t\_PSATA\_SmoothingY\_Uls\_u6p10[10]==>Min  
TS3.86t\_PSATA\_SmoothingY\_Uls\_u6p10[10]==>Max  
TS3.87t\_PSATA\_SmoothingY\_Uls\_u6p10[10]==>Pos/Default  
TS3.88t\_PsaTA\_LxaSmotngSftyFactorX\_Kph\_u9p7[10]==>Min  
TS3.89t\_PsaTA\_LxaSmotngSftyFactorX\_Kph\_u9p7[10]==>Max  
TS3.90t\_PsaTA\_LxaSmotngSftyFactorX\_Kph\_u9p7[10]==>Pos/Default  
TS3.91t\_PsaTA\_LxaSmotngSftyFactorY\_Uls\_u6p10[10]==>Min  
TS3.92t\_PsaTA\_LxaSmotngSftyFactorY\_Uls\_u6p10[10]==>Max  
TS3.93t\_PsaTA\_LxaSmotngSftyFactorY\_Uls\_u6p10[10]==>Pos/Default  
TS3.94t\_PSATA\_LxaOpTrqOvBndX\_Kph\_u9p7[10]==>Min

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

TS3.95t\_PSATA\_LxaOpTrqOvBndX\_Kph\_u9p7[10]==>Max  
 TS3.96t\_PSATA\_LxaOpTrqOvBndX\_Kph\_u9p7[10]==>Pos/Default  
 TS3.97t\_PSATA\_LxaOpTrqOvBndY\_MtrNm\_u4p12[10]==>Min  
 TS3.98t\_PSATA\_LxaOpTrqOvBndY\_MtrNm\_u4p12[10]==>Max  
 TS3.99t\_PSATA\_LxaOpTrqOvBndY\_MtrNm\_u4p12[10]==>Pos/Default  
 TS3.100t\_PSATA\_LxaOpTrqOvSftyBndY\_MtrNm\_u4p12[10]==>Min  
 TS3.101t\_PSATA\_LxaOpTrqOvSftyBndY\_MtrNm\_u4p12[10]==>Max  
 TS3.102t\_PSATA\_LxaOpTrqOvSftyBndY\_MtrNm\_u4p12[10]==>Pos/Default  
 TS3.103t\_PsaTA\_LxaHwVelSftyThdX\_Kph\_u9p7[15]==>Min  
 TS3.104t\_PsaTA\_LxaHwVelSftyThdX\_Kph\_u9p7[15]==>Max  
 TS3.105t\_PsaTA\_LxaHwVelSftyThdX\_Kph\_u9p7[15]==>Pos/Default  
 TS3.106 k\_PsaTA\_ApaPosnServoTran\_Uls\_f32==>Min  
 TS3.107 k\_PsaTA\_ApaPosnServoTran\_Uls\_f32==>Max  
 TS3.108 k\_PsaTA\_ApaPosnServoTran\_Uls\_f32==>Default  
 TS3.109 k\_PsaTA\_LxaPosnServoTran\_Uls\_f32==>Min  
 TS3.110 k\_PsaTA\_LxaPosnServoTran\_Uls\_f32==>Max  
 TS3.111 k\_PsaTA\_LxaPosnServoTran\_Uls\_f32==>Default

## Test Step 3.1 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	1.8828
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0048
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.0958
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0891
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.3257
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.7198
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-15.218
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.8011
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	184.323
target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.9585
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	349.129
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.6778127	1.6778127	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.32569981	-5.32569981	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.7198	1.7198	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.6778127	1.67781 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.6778127	1.67781 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.2 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.3947
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.0386
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.0346
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.6463
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6164
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.1787
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-17.5893		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.9944		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	112.89		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.8188		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	231.29		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.0103371097	-0.0103371097	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.00390625	0.00390625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0200000014	0.0200000014	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.03459978	-6.03459978	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.64630008	-2.64630008	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.640814066	-0.640814066	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.17870045	-8.17870045	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.3 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	5.8906
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.6997
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6099
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.2905
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.3357
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6634
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.8119
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.447
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.1116
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	13.9479
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.9592
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	496.652
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	4.35270834	4.35270834	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.638985932	0.638985932	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.29049969	-8.29049969	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.33570004	-1.33570004	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.638985932	0.638985932	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.81190014	6.81190014	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	4.35270834	4.35271 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	4.35270834	4.35271 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6943
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.5667
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9833
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3631
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.1968
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.9264
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5824
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.5075
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-17.9915
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.6772
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	82.1826
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.4533
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	317.034
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.19680023	5.19680023	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.92639995	-1.92639995	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.557985961	0.557985961	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.50750017	-4.50750017	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.5151217	-2.51512 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.5151217	-2.51512 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.4928
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.7421
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.1209
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.7863
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.2601
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.9189
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	51200
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	11.9998		
target_PSATA_Per1_HwTorque_HwNm_f32.value	1.0388		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	48.1886		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.8818		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	510.482		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-2.84763789	-2.84763789	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.786300004	-0.786300004	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.26009989	7.26009989	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.91890001	-2.91890001	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.84763789	-2.84764 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.84763789	-2.84764 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.6 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.947
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.4952
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3857
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.3859
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.8551
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9752
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7386
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	12.9363
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.971
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	178.228
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.9585
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	38.3568
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.38589954	-8.38589954	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.85510004	-1.85510004	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.7 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.1992
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7343
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.488
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.1971
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.1054
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5914
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.63
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	0
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	9.5832
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.9183
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	93.3764
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.9585
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	330.076
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.19710016	6.19710016	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.10540009	-3.10540009	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.566985965	0.566985965	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.63000011	-4.63000011	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.6251452	-2.62515 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.6251452	-2.62515 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.5272
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.2834
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.1167
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.2208
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5601
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.6329
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	100
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-15.2892		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.4584		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	72.8193		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8387		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	108.887		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0.838699996	0.838699996	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.116700001	-0.116700001	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.22079992	6.22079992	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.9 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.272
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.8866
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.86
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.6428
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.4461
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6155
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.5349
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	13.8113
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.7548
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	42.7623
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.6349
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	314.547
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.316171855	-0.316171855	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-3.63490009	-3.63490009	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.591085911	0.591085911	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.64279997	-1.64279997	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.4461	-2.4461	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.591085911	0.591085911	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.53490001	-0.53490001	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.316171855	-0.316172 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.316171855	-0.316172 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.10 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.6964
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1015
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.5623
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7818
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.5747
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.3069
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.8567
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-6.0028		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.0723		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	144.306		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.8957		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	143.285		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-4.73812819	-4.73812819	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-0.895699978	-0.895699978	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.895699978	-0.895699978	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.57469988	-6.57469988	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.30690002	6.30690002	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.85669994	-4.85669994	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-4.73812819	-4.73813 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-4.73812819	-4.73813 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	3	NTCDiagc_Fn	3	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.11 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.1016
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.6364
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.6886
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.7292
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.128
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-6.8354		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.2147		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	194.995		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.169		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	55.916		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-2.07604694	-2.07604694	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	3.16899991	3.16899991	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3	3	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.68860006	7.68860006	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.72919989	2.72919989	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.12800002	-2.12800002	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.07604694	-2.07605 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.07604694	-2.07605 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3.9824
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.5608
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.1164
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.5939
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7991
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.6667
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	9.9246
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.6037
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	19.6536
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.1847
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	425.492
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-5.18470001	-5.18470001	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3	-3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.1164	-2.1164	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.5939002	-4.5939002	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.13 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.0866
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.4927
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.2306
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.5817
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.0185
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7673
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.1951
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	26.545		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.0749		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	100.435		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.1076		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	498.357		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.58169997	1.58169997	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.01849985	-4.01849985	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.791714072	-0.791714072	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.195099995	0.195099995	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.14 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9912
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.785
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4702
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.9781
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.2746
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.1242
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6857
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.71
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.9203		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.6832		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	126.014		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.999		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	414.981		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-6.99900007	-6.99900007	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	3.11465669	3.11465669	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.661285937	0.661285937	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	1	1	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.27460003	-3.27460003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.12419987	6.12419987	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.661285937	0.661285937	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.71000004	4.71000004	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	3.11465669	3.11466 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	3.11465669	3.11466 ± 0.00001	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.15 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.455
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.3554
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4549
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.7171
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.0265
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.6479
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-7.7277
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.6801
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	36.9465
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.5074
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	483.453
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-6.50740004	-6.50740004	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.717100024	0.717100024	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.02650023	4.02650023	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.16 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.3934
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5527
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.7025
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.2217
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.358
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.939
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.0837
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.4032
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	15.22		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.5634		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	177.824		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	151.006		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.93899989	-3.93899989	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.08370018	-6.08370018	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.17 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.1985
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.1187
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9071
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0204
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.5099
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.1058
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3691
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.9048
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	19.0537		
target_PSATA_Per1_HwTorque_HwNm_f32.value	1.229		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	21.1008		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.3985		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	473.442		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	2.72467351	2.72467351	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.344685942	0.344685942	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.50990009	5.50990009	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.10579991	-2.10579991	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.344685942	0.344685942	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.90479994	7.90479994	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	2.72467351	2.72467 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	2.72467351	2.72467 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.18 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.0695
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9042
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.7908
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.894
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.8729
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.5402
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.5272
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	32.7281
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	90.8509
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0.853399992	0.853399992	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	3.77834678	3.77834678	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.79080009	-5.79080009	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.89400005	-5.89400005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.87290001	3.87290001	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	3.77834678	3.77835 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	3.77834678	3.77835 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.19 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.1011
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4506
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.156
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4141
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.7113
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.7718
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8864
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSstateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-5.2932		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.5432		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	154.914		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	220.469		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSstateForLxa_Cnt_enum	target_PSATA_Per1_EPSSstateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0.853399992	0.853399992	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	7.5854764	7.5854764	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.861985922	0.861985922	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.7112999	3.7112999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.77180004	7.77180004	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.861985922	0.861985922	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	7.5854764	7.58548 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	7.5854764	7.58548 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	2	NTCDiagc_Fn	2	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	2	NTCDiagc_Fn	2	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.20 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9621
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.373
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6273
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8084
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.3925
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.7416
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0805
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.8728
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	25.0075		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.9542		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	158.839		
target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.5073		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	166.083		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	6.5072999	6.5072999	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.217209622	0.217209622	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0560859367	0.0560859367	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.0020000009	0.0020000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.39249992	5.39249992	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.74160004	-8.74160004	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0560859367	0.0560859367	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.87280011	3.87280011	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.217209622	0.21721 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.217209622	0.21721 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.6026
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0191
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.6239
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.3838
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6331
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.1661
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	19.6679
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.5935
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	128.552
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.858
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	317.834
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.62390006	1.62390006	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.38380003	3.38380003	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.657514036	-0.657514036	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.16610003	1.16610003	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.22 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.4371
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0968
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.0849
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.0076
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.3389
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5358
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9284
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-30.063		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.806		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	11.1377		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.9746		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	504.801		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.00759983	-6.00759983	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.33889961	8.33889961	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.5825
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4668
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.8873
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.4358
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.8176
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6157
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.2027
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.5666		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.9007		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	171.644		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.6952		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	289.245		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-4.69519997	-4.69519997	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.11985366	0.11985366	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.591285944	0.591285944	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.43580008	7.43580008	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.81759977	7.81759977	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.591285944	0.591285944	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.202700004	0.202700004	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.11985366	0.119854 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.11985366	0.119854 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.1836
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7128
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.0446
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9871
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.2462
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.2628
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8933
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.1666
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	17.8044
target_PSATA_Per1_HwTorque_HwNm_f32.value	-9.0493
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	102.026
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.8818
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	251.882
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-2.7514143	-2.7514143	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.868885934	0.868885934	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.24619961	8.24619961	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.26279998	-2.26279998	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.868885934	0.868885934	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.16659999	-3.16659999	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.7514143	-2.75141 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.7514143	-2.75141 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.25 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-2.2133
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2823
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.9416
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.1689
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.9488
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6763
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.6322
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.6015
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.632
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	168.116
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.1967
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	336.064
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.16890001	-7.16890001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.94880009	5.94880009	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.651885927	0.651885927	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.6322	-2.6322	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.7158941	-1.71589 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.7158941	-1.71589 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.26 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.0128
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.6438
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0775
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.1156
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9098
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0425
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.7229
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-22.3688		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.9309		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	134.324		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.5595		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	263.237		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.0854180753	0.0854180753	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0180859379	0.0180859379	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.11560011	7.11560011	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.90980005	-4.90980005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0180859379	0.0180859379	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.72289991	4.72289991	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.0854180753	0.0854181 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.0854180753	0.0854181 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.27 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFit_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFit_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFit_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.21

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	26.0761
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.7525
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	131.705
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.1057
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	166.561
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-3.44362307	-3.44362307	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.11789989	-6.11789989	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.52540016	-5.52540016	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.52979994	-3.52979994	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-3.44362307	-3.44362 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-3.44362307	-3.44362 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.28 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.6758
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.0723
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4234
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.1692
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.2897
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.9168
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5333
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.528
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	24.7499		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.5946		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	87.2378		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.3671		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	218.057		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.28970003	-5.28970003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.91680002	-2.91680002	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.557714045	-0.557714045	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.52799988	6.52799988	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	3	NTCDiagc_Fn	3	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.29 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.9546
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.9203
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.4097
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.472
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.9567
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.3904		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.7249		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	63.5462		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.7458		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	235.162		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.92030001	-3.92030001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.40969992	4.40969992	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.30 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.2798
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8936
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.4965
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.739
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.4789
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.4687
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.5796
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.3685
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.086
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.7458
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	277.066
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-1.74580002	-1.74580002	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.49650002	2.49650002	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.73900008	-3.73900008	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.31 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.9549
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.7506
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.442
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.991
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.952
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3232
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.0342
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	13.6637		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.582		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	77.5079		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.1389		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	95.6998		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.2053622	-1.2053622	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.298785925	0.298785925	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.99099994	-2.99099994	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.95200014	5.95200014	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.298785925	0.298785925	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.03420019	-4.03420019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.2053622	-1.20536 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.2053622	-1.20536 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-2.0233
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4675
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.1822
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.3337
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.5708
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.2687
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6357
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.108
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-15.6291		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-5.0323		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	200		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.7215		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	134.766		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.28859079	-1.28859079	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.611285925	0.611285925	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.57080007	3.57080007	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.26869988	-3.26869988	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.611285925	0.611285925	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.10800004	-2.10800004	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.28859079	-1.28859 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.28859079	-1.28859 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.33 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.7234
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.4571
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.1201
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.1755
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.8745
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3549
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.2488
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	11.6253
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.8384
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	121.164
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.8657
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	476.662
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-5.86569977	-5.86569977	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.412710845	0.412710845	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.33048594	0.33048594	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.17549992	-6.17549992	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.874499977	0.874499977	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.33048594	0.33048594	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.24880004	1.24880004	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.412710845	0.412711 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.412710845	0.412711 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.34 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.6693
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0626
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.2893
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.2485
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.2664
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.6894
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2107
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.1905
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-22.3281		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.2385		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	29.524		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.369		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	217.693		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.2664001	-3.2664001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.6894002	6.6894002	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.235114068	-0.235114068	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.190500006	-0.190500006	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.35 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-3.8057
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.2478
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.7334
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.5516
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.3209
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	13.2904		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.5306		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	77.5705		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.814		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	454.442		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.313065529	0.313065529	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.733399987	0.733399987	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.55159998	-1.55159998	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0.320899993	0.320899993	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.313065529	0.313066 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.313065529	0.313066 ± 0.00001	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.36 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.6628
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8017
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.7079
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.4222
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.5396
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.3484
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7771
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.2562
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.5976
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.9206
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	48.7387
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.0728
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	180.664
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.192838132	-0.192838132	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.752685964	0.752685964	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.539600015	0.539600015	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.34840012	8.34840012	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.752685964	0.752685964	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.256199986	-0.256199986	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.192838132	-0.192838 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.192838132	-0.192838 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.37 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	5.0674
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.4623
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0892
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5104
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.742
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.3549
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0363
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.2083
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.1249		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.7211		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	78.2488		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.1058		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	172.592		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.74199998	1.74199998	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.35489988	3.35489988	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0607140623	-0.0607140623	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.20830011	-7.20830011	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.38 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.339
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7452
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5227
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2405
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.5032
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.3361
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.3828		
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.7795		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	97.8616		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.8279		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	221.026		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.240500003	-0.240500003	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.50320005	3.50320005	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.39 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.339
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7452
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5227
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2405
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.5032
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.3361
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.3828
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.7795
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	97.8616
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.8279
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	221.026
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-4.82789993	-4.82789993	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.240500003	-0.240500003	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.50320005	3.50320005	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.40 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.255
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0626
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.8696
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6596
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.108
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3451
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.4742
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	10.0271		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.582		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	104.032		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.7497		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	227.069		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.108000003	-0.108000003	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.41 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.255
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0626
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.8696
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6596
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.108
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3451
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.4742
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	10.0271		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.582		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	104.032		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.7497		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	227.069		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.108000003	-0.108000003	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.108000003	-0.108000003	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.108000003	-0.108000003	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.108000003	-0.108000003	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.108000003	-0.108 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.108000003	-0.108 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.42 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.6356
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8724
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.0565
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4779
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.9327
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.1881
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.4255
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-14.9707
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.2309
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	129.184
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9297
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	106.908
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.93269992	2.93269992	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.18809986	-4.18809986	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.43 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.6763
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9615
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.5177
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6863
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8362
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.7033
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.1688
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.8918
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	7.9046
target_PSATA_Per1_HwTorque_HwNm_f32.value	-5.1465
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	10.6786
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.8864
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	229.486
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.7033	-7.7033	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.16879988	-4.16879988	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.44 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.2177
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0141
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.6049
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6865
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8218
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.3432
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.2762
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.648
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	300
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	26.5707		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-5.6259		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	30.5274		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9076		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	159.47		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-1.90760005	-1.90760005	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.90760005	-1.90760005	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.34319997	2.34319997	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.27620006	-1.27620006	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.45 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.6512
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2722
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.0007
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.726
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.9318
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.2323
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.1165
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.0717
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	161.418
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.5489
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	212.362
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	5.10455799	5.10455799	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	7.54890013	7.54890013	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.72599983	8.72599983	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.93179989	7.93179989	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.2322998	5.2322998	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	5.10455799	5.10456 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	5.10455799	5.10456 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.46 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	1.4533
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.6425
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.3984
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.7624
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.6934
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-14.5504		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.3945		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	39.1218		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.7567		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	365.153		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.65205729	1.65205729	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-3.75670004	-3.75670004	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.39839983	4.39839983	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.76240015	-4.76240015	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.69340003	1.69340003	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.65205729	1.65206 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.65205729	1.65206 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.47 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.5664
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0597
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.2961
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4179
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.9684
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.9918
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	65535

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	23.9252		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.5079		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	84.8207		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.7056		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	476.374		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	5.70559978	5.70559978	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3	3	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.9684	5.9684	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.48 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.6483
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.9982
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.4441
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.5187
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5468
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.3111
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	7.9144
target_PSATA_Per1_HwTorque_HwNm_f32.value	-5.7347
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	44.4822
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.6698
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	65.9313
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.4440999	5.4440999	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.51869988	3.51869988	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.49 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.6252
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.3943
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.3665
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.1778
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2312
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.1077
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800





# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	29.8858		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.084		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	63.6606		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.5616		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	215.603		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	2.56159997	2.56159997	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	1	1	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.3664999	-6.3664999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.17780018	4.17780018	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.255614042	-0.255614042	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.10769987	7.10769987	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.50 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.5695
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1396
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.5458
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0556
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7694
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.7048
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.1419
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.7314
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	5.5925		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.9004		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	148.325		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.4202		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	291.557		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0.42019999	0.42019999	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.42019999	0.42019999	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.70479989	-2.70479989	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.14190006	-2.14190006	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.51 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.0992
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.5789
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.1658
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.0577
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.5183
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.0284
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-1.7025
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.5716
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	158.672
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.0204
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	504.787
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.05770016	6.05770016	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.51830006	-8.51830006	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.52 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.06
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.5854
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.8954
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.562
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.3727
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-3.3555		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.6154		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	75.7971		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.7165		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	173.817		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.58539999	-1.58539999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.89540005	-2.89540005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.586414039	-0.586414039	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	2.37269998	2.37269998	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.53 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.7848
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1468
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.6159
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.8515
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.3082
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6044
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.4372
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	65535



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	19.4048		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.9032		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	65.3578		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.7165		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	464.572		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-4.71649981	-4.71649981	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-7.85150003	-7.85150003	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.30819988	-8.30819988	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.628814042	-0.628814042	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	5.43720007	5.43720007	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.54 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.3738
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.0352
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2797
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.9504
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.778
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-8.8422
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.8381
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	107.557
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.862
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	2.7757
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-2.86199999	-2.86199999	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	1.76758206	1.76758206	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.994140625	0.994140625	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.279700011	-0.279700011	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.95039988	4.95039988	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.994140625	0.994140625	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.778	1.778	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	1.76758206	1.76758 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	1.76758206	1.76758 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.55 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.5865
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4637
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.1985
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0579
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.273
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.4478
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0708
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	0.4847		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.8801		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	36.2508		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.6099		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	124.757		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	5.6099	5.6099	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.408196241	0.408196241	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0463859364	0.0463859364	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.44779968	-8.44779968	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.0463859364	0.0463859364	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.408196241	0.408196 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.408196241	0.408196 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.56 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.6147
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9031
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.3465
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.6988
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.2615
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	8
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.3162		
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.43		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	21.4816		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	61.0321		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.34649992	-2.34649992	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.69880009	-6.69880009	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.57 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.3159
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4985
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.4625
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.9966
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.3873
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.0721
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.9589
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.9773
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-2.2657
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.8959
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	136.294
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.7911
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	81.5502
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.38730001	7.38730001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.0720999986	-0.0720999986	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.983314037	-0.983314037	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.97729993	3.97729993	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.58 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.1174
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6372
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.064
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.8126
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.5855
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5178
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.0943
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.9233		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.7975		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	20.0311		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.0544		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	371.965		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-2.05439997	-2.05439997	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	1	1	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.81260014	-5.81260014	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.58549976	-5.58549976	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.59 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.5899
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4504
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.5736
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0987
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.8719
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.2204
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.8923
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.6862
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.3693
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.2556		
target_PSATA_Per1_HwTorque_HwNm_f32.value	0.5075		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	174.235		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.4621		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	460.815		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.22040009	3.22040009	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.89229989	3.89229989	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.60 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.4027
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.6106
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9904
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7714
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.091
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.1335
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.083
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.397
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	9.0383
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.1943
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	31.562
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.76
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	481.984
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.09100008	-3.09100008	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.133499995	0.133499995	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.107414059	-0.107414059	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.39699984	7.39699984	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.61 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.3294
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.1821
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	1.3233
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.874
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.5409
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFiltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFiltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFiltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFiltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-7.0809		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.879		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	90.2023		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4382		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	291.32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.18210006	-1.18210006	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	1.3233	1.3233	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.898414075	-0.898414075	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.54090023	-4.54090023	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.62 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.68
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.6457
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.2004
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.1548
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.6014
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5057
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.1271
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	6.0274		
target_PSATA_Per1_HwTorque_HwNm_f32.value	2.0674		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	55.1065		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.643		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	60.7809		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	3.43017292	3.43017292	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.48128593	0.48128593	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.15480006	1.15480006	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.6013999	3.6013999	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.48128593	0.48128593	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.12709999	7.12709999	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	3.43017292	3.43017 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	3.43017292	3.43017 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.63 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.3391
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4173
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.216
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.9905
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.3384
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8408
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.5477
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_Ulpsps_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-12.8621
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.9863
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	26.8559
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-4.0932
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	55.9915
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.99049997	5.99049997	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-3.33839989	-3.33839989	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.86521405	-0.86521405	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.54770005	1.54770005	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.64 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.3758
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2604
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.1352
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9309
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.3684
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.6853
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.2495
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-21.5625		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.8568		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	47.4091		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	207.578		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.3684001	-3.3684001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.68530011	2.68530011	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.65 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.089
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.192
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.1249
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.1993
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.0611
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	65535
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	17.833		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.3694		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	165.783		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	77.1617		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.58515644	8.58515644	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0.853399992	0.853399992	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0.853399992	0.853399992	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.19930005	-1.19930005	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.0610999987	-0.0610999987	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.66 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.5983
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0203
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.4038
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.4087
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.8135
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.2011
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.7128
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	112.247
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	0.8534
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	359.38
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.40380001	8.40380001	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.40869999	-6.40869999	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.67 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3.8983
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6943
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.2292
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0327
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.501
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.5678
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1556
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	4.3426
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.1212		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.3185		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	59.0358		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.7899		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	319.819		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.50099993	4.50099993	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.567799985	-0.567799985	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace ✓

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.68 (Repeat Count = 1) ✓

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.7777
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.4528
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5058
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.7779
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.4026
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.4546
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.7537
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	11.2253		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.7568		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	194.317		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.2589		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	161.221		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-7.25890017	-7.25890017	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.777899981	0.777899981	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.40259981	6.40259981	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.69 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3.2498
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9824
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.3237
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.0208
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.4503
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.9877
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	26.507
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.762
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	49.9677
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.2589
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	156.857
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.93917215	-1.93917215	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.02079964	-8.02079964	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.45030022	-5.45030022	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.98769999	-1.98769999	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.93917215	-1.93917 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.93917215	-1.93917 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.70 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.894
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8218
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7742
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.9334
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.8786
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.3752
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.9018
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	12.3169		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.2815		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	138.221		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.2589		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	426.419		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.93340003	-1.93340003	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.87859988	-2.87859988	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.399614066	-0.399614066	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.90179992	-3.90179992	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.71 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.6917
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4879
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.4386
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.1518
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.3675
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.7952
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3561
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.7572
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	224
t_PsaTA_DisableRateX_HwNm_u11p5[1]	224
t_PsaTA_DisableRateX_HwNm_u11p5[2]	224
t_PsaTA_DisableRateX_HwNm_u11p5[3]	224
t_PsaTA_DisableRateX_HwNm_u11p5[4]	224
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.61		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-4.5677		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	191.781		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-7.2589		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	352.328		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.582838535	-0.582838535	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.33168593	0.33168593	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	2.36750007	2.36750007	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-5.79519987	-5.79519987	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.33168593	0.33168593	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.7572	-1.7572	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.582838535	-0.582839 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.582838535	-0.582839 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.72 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.4263
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8114
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.3862
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.5064
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.3371
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.9539
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6304
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.4395
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-20.4997
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.04
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	18.3297
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.9221
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	2.8197
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.337099999	-0.337099999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.95389986	-7.95389986	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.73 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.3856
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.7546
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.9204
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.1855
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2843
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.6458
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	16.7077
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.4742
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	162.434
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.8048
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	430.687
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvo□	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.9203999	3.9203999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.18549991	2.18549991	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.308714062	-0.308714062	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.64580011	3.64580011	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.74 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.495
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.3289
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.0953
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.8355
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9751
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	160
t_PsaTA_DisableRateY_UlspS_u11p5[1]	160
t_PsaTA_DisableRateY_UlspS_u11p5[2]	160
t_PsaTA_DisableRateY_UlspS_u11p5[3]	160
t_PsaTA_DisableRateY_UlspS_u11p5[4]	160
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-17.112		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.0867		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	92.5563		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.245		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	337.053		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	2.24499989	2.24499989	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.80480957	6.80480957	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.09529996	-3.09529996	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.83549976	-7.83549976	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.97510004	6.97510004	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.80480957	6.80481 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.80480957	6.80481 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.75 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.1424
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.038
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.9696
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.1459
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.4814
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.9263
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.3515
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.47
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	25.6271
target_PSATA_Per1_HwTorque_HwNm_f32.value	-1.5584
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	106.821
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	1.228
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	88.0495
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.480816334	-0.480816334	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.327085942	0.327085942	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.481400013	-0.481400013	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-2.92630005	-2.92630005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.327085942	0.327085942	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.47000003	-1.47000003	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.480816334	-0.480816 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.480816334	-0.480816 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.76 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.3194
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7348
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.1842
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.3363
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.4949
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1462
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.5256
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.5067		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-6.0133		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	185.322		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-5.5306		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	472.779		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.3362999	-5.3362999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.49490023	5.49490023	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.170614064	-0.170614064	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.525600016	-0.525600016	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.77 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9974
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7056
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.4056
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.678
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8339
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.0985
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.7648		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	176.373		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.812		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	117.434		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.40559959	8.40559959	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.67799997	-6.67799997	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.78 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.9974
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7056
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.4056
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.678
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8339
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.0985
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.7648
target_PSATA_Per1_HwTorque_HwNm_f32.value	10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	176.373
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.812
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	117.434
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	5.8119998	5.8119998	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.40559959	8.40559959	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.67799997	-6.67799997	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.79 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.0358
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	5.9377
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.352
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.1882
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.0365
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.3566
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.8336
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.9769
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	21.7375		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	164.243		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.0716		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	162.939		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	2.07159996	2.07159996	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	1.03649998	1.03649998	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.35660005	3.35660005	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.000200000009	0.000200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.80 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-2.5157
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.6207
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.1889
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.2568
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1691
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.4681
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	500
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-14.7334		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.4178		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	3.4992		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	4.968		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	496.015		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	4.96799994	4.96799994	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-5.18889999	-5.18889999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.25680017	-4.25680017	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.193514064	-0.193514064	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.46809995	1.46809995	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.81 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	3.7469
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4969
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.9275
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.2007
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4027
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.7207
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-21.725
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.0215
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	9.3239
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.1592
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	232.007
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-2.16406035	-2.16406035	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-3.15919995	-3.15919995	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.378285944	0.378285944	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.20069981	-8.20069981	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.378285944	0.378285944	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.72069979	-5.72069979	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.16406035	-2.16406 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.16406035	-2.16406 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.82 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.0217
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.3011
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6012
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0561
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.7709
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.8794
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.2792
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	0		
t_PsaTA_SmoothingX_Uls_u6p10[2]	0		
t_PsaTA_SmoothingX_Uls_u6p10[3]	0		
t_PsaTA_SmoothingX_Uls_u6p10[4]	0		
t_PsaTA_SmoothingX_Uls_u6p10[5]	0		
t_PsaTA_SmoothingX_Uls_u6p10[6]	0		
t_PsaTA_SmoothingX_Uls_u6p10[7]	0		
t_PsaTA_SmoothingX_Uls_u6p10[8]	0		
t_PsaTA_SmoothingX_Uls_u6p10[9]	0		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-29.3501		
target_PSATA_Per1_HwTorque_HwNm_f32.value	7.2157		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	1.5048		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.226		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	66.3771		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.07707024	8.07707024	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-0.225999996	-0.225999996	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-0.879400015	-0.879400015	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.2791996	8.2791996	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.07707024	8.07707 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.07707024	8.07707 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.83 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.2363
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.5376
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	4.0131
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4681
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.684
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.4755
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.8835
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	1024
t_PsaTA_SmoothingX_Uls_u6p10[1]	1024
t_PsaTA_SmoothingX_Uls_u6p10[2]	1024
t_PsaTA_SmoothingX_Uls_u6p10[3]	1024
t_PsaTA_SmoothingX_Uls_u6p10[4]	1024
t_PsaTA_SmoothingX_Uls_u6p10[5]	1024
t_PsaTA_SmoothingX_Uls_u6p10[6]	1024
t_PsaTA_SmoothingX_Uls_u6p10[7]	1024
t_PsaTA_SmoothingX_Uls_u6p10[8]	1024
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-22.2369		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.304		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	33.6846		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	3.0068		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	204.234		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-2.81310225	-2.81310225	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-1.68400002	-1.68400002	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.47550011	3.47550011	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.8835001	-2.8835001	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-2.81310225	-2.8131 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-2.81310225	-2.8131 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.84 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.2441
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9709
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.1164
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.9487
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.7042
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.2924
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.4409
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.6813
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-16.603
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.0358
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	199.915
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.5484
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	323.757
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	2.54839993	2.54839993	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.70419979	-6.70419979	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	3.29239988	3.29239988	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.46531406	-0.46531406	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.68130016	6.68130016	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.85 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	1.3713
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-2.3237
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.026
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.6008
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.884
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.17
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.206
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	0		
t_PsaTA_SmoothingY_Uls_u6p10[2]	0		
t_PsaTA_SmoothingY_Uls_u6p10[3]	0		
t_PsaTA_SmoothingY_Uls_u6p10[4]	0		
t_PsaTA_SmoothingY_Uls_u6p10[5]	0		
t_PsaTA_SmoothingY_Uls_u6p10[6]	0		
t_PsaTA_SmoothingY_Uls_u6p10[7]	0		
t_PsaTA_SmoothingY_Uls_u6p10[8]	0		
t_PsaTA_SmoothingY_Uls_u6p10[9]	0		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	3		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	11.512		
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.3962		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	56.1026		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	7.5915		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	509.908		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-1.04909229	-1.04909229	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	7.59149981	7.59149981	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.145585939	0.145585939	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.60079956	-8.60079956	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.88399982	5.88399982	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.145585939	0.145585939	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.20599985	-7.20599985	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-1.04909229	-1.04909 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-1.04909229	-1.04909 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.86 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.5636
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.6249
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.185
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.5664
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0.2982
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.1197
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0993
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.478
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024
t_PsaTA_SmoothingY_Uls_u6p10[1]	1024
t_PsaTA_SmoothingY_Uls_u6p10[2]	1024
t_PsaTA_SmoothingY_Uls_u6p10[3]	1024
t_PsaTA_SmoothingY_Uls_u6p10[4]	1024
t_PsaTA_SmoothingY_Uls_u6p10[5]	1024
t_PsaTA_SmoothingY_Uls_u6p10[6]	1024
t_PsaTA_SmoothingY_Uls_u6p10[7]	1024
t_PsaTA_SmoothingY_Uls_u6p10[8]	1024
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-0.6958		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-0.7001		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	13.3416		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.1044		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	77.2991		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.1197	0.1197	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.87 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-4.3552
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.7913
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.6394
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.5687
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.923
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-22.0462
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6766
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	194.905
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.2777
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	325.593
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-7.72956705	-7.72956705	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-4.63940001	-4.63940001	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.56869984	5.56869984	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-7.92299986	-7.92299986	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-7.72956705	-7.72957 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-7.72956705	-7.72957 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.88 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-7.2039
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.4057
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.84
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4228
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.4672
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.9914
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.7626
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9823
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	64000
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	64000

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	0.6142		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.2206		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	181.312		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.2777		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	137.305		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-3.27769995	-3.27769995	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.467200011	-0.467200011	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.99139977	4.99139977	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.762600005	-0.762600005	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9822998	6.9822998	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.89 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	5.6504
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	2.4664
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.2363
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	3.8258
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0.1013
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2392
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.7929
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2

TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-12.2576
target_PSATA_Per1_HwTorque_HwNm_f32.value	5.7542
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	173.988

target\_PSATA\_Per1\_Ve l p ed p \_f32.value 152. 8

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.90 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-5.1498
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2963
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.4222
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.4553
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.575
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1304
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.8694
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.2167
target_PSATA_Per1_HwTorque_HwNm_f32.value	-8.9054
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	126.707
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.3786
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	444.481
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	2.37859988	2.37859988	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0.504569769	0.504569769	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.130400002	0.130400002	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.45529985	4.45529985	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	5.57499981	5.57499981	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.130400002	0.130400002	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.86940002	3.86940002	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0.504569769	0.50457 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0.504569769	0.50457 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.91 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	7.9005
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.8628
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.6781
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0229
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.5708
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.6685
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.1703
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.053
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	1024		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-10.3452		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.0625		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	111.997		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.5193		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	444.173		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.57079983	-6.57079983	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.66849995	-7.66849995	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1.17030001	-1.17030001	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.05299997	3.05299997	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.92 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.3882
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.2173
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.0422
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.7668
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5743
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.4505
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-23.555		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.7535		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	110.721		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.5193		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	345.191		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.04220009	-3.04220009	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.76679993	2.76679993	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.93 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.3882
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.2173
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.0422
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.7668
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.5743
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	1.4505
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlpsP_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-23.555
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.7535
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	110.721
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.5193
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	345.191
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	5.51929998	5.51929998	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-3.04220009	-3.04220009	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	2.76679993	2.76679993	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosSrvoSmoothingFactor	1	Cal_PosSrvoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.94 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.7536
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6843
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	3.6624
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.1081
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.034
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8815
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.3495
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-21.3014		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-3.5416		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	187.951		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	2.1775		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	13.856		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	4.10809994	4.10809994	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.03399992	7.03399992	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.905914068	-0.905914068	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.34949994	-1.34949994	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.95 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.4048
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.988
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.9157
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.6134
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.2503
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0897
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.84
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	65408
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	31.2526		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.3228		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	125.498		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	6.0283		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	44.7036		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.61339998	-6.61339998	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	7.25029993	7.25029993	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.114114061	-0.114114061	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-3.83999991	-3.83999991	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.96 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	2.1162
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.9158
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.2995
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.4178
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.1506
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.0649
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0132
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.2404
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-15.9165
target_PSATA_Per1_HwTorque_HwNm_f32.value	4.4797
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	43.831
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	5.8003
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	121.63
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.15059996	6.15059996	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-1.06490004	-1.06490004	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.0376140624	-0.0376140624	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	7.24039984	7.24039984	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.97 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.9615
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.7586
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.472
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3544
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.2971
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.5171
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.4401
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-31.2003		
target_PSATA_Per1_HwTorque_HwNm_f32.value	8.6967		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	178.848		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.7507		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	374.446		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	6.29710007	6.29710007	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.51710033	8.51710033	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.98 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.3469
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-1.1641
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8573
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7511
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.6295
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.8511
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.2747
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.9573
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	36044

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	25.9618		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.17		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	40.9335		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.4053		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	309.216		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	5.62949991	5.62949991	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-7.85109997	-7.85109997	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.299114048	-0.299114048	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.95730019	6.95730019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.99 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	1.3529
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.9081
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.3172
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.1457
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.3421
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.6147
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	27.731
target_PSATA_Per1_HwTorque_HwNm_f32.value	-2.1857
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	42.7042
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-2.4053
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	185.249
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-2.4052999	-2.4052999	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-5.47762203	-5.47762203	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	1	1	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-2.14569998	-2.14569998	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-6.34210014	-6.34210014	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-5.61469984	-5.61469984	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-5.47762203	-5.47762 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-5.47762203	-5.47762 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.100 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-6.8396
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.4933
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.5014
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.0949
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.578
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-13.9051		
target_PSATA_Per1_HwTorque_HwNm_f32.value	6.3765		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	105.001		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-6.4468		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	332.119		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	3.4906466	3.4906466	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	7.50139999	7.50139999	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-4.09490013	-4.09490013	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	3.57800007	3.57800007	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.0020000009	0.0020000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	3.4906466	3.49065 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	3.4906466	3.49065 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.101 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	4.4042
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.2174
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	7.1181
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.438
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.0511
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.1666
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.8786
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6442
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.0427
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	4.6759		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.8579		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	4.2194		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	22.601		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0.0264648609	-0.0264648609	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.619785964	0.619785964	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.166600004	-0.166600004	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	4.87860012	4.87860012	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.619785964	0.619785964	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0.0427000001	-0.0427000001	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0.0264648609	-0.0264649 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0.0264648609	-0.0264649 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.102 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6945
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.544
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8719
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2956
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.6997
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8794
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.415
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.8522
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6758
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.424
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	141.875
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadps_f32	target_PSATA_Per1_HandwheelVelocity_HwRadps_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmps_f32	target_PSATA_Per1_HwTrqRate_HwNmps_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32

Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.295599997	-0.295599997	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.69970036	8.69970036	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.903814077	-0.903814077	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.41499996	-6.41499996	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.103 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6945
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.544
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8719
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2956
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.6997
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8794
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.415
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.8522		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6758		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.424		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	141.875		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.295599997	-0.295599997	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.69970036	8.69970036	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.903814077	-0.903814077	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.41499996	-6.41499996	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.104 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6945
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.544
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8719
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2956
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.6997
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8794
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.415
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	32640
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	2		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.8522		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6758		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.424		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	141.875		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.295599997	-0.295599997	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.69970036	8.69970036	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.903814077	-0.903814077	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.41499996	-6.41499996	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.105 (Repeat Count = 1)

Name

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	1
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.8522
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6758
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.424
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	141.875
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.295599997	-0.295599997	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.69970036	8.69970036	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.879400015	-0.879400015	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.41499996	-6.41499996	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.106 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.6945
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.544
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.8719
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-0.2956
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.6997
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.8794
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.415
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	1		
target_PSATA_Per1_EPSSstateForLxa_Cnt_enum.value	1		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	3.8522		
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.6758		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	186.424		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.4389		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	141.875		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSstateForLxa_Cnt_enum	target_PSATA_Per1_EPSSstateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.69970036	8.69970036	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-0.879400015	-0.879400015	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-6.41499996	-6.41499996	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.107 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.653
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7643
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	0.3406
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.4535
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9816
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	1
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00400000019	0.00400000019	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.800000019	8.800000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.45349979	8.45349979	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 3.108 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.653
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7643
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	0.3406
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.4535
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9816
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.00825537089	0.00825537089	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00600000005	0.00600000005	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.45349979	8.45349979	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.109 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.653
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7643
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	0.3406
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.4535
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9816
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00800000038	0.00800000038	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✓

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.110 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpsP_S_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	*none*
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	*none*
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	*none*
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	6.653
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.7643
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	*none*
PSATA_PNAccumulator_Cnt_M_u16	*none*
PSATA_PosServoSmoothing_Uls_M_f32	0.3406
PSATA_PosSrvoEnable_Cnt_M_f32	*none*
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.4535
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.9816
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00675
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsP_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsP_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	1
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.0165107418	0.0165107418	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0.0100000007	0.0100000007	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.45349979	8.45349979	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✗
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✗
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✗
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 3.111 (Repeat Count = 1)

Name

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-19.4034
target_PSATA_Per1_HwTorque_HwNm_f32.value	-7.2196
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	141.77
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-1.9637
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	268.767
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0.00859375019	0.00859375019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	6.50637579	6.50637579	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0.0165107418	0.0165107418	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0.001953125	0.001953125	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0.0009765625	0.0009765625	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.012000001	0.012000001	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.45349979	8.45349979	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.957185924	0.957185924	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	6.7974	6.7974	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00875000004	0.00875000004	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0.00859375019	0.00859375 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	6.50637579	6.50638 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Case 4: Path Coverage

## Specification

Performance Metrics:  
(With "None" Instrumentation and  
"WithPS" Environment)  
CPU Cycles:

TS 4.1 4213.00 Cycles  
TS 4.2 4067.00 Cycles  
TS 4.3 3616.00 Cycles  
TS 4.4 2612.00 Cycles  
TS 4.5 2612.00 Cycles  
TS 4.6 3562.00 Cycles  
TS 4.7 3513.00 Cycles  
TS 4.8 2109.00 Cycles  
TS 4.9 4106.00 Cycles  
TS 4.10 2644.00 Cycles  
TS 4.11 2644.00 Cycles  
TS 4.12 2546.00 Cycles  
TS 4.13 4109.00 Cycles  
TS 4.14 4010.00 Cycles  
TS 4.15 2627.00 Cycles  
TS 4.16 2567.00 Cycles  
TS 4.17 2567.00 Cycles

## Description

Vector Description:

```
TS4.1if((Abs_f32_m(PosSrvoCmd_MtrNm_T_f32)) > k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32)==>True
TS4.2if ((CmdOvrThresh_Cnt_T_lgc == TRUE)==>True &&
  (PreDiagCondition_Cnt_T_lgc == TRUE))=>False"
TS4.3PSATA_PNAccumulator_Cnt_M_u16 = DiagPStep_m(PSATA_PNAccumulator_Cnt_M_u16,
  k_PsaTA_ApaPosServFiltMode_Cnt_str);=>True
TS4.4Limit_m(PSATA_LxaOpTrqOv_MtrNm_M_f32) >= (D_MOTORTORQUEHIGHLIMIT_MTRNM_F32)==>True
TS4.5Limit_m(PSATA_LxaOpTrqOv_MtrNm_M_f32) <= (D_MOTORTORQUELOWLIMIT_MTRNM_F32)==>True
TS4.6(PSATA_APAOpTrqOv_MtrNm_M_f32)>=(D_MOTORTORQUEHIGHLIMIT_MTRNM_F32);=>True
TS4.7(PSATA_APAOpTrqOv_MtrNm_M_f32)<=(D_MOTORTORQUELOWLIMIT_MTRNM_F32);=>True
TS4.8"( (EPSStateForLxa_Cnt_T_enum == PSAEPSSTLXA_ACTV)==>True && (ApaState_Cnt_T_enum != PSAAPAST_CTRLPRGS) )
==>False||
  (D_ZERO_ULS_F32 < PSATA_LxaOpTrqOv_MtrNm_M_f32) )=>False"
TS4.9if ( ( (EPSStateForLxa_Cnt_T_enum != PSAEPSSTLXA_ACTV)==>True && (ApaState_Cnt_T_enum ==
  PSAAPAST_CTRLPRGS)==>True &&
  (k_PsaTA_MaxVehicleSpeedSafety_Kph_f32 >= VehicleSpeed_Kph_T_f32) )=>False ||
  ( (D_ZERO_ULS_F32 < PSATA_APAOpTrqOv_MtrNm_M_f32)==>False && (ApaState_Cnt_T_enum != PSAAPAST_CTRLPRGS) ) )"
TS4.10"( (D_FLTEPSILON_ULS_F32 < Abs_f32_m(PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32)) ==> True&&
  (k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32 < Abs_f32_m(HwTorque_HwNm_T_f32)) ) ==> True"
TS4.11if ( (D_FLTEPSILON_ULS_F32 < Abs_f32_m(PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32)) ==> True &&
  (HwTrqRate_HwNmpS_T_f32 > k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32) )=>False"
TS4.12"(Limit_m((PosSrvoEnable_Cnt_T_f32 - PSATA_PosSrvoEnable_Cnt_M_f32)<=
  (-LxaTranRate_MtrNmpS_f32*D_2MS_SEC_F32)==>True"
TS4.13(PSATA_APAOpTrqOv_MtrNm_M_f32>=8.8F)==>True
TS4.14(PSATA_APAOpTrqOv_MtrNm_M_f32<=-8.8F)==>True
TS4.15" if ((LxaHwAgDetd_Cnt_T_lgc == D_TRUE_CNT_LGC)==>F||
  (LxaDrvAbsntHwVelDetd_Cnt_T_lgc == D_TRUE_CNT_LGC)==>F||
  (PSATA_LxaHwTrqRateFilt_Cnt_M_lgc == D_TRUE_CNT_LGC)==>F||
  (PSATA_LxaHwVelFilt_Cnt_M_lgc == D_TRUE_CNT_LGC)==>F||
  (PSATA_LxaHwTrqFilt_Cnt_M_lgc == D_TRUE_CNT_LGC)==>T||
  (PSATA_LxaTrqOvFilt_Cnt_M_lgc == D_TRUE_CNT_LGC) ||
  (D_POSSRVOTRANSITION_ULS_F32 <= PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32))"
TS4.16 "if(PSATA_LxaOpTrqOv_MtrNm_M_f32>=8.8)==>True"
TS4.17 "if(PSATA_LxaOpTrqOv_MtrNm_M_f32<=-8.8)==>True"
```

## Test Step 4.1 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFilt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFilt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFilt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFilt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsS_f32	0.1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.2 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.000200000009	0.000200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.3 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmps_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	65535
t_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmps_f32.value	0
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaDrvrsAbsntHwVelDtd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDtd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrsAbsntHwVelDtd_Cnt_lgc	target_PSATA_Per1_LxaDrvrsAbsntHwVelDtd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDtd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDtd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Test Step 4.5 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.6 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.7 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpsS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
t_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmps_f32.value	0
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.8 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	*none*
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	*none*
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	-8.8
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	10
t_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	2
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	18.3298
target_PSATA_Per1_HwTorque_HwNm_f32.value	9.9407
target_PSATA_Per1_HwTrqRate_HwNmps_f32.value	83.0381
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-3.6795		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	298.971		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0.00200000009	0.00200000009	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.00200000009	0.00200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Test Step 4.9 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	102		
t_PsaTA_SmoothingX_Uls_u6p10[2]	205		
t_PsaTA_SmoothingX_Uls_u6p10[3]	307		
t_PsaTA_SmoothingX_Uls_u6p10[4]	410		
t_PsaTA_SmoothingX_Uls_u6p10[5]	614		
t_PsaTA_SmoothingX_Uls_u6p10[6]	717		
t_PsaTA_SmoothingX_Uls_u6p10[7]	819		
t_PsaTA_SmoothingX_Uls_u6p10[8]	922		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	25		
t_PsaTA_SmoothingY_Uls_u6p10[2]	97		
t_PsaTA_SmoothingY_Uls_u6p10[3]	211		
t_PsaTA_SmoothingY_Uls_u6p10[4]	358		
t_PsaTA_SmoothingY_Uls_u6p10[5]	671		
t_PsaTA_SmoothingY_Uls_u6p10[6]	813		
t_PsaTA_SmoothingY_Uls_u6p10[7]	927		
t_PsaTA_SmoothingY_Uls_u6p10[8]	999		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	2		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.10 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	120
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	65535
PSATA_LxaHwVelFlt_Cnt_M_lgc	1
PSATA_LxaHwVelPNAccum_Cnt_M_u16	120
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	1
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	10
PSATA_PNAccumulator_Cnt_M_u16	65535
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	1
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	1
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	500
k_PsaTA_ApaPosServFitMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	65535
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	65535
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	1
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	100
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaMaxRate_UlspS_f32	10
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	65535
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	0
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	8
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	224
t_PsaTA_DisableRateX_HwNm_u11p5[1]	224
t_PsaTA_DisableRateX_HwNm_u11p5[2]	224
t_PsaTA_DisableRateX_HwNm_u11p5[3]	224
t_PsaTA_DisableRateX_HwNm_u11p5[4]	224
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	160
t_PsaTA_DisableRateY_UlspS_u11p5[1]	160
t_PsaTA_DisableRateY_UlspS_u11p5[2]	160
t_PsaTA_DisableRateY_UlspS_u11p5[3]	160
t_PsaTA_DisableRateY_UlspS_u11p5[4]	160
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	32640
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	51200
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	65408



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	65408
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	1024
t_PsaTA_SmoothingX_Uls_u6p10[1]	1024
t_PsaTA_SmoothingX_Uls_u6p10[2]	1024
t_PsaTA_SmoothingX_Uls_u6p10[3]	1024
t_PsaTA_SmoothingX_Uls_u6p10[4]	1024
t_PsaTA_SmoothingX_Uls_u6p10[5]	1024
t_PsaTA_SmoothingX_Uls_u6p10[6]	1024

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingX_Uls_u6p10[7]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[8]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[1]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[2]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[3]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[4]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[5]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[6]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[7]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[8]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	200		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10	10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.58515644	8.58515644	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	1	1	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	1	1	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 4.11 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_UlspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	120
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	65535
PSATA_LxaHwVelFlt_Cnt_M_lgc	1
PSATA_LxaHwVelPNAccum_Cnt_M_u16	120
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	1
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	10
PSATA_PNAccumulator_Cnt_M_u16	65535
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	1
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	1
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	500
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	65535
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	100
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaMaxRate_UlspS_f32	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	100

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	8
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	224
t_PsaTA_DisableRateX_HwNm_u11p5[1]	224
t_PsaTA_DisableRateX_HwNm_u11p5[2]	224
t_PsaTA_DisableRateX_HwNm_u11p5[3]	224
t_PsaTA_DisableRateX_HwNm_u11p5[4]	224
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	160
t_PsaTA_DisableRateY_UlspS_u11p5[1]	160
t_PsaTA_DisableRateY_UlspS_u11p5[2]	160
t_PsaTA_DisableRateY_UlspS_u11p5[3]	160
t_PsaTA_DisableRateY_UlspS_u11p5[4]	160
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	32640
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	51200
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	65408
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	1024
t_PsaTA_SmoothingX_Uls_u6p10[1]	1024
t_PsaTA_SmoothingX_Uls_u6p10[2]	1024
t_PsaTA_SmoothingX_Uls_u6p10[3]	1024
t_PsaTA_SmoothingX_Uls_u6p10[4]	1024
t_PsaTA_SmoothingX_Uls_u6p10[5]	1024
t_PsaTA_SmoothingX_Uls_u6p10[6]	1024
t_PsaTA_SmoothingX_Uls_u6p10[7]	1024
t_PsaTA_SmoothingX_Uls_u6p10[8]	1024
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024
t_PsaTA_SmoothingY_Uls_u6p10[1]	1024
t_PsaTA_SmoothingY_Uls_u6p10[2]	1024
t_PsaTA_SmoothingY_Uls_u6p10[3]	1024
t_PsaTA_SmoothingY_Uls_u6p10[4]	1024
t_PsaTA_SmoothingY_Uls_u6p10[5]	1024
t_PsaTA_SmoothingY_Uls_u6p10[6]	1024
t_PsaTA_SmoothingY_Uls_u6p10[7]	1024
t_PsaTA_SmoothingY_Uls_u6p10[8]	1024
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	3
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	32
target_PSATA_Per1_HwTorque_HwNm_f32.value	10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCorrnFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10	10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.58515644	8.58515644	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	1	1	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	1	1	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

## Test Step 4.12 (Repeat Count = 1)

Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195
PSATA_LxaHwTrqFlt_Cnt_M_lgc	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	120
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	65535
PSATA_LxaHwVelFlt_Cnt_M_lgc	1
PSATA_LxaHwVelPNAccum_Cnt_M_u16	120
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	1
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	10
PSATA_PNAccumulator_Cnt_M_u16	65535
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	1
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	1
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	500
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	65535
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	65535
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	100
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaMaxRate_UlspS_f32	10
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	65535
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	100
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	8
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	10
t_PsaTA_DisableRateX_HwNm_u11p5[0]	224
t_PsaTA_DisableRateX_HwNm_u11p5[1]	224
t_PsaTA_DisableRateX_HwNm_u11p5[2]	224
t_PsaTA_DisableRateX_HwNm_u11p5[3]	224
t_PsaTA_DisableRateX_HwNm_u11p5[4]	224
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	160
t_PsaTA_DisableRateY_UlspS_u11p5[1]	160
t_PsaTA_DisableRateY_UlspS_u11p5[2]	160
t_PsaTA_DisableRateY_UlspS_u11p5[3]	160
t_PsaTA_DisableRateY_UlspS_u11p5[4]	160
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	32640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	32640
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	51200



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	51200
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	51200
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	65408
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	65408
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	36044
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	36044
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25		
t_PsaTA_SmoothingX_Uls_u6p10[0]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[1]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[2]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[3]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[4]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[5]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[6]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[7]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[8]	1024		
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[1]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[2]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[3]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[4]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[5]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[6]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[7]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[8]	1024		
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024		
target_PSATA_Per1_ApaState_Cnt_enum.value	3		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	4		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	200		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	1		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	511		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	10	10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0.0248195007	0.0248195007	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.58515644	8.58515644	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	1	1	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.99000001	0.99000001	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.58515644	8.58516 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	3	NTCDiagc_Fn	3	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.13 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	1
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	1
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	1
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	0
t_PsaTA_SmoothingX_Uls_u6p10[2]	0
t_PsaTA_SmoothingX_Uls_u6p10[3]	0
t_PsaTA_SmoothingX_Uls_u6p10[4]	0
t_PsaTA_SmoothingX_Uls_u6p10[5]	0
t_PsaTA_SmoothingX_Uls_u6p10[6]	0
t_PsaTA_SmoothingX_Uls_u6p10[7]	0
t_PsaTA_SmoothingX_Uls_u6p10[8]	0
t_PsaTA_SmoothingX_Uls_u6p10[9]	0
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024
t_PsaTA_SmoothingY_Uls_u6p10[1]	0
t_PsaTA_SmoothingY_Uls_u6p10[2]	0
t_PsaTA_SmoothingY_Uls_u6p10[3]	0
t_PsaTA_SmoothingY_Uls_u6p10[4]	0
t_PsaTA_SmoothingY_Uls_u6p10[5]	0
t_PsaTA_SmoothingY_Uls_u6p10[6]	0
t_PsaTA_SmoothingY_Uls_u6p10[7]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[8]	0		
t_PsaTA_SmoothingY_Uls_u6p10[9]	0		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	8.80000019	8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	8.80000019	8.8 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.8 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.14 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	1
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	1
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	1
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	1.00098
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_Ulpsps_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_Ulpsps_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	1
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	0
t_PsaTA_SmoothingX_Uls_u6p10[2]	0
t_PsaTA_SmoothingX_Uls_u6p10[3]	0
t_PsaTA_SmoothingX_Uls_u6p10[4]	0
t_PsaTA_SmoothingX_Uls_u6p10[5]	0
t_PsaTA_SmoothingX_Uls_u6p10[6]	0
t_PsaTA_SmoothingX_Uls_u6p10[7]	0
t_PsaTA_SmoothingX_Uls_u6p10[8]	0
t_PsaTA_SmoothingX_Uls_u6p10[9]	0
t_PsaTA_SmoothingY_Uls_u6p10[0]	1024
t_PsaTA_SmoothingY_Uls_u6p10[1]	0
t_PsaTA_SmoothingY_Uls_u6p10[2]	0
t_PsaTA_SmoothingY_Uls_u6p10[3]	0
t_PsaTA_SmoothingY_Uls_u6p10[4]	0
t_PsaTA_SmoothingY_Uls_u6p10[5]	0
t_PsaTA_SmoothingY_Uls_u6p10[6]	0
t_PsaTA_SmoothingY_Uls_u6p10[7]	0
t_PsaTA_SmoothingY_Uls_u6p10[8]	0
t_PsaTA_SmoothingY_Uls_u6p10[9]	0
target_PSATA_Per1_ApaState_Cnt_enum.value	0
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFit_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFit_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	1	1	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	-1	-1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-8.80000019	-8.8 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFit_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.8 ± 0.00001	✔

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.15 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_Ulpsps_M_f32	0
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	1
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-1.6964
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.1015
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	1.5623
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	1
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0.7818
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.5747
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.3069
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.8567
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpsS_f32	1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	10
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	1
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	1
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlpsS_f32	0.2
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	1
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	1
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	1
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	120
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	1

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	1
k_PsaTA_LxaMaxRate_UlspS_f32	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	1
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	1
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	5
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	1
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	2
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	32
t_PsaTA_DisableRateX_HwNm_u11p5[2]	64
t_PsaTA_DisableRateX_HwNm_u11p5[3]	96
t_PsaTA_DisableRateX_HwNm_u11p5[4]	128
t_PsaTA_DisableRateX_HwNm_u11p5[5]	224
t_PsaTA_DisableRateY_UlspS_u11p5[0]	10
t_PsaTA_DisableRateY_UlspS_u11p5[1]	13
t_PsaTA_DisableRateY_UlspS_u11p5[2]	32
t_PsaTA_DisableRateY_UlspS_u11p5[3]	32
t_PsaTA_DisableRateY_UlspS_u11p5[4]	64
t_PsaTA_DisableRateY_UlspS_u11p5[5]	160
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	3840
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	6400
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	7680
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	8960
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	12800
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	14080
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	15360
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	16640
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	17920
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	19200
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	20480
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	21760
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	23040
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	25600
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	12800
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	10240
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	9216
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	8192
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	7680
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	7168
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	6656
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	6144
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	5632
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	5120
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	4608
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	1280
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	2560
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	3840
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	5120
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	6400
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	7680
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	8960
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	23040
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	30720
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	12288

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	12288
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	12288
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	102
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	204
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	307
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	409
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	614
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	716
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	819
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	921
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	24
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	97
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	210
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	353
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	670
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	813
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	926
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	999
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	1024
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	1280
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	2560
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	3840
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	5120
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	6400
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	7680
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	8960
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	23040
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	30720
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	25
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	25
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	102
t_PsaTA_SmoothingX_Uls_u6p10[2]	205
t_PsaTA_SmoothingX_Uls_u6p10[3]	307
t_PsaTA_SmoothingX_Uls_u6p10[4]	410
t_PsaTA_SmoothingX_Uls_u6p10[5]	614
t_PsaTA_SmoothingX_Uls_u6p10[6]	717
t_PsaTA_SmoothingX_Uls_u6p10[7]	819
t_PsaTA_SmoothingX_Uls_u6p10[8]	922
t_PsaTA_SmoothingX_Uls_u6p10[9]	1024
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	25
t_PsaTA_SmoothingY_Uls_u6p10[2]	97
t_PsaTA_SmoothingY_Uls_u6p10[3]	211
t_PsaTA_SmoothingY_Uls_u6p10[4]	358
t_PsaTA_SmoothingY_Uls_u6p10[5]	671
t_PsaTA_SmoothingY_Uls_u6p10[6]	813
t_PsaTA_SmoothingY_Uls_u6p10[7]	927
t_PsaTA_SmoothingY_Uls_u6p10[8]	999
t_PsaTA_SmoothingY_Uls_u6p10[9]	1024
target_PSATA_Per1_ApaState_Cnt_enum.value	1
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	3
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-6.0028
target_PSATA_Per1_HwTorque_HwNm_f32.value	3.0723
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	144.306

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	1		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	0		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-0.8957		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	0		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	143.285		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	-0	-0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	0	0	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-4.73812819	-4.73812819	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	-0.895699978	-0.895699978	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-0.895699978	-0.895699978	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	0	0	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-6.57469988	-6.57469988	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	6.30690002	6.30690002	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	0.975585938	0.975585938	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-4.85669994	-4.85669994	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0	0	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	-0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-4.73812819	-4.73813 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	0	0	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-4.73812819	-4.73813 ± 0.00001	✔

## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	1	NTCDiagc_Fn	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
NTCDiagc_Fn	3	NTCDiagc_Fn	3	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

## Test Step 4.16 (Repeat Count = 1)



Name	Input Value
LxaDiTranRate_UlspS_M_f32	0.5
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0
k_PsaTA_ApaPosServFltMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFltMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFltMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFltMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFltMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFltMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	0
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0		
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0		
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0		
t_PsaTA_SmoothingX_Uls_u6p10[0]	0		
t_PsaTA_SmoothingX_Uls_u6p10[1]	0		
t_PsaTA_SmoothingX_Uls_u6p10[2]	0		
t_PsaTA_SmoothingX_Uls_u6p10[3]	0		
t_PsaTA_SmoothingX_Uls_u6p10[4]	0		
t_PsaTA_SmoothingX_Uls_u6p10[5]	0		
t_PsaTA_SmoothingX_Uls_u6p10[6]	0		
t_PsaTA_SmoothingX_Uls_u6p10[7]	0		
t_PsaTA_SmoothingX_Uls_u6p10[8]	0		
t_PsaTA_SmoothingX_Uls_u6p10[9]	0		
t_PsaTA_SmoothingY_Uls_u6p10[0]	0		
t_PsaTA_SmoothingY_Uls_u6p10[1]	0		
t_PsaTA_SmoothingY_Uls_u6p10[2]	0		
t_PsaTA_SmoothingY_Uls_u6p10[3]	0		
t_PsaTA_SmoothingY_Uls_u6p10[4]	0		
t_PsaTA_SmoothingY_Uls_u6p10[5]	0		
t_PsaTA_SmoothingY_Uls_u6p10[6]	0		
t_PsaTA_SmoothingY_Uls_u6p10[7]	0		
t_PsaTA_SmoothingY_Uls_u6p10[8]	0		
t_PsaTA_SmoothingY_Uls_u6p10[9]	0		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCormFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCormFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✓
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✓
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✓
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✓
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✓
PSATA_LxaOpTrqOv_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✓

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Actual Value	Expected Value	Result
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✓
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✓
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✓
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✓
PSATA_PNAccumulator_Cnt_M_u16	0	0	✓
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✓
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✓
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✓
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✓
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	8.80000019	8.80000019	✓
PSATA_PrevSlewRateLmt_Uls_M_f32	0.000200000009	0.000200000009	✓
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✓
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	8.80000019	8.8 ± 0.00001	✓
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✓
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	8.80000019	8.8 ± 0.00001	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓

Test Step 4.17 (Repeat Count = 1)	
Name	Input Value
LxaDiTranRate_UlpspS_M_f32	0.5
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	0
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0
PSATA_LxaHwTrqFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqPNAccum_Cnt_M_u16	0
PSATA_LxaHwTrqRateFlt_Cnt_M_lgc	0
PSATA_LxaHwTrqRatePNAccum_Cnt_M_u16	0
PSATA_LxaHwVelFlt_Cnt_M_lgc	0
PSATA_LxaHwVelPNAccum_Cnt_M_u16	0
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	0
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	8.8
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0
PSATA_LxaTrqOvFlt_Cnt_M_lgc	0
PSATA_LxaTrqOvPNAccum_Cnt_M_u16	0
PSATA_PNAccumulator_Cnt_M_u16	0
PSATA_PosServoSmoothing_Uls_M_f32	0
PSATA_PosSrvoEnable_Cnt_M_f32	0
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.8
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.8
PSATA_PrevSlewRateLmt_Uls_M_f32	0
Rte_Inst_Ap_PSATA	target_Rte_Inst_Ap_PSATA
k_PsaTA_APAEnableRate_UlpspS_f32	0.1
k_PsaTA_ApaPosServFltMode_Cnt_str.Threshold	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
k_PsaTA_ApaPosServFitMode_Cnt_str.PStep	0
k_PsaTA_ApaPosServFitMode_Cnt_str.NStep	0
k_PsaTA_ApaPosnServoTran_Uls_f32	0.02
k_PsaTA_DisableRate_UlspS_f32	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwTrqRateFitMode_Cnt_str.NStep	0
k_PsaTA_LxaHwTrqRateSftyThd_HwNmpS_f32	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaHwVelFitMode_Cnt_str.PStep	0
k_PsaTA_LxaHwVelFitMode_Cnt_str.NStep	0
k_PsaTA_LxaMaxRate_UlspS_f32	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.Threshold	65535
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.PStep	0
k_PsaTA_LxaOpTrqOvFitMode_Cnt_str.NStep	0
k_PsaTA_LxaPosnServoTran_Uls_f32	0.02
k_PsaTA_LxaSftyMaxHwTorque_HwNm_f32	0
k_PsaTA_MaxPosServoCmdSafety_MtrNm_f32	0
k_PsaTA_MaxVehicleSpeedSafety_Kph_f32	0
t_PsaTA_DisableRateX_HwNm_u11p5[0]	0
t_PsaTA_DisableRateX_HwNm_u11p5[1]	0
t_PsaTA_DisableRateX_HwNm_u11p5[2]	0
t_PsaTA_DisableRateX_HwNm_u11p5[3]	0
t_PsaTA_DisableRateX_HwNm_u11p5[4]	0
t_PsaTA_DisableRateX_HwNm_u11p5[5]	0
t_PsaTA_DisableRateY_UlspS_u11p5[0]	6
t_PsaTA_DisableRateY_UlspS_u11p5[1]	6
t_PsaTA_DisableRateY_UlspS_u11p5[2]	6
t_PsaTA_DisableRateY_UlspS_u11p5[3]	6
t_PsaTA_DisableRateY_UlspS_u11p5[4]	6
t_PsaTA_DisableRateY_UlspS_u11p5[5]	6
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[0]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[1]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[2]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[3]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[4]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[5]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[6]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[7]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[8]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[9]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[10]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[11]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[12]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[13]	0
t_PsaTA_LxaHwVelSftyThdX_Kph_u9p7[14]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[0]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[1]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[2]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[3]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[4]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[5]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[6]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[7]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[8]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[9]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[10]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[11]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[12]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[13]	0
t_PsaTA_LxaHwVelSftyThdY_HwRadpS_u7p9[14]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[0]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[1]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[2]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[3]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[4]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[5]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[6]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[7]	0
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[8]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value
t_PsaTA_LxaOpTrqOvBndX_Kph_u9p7[9]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[0]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[1]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[2]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[3]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[4]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[5]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[6]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[7]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[8]	0
t_PsaTA_LxaOpTrqOvSftyBndY_MtrNm_u4p12[9]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorX_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngFactorY_Uls_u6p10[9]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[0]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[1]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[2]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[3]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[4]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[5]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[6]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[7]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[8]	0
t_PsaTA_LxaSmotngSftyFactorX_Kph_u9p7[9]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[0]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[1]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[2]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[3]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[4]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[5]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[6]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[7]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[8]	0
t_PsaTA_LxaSmotngSftyFactorY_Uls_u6p10[9]	0
t_PsaTA_SmoothingX_Uls_u6p10[0]	0
t_PsaTA_SmoothingX_Uls_u6p10[1]	0
t_PsaTA_SmoothingX_Uls_u6p10[2]	0
t_PsaTA_SmoothingX_Uls_u6p10[3]	0
t_PsaTA_SmoothingX_Uls_u6p10[4]	0
t_PsaTA_SmoothingX_Uls_u6p10[5]	0
t_PsaTA_SmoothingX_Uls_u6p10[6]	0
t_PsaTA_SmoothingX_Uls_u6p10[7]	0
t_PsaTA_SmoothingX_Uls_u6p10[8]	0
t_PsaTA_SmoothingX_Uls_u6p10[9]	0
t_PsaTA_SmoothingY_Uls_u6p10[0]	0
t_PsaTA_SmoothingY_Uls_u6p10[1]	0

# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530



PSATA\_Per1

Name	Input Value		
t_PsaTA_SmoothingY_Uls_u6p10[2]	0		
t_PsaTA_SmoothingY_Uls_u6p10[3]	0		
t_PsaTA_SmoothingY_Uls_u6p10[4]	0		
t_PsaTA_SmoothingY_Uls_u6p10[5]	0		
t_PsaTA_SmoothingY_Uls_u6p10[6]	0		
t_PsaTA_SmoothingY_Uls_u6p10[7]	0		
t_PsaTA_SmoothingY_Uls_u6p10[8]	0		
t_PsaTA_SmoothingY_Uls_u6p10[9]	0		
target_PSATA_Per1_ApaState_Cnt_enum.value	0		
target_PSATA_Per1_EPSStateForLxa_Cnt_enum.value	0		
target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32.value	-32		
target_PSATA_Per1_HwTorque_HwNm_f32.value	-10		
target_PSATA_Per1_HwTrqRate_HwNmpS_f32.value	0		
target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32.value	0		
target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc.value	0		
target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc.value	1		
target_PSATA_Per1_PosSrvoCmd_MtrNm_f32.value	-8.8		
target_PSATA_Per1_PosSrvoEnable_Cnt_lgc.value	1		
target_PSATA_Per1_VehicleSpeed_Kph_f32.value	0		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaOpTrqOv_MtrNm_f32	target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc	target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_ApaState_Cnt_enum	target_PSATA_Per1_ApaState_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_EPSStateForLxa_Cnt_enum	target_PSATA_Per1_EPSStateForLxa_Cnt_enum		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HandwheelVelocity_HwRadpS_f32	target_PSATA_Per1_HandwheelVelocity_HwRadpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTorque_HwNm_f32	target_PSATA_Per1_HwTorque_HwNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_HwTrqRate_HwNmpS_f32	target_PSATA_Per1_HwTrqRate_HwNmpS_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaCornFacReqLimd_Uls_f32	target_PSATA_Per1_LxaCornFacReqLimd_Uls_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc	target_PSATA_Per1_LxaDrvrAbsntHwVelDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaHwAgDetd_Cnt_lgc	target_PSATA_Per1_LxaHwAgDetd_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaOpTrqOv_MtrNm_f32	target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_LxaTqLimnActv_Cnt_lgc	target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_OpTrqOv_MtrNm_f32	target_PSATA_Per1_OpTrqOv_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoCmd_MtrNm_f32	target_PSATA_Per1_PosSrvoCmd_MtrNm_f32		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_PosSrvoEnable_Cnt_lgc	target_PSATA_Per1_PosSrvoEnable_Cnt_lgc		
target_Rte_Inst_Ap_PSATA.PSATA_Per1_VehicleSpeed_Kph_f32	target_PSATA_Per1_VehicleSpeed_Kph_f32		
Name	Actual Value	Expected Value	Result
PSATA_APAOpTrqOv_MtrNm_M_f32	0	0	✔
PSATA_APAPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_ApaPosSrvoFlt_Cnt_M_lgc	1	1	✔
PSATA_FilterdTrqSV_HwNm_M_Str.SV_Uls_f32	-10	-10	✔
PSATA_FilterdTrqSV_HwNm_M_Str.K_Uls_f32	0	0	✔
PSATA_LxaOpTrqOv_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_LxaPosSrvoCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoLimdCmd_MtrNm_M_f32	0	0	✔
PSATA_LxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_LxaPosSrvoSmotdCmd_MtrNm_M_f32	-0	-0	✔
PSATA_LxaPosSrvoSmotngFactor_Uls_M_f32	0	0	✔
PSATA_LxaTqLimnActv_Cnt_M_lgc	1	1	✔
PSATA_PNAccumulator_Cnt_M_u16	0	0	✔
PSATA_PosServoSmoothing_Uls_M_f32	0	0	✔
PSATA_PosSrvoEnable_Cnt_M_f32	0	0	✔
PSATA_PrevIntermPosSrvoCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoLimdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevLxaPosSrvoSftySmotngFactor_Uls_M_f32	1	1	✔
PSATA_PrevLxaPosSrvoSmotdCmd_MtrNm_M_f32	-8.80000019	-8.80000019	✔
PSATA_PrevSlewRateLmt_Uls_M_f32	0.000200000009	0.000200000009	✔
target_PSATA_Per1_ApaOpTrqOv_MtrNm_f32.value	0	0 ± 0.00001	✔
target_PSATA_Per1_ApaPosSrvoFlt_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_LxaOpTrqOv_MtrNm_f32.value	-8.80000019	-8.8 ± 0.00001	✔
target_PSATA_Per1_LxaTqLimnActv_Cnt_lgc.value	1	1	✔
target_PSATA_Per1_OpTrqOv_MtrNm_f32.value	-8.80000019	-8.8 ± 0.00001	✔



# TEST DETAILS REPORT

2018-03-02, 15:53:34+0530

PSATA\_Per1



## Test Step Call Trace

Actual Function	Count	Expected Function	Count	Result
TrqArbn_Fn	1	TrqArbn_Fn	1	✓
APASupervision_Fn	1	APASupervision_Fn	1	✓
Cal_ApaNTC	1	Cal_ApaNTC	1	✓
Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_PSATA_NxtrDiagMgr_SetNTCStatus	1	✓
Cal_PosServoSmoothingFactor	1	Cal_PosServoSmoothingFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
LxaSupervision_Fn	1	LxaSupervision_Fn	1	✓
Cal_LxaPosSrvoCmdLimit	1	Cal_LxaPosSrvoCmdLimit	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotngFactor	1	Cal_LxaPosSrvoSmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaPosSrvoSmotng	1	Cal_LxaPosSrvoSmotng	1	✓
LxaNTC	1	LxaNTC	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	2	IntplVarXY_u16_u16Xu16Y_Cnt	2	✓
NTCDiagc_Fn	4	NTCDiagc_Fn	4	✓
Cal_LxaPosSrvoSftySmotngFactor	1	Cal_LxaPosSrvoSftySmotngFactor	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
Cal_LxaOpTrqOv	1	Cal_LxaOpTrqOv	1	✓
Cal_OpTrqOv	1	Cal_OpTrqOv	1	✓