

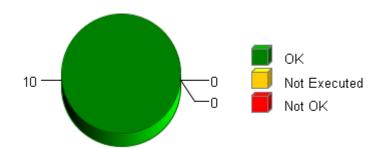
Summary

Overall Test Object Results (including Coverage)

Total Test Objects: 10 Successful: 10 Failed: 0 0 Not Executed:

2017-02-06 Date:

Time: 14:44:38+0530



Selected Project Items

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Init1"

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Per1"

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per2"

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos Per3"

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per4"

Test Object "CBD UnitTest/AbsHwPos Tcl2IVd/AbsHwPos SCom CustClrTrim"

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_CustSetTrim"

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_NxtClearTrim"

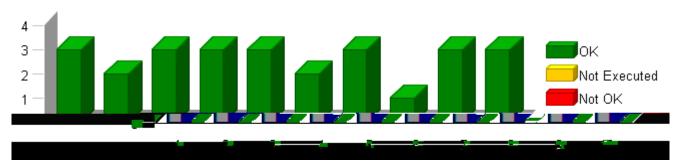
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_NxtSetTrim"

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/TrimNotPerfDiag"

Used Test Environments

TI TMS 570 PLS UDE (Default)

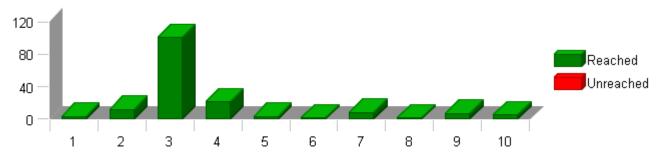
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

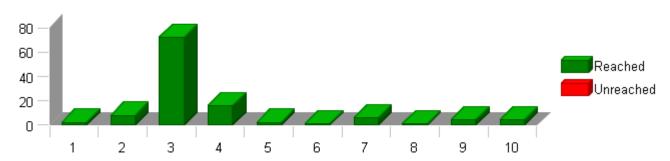


Statement (C0) Coverage: Total Statements for Each Test Object



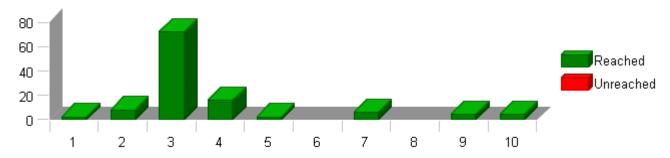
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

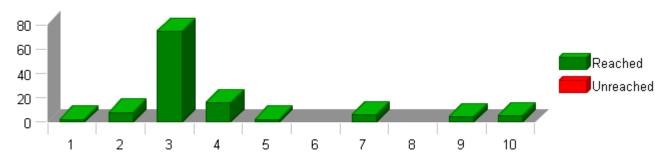


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



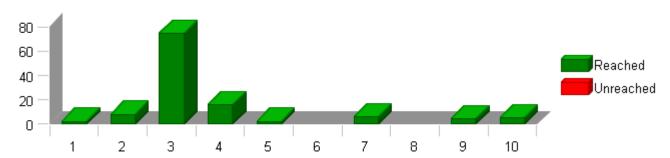
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	CO	C1	DC	MC/DC	MCC	Test Cases Result
	AbsHwPos	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
	AbsHwPos_Tcl2IVd	100 %	100 %	100 %	100 %	100 %	26 of 26 passed
1	AbsHwPos_Init1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
2	AbsHwPos_Per1	100 %	100 %	100 %	100 %	100 %	2 of 2 passed
3	AbsHwPos_Per2	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
4	AbsHwPos Per3	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
5	AbsHwPos Per4	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
6	AbsHwPos SCom CustClrTrim	100 %	100 %	-	-	-	2 of 2 passed
7	AbsHwPos SCom CustSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
8	AbsHwPos SCom NxtClearTrim	100 %	100 %	-	-	-	1 of 1 passed
9	AbsHwPos SCom NxtSetTrim	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
10	<u>TrimNotPerfDiag</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed

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2017-02-06, 14:39:51+0530





Project AbsHwPos

ModuleAbsHwPos_Tcl2lVdTest ObjectTrimNotPerfDiag

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total FAASH Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	Cycles
Timer Enabled	False

2017-02-06, 14:39:51+0530



TrimNotPerfDiag

Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 740.00 Cycles TS1.2 1728.00 Cycles

Description

Vector Description:

TS 1.1 Shortest Execution Path:

(k_UseTurnsCntr_Cnt_lgc == TRUE)=False, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And_ (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>True

TS 1.2 Longest Execution Path:

(k_UseTurnsCntr_Cnt_lgc == TRUE)=True, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And _ (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>False

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)				
Name	Input Value			
ManufMode_Cnt_T_enum	2			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓	

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	



Test Case 2: Range Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 740.00 Cycles
TS2.2 1728.00 Cycles
TS2.3 739.00 Cycles
TS2.4 1731.00 Cycles
TS2.5 739.00 Cycles
TS2.6 1521.00 Cycles
TS2.7 739.00 Cycles
TS2.8 1731.00 Cycles
TS2.8 1731.00 Cycles
TS2.9 739.00 Cycles
TS2.10 1520.00 Cycles

Vector Description: Description

TS 2.1 All Min TS 2.2 All Max

TS 2.2 All Max
TS 2.3 ManufMode_Cnt_T_enum=>Min
TS 2.4 ManufMode_Cnt_T_enum=>Max
TS 2.5 ManufMode_Cnt_T_enum=>Pos
TS 2.6 Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Min
TS 2.7 Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Max
TS 2.8 Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Pos
TS 2.9 k_UseTurnsCntr_Cnt_lgc=>Min
TS 2.10 k_UseTurnsCntr_Cnt_lgc=>Max

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T				9	•
Actual Function	Count	Expected Function	Coun	Resul	t
none	0	*** No Call Expected ***	0		,

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~



TrimNotPerfDiag

T					
Actual Function	Count	Expected Function	Count	Resu	lt
none	0	*** No Call Expected ***	0		V

Test Step 2.4 (Repeat Count = 1)	Test Step 2.4 (Repeat Count = 1)			
Name	Input Value			
ManufMode_Cnt_T_enum	2			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~



Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

T				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1284		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 2.10 (Repeat Count = 1)			~
Name	Input Value		
ManufMode_Cnt_T_enum	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6258		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓





Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 741.00 Cycles TS3.2 1727.00 Cycles TS3.3 1731.00 Cycles

Description

Vector Description:

TS 3.1 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=False, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And _ (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>True"
TS 3.2 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=True, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And _ (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>False"
TS 3.3 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=True,((ManufMode_Cnt_T_enum != ManufacturingMode) && (Rte_Pim_EOLVehCntrOffset()->EOLHwPosTrimPerformed_Cnt_u16 != D_TRIMPERFORMED_CNT_U16))=>False"

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓

T					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			
Name	Input Value		
ManufMode_Cnt_T_enum	2		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

2017-02-06, 14:39:51+0530



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T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

2017-02-06, 14:35:55+0530





Project AbsHwPos

ModuleAbsHwPos_Tcl2lVdTest ObjectAbsHwPos_Per3

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total FAM Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4

2017-02-06, 14:35:55+0530



Attributes			
Name Value			
Timer Prescale	0		
Timer Resolution			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP		



Test Case 1: Metrics Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

TS1.1 969.00 Cycles TS1.2 1585.00 Cycles

Description

Vector Description:

TS1.1"Shortest Execution Path:
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False"
TS1.2"Longest Execution Path:
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True
(FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=True
if (Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HWAtoMtrADiffLimit_HwDeg_f32)=>False
(AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 == 0U)=True

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	420		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	182		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~





T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	-

Test Case 2: Range Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1029.00 Cycles
TS2.2 1585.00 Cycles
TS2.3 1575.00 Cycles
TS2.3 1575.00 Cycles
TS2.4 969.00 Cycles
TS2.5 1565.00 Cycles
TS2.6 1119.00 Cycles
TS2.7 1104.00 Cycles
TS2.7 1104.00 Cycles
TS2.10 1056.00 Cycles
TS2.11 1549.00 Cycles
TS2.11 1549.00 Cycles
TS2.12 1056.00 Cycles
TS2.13 1086.00 Cycles
TS2.14 1056.00 Cycles
TS2.15 1069.00 Cycles
TS2.15 1069.00 Cycles
TS2.16 1056.00 Cycles
TS2.17 1549.00 Cycles
TS2.18 1549.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.19 1060.00 Cycles
TS2.20 1529.00 Cycles
TS2.21 1060.00 Cycles
TS2.22 1069.00 Cycles
TS2.23 1060.00 Cycles
TS2.24 1559.00 Cycles
TS2.25 1549.00 Cycles
TS2.26 1056.00 Cycles
TS2.27 1056.00 Cycles
TS2.28 1034.00 Cycles
TS2.28 1034.00 Cycles
TS2.29 1056.00 Cycles
TS2.31 949.00 Cycles
TS2.31 949.00 Cycles
TS2.33 1956.00 Cycles
TS2.33 1956.00 Cycles
TS2.33 1060.00 Cycles
TS2.33 1549.00 Cycles
TS2.33 1549.00 Cycles
TS2.34 1069.00 Cycles
TS2.35 1549.00 Cycles

Description

Vector Description:

TS2.1All Min TS2.2All Max TS2.3I2CHwAbsPosValid Cnt lgc=>True TS2.4I2CHwAbsPosValid_Cnt_lgc=>False TS2.5I2CHwAbsPos_HwDeg_f32==>Min TS2.6I2CHwAbsPos_HwDeg_f32==>Max TS2.7I2CHwAbsPos_HwDeg_f32==>Zero TS2.8l2CHwAbsPos_HwDeg_f32==>Pos TS2.9l2CHwAbsPos_HwDeg_f32==>Neg TS2.10AbsHwPos_RelHwPos_HwDeg_M_f32==>Min TS2.11AbsHwPos_RelHwPos_HwDeg_M_f32==>Max TS2.12AbsHwPos_RelHwPos_HwDeg_M_f32==>Zero TS2.13AbsHwPos_RelHwPos_HwDeg_M_f32==>Pos TS2.14AbsHwPos_RelHwPos_HwDeg_M_[32==>Neg TS2.15AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16==>Min TS2.16AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16==>Max TS2.17AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16==>Pos TS2.18k_HWAtoMtrADiffLimit_HwDeg_f32==>Min TS2.19k_HWAtoMtrADiffLimit_HwDeg_f32==>Max TS2.20k_HWAtoMtrADiffLimit_HwDeg_f32==>Pos/Default TS2.21k_HwAtoMtrAError_str.Threshold==>Min TS2.22k_HwAtoMtrAError_str.Threshold==>Max TS2.23k_HwAtoMtrAError_str.Threshold==>Pos/Default TS2.24k_HwAtoMtrAError_str.PStep==>Min
TS2.25k_HwAtoMtrAError_str.PStep==>Max
TS2.26k_HwAtoMtrAError_str.PStep==>Pos/Default TS2.20K_HWAtoMtrAError_str.PStep==>Pos/Default
TS2.27K_HWAtoMtrAError_str.NStep==>Min
TS2.28K_HWAtoMtrAError_str.NStep==>Max
TS2.29K_HWAtoMtrAError_str.NStep==>Pos/Default
TS2.30AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc==>Min
TS2.31AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Min
TS2.32AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Min
TS2.33AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Max TS2.33AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Max TS2.34AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Pos TS2.35AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Zero TS2.36AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32==>Neg

Test Step 2.1 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0

AbsHwPos_Per3

2017-02-06, 14:35:55+0530



Name	Input Value		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.3 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	125	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996	
AbsHwPos_RelHwPos_HwDeg_M_f32	420	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5	
k_HwAtoMtrAError_str.Threshold	470	
k_HwAtoMtrAError_str.PStep	124	
k_HwAtoMtrAError_str.NStep	182	
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1	

2017-02-06, 14:35:55+0530



Name	Input Value		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	148		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	-329.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	355.600006		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	48		
k_HwAtoMtrAError_str.NStep	173		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-457		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6214.20996	-6214.20996 ± 0.009	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	*none*	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)	1 434.1		
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	1718		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	238.600006		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	66		
k_HwAtoMtrAError_str.NStep	164		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	.bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	516	516	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2553.20996	-2553.20996 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	•

2017-02-06, 14:35:55+0530



Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	194		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1107.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	147.899994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	10		
k_HwAtoMtrAError_str.NStep	155		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosVa	id_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_H	wDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	39	39	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-752.099976	-752.099976 ± 0.0009	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	217		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	57.2000008		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	77		
k_HwAtoMtrAError_str.NStep	146		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsP	osValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsP	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	294	294	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓



Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	2840		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	8429.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	-33.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	91		
k_HwAtoMtrAError_str.NStep	137		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	145.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	2703	2703	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-179.100006	-179.100006 ± 0.0009	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	263		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1000		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	105		
k_HwAtoMtrAError_str.NStep	128		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-558.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDo	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	135	135	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1245.22998	1245.22998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 2.10 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-800.210022	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012	
k_HwAtoMtrAError_str.Threshold	7000	
k_HwAtoMtrAError_str.PStep	119	



Name	Input Value		
k_HwAtoMtrAError_str.NStep	119		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	123.800003		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	167	167	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10248.7998	-10248.7998 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	3709		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-2143.19995		
AbsHwPos RelHwPos HwDeg M f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k HWAtoMtrADiffLimit HwDeg f32	517.299988		
k HwAtoMtrAError str.Threshold	125		
k HwAtoMtrAError str.PStep	133		
k HwAtoMtrAError str.NStep	110		
tgt AbsHwPos Per3 I2CHwAbsPosValid Cnt Igc.value	1		
tgt AbsHwPos Per3 I2CHwAbsPos HwDeg f32.value	756		
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per3 I2CHwAbsPosValid Cnt Igc	tgt AbsHwPos Per3 I2CHwA	bsPosValid Cnt Igc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	125	125	-
AbsHwPos MtrToHwAOfstLearnt Cnt M Igc	1	1	-
AbsHwPos MtrtoHwAOfst HwDeg M f32	-2143.19995	-2143.19995 ± 0.009	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	117	117	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	_
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)			

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	332		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-3486.18994		
AbsHwPos_RelHwPos_HwDeg_M_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	540.400024		
k_HwAtoMtrAError_str.Threshold	148		
k_HwAtoMtrAError_str.PStep	147		
k_HwAtoMtrAError_str.NStep	101		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-465.5		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	231	231	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos MtrtoHwAOfst HwDeg M f32	465.5	465.5 ± 0.0009	•

2017-02-06, 14:35:55+0530



Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte Call An AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	Rte Call AbsHwPos Per3 CP1 CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	355		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018		
AbsHwPos_RelHwPos_HwDeg_M_f32	465.299988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	563.5		
k_HwAtoMtrAError_str.Threshold	1718		
k_HwAtoMtrAError_str.PStep	161		
k_HwAtoMtrAError_str.NStep	92		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	312.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbs	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbs	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516	516	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-4829.18018	-4829.18018 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~	

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	378		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-6172.16992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-88.9899979		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	586.599976		
k_HwAtoMtrAError_str.Threshold	194		
k_HwAtoMtrAError_str.PStep	175		
k_HwAtoMtrAError_str.NStep	83		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-469.399994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPos	Valid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos	_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	295	295	•
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	380.410004	380.410004 ± 0.0009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•	



Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016		
AbsHwPos_RelHwPos_HwDeg_M_f32	101.400002		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	212.399994		
k_HwAtoMtrAError_str.Threshold	217		
k_HwAtoMtrAError_str.PStep	189		
k_HwAtoMtrAError_str.NStep	74		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	626.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	189	189	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7515.16016	-7515.16016 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•	

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-8858.15039		
AbsHwPos_RelHwPos_HwDeg_M_f32	-457		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	62.7999992		
k_HwAtoMtrAError_str.Threshold	2840		
k_HwAtoMtrAError_str.PStep	203		
k_HwAtoMtrAError_str.NStep	65		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-782.799988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPo	osValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPo	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65470	65470	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	325.799988	325.799988 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓	

Test Step 2.17 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	4475	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	
AbsHwPos_RelHwPos_HwDeg_M_f32	123.800003	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	655.900024	
k_HwAtoMtrAError_str.Threshold	263	





Name	Input Value		
k_HwAtoMtrAError_str.PStep	217		
k_HwAtoMtrAError_str.NStep	105		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	263	263	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-10201.1396	-10201.1396 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)			•
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	470		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	286		
k_HwAtoMtrAError_str.PStep	231		
k_HwAtoMtrAError_str.NStep	119		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	286	286	✓
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	700.22998	700.22998 ± 0.0009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓

T							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•			
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•			

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	493		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1897.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	3709		
k_HwAtoMtrAError_str.PStep	245		
k_HwAtoMtrAError_str.NStep	133		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosV	alid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_I	HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	360	360	-

2017-02-06, 14:35:55+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-976.200012	-976.200012 ± 0.0009	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓





T							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~			
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓			

Test Step 2.22 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984		
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1021.70001		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	218		
k_HwAtoMtrAError_str.NStep	175		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAb	sPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAb	sPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	780	780	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	5488.58984	5488.58984 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	585		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	6685.68018		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	179.399994		
k_HwAtoMtrAError_str.Threshold	200		
k_HwAtoMtrAError_str.PStep	182		
k_HwAtoMtrAError_str.NStep	189		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	147.899994		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	396	396	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-930.699951	-930.700012 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T ·							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~			
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓			



Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	7882.77002		
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	137.100006		
k_HwAtoMtrAError_str.Threshold	489		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	203		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	600		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	92.0999756	92.0999985 ± 0.00009	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035		
AbsHwPos_RelHwPos_HwDeg_M_f32	756		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	694.799988		
k_HwAtoMtrAError_str.Threshold	646		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-33.5		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	646	646	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	9079.86035	9079.86035 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	654	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	10276.9502	
AbsHwPos_RelHwPos_HwDeg_M_f32	819.900024	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	123	

2017-02-06, 14:35:55+0530



Name	Input Value		
k_HwAtoMtrAError_str.Threshold	8974		
k_HwAtoMtrAError_str.PStep	4		
k_HwAtoMtrAError_str.NStep	199		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-897.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	455	455	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1717	1717 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	677		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-5478.31982		
AbsHwPos_RelHwPos_HwDeg_M_f32	-883.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1310.19995		
k_HwAtoMtrAError_str.Threshold	424		
k_HwAtoMtrAError_str.PStep	173		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	465.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwA	bsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwA	bsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	677	677	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1349.09998	-1349.09998 ± 0.009	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	→
		none	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	•
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	7000		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1		
AbsHwPos_RelHwPos_HwDeg_M_f32	947.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1367.90002		
k_HwAtoMtrAError_str.Threshold	4477		
k_HwAtoMtrAError_str.PStep	164		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-88.9899979		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPos\	/alid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_	HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6745	6745	-
AbsHwPos MtrToHwAOfstLearnt Cnt M Igc	1	1	✓

2017-02-06, 14:35:55+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1	1 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	723		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1457.31995		
AbsHwPos_RelHwPos_HwDeg_M_f32	-1011.59998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1425.59998		
k_HwAtoMtrAError_str.Threshold	1470		
k_HwAtoMtrAError_str.PStep	155		
k_HwAtoMtrAError_str.NStep	1		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	75.75		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	722	722	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1087.34998	-1087.34998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998		
AbsHwPos_RelHwPos_HwDeg_M_f32	-465.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	378.700012		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	203		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	692.099976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	eg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-1245.59998	-1245.59998 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~



Test Step 2.31 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	5398		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247		
AbsHwPos_RelHwPos_HwDeg_M_f32	312.700012		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	493		
k_HwAtoMtrAError_str.PStep	32		
k_HwAtoMtrAError_str.NStep	217		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-601.400024		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1247	1247 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	562		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994		
k_HwAtoMtrAError_str.Threshold	516		
k_HwAtoMtrAError_str.PStep	145		
k_HwAtoMtrAError_str.NStep	231		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	510.700012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPos	sValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos	s_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	516	516	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	•

Test Step 2.33 (Repeat Count = 1)	→
Name	Input Value
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	585
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026
AbsHwPos_RelHwPos_HwDeg_M_f32	1016.09998
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos

2017-02-06, 14:35:55+0530



Name	Input Value		
k_HWAtoMtrADiffLimit_HwDeg_f32	448		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	99		
k_HwAtoMtrAError_str.NStep	245		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	420		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	340	340	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	596.099976	596.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	108		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992		
AbsHwPos_RelHwPos_HwDeg_M_f32	-782.799988		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	471.100006		
k_HwAtoMtrAError_str.Threshold	562		
k_HwAtoMtrAError_str.PStep	87		
k_HwAtoMtrAError_str.NStep	236		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-329.299988		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHw/	AbsPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHw/	AbsPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	195	195	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	2145.66992	2145.66992 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	6317		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0		
AbsHwPos_RelHwPos_HwDeg_M_f32	692.099976		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	494.200012		
k_HwAtoMtrAError_str.Threshold	654		
k_HwAtoMtrAError_str.PStep	128		
k_HwAtoMtrAError_str.NStep	227		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-238.600006		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsI	PosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsI	Pos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	654	654	•

2017-02-06, 14:35:55+0530





Name	Actual Value	Expected Value	Result
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	0	0 ± 0.000009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	-

Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	654		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-7894.12012		
AbsHwPos_RelHwPos_HwDeg_M_f32	469.399994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	517.299988		
k_HwAtoMtrAError_str.Threshold	677		
k_HwAtoMtrAError_str.PStep	214		
k_HwAtoMtrAError_str.NStep	91		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	100		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	563	563	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	369.399994	369.399994 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Case 3: Path Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment) $\,$

CPU Cycles:

TS3.1 1029.00 Cycles TS3.2 1585.00 Cycles TS3.3 1575.00 Cycles TS3.4 1119.00 Cycles TS3.5 1104.00 Cycles

Description Vector Description:

TS3.1(I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=False
TS3.2"(I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=True
(FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=False
(Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HWAtoMtrADiffLimit_HwDeg_f32)=True
(DiagFailed_m(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16, k_HwAtoMtrAError_str) == TRUE)=True"
TS3.3"(FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=True
(Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HWAtoMtrADiffLimit_HwDeg_f32)=False
(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 == 0U)=True"
TS3.4(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 == 0U)=False
TS3.5(DiagFailed_m(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16, k_HwAtoMtrAError_str) == TRUE)=False

Test Step 3.1 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125	

2017-02-06, 14:35:55+0530



Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	0		
k_HwAtoMtrAError_str.Threshold	0		
k_HwAtoMtrAError_str.PStep	0		
k_HwAtoMtrAError_str.NStep	0		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	0		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	-900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0	0	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-11026	-11026 ± 0.09	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos HwAtoMtrAFltAcc Cnt M u16	65535		
AbsHwPos MtrToHwAOfstLearnt Cnt M Igc	1		
AbsHwPos MtrtoHwAOfst HwDeg M f32	11026		
AbsHwPos RelHwPos HwDeg M f32	10125		
Rte Inst Ap AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	1600		
k_HwAtoMtrAError_str.Threshold	65535		
k_HwAtoMtrAError_str.PStep	255		
k_HwAtoMtrAError_str.NStep	255		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAb	osPosValid_Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAt	osPos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	65535	65535	-
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	•
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	11026	11026 ± 0.09	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	-

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~



Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16	125		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-9875.20996		
AbsHwPos_RelHwPos_HwDeg_M_f32	420		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	332.5		
k_HwAtoMtrAError_str.Threshold	470		
k_HwAtoMtrAError_str.PStep	124		
k_HwAtoMtrAError_str.NStep	182		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	666.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	0	0	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-246.200012	-246.199997 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	117	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	✓

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	194		
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	0		
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	1107.79004		
AbsHwPos_RelHwPos_HwDeg_M_f32	147.899994		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HWAtoMtrADiffLimit_HwDeg_f32	401.799988		
k_HwAtoMtrAError_str.Threshold	5398		
k_HwAtoMtrAError_str.PStep	10		
k_HwAtoMtrAError_str.NStep	155		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	900		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	39	39	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	✓
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	-752.099976	-752.099976 ± 0.0009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	217	
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	
AbsHwPos_RelHwPos_HwDeg_M_f32	57.2000008	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HWAtoMtrADiffLimit_HwDeg_f32	424.899994	
k_HwAtoMtrAError_str.Threshold	562	

2017-02-06, 14:35:55+0530



Name	Input Value		
k_HwAtoMtrAError_str.PStep	77		
k_HwAtoMtrAError_str.NStep	146		
tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value	1		
tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc	tgt_AbsHwPos_Per3_I2CHwAbsPosValid_C	:nt_lgc	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32	tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDe	g_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_HwAtoMtrAFltAcc_Cnt_M_u16	294	294	~
AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc	1	1	~
AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32	4768.79004	4768.79004 ± 0.009	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	117	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached	1	~

2017-02-06, 14:38:49+0530



AbsHwPos_SCom_NxtSetTrim		MACILAL
Project		
Module		
Test Object		
Instrumentation: Test Obje	ct Only	
Statement (C0) Coverage		
Decision Coverage		
Branch (C1) Coverage		
MCC Coverage		
MC/DC Coverage		
Statistics		
Total Testcases		
Successful	✓	
Failed		
Not Executed		

Module Properties

Project Root Directory
Configuration File
Target Environment
Kind of Test
Linker Options
Source File(s)
File
Compiler Options

ame	Text			

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit ET 0 a 0 88235	0 88235 0 88235 rg 1 0 0 1 178 87 84 u1 72 t0 0 1 0 0 TN Path

AbsHwPos_SCom_NxtSetTrim



Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



AbsHwPos_SCom_NxtSetTrim

1651	Case '	I. Mer	1105	1651

Specification "Performa

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 937.00 Cycles TS1.2 1724.00 Cycles

Description

Name

Name

Test Step 1.1 (Repeat Count = 1)					V
Name		Input Value			
Name		Actual Value	Expected Value		Result
					~
					·
					~
					~
Τ					✓
Actual Function	Count	Expected Function		Count	Result
					~
Test Step 1.2 (Repeat Count = 1)					V

Input Value

				_
				✓
				~
T				✓
Actual Function	Count	Expected Function	Count	Result
				~
				✓

Actual Value

Expected Value

Result



Count Result

AbsHwPos_SCom_NxtSetTrim Test Case 2: Range Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

CPU Cycles:
TS'2.1 937.00 Cycles
TS2.2 936.00 Cycles
TS2.3 1511.00 Cycles
TS2.4 1512.00 Cycles
TS2.4 1512.00 Cycles
TS2.5 1725.00 Cycles
TS2.6 1511.00 Cycles
TS2.7 1511.00 Cycles
TS2.8 936.00 Cycles
TS2.9 936.00 Cycles
TS2.10 934.00 Cycles
TS2.11 1511.00 Cycles
TS2.12 1512.00 Cycles
TS2.13 1512.00 Cycles
TS2.14 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.16 1512.00 Cycles
TS2.17 1725.00 Cycles
TS2.18 1511.00 Cycles

Description

Actual Function

		✓
Input Value		
Actual Value	Expected Value	Result
		Y
		V
		J.
	Actual Value	

Count Expected Function

Test Step 2.2 (Repeat Count = 1)			
	Input Value		
Name	Actual Value	Expected Value	Result
			~



AbsHwPos_SCom_NxtSetTrim		,		RAZON	'at
Name		Actual Value	Expected Value		Resul
					,
					•
Τ					
Actual Function	Count	Expected Function		Count	Resul
					,
Test Step 2.3 (Repeat Count = 1)					•
Name		Input Value			
Name		Actual Value	Expected Value		Resul
					•
					•
T					•
Actual Function	Count	Expected Function		Count	Resul
					•
Test Step 2.4 (Repeat Count = 1)					•
Name		Input Value			
Name		Actual Value	Funcated Value		Resul
Name		Actual value	Expected Value		Resul
					•
			'		
Τ					•
Actual Function	Count	Expected Function		Count	Resul
Took Ston 2.5 (Domest Occupt of the					
Test Step 2.5 (Repeat Count = 1) Name		Input Value			•



AbsHwPos_SCom_NxtSetTrim				razul (at
Name		Input Value			
Name		Actual Value	Expected Value		Result
					-
					•
Т					✓
Actual Function	Count	Expected Function		Count	Result
Test Step 2.6 (Repeat Count = 1)					✓
Name		Input Value			Ť
Name		Actual Value	Expected Value		Result
			ZAPOGIOU TUILO		-
			'		
Т					~
Actual Function	Count	Expected Function		Count	
Test Step 2.7 (Repeat Count = 1)					✓
Name		Input Value			
					1
Name		Actual Value	Expected Value		Result
					•
					-
					✓
Т					V
Actual Function	Count	Expected Function		Count	
					•
Test Step 2.8 (Repeat Count = 1)					✓
Name		Input Value			

AbsHwPos SCom NxtSetTrim



ADSTWF0S_SCOTI_NXISELTIIII			(OLC	1000	•
Name		Input Value			
Name		Actual Value	Expected Value	Res	sul
					•
					•
					•
Τ					v
Actual Function	Count	Expected Function	Cou	ınt Res	sul
					•
Test Step 2.9 (Repeat Count = 1)					•
Name		Input Value			
Name		Actual Value	Expected Value	Res	sul
					•
					•
					•
T					v
Actual Function	Count	Expected Function	Coi	ınt Res	
					•
Test Step 2.10 (Repeat Count = 1)					•
Name		Input Value			
Name		Actual Value	Expected Value	Res	sul
					•
					•
					•
T					v
	Count	Expected Function	Cor	ınt Res	
				1.50	V

Test Step 2.11 (Repeat Count = 1)	✓
Name	Input Value

AbsHwPos_SCom_NxtSetTrim



Name		Input Value			
			I		
Name		Actual Value	Expected Value		Result
					~
					~
					✓
T					V
Actual Function	Count	Expected Function	Cou	nt	Result
					~
Test Step 2.12 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					✓
					Ž
					✓
Т					✓
Actual Function	Count	Expected Function	Cou	nt	Result
					~
Test Step 2.13 (Repeat Count = 1)					V
Name		Input Value			
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		~
Name		Actual Value	Expected Value		~
Name		Actual Value	Expected Value		٠ ٧
Name		Actual Value	Expected Value		~
		Actual Value	Expected Value		\ \ \ \
T	Count				\rightarrow \right
	Count	Actual Value Expected Function			v v v
Т	Count				\rightarrow \right



_	_	
AbsHwPos_SC	com_NxtSetTrin	n

T (0) 044/D (0) (1)					
Test Step 2.14 (Repeat Count = 1)		Innut Value			✓
Name		Input Value			
Name		Actual Value	Formanda d Malora		D14
Name		Actual value	Expected Value		Result
					•
					~
					~
T					
Actual Function	Caust	Expected Function		Caust	Result
Actual Function	Count	Expected Function		Count	Result
Test Step 2.15 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
Name		Actual Value	Expected Value		~
Name		Actual Value	Expected Value		
Name		Actual Value	Expected Value		*
		Actual Value	Expected Value		· · ·
T					· · · · · · · · · · · · · · · · · · ·
	Count	Actual Value Expected Function		Count	v v
T	Count			Count	· · · · · · · · · · · · · · · · · · ·
T	Count			Count	v v
T	Count			Count	v v
T Actual Function	Count			Count	v v
T	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count			Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
T Actual Function Test Step 2.16 (Repeat Count = 1)	Count	Expected Function		Count	V V V Result
Test Step 2.16 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.16 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.16 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.16 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.16 (Repeat Count = 1) Name	Count	Expected Function Input Value		Count	Result
Test Step 2.16 (Repeat Count = 1) Name		Expected Function Input Value	Expected Value		Result
Test Step 2.16 (Repeat Count = 1) Name Name		Expected Function Input Value Actual Value	Expected Value		Result

Test Step 2.17 (Repeat Count = 1)



AbsHwPos_SCom_NxtSetTrim

Name		Input Value			
Name		Actual Value	Expected Value		Result
		1 100000			
					•
					J
					~
					-
T					
					✓
Actual Function	Count	Expected Function	Cou	nt l	Result
					~
					•
Test Step 2.49 (Beneat Count = 4)					
Test Step 2.18 (Repeat Count = 1)					✓
Name		Input Value			

Actual Function	Count	Expected Function	Count	Result	
				•	

Actual Value

Expected Value

Result

Test Case 3: Path Test

Specification

Name

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 937.00 Cycles TS3.2 1724.00 Cycles TS3.3 1513.00 Cycles

Description

Test Step 3.1 (Repeat Count = 1)	✓
Name	Input Value

AbsHwPos_SCom_NxtSetTrim



Name		Input Value			
Name		Actual Value	Expected Value		Result
					V
					*
					~
Т					✓
Actual Function	Count	Expected Function	C	ount	Result
					~
Test Step 3.2 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					✓
					~
					~
_					
T Advis Francisco	0	Francisco de Français de			✓
Actual Function	Count	Expected Function	C	ount	Result
					~
			·		
Test Step 3.3 (Repeat Count = 1)					✓
Name		Input Value			
Name		Actual Value	Expected Value		Result
					resuit ✓
					✓
					•
					~
Т					V
Actual Function	Count	Expected Function	C	ount	Result
					~

2017-02-06, 14:37:50+0530



AbsHwPos_SCom_NxtClearTrim

Project AbsHwPos

Module AbsHwPos_Tcl2lVd

Test Object AbsHwPos_SCom_NxtClearTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Unit Test Description* Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>
Time Unit	Cycles
Timer Enabled	False
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

AbsHwPos_SCom_NxtClearTrim

2017-02-06, 14:37:50+0530



Workspace File

D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Range Test

AbsHwPos_SCom_NxtClearTrim

Specification "Perfo

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 381.00 Cycles TS1.2 381.00 Cycles TS1.3 381.00 Cycles TS1.4 381.00 Cycles TS1.5 381.00 Cycles

Description Vector Description:

TS1.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Min TS1.2 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Max TS1.3 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Mid TS1.4 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Pos TS1.5 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Neg

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T .			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.4 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_VehCntrOffset_HwDeg_M_f32	12564.01
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos

AbsHwPos_SCom_NxtClearTrim

2017-02-06, 14:37:50+0530



Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1523.02		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~

2017-02-06, 14:24:44+0530





Project AbsHwPos

 Module
 AbsHwPos_Tcl2lVd

 Test Object
 AbsHwPos_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	2	
Successful	2	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	Cycles
Timer Enabled	False

2017-02-06, 14:24:44+0530



Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1158.00 Cycles TS1.2 1714.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $(AbsHwPos_RelHwPos_HwDeg_M_f32>=D_RELHWPOSMAX_HWDEG_F32)=>True"$

TS1.2 Longest Execution Path:

 $(AbsHwPos_RelHwPos_HwDeg_M_f32 <= D_RELHWPOSMAX_HWDEG_F32) => True"$

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	1_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9875	9875	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	•
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	3200	3200	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)			·	
Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_GearRatio_Uls_f32	0.00999999978			
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000			
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125			
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000	-10000		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Derivation{ $	Deg_1 tgt_AbsHwPos_Per1_AlignedC	g_1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_Aligne	dRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErr	or_HwDeg_f32		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32$	tgt_AbsHwPos_Per1_CumMec	hMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPo	s_HwDeg_f32		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	25	25	-	
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	•	
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	25	25	•	
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	25	25 ± 0.1	✓	

T				•
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•



Test Case 2: Range Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1718.00 Cycles
TS2.2 1158.00 Cycles
TS2.3 1715.00 Cycles
TS2.4 1714.00 Cycles
TS2.5 1715.00 Cycles
TS2.6 1715.00 Cycles
TS2.7 1721.00 Cycles
TS2.8 1714.00 Cycles
TS2.9 1715.00 Cycles
TS2.10 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.12 1715.00 Cycles
TS2.13 1715.00 Cycles
TS2.14 1715.00 Cycles
TS2.14 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles TS2.14 TS2.15 TS2.16 TS2.17 TS2.18 TS2.18 TS2.19 TS2.20 TS2.21 TS2.22 TS2.23 TS2.24 TS2.25 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles 1715.00 Cycles

Description

Vector Description:

TS 2.1All Min

TS 2.2All Max

IS 2.2AlI Max

S 2.3AlignedCumMechMtrPosCRF_Deg_f32==>Min

TS 2.4AlignedCumMechMtrPosCRF_Deg_f32==>Max

TS 2.5AlignedCumMechMtrPosCRF_Deg_f32==>Pos

TS 2.5AlignedCumMechMtrPosCRF_Deg_f32==>Zero

TS 2.7AlignedCumMechMtrPosCRF_Deg_f32==>Neg

TS 2.7AlignedCumMechMtrPosCRF_Deg_f32==>Neg

TS 2.8ComplError_HwDeg_f32==>Max

TS 2.9ComplError_HwDeg_f32==>Max

IS 2.9ComplError_HwDeg_132==>Max
TS 2.10ComplError_HwDeg_f32==>Pos
TS 2.11ComplError_HwDeg_f32==>Pos
TS 2.12ComplError_HwDeg_f32==>Neg
TS 2.13CumMechMtrPosCRF_Deg_f32==>Min
TS 2.14CumMechMtrPosCRF_Deg_f32==>Max
TS 2.15CumMechMtrPosCRF_Deg_f32==>Pos
TS 2.15CumMechMtrPosCRF_Deg_f32==>Pos
TS 2.17CumMechMtrPosCRF_Deg_f32==>Neg
TS 2.18k_GearRatio_Uls_f32==>Min
TS 2.18k_GearRatio_Uls_f32==>Max

TS 2.19k_GearRatio_Uls_f32==>Max
TS 2.20k_GearRatio_Uls_f32==>Pos/Default
TS 2.21AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Min TS 2.22AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Max TS 2.23AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Zero TS 2.24AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Pos

TS 2.25AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Neg

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.00999999978		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	t tgt_AbsHwPos_Per1_AlignedCumMechMtr	PosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos	_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_	f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRI	F_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	25	25.0000019	~
AbsHwPos_RelHwPos_HwDeg_M_f32	25	25 ± 0.00009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	25	25.0000019	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	25	25 ± 0.1	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	✓	



Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg	eg 1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9875	9875	~
AbsHwPos_RelHwPos_HwDeg_M_f32	9875	9875 ± 0.009	•
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	3200	3200	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	3200	3200 ± 0.1	~

Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•		

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2171.08569		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	647.200012		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_De	t tgt_AbsHwPos_Per1_AlignedCumMechMt	rPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPo	s_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg	_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCF	RF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_	<u>f</u> 32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125	-10125	~
AbsHwPos_RelHwPos_HwDeg_M_f32	522.200012	522.200012 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-3200	-3200	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	522.200012	522.200012 ± 0.0009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2617.10376		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.029999993		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	10000		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	101.199997		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-361		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCF	RF_Deg_1 tgt_AbsHwPos_Per1_Aligned0	CumMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_	_f32 target_AbsHwPos_Per1_Align	edRelHwPos_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplEi	ror_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg	_f32 tgt_AbsHwPos_Per1_CumMed	chMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwP	os_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	198.800003	198.800003	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-112.029999	-112.029999 ± 0.0009	✓
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	198.800003	198.800003	~
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	-112.029999	-112.029999 ± 0.1	·





T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	500		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.0399999991		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	543.869995		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-54.7999992		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	912		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	76.5547943	76.5548019	~
AbsHwPos_RelHwPos_HwDeg_M_f32	91.2799988	91.2799988 ± 0.00009	•
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	76.5547943	76.5548019	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	91.2799988	91.2799988 ± 0.1	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	600		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.0500000007		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	0		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	31.7000008		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	6123.6001		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_De	g_1 tgt_AbsHwPos_Per1_AlignedCumMechMt	rPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPo	s_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg	_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCF	RF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_	<u>f</u> 32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-31.7000008	-31.7000008	~
AbsHwPos_RelHwPos_HwDeg_M_f32	274.480011	274.480011 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-31.7000008	-31.7000008	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	274.480011	274.480011 ± 0.1	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)	v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1639.56409
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_GearRatio_Uls_f32	1
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-879.599976
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	89
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-4456
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg 1	tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32

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Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-968.599976	-968.599976	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-4545	-4545 ± 00.009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-968.599976	-968.599976	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-3200	-3200 ± 0.1	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-648.309998		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.0700000003		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7645.12012		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	1023.53998		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	660.158386	660.158386	~
AbsHwPos_RelHwPos_HwDeg_M_f32	196.647797	196.647797 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	660.158386	660.158386	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	196.647797	196.647797 ± 0.1	~

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~	

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	988.5		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.0799999982		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	8123.3999		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	125		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4789.6001		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Inst_Ap_AbsHwPosAbsHw$	_Deg_1 tgt_AbsHwPos_Per1_Aligne	dCumMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_Alig	nedRelHwPos_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_Compl	Error_HwDeg_f32	
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f3$	2 tgt_AbsHwPos_Per1_CumM	echMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHw	Pos_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	524.871948	524.871948	✓
AbsHwPos_RelHwPos_HwDeg_M_f32	258.167999	258.167999 ± 0.0009	✓
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	524.871948	524.871948	✓
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	258.167999	258.167999 ± 0.1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~



Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1024		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.090000036		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-8601.67969		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	66.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	8555.66016		
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg 1	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-840.651184	-840.651184	~
AbsHwPos_RelHwPos_HwDeg_M_f32	703.50946	703.509399 ± 0.0009	✓
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-840.651184	-840.651184	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	703.50946	703.509399 ± 0.1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.11 (Repeat Count = 1)			✓.
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	125.146599		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.100000001		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	9079.95996		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	0		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	648.599976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	907.996033	907.996033	~
AbsHwPos_RelHwPos_HwDeg_M_f32	64.8600006	64.8600006 ± 0.00009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	907.996033	907.996033	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	64.8600006	64.8600006 ± 0.1	~

Τ				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	102.424004		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.109999999		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	647.200012		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-75.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	987.099976		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosC$	RF_Deg_1 tgt_AbsHwPos_Per1_AlignedCu	mMechMtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg	_f32 target_AbsHwPos_Per1_Aligned	IRelHwPos_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErro	r_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg	g_f32 tgt_AbsHwPos_Per1_CumMech	MtrPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos	_HwDeg_f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	146.692001	146.692001	~
AbsHwPos_RelHwPos_HwDeg_M_f32	184.080994	184.080994 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	146.692001	146.692001	~
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	184.080994	184.080994 ± 0.1	✓



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	135.449997		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.119999997		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-361		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	41.9000015		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-85.2200012	-85.2200012	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-1241.90002	-1241.90002 ± 0.009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-85.2200012	-85.2200012	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-1241.90002	-1241.90002 ± 0.1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.14 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1245.01001		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.129999995		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	912		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	69.4000015		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	10000		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_1	tgt_AbsHwPos_Per1_AlignedCumMechMtrF	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	_HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedReIHwPos_HwDeg_M_f32	49.159996	49.159996	✓
AbsHwPos_RelHwPos_HwDeg_M_f32	1230.59998	1230.59998 ± 0.009	•
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	49.159996	49.159996	•
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	1230.59998	1230.59998 ± 0.1	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	Rte Call AbsHwPos Per1 CP1 CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-3264.02002
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_GearRatio_Uls_f32	0.140000001
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	545.299988
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-50.7999992
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	743.869995
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg 1	tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32

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Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	127.141998	127.141998	~
AbsHwPos_RelHwPos_HwDeg_M_f32	154.941803	154.941803 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	127.141998	127.141998	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	154.941803	154.941803 ± 0.1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2590.63379		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.150000006		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-3479		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	99		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-620.850037	-620.850037	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-99	-99 ± 0.00009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-620.850037	-620.850037	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-99	-99 ± 0.1	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

Test Step 2.17 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	104.297646		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.159999996		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	7258		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-105.599998		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-849.599976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_	tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1266.88	1266.88	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-30.3359909	-30.3360004 ± 0.00009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	1266.88	1266.88	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-30.3359909	-30.3360004 ± 0.1	~

T						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~		



Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1481.36023		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.0099999978		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-6123.6001		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	120.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4426		
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg 1	eg 1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-181.735992	-181.735992	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-76.2400055	-76.2399979 ± 0.00009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-181.735992	-181.735992	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-76.2400055	-76.2399979 ± 0.1	~

T .						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	•		

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-655.123474		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	1		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-2456		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	96.5999985		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	589		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_Inst_Results (Ap_AbsHwPosCRF_Deg_Inst_Results (Ap_AbsHwPosCRF_Deg_Inst_Results (Ap_AbsHwPosCRF_Deg_Inst_Results (Ap_AbsHwPosCRF_Deg_Inst_Results (Ap_AbsHwPosCRF_Deg_Inst_R$	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2552.6001	-2552.6001	~
AbsHwPos_RelHwPos_HwDeg_M_f32	492.399994	492.399994 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-2552.6001	-2552.6001	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	492.399994	492.399994 ± 0.1	~

T .						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~		

Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1324.41345	1324.41345		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_GearRatio_Uls_f32	0.0454545468			
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	1023.53998			
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-44.7999992			
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-456.5			
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCapacition AbsHwPosAbsHwPosPer1_AlignedCumMechMtrPosCapacition AbsHwPosAb$	RF_Deg_1 tgt_AbsHwPos_Per1_AlignedCเ	1 tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg	_f32 target_AbsHwPos_Per1_Aligned	dRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplErro	tgt_AbsHwPos_Per1_ComplError_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_De	g_f32 tgt_AbsHwPos_Per1_CumMech	nMtrPosCRF_Deg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos	s_HwDeg_f32		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	91.3245468	91.3245468	~	
AbsHwPos_RelHwPos_HwDeg_M_f32	24.0499992	24.0499992 ± 0.0009	~	
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	91.3245468	91.3245468	~	
tgt AbsHwPos Per1 RelHwPos HwDeg f32.value	24.0499992	24.0499992 ± 0.0009	✓	





T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.100000001		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	8123.3999		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-50.7999992		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	-4456		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	863.140015	863.140015	~
AbsHwPos_RelHwPos_HwDeg_M_f32	-394.800018	-394.799988 ± 0.0009	•
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	863.140015	863.140015	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	-394.800018	-394.799988 ± 0.0009	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_UIs_f32	0.109999999		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	-8601.67969		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	99		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	1023.53998		
$tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_transformed and the property of the prope$	tgt_AbsHwPos_Per1_AlignedCumMechMt	rPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg	_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCF	RF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_	f32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1045.18481	-1045.18481	~
AbsHwPos_RelHwPos_HwDeg_M_f32	13.5893936	13.5894003 ± 0.0009	✓
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-1045.18481	-1045.18481	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	13.5893936	13.5894003 ± 0.0009	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)	y de la companya de
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	0
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
k_GearRatio_Uls_f32	0.119999997
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	9079.95996
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	-105.599998
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	4789.6001
tgt Rte Inst Ap AbsHwPos.AbsHwPos Per1 AlignedCumMechMtrPosCRF Deg	tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32

2017-02-06, 14:24:44+0530



Name	Input Value		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1195.19519	1195.19519	~
AbsHwPos_RelHwPos_HwDeg_M_f32	680.35199	680.35199 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	1195.19519	1195.19519	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	680.35199	680.35199 ± 0.0009	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1245.01001		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.129999995		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	647.200012		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	120.5		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	8555.66016		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_t	tgt_AbsHwPos_Per1_AlignedCumMechMtrP	osCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_	HwDeg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg_f3	32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCRF_	_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f3:	2	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-36.3639984	-36.3640022	~
AbsHwPos_RelHwPos_HwDeg_M_f32	991.73584	991.735779 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-36.3639984	-36.3640022	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	991.73584	991.735779 ± 0.0009	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-3264.02002		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_GearRatio_Uls_f32	0.239999995		
tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value	12.4499998		
tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value	12.4499998		
tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value	648.599976		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg	_1 tgt_AbsHwPos_Per1_AlignedCumMechMt	rPosCRF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32	target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32	tgt_AbsHwPos_Per1_ComplError_HwDeg	_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32	tgt_AbsHwPos_Per1_CumMechMtrPosCF	RF_Deg_f32	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32	tgt_AbsHwPos_Per1_RelHwPos_HwDeg_	<u>f</u> 32	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9.46199989	-9.46199989	~
AbsHwPos_RelHwPos_HwDeg_M_f32	143.213989	143.214005 ± 0.0009	~
target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value	-9.46199989	-9.46199989	~
tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value	143.213989	143.214005 ± 0.0009	~

Т						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached	1	~		

2017-02-06, 14:43:28+0530





Project AbsHwPos

Module AbsHwPos_Tcl2lVd

Test Object AbsHwPos_SCom_CustClrTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	2	
Successful	2	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Unit Test Description* Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):65 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>
Time Unit	Cycles
Timer Enabled	False
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

AbsHwPos_SCom_CustClrTrim

2017-02-06, 14:43:28+0530



Workspace File

D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



AbsHwPos_SCom_CustClrTrim

Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 792.00 Cycles TS1.2 1012.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

If ((ManufMode_Cnt_T_enum <> ManufacturingMode) And D_TRIMPERFORMED_CNT_U16))=>False"

(Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <>

TS1.2 Longest Execution Path:

If ((ManufMode_Cnt_T_enum <> ManufacturingMode) And D_TRIMPERFORMED_CNT_U16))=>True"

(Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <>

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	47		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1.		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 1.2 (Repeat Count = 1)			V
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMod	e_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~



Test Case 2: Range Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 792.00 Cycles TS2.2 1012.00 Cycles TS2.3 792.00 Cycles TS2.4 1012.00 Cycles TS2.5 792.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ManufMode_Cnt_enum==>Min
TS2.4 ManufMode_Cnt_enum==>Max
TS2.5 ManufMode_Cnt_enum==>Pos

Test Step 2.1 (Repeat Count = 1)				
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	nufMode_Cnt_enum_data		
k_UseTurnsCntr_Cnt_lgc	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓	
tgt Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	✓	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			✓	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_I	ManufMode_Cnt_enum_data		
k_UseTurnsCntr_Cnt_lgc	1			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt Pim EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2			
Name	Actual Value	Expected Value	Result	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓	
tgt Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0	0	✓	

Τ		✓		
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	✓

Test Step 2.3 (Repeat Count = 1)	✓
Name	Input Value
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data

2017-02-06, 14:43:28+0530



AbsHwPos_SCom_CustClrTrim

Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1542		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Τ		V		
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	1		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	364		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufM	lode_Cnt_enum_data	
k_UseTurnsCntr_Cnt_lgc	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	47		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tat Pim FOI VehCntrOffset FOI HwPosTrimPerformed Cnt u16	0	0	_

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

AbsHwPos_SCom_CustClrTrim

2017-02-06, 14:43:28+0530



2017-02-06, 14:37:18+0530



AbsHwPos_SCom_CustSetTrim

Project AbsHwPos

Module AbsHwPos_Tcl2lVd

Test Object AbsHwPos_SCom_CustSetTrim

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>			
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>			
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4			
Time Unit	Cycles			
Timer Enabled	False			

AbsHwPos_SCom_CustSetTrim

2017-02-06, 14:37:18+0530



Attributes

Name Value

Timer Prescale 0

Timer Resolution 1

UDE Config File \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

Workspace File D:\Synergy_Work_Area\AbsHwPos_Tc12cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP





Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 2163.00 Cycles TS1.2 2974.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $\label{eq:cont_u08} \begin{tabular}{ll} if (TurnsCntrValidiy_Cnt_T_u08 == D_TCVVALID_CNT_U08) => False \\ if (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE) => True" \\ \end{tabular}$

TS1.2 Longest Execution Path:

 $\label{eq:control} \mbox{if } (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) => True \\ \mbox{if } (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE) => False" \\$

Test Step 1.1 (Repeat Count = 1)			✓	
Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data			
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_CustSetTrim()	34	34	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~	
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~	
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~	

Τ								
Actual Function	Count	Expected Function	Count	Result				
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~				
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~				
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~				
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓				

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	nst_Ap_AbsHwPos tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.011292 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

2017-02-06, 14:37:18+0530



AbsHwPos_SCom_CustSetTrim

Name	Actual Value	Expected Value	Result
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.011292 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	•
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.1 (Repeat Count = 1)			✓	
Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600			
AbsHwPos_VehCntrValid_Cnt_M_lgc	0			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_N	ManufMode_Cnt_enum_data		
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_T	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data		
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	0			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0			
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset			
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0			
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0			
Name	Actual Value	Expected Value	Result	
AbsHwPos_SCom_CustSetTrim()	34	34	~	

2017-02-06, 14:37:18+0530



AbsHwPos_SCom_	CustSetTrim
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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2783.01367		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.47595		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufM	lode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCi	ntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	987.40387		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓

AbsHwPos_SCom_CustSetTrim

2017-02-06, 14:37:18+0530



Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tat Dim FOLVahCatrOffeet FOLHu/DesTrimPerformed Cat u16	0	0	-

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1298.70605		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1377.42493		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntr	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1209.60217		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	55		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_M	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_T	urnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	16		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

AbsHwPos_SCom_CustSetTrim

2017-02-06, 14:37:18+0530



Name	Actual Value	Expected Value	Result	
tat Pim FOI VehCntrOffset FOI HwPosTrimPerformed Cnt u16	0	0	~	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Step 2.6 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2075.32788		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-64.231987		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	e_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1386.30811		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	358		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.7 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2223.71802		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1012.77295		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1031.59229		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	673		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓



T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2778.2627		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	876.887268		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrV	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1053.97559		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	988		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	*

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.9 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1324.01794		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMo	de_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCnt	Validity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	409.805206		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1303		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	123		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓



T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	

Test Step 2.10 (Repeat Count = 1)			~
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-524.091675		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	771.315552		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1618		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	-
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVal	idity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.011292 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.011292 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	0		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1140.87488		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	988.865723		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2248		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	64		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-619.01001		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-452.819061		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	205.275681		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2563		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	33		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓



T			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	-

Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	166.198761		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	404.824768		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2878		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	185		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	-
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-612.046448		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	870.147095		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMe	ode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCn	trValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3193		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-612.046448	-612.046448 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-612.046448	-612.046448 ± 0.0009	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	✓



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3508		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	78		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	907.880615		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	575.52533		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3823		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	50		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1563.76599		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	alidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	178.001389		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4138		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	-
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2688.78174		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1172.12		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1164.8783		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4453		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~



T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-941.775391		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1122.76965		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4768		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	2		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	255		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	933.228271		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-578.130005		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1599.21655		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	933.228271	933.228271 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	933.228271	933.228271 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_	_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrVa	lidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5398		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	14		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	~

				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	804.195251		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1562.1189		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	904.213257		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5713		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	225		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•

Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 2163.00 Cycles TS3.2 2974.00 Cycles TS3.3 2973.00 Cycles TS3.4 2980.00 Cycles

Description Vector Description:

 $TS3.1 \quad \text{if (TurnsCntrValidity_Cnt_T_u08} == D_TCVVALID_CNT_U08) => False \\ TS3.2 \quad \text{"if (TurnsCntrValidity_Cnt_T_u08} == D_TCVVALID_CNT_U08) => True \\ \text{if (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32)} <= k_VehCntrOffValidLimit_HwDeg_f32) => False \\ \text{if (AbsHwPos_VehCntrValid_Cnt_M_igc} == FALSE] => True'' \\ TS3.3 \quad \text{"if (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32)} <= k_VehCntrOffValidLimit_HwDeg_f32) => True \\ \text{if (AbsHwPos_VehCntrValid_Cnt_M_igc} == FALSE) => False'' \\ TS3.4 \quad \text{"((ManufMode_Cnt_T_enum != ManufacturingMode)} = True&& \\ \text{(Rte_Pim_EOLVehCntrOffset()} >> EOLHwPosTrimPerformed_Cnt_u16 != D_TRIMPERFORMED_CNT_U16)}) = False'' \\ \end{aligned}$

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_Ma	nufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tui	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	0		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	34	34	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	

Test Step 3.2 (Repeat Count = 1)	· ·
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2783.01367
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.47595
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	987.40387

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Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	0	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	468.011292		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-820.067871		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_M	anufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_Tu	rnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1074.49158		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1933		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	1		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	468.011292	468.011292 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	468.011292	468.011292 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus	1	~

Test Step 3.4 (Repeat Count = 1)		V
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	153.139999	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1077.47595	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data)	tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data)	tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	425.119995	
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	

2017-02-06, 14:37:18+0530



Name	Input Value		
tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data	0		
tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data	100		
Name	Actual Value	Expected Value	Result
AbsHwPos_SCom_CustSetTrim()	0	0	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	153.139999	153.139999 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	153.139999	153.139999 ± 0.0009	~
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690	43690	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum	1	~
Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

2017-02-06, 14:32:19+0530

AbsHwPos_Per4



Project AbsHwPos

 Module
 AbsHwPos_Tcl2lVd

 Test Object
 AbsHwPos_Per4

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDefinclude -I\$(Compiler Install Path)\include

Name	Text
Module 'AbsHwPos_Tcl2iVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	Cycles
Timer Enabled	False

2017-02-06, 14:32:19+0530

AbsHwPos_Per4



Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 1354.00 Cycles TS1.2 1339.00 Cycles

Description Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32) => True"$

TS1.2 Longest Execution Path:

 $If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32) => False "$

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	Rte Call AbsHwPos Per4 CP1 CheckpointReached	1	✓

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	-



Test Case 2: Range Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1354.00 Cycles TS2.2 1346.00 Cycles TS2.3 1344.00 Cycles TS2.4 1344.00 Cycles TS2.5 1339.00 Cycles TS2.6 1344.00 Cycles TS2.7 138.00 Cycles TS2.8 1344.00 Cycles TS2.8 1344.00 Cycles TS2.10 1336.00 Cycles

Description Vector Description:

TS 2.1All Min TS 2.2All Max

TS 2.2All Max
TS 2.3AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Min
TS 2.4AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Max
TS 2.5AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Mid
TS 2.6AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Pos
TS 2.7AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Neg
TS 2.8k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Min
TS 2.9k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Max
TS 2.10k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Pos/Default

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	5000		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~	

Test Step 2.3 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	1478			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rta Call An AhsHwPos NytrDiagMar SetNTCStatus(Status Cnt T enum)	1	1	_	



T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	_

Test Step 2.4 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	99.0100021			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	✓	

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~

Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

Τ					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~	

Test Step 2.6 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedHwPos_HwDeg_M_f32	365.02			
k_KinmIntDiagMaxRackLimit_HwDeg_f32	358.01001			
Name	Actual Value	Expected Value	Result	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~	

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-254.12		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	2769		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~

2017-02-06, 14:32:19+0530



AbsHwPos_Per4

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	~



Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1354.00 Cycles TS3.2 1341.00 Cycles

Description Vector Description:

 $\label{eq:total_$

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	0		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	•

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0		
k_KinmIntDiagMaxRackLimit_HwDeg_f32	647		
Name	Actual Value	Expected Value	Result
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	144	144	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached	1	•

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	1
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	204.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
AbsHwPos_HwPosSource_Cnt_M_u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	442.940002
AbsHwPos RelHwPos HwDeg M f32	442.940002
AbsHwPos SrlComHwPosStatus Cnt M u16	26817
AbsHwPos SrlComHwPos HwDeg M f32	1700.22998
AbsHwPos TargetHwAuthority Uls M f32	1
AbsHwPos TempHwPos HwDeg M f32	5054.37012
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0
C HwPosOutputLPFError HwDeg f32	674.299988
C I2CHwAuthority Uls f32	0.75
<pre></pre>	0.839999974
<pre>(_MaxVehCntrOffDiff_HwDeg_f32</pre>	822.799988
MinSensorlessAuthority Uls f32	0.75
C TurnsCntrAuthority Uls f32	0.75
UseTurnsCntr Cnt lgc	0
C VehCntrOffValidLimit HwDeq f32	674.299988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1587.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2325.1001
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9299
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPe	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1700.22998	1700.22998 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 1.2 (Repeat Count = 1)	J.
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	3787.70996
AbsHwPos HandwheelAuthority Uls M f32	0.280000001
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1112.18005
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.280000001
AbsHwPos HwPosSource Cnt M u16	296
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
AbsHwPos PrevHandwheelPosition HwDeq M f32	1207.09998
AbsHwPos RelHwPos HwDeg M f32	1207.09998
AbsHwPos SrlComHwPosStatus Cnt M u16	23721
AbsHwPos SrlComHwPos HwDeq M f32	1232,09998
AbsHwPos TargetHwAuthority Uls M f32	0.280000001
AbsHwPos TempHwPos HwDeg M f32	8009.91016
AbsHwPos VehCntrOffset HwDeg M f32	-1132.57996
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.270000011
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_Uls_f32	0.270000011
k_MaxSensorlessAuthority_Uls_f32	0.340000004
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.270000011
k_TurnsCntrAuthority_Uls_f32	0.270000011
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012

2017-02-06, 14:29:50+0530





Name	Input Value				
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100				
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5				
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347				
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.	2 target_AbsHwPos_Per2_HandwheelPositio	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	wDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32			
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset				
Name	Actual Value	Expected Value	Result		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.0009	~		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	~		
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	~		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1232.09998	1232.09998 ± 0.1	~		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~		
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	~		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	~		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	~		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	~		
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~		
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•		
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1232.09998	1232.09998 ± 0.1	~		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓		

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



Test Case 2: Range Test

2017-02-06, 14:29:50+0530



Specification

AbsHwPos_Per2

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1467.00 Cycles TS2.2 2917.00 Cycles TS2.3 1877.00 Cycles TS2.4 1933.00 Cycles TS2.5 1838.00 Cycles TS2.6 1921.00 Cycles TS2.7 1341.00 Cycles TS2.8 1885.00 Cycles TS2.9 1360.00 Cycles TS2.9 1360.00 Cycles TS2.10 1862.00 Cycles TS2.11 1360.00 Cycles TS2.12 1417.00 Cycles TS2.13 1352.00 Cycles TS2.14 1868.00 Cycles TS2.15 1347.00 Cycles TS2.16 1895.00 Cycles TS2.17 1347.00 Cycles TS2.18 1902.00 Cycles TS2.19 1450.00 Cycles TS2.20 1904.00 Cycles TS2.21 1388.00 Cycles TS2.22 1428.00 Cycles TS2.23 1362.00 Cycles TS2.24 1428.00 Cycles TS2.24 1428.00 Cycles TS2.25 1341.00 Cycles TS2.26 1428.00 Cycles TS2.27 1908.00 Cycles TS2.28 1889.00 Cycles TS2.29 1844.00 Cycles TS2.30 1889.00 Cycles TS2.31 1361.00 Cycles TS2.32 1902.00 Cycles TS2.32 1902.00 Cycles
TS2.33 1373.00 Cycles
TS2.35 1428.00 Cycles
TS2.35 1428.00 Cycles
TS2.36 1861.00 Cycles
TS2.37 1844.00 Cycles
TS2.38 1904.00 Cycles
TS2.39 1824.00 Cycles
TS2.40 1904.00 Cycles
TS2.41 1844.00 Cycles
TS2.42 1861.00 Cycles
TS2.42 1861.00 Cycles
TS2.43 2347.00 Cycles
TS2.44 2812.00 Cycles
TS2.45 1373.00 Cycles
TS2.46 2763.00 Cycles
TS2.48 1907.00 Cycles
TS2.49 1373.00 Cycles
TS2.49 1373.00 Cycles
TS2.51 1352.00 Cycles
TS2.51 1470.00 Cycles
TS2.53 1352.00 Cycles TS2.33 1373.00 Cycles TS2.52 1417.00 Cycles TS2.53 1352.00 Cycles TS2.54 1868.00 Cycles TS2.55 1815.00 Cycles TS2.56 1840.00 Cycles TS2.57 1352.00 Cycles TS2.57 1352.00 Cycles TS2.59 1352.00 Cycles TS2.60 1840.00 Cycles TS2.61 1426.00 Cycles TS2.62 2799.00 Cycles TS2.63 2358.00 Cycles TS2.63 2358.00 Cycles TS2.64 1922.00 Cycles TS2.65 1943.00 Cycles TS2.66 1384.00 Cycles TS2.66 1386.00 Cycles TS2.68 1361.00 Cycles TS2.69 1904.00 Cycles TS2.70 1373.00 Cycles TS2.71 1904.00 Cycles TS2.72 1361.00 Cycles TS2.72 1361.00 Cycles TS2.73 1904.00 Cycles TS2.74 1904.00 Cycles TS2.75 1373.00 Cycles TS2.76 1904.00 Cycles TS2.77 1361.00 Cycles TS2.78 1889.00 Cycles TS2.79 1361.00 Cycles TS2.80 1904.00 Cycles TS2.81 1361.00 Cycles TS2.82 1889.00 Cycles TS2.83 1361.00 Cycles TS2.84 1904.00 Cycles TS2.85 1373.00 Cycles TS2.86 1904.00 Cycles TS2.87 1361.00 Cycles TS2.88 1904.00 Cycles TS2.89 1373.00 Cycles TS2.90 1904.00 Cycles TS2.90 1904.00 Cycles TS2.91 1368.00 Cycles TS2.92 1889.00 Cycles TS2.93 1338.00 Cycles TS2.94 1889.00 Cycles TS2.95 1303.00 Cycles TS2.96 1840.00 Cycles TS2.97 1359.00 Cycles TS2.98 1885.00 Cycles TS2.99 1352.00 Cycles TS2.99 1352.00 Cycles TS2.100 1840.00 Cycles TS2.101 2310.00 Cycles TS2.102 2740.00 Cycles TS2.103 2287.00 Cycles TS2.104 2753.00 Cycles TS2.105 2288.00 Cycles TS2.106 1838.00 Cycles TS2.107 1465.00 Cycles TS2.108 1945.00 Cycles TS2.109 1815.00 Cycles

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



TS2.110 1898.00 Cycles
TS2.111 1386.00 Cycles
TS2.112 1874.00 Cycles
TS2.113 1862.00 Cycles
TS2.114 1926.00 Cycles
TS2.115 1379.00 Cycles
TS2.116 1839.00 Cycles
TS2.117 1451.00 Cycles
TS2.118 2812.00 Cycles
TS2.119 2358.00 Cycles
TS2.121 2306.00 Cycles
TS2.121 2306.00 Cycles
TS2.122 2358.00 Cycles
TS2.123 2792.00 Cycles

AbsHwPos_Per2



Description Vector Description:

TS2.1All Min TS2.2All Max TS2.3TurnsCntrValidity_Cnt_u08=>Min TS2.4TurnsCntrValidity_Cnt_u08=>Max TS2.5TurnsCntrValidity_Cnt_u08=>Pos TS2.6ManufMode_Cnt_enum=>Min TS2.7ManufMode_Cnt_enum=>Max TS2.8ManufMode_Cnt_enum=>Pos TS2.9DiagStatusHwPosReducedPerf_Cnt_lgc=>Min TS2.10DiagStatusHwPosReducedPerf_Cnt_lgc=>Max TS2.11l2CHwAbsPosValid_Cnt_lgc=>Min IS2.11/I2CHwAbsPosValid_Cnt_lgc=>Mln
TS2.12/CHwAbsPosValid_Cnt_lgc=>Max
TS2.13SensorlessAuthority_Uls_f32=>Min
TS2.14SensorlessAuthority_Uls_f32=>Max
TS2.15SensorlessAuthority_Uls_f32=>Max
TS2.15SensorlessHwPos_HwDeg_f32=>Min
TS2.16SensorlessHwPos_HwDeg_f32=>Min
TS2.18SensorlessHwPos_HwDeg_f32=>Zero TS2.19SensorlessHwPos_HwDeg_f32=>Pos TS2.20SensorlessHwPos_HwDeg_f32=>Neg TS2.21I2CHwAbsPos_HwDeg_f32=>Min TS2.22I2CHwAbsPos_HwDeg_f32=>Max TS2.23I2CHwAbsPos_HwDeg_f32=>Zero TS2.24I2CHwAbsPos_HwDeg_f32=>Pos TS2.25I2CHwAbsPos_HwDeg_f32=>Neg TS2.26k_UseTurnsCntr_Cnt_lgc=>Min TS2.27k_UseTurnsCntr_Cnt_lgc=>Max TS2.28k_MinSensorlessAuthority_Uls_f32=>Min TS2.29k_MinSensorlessAuthority_Uls_f32=>Max/Default TS2.30k_MinSensorlessAuthority_Uls_f32=>Pos TS2.31k_MaxSensorlessAuthority_Uls_f32=>Min TS2.32k_MaxSensorlessAuthority_Uls_f32=>Min TS2.32k_MaxSensorlessAuthority_Uls_f32=>Max/Default TS2.33k_MaxSensorlessAuthority_Uls_f32=>Pos TS2.34k_VehCntrOffValidLimit_HwDeg_f32=>Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntrOffValidLimit_HwDeg_f32=Min TS2.34k_VehCntr TS2.35k_VehCntrOffValidLimit_HwDeg_f32=>Max/Default
TS2.36k_VehCntrOffValidLimit_HwDeg_f32=>Pos
TS2.37Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Min TS2.38Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Max TS2.39Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Zero TS2.40Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_Hwbeg_f32=>Pos TS2.41Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Neg TS2.42k_MaxVehCntrOffDiff_HwDeg_f32=>Min TS2.43k_MaxVehCntrOffDiff_HwDeg_f32=>Max TS2.44k_MaxVehCntrOffDiff_HwDeg_f32=>Pos/Default TS2.45k_HwPosOutputLPFError_HwDeg_f32=>Min
TS2.45k_HwPosOutputLPFError_HwDeg_f32=>Max
TS2.47k_HwPosOutputLPFError_HwDeg_f32=>Max
TS2.47k_HwPosOutputLPFError_HwDeg_f32=>Pos/Default
TS2.48k_HwPosAuthorityStep_UIs_f32=>Min
TS2.49k_HwPosAuthorityStep_UIs_f32=>Max TS2.50k_HwPosAuthorityStep_Uls_f32=>Pos/Default TS2.51k_l2CHwAuthority_Uls_f32=>Min TS2.52k_l2CHwAuthority_Uls_f32=>Max/Default TS2.53k_l2CHwAuthority_Uls_f32=>Pos TS2.54k_TurnsCntrAuthority_Uls_f32=>Min TS2.55k_TurnsCntrAuthority_Uls_f32=>Max/Default TS2.56k_TurnsCntrAuthority_Uls_f32=>Pos TS2.57Rte Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Min TS2.58Rte Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Max TS2.59Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Pos TS2.60AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Min TS2.61AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Max TS2.62AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Zero TS2.63AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Pos TS2.64AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Neg TS2.65AbsHwPos_HandwheelAuthority_Uls_M_f32=>Min TS2.66AbsHwPos_HandwheelAuthority_Uls_M_f32=>Max TS2.67AbsHwPos_HandwheelAuthority_Uls_M_f32=>Pos TS2.68AbsHwPos_HwPosSource_Cnt_M_u16=>Min TS2.69AbsHwPos_HwPosSource_Cnt_M_u16=>Max TS2.70AbsHwPos_HwPosSource_Cnt_M_u16=>Pos
TS2.71AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_SENSORVALID
TS2.72AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_INPUTINVALID TS2.73AbsHwPos HwPosState Cnt M enum=>HWPOS STATE INIT TS2.74AbsHwPos HwPosState Cnt M enum=>HWPOS STATE SENSORINVALID TS2.75AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Min TS2.76AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Max TS2.77AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Zero TS2.78AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Pos TS2.78AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Pos
TS2.79AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Neg
TS2.80AbsHwPos_RelHwPos_HwDeg_M_f32=>Min
TS2.81AbsHwPos_RelHwPos_HwDeg_M_f32=>Max
TS2.82AbsHwPos_RelHwPos_HwDeg_M_f32=>Zero
TS2.83AbsHwPos_RelHwPos_HwDeg_M_f32=>Pos
TS2.84AbsHwPos_RelHwPos_HwDeg_M_f32=>Neg
TS2.85AbsHwPos_SriComHwPos_HwDeg_M_f32=>Min
TS2.86AbsHwPos_SriComHwPos_HwDeg_M_f32=>Max
TS2.87AbsHwPos_SriComHwPos_HwDeg_M_f32=>Zero
TS2.88AbsHwPos_SriComHwPos_HwDeg_M_f32=>Pos
TS2.89AbsHwPos_SriComHwPos_HwDeg_M_f32=>Pos
TS2.89AbsHwPos_SriComHwPos_HwDeg_M_f32=>Neg TS2.89AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Neg TS2.90AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Min TS2.91AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Max TS2.92AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Pos



```
TS2.95AbsHwPos_TargetHwAuthority_UIs_M_f32=>Pos
TS2.96AbsHwPos_TempHwPos_HwDeg_M_f32=>Min
TS2.97AbsHwPos_TempHwPos_HwDeg_M_f32=>Max
TS2.98AbsHwPos_TempHwPos_HwDeg_M_f32=>Zero
TS2.99AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Neg
TS2.101AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Min
TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Min
TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
TS2.103AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
TS2.105AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Min
TS2.105AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Min
TS2.105AbsHwPos_VehCntrValid_Cnt_M_lgc=>Min
TS2.105AbsHwPos_VehCntrValid_Cnt_M_lgc=>Max
TS2.110AbsHwPos_VehCntrValid_Cnt_M_lgc=>Max
TS2.110AbsHwPos_HandwheelPositionLPF_M_str.SV=>Min
TS2.111AbsHwPos_HandwheelPositionLPF_M_str.SV=>Max
TS2.112AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.115AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Min
TS2.116AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Neg
TS2.118Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_OK
TS2.119Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NT_OK
TS2.121Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NWM_REQ_NT_INVALIDATED
TS2.122Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NV_INVALIDATED
```

Test Step 2.1 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0
AbsHwPos_HwPosSource_Cnt_M_u16	1
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
AbsHwPos SrlComHwPos HwDeg M f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725
AbsHwPos VehCntrOffset HwDeg M f32	-1600
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0
k HwPosOutputLPFError HwDeg f32	0
k I2CHwAuthority Uls f32	0
k MaxSensorlessAuthority Uls f32	0
k MaxVehCntrOffDiff HwDeg f32	0
k MinSensorlessAuthority Uls f32	0
k TurnsCntrAuthority Uls f32	0
k_UseTurnsCntr_Cnt_lgc	0
k VehCntrOffValidLimit HwDeg f32	0
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Iqc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
target AbsHwPos Per2 ManufMode Cnt enum.value	0
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-1600
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	0
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-1600
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	0
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	1
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	1 2 2
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
target_Rte_linst_Ap_AbsHwPos.AbsHwPos_Fer2_inantininode_Cit_entitiii target_Rte_linst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32_	target AbsHwPos Per2 SensorlessAuthority Uls f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cfit_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbshwPos.AbshwPos.Per2_ShCoffnwPos_nwbeg_i32 target_Rte_Inst_Ap_AbshwPos.AbshwPos.Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity Cnt u08
target_Ntc_mat_np_nuariwr 03.nuariwr 05_Fetz_TurnsOnti valiuity_Cht_u06	target_Abarriwr 05_F Gt2_Turnsoniti validity_ont_d00

AbsHwPos_Per2

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-11725	-11725 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725	-11725 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-1600	-1600 ± 0.009	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Fest Step 2.2 (Repeat Count = 1)	Input Value
	10125
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	11725
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1
AbsHwPos_HwPosSource_Cnt_M_u16	65534
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725
AbsHwPos_RelHwPos_HwDeg_M_f32	10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	1
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535
AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	1
_HwPosOutputLPFError_HwDeg_f32	1620
_I2CHwAuthority_Uls_f32	1
_MaxSensorlessAuthority_Uls_f32	1
_MaxVehCntrOffDiff_HwDeg_f32	1600
_MinSensorlessAuthority_Uls_f32	1
_TurnsCntrAuthority_Uls_f32	1
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1600
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	43690
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPer	f (target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt lgc





Name	Input value
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset

target_Rte_inst_Ap_Abshwros.Filii_EOLVerichtiOnset	target_Fim_EOLVERCHILORSet		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Sten 2.2 (Perset Count = 4)	
Test Step 2.3 (Repeat Count = 1)	· ·
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_UIs_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.100000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_UIs_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1

2017-02-06, 14:29:50+0530





Input Value			
50.5999985			
0			
0			
100.099998			
0			
0.100000001			
100.099998			
0			
90.3000031			
123			
1			
(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc		
target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32		
target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32		
target_AbsHwPos_Per2_HwPosSource_Cn	t_u16		
target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc		
target_AbsHwPos_Per2_I2CHwAbsPos_Hv	/Deg_f32		
target_AbsHwPos_Per2_ManufMode_Cnt_e	enum		
target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32		
target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32		
target_AbsHwPos_Per2_SrlComHwPosStar	us_Cnt_u16		
target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32		
target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08		
target_Pim_EOLVehCntrOffset			
Actual Value	Expected Value	Result	
1487	1487 ± 0.0009	-	
0	0 ± 0.004	✓	
806.663025	806.663025 ± 0.1	✓	
65534	65534	✓	
HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓	
806.663025	806.663025 ± 0.1	✓	
65534	65534	✓	
9026.62012	9026.62012 ± 0.1	✓	
0	0 ± 0.004	✓	
2802.53003	2802.53003 ± 0.1	✓	
1328.5	1328.5 ± 0.009	✓	
0	0	✓	
0	0	✓	
504	504	✓	
1	1	✓	
1	1	✓	
0	0 ± 0.004	✓	
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32_value target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08_value target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_Father_Size_pim_EoLVehCntrOffset_Father_Size_pim_Father			
	0 0 100.099998 0 0.100000001 100.099998 0 90.3000031 123 1 (target_AbsHwPos_Per2_DiagStatusHwPos! target_AbsHwPos_Per2_HandwheelAuthori target_AbsHwPos_Per2_HandwheelAuthori target_AbsHwPos_Per2_HandwheelPosition target_AbsHwPos_Per2_I2CHwAbsPos_Vali target_AbsHwPos_Per2_I2CHwAbsPos_Hw target_AbsHwPos_Per2_I2CHwAbsPos_Hw target_AbsHwPos_Per2_SensorlessAuthori target_AbsHwPos_Per2_SensorlessAuthori target_AbsHwPos_Per2_SensorlessHwPos_target_AbsHwPos_Per2_SensorlessHwPos_target_AbsHwPos_Per2_SensorlessHwPos_Hw target_AbsHwPos_Per2_TurnsCntrValidity_target_Pim_EOLVehCntrOffset **Actual Value** 1487 0 806.663025 65534 HWPOS_STATE_INIT 806.663025 65534 9026.62012 0 2802.53003 1328.5 0 0 0 504 1 1	50.5999985 0 100.099998 0 0.100.000001 100.099998 0 90.3000031 123 1 (target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 target_AbsHwPos_Per2_TurmsCntrValidity_Cnt_u08 target_Pim_EOLVehCntrOffset Actual Value Expected Value 1487 1487 1487 1487 ± 0.0009 0 ± 0.004 806.663025 ± 0.1 65534 65534 HWPOS_STATE_INIT 806.663025 806.663025 ± 0.1 65534 9026.62012 9026.62012 9026.62012 ± 0.1 0 ± 0.004 2802.53003 2802.53003 ± 0.1 1328.5 1328.5 ± 0.009 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

65534

65534

1600

90.3000031

65534

65534

1600 ± 0.1

90.3000031 ± 0.00009

Test Step 2.4 (Repeat Count = 1)		
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	269.209991	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	605.179993	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003	
AbsHwPos_HwPosSource_Cnt_M_u16	96	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998	
AbsHwPos_RelHwPos_HwDeg_M_f32	1507.09998	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21921	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	
AbsHwPos VehCntrOffset HwDeg M f32	1358.69995	

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value\\ target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

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AbsHwPos_Per2		MAC	
Name	Input Value		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	nCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.109999999		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.200000003		
k_TurnsCntrAuthority_Uls_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.109999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	120.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	247		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		osReducedPerf Cnt lac	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	921 2111	
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1507.09998	1507.09998 ± 0.0009	Resul
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	524.650024	524.650024 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
	524.650024	524.650024 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos TargetHwAuthority Uls M f32	8920.66016 0	8920.66016 ± 0.1 0 ± 0.004	
	1		
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	,
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	,
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	524.650024	524.650024 ± 0.1	•

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

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65534

1600

120.5

65534

65534

1600 ± 0.1

120.5 ± 0.0009

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.300000012		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	625.460022		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.300000012		
AbsHwPos_HwPosSource_Cnt_M_u16	104		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1527.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32 AbsHwPos SrlComHwPosStatus Cnt M u16	1527.19995 21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos TargetHwAuthority Uls M f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
:_HwPosAuthorityStep_Uls_f32	0.300000012		
x_HwPosOutputLPFError_HwDeg_f32	466.399994		
x_I2CHwAuthority_Uls_f32	0.300000012		
<_MaxSensorlessAuthority_Uls_f32	0.119999997		
< MinSchool Labority Unit 123	169.399994		
(_MinSensorlessAuthority_Uls_f32	0.300000012		
<pre>c_TurnsCntrAuthority_Uls_f32 c_UseTurnsCntr_Cnt_lgc</pre>	0.300000012		
COSETUTION ON THE CONTROL OF THE CON	110		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
arget AbsHwPos Per2 ManufMode Cnt enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	371		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatu	s 3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	erf_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C target AbsHwPos Per2 SensorlessAut		
:arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensoriessAuthority_Uls_f32 :arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensoriessHwPos_HwDeg_f:		·	
arget_Rte_irist_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValic		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	,	
Name	Actual Value	Expected Value	Resu
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32	1527.19995	1527.19995 ± 0.0009	Resu
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027	9802.53027 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002	1388.90002 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
, ,	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1 0	0 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1 0 1600	0 ± 0.004 1600 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	1 0	0 ± 0.004	

65534

1600

65534

1600 ± 0.1

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997	150.699997 ± 0.0009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedRelHwPos HwDeg M f32	550.690002		
AbsHwPos HandwheelAuthority Uls M f32	0.40000006		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	645.73999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
AbsHwPos HwPosSource Cnt M u16	112		
AbsHwPos HwPosState Cnt M enum	5		
AbsHwPos PrevHandwheelPosition HwDeg M f32	157.300003		
AbsHwPos_RelHwPos_HwDeg_M_f32	157.300003		
AbsHwPos SrlComHwPosStatus Cnt M u16	22065		
AbsHwPos SrlComHwPos HwDeg M f32	-8708.74023		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.400000006		
AbsHwPos TempHwPos HwDeg M f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos		
k HwPosAuthorityStep Uls f32	0.40000006		
k HwPosOutputLPFError HwDeg f32	496.100006		
k I2CHwAuthority Uls f32	0.40000006		
k MaxSensorlessAuthority Uls f32	0.129999995		
k_MaxVehCntrOffDiff_HwDeg_f32	199.100006		
k MinSensorlessAuthority Uls f32	0.40000006		
k TurnsCntrAuthority UIs f32	0.40000006		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	139.699997		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	160.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.129999995		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	160.399994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	10		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	180.899994		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	495		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	16 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	157.300003	157.300003 ± 0.0009	~

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	157.300003	157.300003 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1677.34399	1677.34399 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1677.34399	1677.34399 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1419.09998	1419.09998 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.7 (Repeat Count = 1)	Innut Value
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	691.429993
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-666.02002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	120
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
AbsHwPos_RelHwPos_HwDeg_M_f32	567.400024
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.5
_HwPosOutputLPFError_HwDeg_f32	525.799988
_I2CHwAuthority_Uls_f32	0.5
_MaxSensorlessAuthority_Uls_f32	0.140000001
:_MaxVehCntrOffDiff_HwDeg_f32	228.800003
_MinSensorlessAuthority_Uls_f32	0.5
_TurnsCntrAuthority_Uls_f32	0.5
_UseTurnsCntr_Cnt_lgc	0
_VehCntrOffValidLimit_HwDeg_f32	169.399994
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.140000001
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	180.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	14
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	619
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStat	tus 5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedF	Perf_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3	2 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg	f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igo	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f3.	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg	





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	-832.169983
AbsHwPos HandwheelAuthority Uls M f32	0.600000024
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	686.299988
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.600000024
AbsHwPos HwPosSource Cnt M u16	128
AbsHwPos HwPosState Cnt M enum	7
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1587.5
AbsHwPos_RelHwPos_HwDeg_M_f32	1587.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22209
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8496.82031
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	140.300003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-960.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.600000024
k_HwPosOutputLPFError_HwDeg_f32	555.5
k_I2CHwAuthority_Uls_f32	0.600000024
k_MaxSensorlessAuthority_Uls_f32	0.150000006
k_MaxVehCntrOffDiff_HwDeg_f32	258.5
k_MinSensorlessAuthority_Uls_f32	0.600000024
k_TurnsCntrAuthority_Uls_f32	0.600000024
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	199.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.150000006
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	241.300003

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	743		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	s 0		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPersUnited StatusHwPosReducedPersUnited StatusHwPosReducedPersUnit$	erf_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32$	target_AbsHwPos_Per2_HandwheelAu	thority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_transfer = 100000000000000000000000000000000000$	f32 target_AbsHwPos_Per2_HandwheelPo	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc$	target_AbsHwPos_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32$	target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	32 target_AbsHwPos_Per2_SensorlessHw	/Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1587.5	1587.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	358.699982	358.700012 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	358.699982	358.700012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	358.699982	358.700012 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	✓
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32			

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.69999988





Name	Input Value			
k_MaxSensorlessAuthority_Uls_f32	0.159999996			
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012			
k_MinSensorlessAuthority_Uls_f32	0.69999988	.69999988		
k_TurnsCntrAuthority_Uls_f32	0.69999988			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	228.800003			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.159999996			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	220.699997			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	26			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	867			
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	1			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	n_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	wDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.0009		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	324.253998	324.253998 ± 0.1	✓	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.253998	324.253998 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.253998	324.253998 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	324.253998	324.253998 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5	271.5 ± 0.0009	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1113.65002	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-726.859985	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012	
AbsHwPos_HwPosSource_Cnt_M_u16	144	
AbsHwPos_HwPosState_Cnt_M_enum	9	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-980.599976	
AbsHwPos_RelHwPos_HwDeg_M_f32	-980.599976	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22353	

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AbsHwPos_Per2

ADSHWF0S_FE12		- ALC	71000	
Name	Input Value			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8284.88965	8284.88965		
AbsHwPos TargetHwAuthority Uls M f32	0.800000012			
AbsHwPos TempHwPos HwDeg M f32	180.5			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	603.700012			
AbsHwPos VehCntrOfstLearn Cnt M Igc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	nCntrOffset GetErrorStatus ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k HwPosAuthorityStep Uls f32	0.800000012			
k_HwPosOutputLPFError_HwDeg_f32	614.900024			
k_I2CHwAuthority_Uls_f32	0.800000012			
k_MaxSensorlessAuthority_Uls_f32	0.170000002			
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994			
k_MinSensorlessAuthority_Uls_f32	0.800000012			
k_TurnsCntrAuthority_UIs_f32	0.800000012			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	258.5			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1			
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003			
	1			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value				
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.170000002			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	240.800003			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	32			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	301.700012			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	991			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2	D 1 1D 1011		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Resul	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	Ō	0 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-0.971984863	-0.972000003 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	٠,	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-0.971984863	-0.972000003 ± 0.1		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	٠,	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	٠,	
AbsHwPos_TempHwPos_HwDeg_M_f32	180.5	180.5 ± 0.1		
AbsHwPos VehCntrOffset HwDeg M f32	603.700012	603.700012 ± 0.0009	٠,	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504		
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	0	0		
Dto Coll An AboliviDes Nutr Display Mar Set NT Cotatus (Status Cot T. anum)	0	0		

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	-

-0.971984863

301.700012

65534

65535

0

0

0 ± 0.004 -0.972000003 ± 0.1

65534

65535

0 ± 0.1

301.700012 ± 0.0009

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



Test Step 2.11 (Repeat Count = 1)	Input Value		
Name AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1254.39001		
AbshwPos_AlignedReinwPos_nwDeg_M_i32 AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	747.140015		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.89999976		
AbsHwPos_HwPosSource_Cnt_M_u16	152		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-960.900024		
AbsHwPos_RelHwPos_HwDeg_M_f32	-960.900024		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22425		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8178.93018		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.89999976		
AbsHwPos_TempHwPos_HwDeg_M_f32	200.600006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	633.900024		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc Pto_Call_Abs_AbsHwPos_EQL\AbsCatrOffcet_CatErrorStatus(ErrorStatus)	0	CatrOffeet CetErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) Rte_Inst_Ap_AbsHwPos	target_Rte_Call_Ap_AbsHwPos_EOLVet target_Rte_Inst_Ap_AbsHwPos	icilionset_GetErrorStatus_ErrorStatus	
k_HwPosAuthorityStep_Uls_f32	0.899999976		
k_HwPosOutputLPFError_HwDeg_f32	644.599976		
k_12CHwAuthority_Uls_f32	0.89999976		
k_MaxSensorlessAuthority_Uls_f32	0.180000007		
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.89999976		
k_TurnsCntrAuthority_UIs_f32	0.899999976		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	260.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	38		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1115		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe		osReducedPerf Cnt Ido	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg t			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosV	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-960.900024	-960.900024 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	255.253998	255.253998 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	255.253998	255.253998 ± 0.1	
Abol huDoo CalComil huDooCaster Cost 84 ::40	65534	65534	1 '
		9179 03019 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8178.93018	8178.93018 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	8178.93018 0	0 ± 0.004	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018 0 200.600006	0 ± 0.004 200.600006 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018 0 200.600006 633.900024	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	8178.93018 0 200.600006	0 ± 0.004 200.600006 ± 0.1	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	8178.93018 0 200.600006 633.900024 0	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	8178.93018 0 200.600006 633.900024 0	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0	,
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	8178.93018 0 200.600006 633.900024 0 0 504	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0 o *none*	
AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	8178.93018 0 200.600006 633.900024 0 0 504	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0 0 *none* *none*	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	8178.93018 0 200.600006 633.900024 0 0 504 0	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0 *none* *none*	
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	8178.93018 0 200.600006 633.900024 0 0 504 0 0	0 ± 0.004 200.600006 ± 0.1 633.900024 ± 0.0009 0 *none* *none* *none* 0 ± 0.004	

65534

65534

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	331.899994	331.899994 ± 0.0009	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1)	Innut Value			
Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1395.13			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999			
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	767.419983			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999			
AbsHwPos_HwPosSource_Cnt_M_u16	160			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1547.30005			
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497			
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8072.97021			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999			
AbsHwPos_TempHwPos_HwDeg_M_f32	220.699997			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k_HwPosAuthorityStep_Uls_f32	0.100000001			
k_HwPosOutputLPFError_HwDeg_f32	674.299988			
k_I2CHwAuthority_UIs_f32	0.100000001			
k_MaxSensorlessAuthority_Uls_f32	0.189999998			
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988			
k_MinSensorlessAuthority_Uls_f32	0.100000001			
k_TurnsCntrAuthority_Uls_f32	0.100000001			
k_UseTurnsCntr_Cnt_lgc	0			
k VehCntrOffValidLimit HwDeg f32	317.899994			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	362.100006			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1239			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (tosReducedPerf Cnt Ido		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuth			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	,		
	target AbsHwPos Per2 HwPosSource			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16		_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Resul	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.0009	•	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•	
AbsHwPos PrevHandwheelPosition HwDea M f32	713 013757	713 013818 + 0 1		

713.913757

0.100000001

21845

281

AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 713.913818 ± 0.1

0.100000001 ± 0.004

21845

281 ± 0.1

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976	664.099976 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757	713.913818 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	281	281 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.13 (Repeat Count = 1)	and the second s
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1535.87
AbsHwPos HandwheelAuthority Uls M f32	0.119999997
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos HwPosSource Cnt M u16	168
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos SrlComHwPos HwDeg M f32	7967.00977
AbsHwPos TargetHwAuthority Uls M f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k MaxSensorlessAuthority Uls f32	0.20000003
k MaxVehCntrOffDiff HwDeg f32	407
k MinSensorlessAuthority Uls f32	0.109999999
k TurnsCntrAuthority Uls f32	0.109999999
k UseTurnsCntr Cnt Igc	0
k VehCntrOffValidLimit HwDeg f32	347.600006
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
target AbsHwPos Per2 ManufMode Cnt enum.value	1
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	301.100006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	392.299988
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1363
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (1
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition HwDeg f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999791	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	722.072021	722.072021 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	168	168	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.14 (Repeat Count = 1)	ullet
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.12999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.12999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.119999997
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_UIs_f32	0.119999997
k_MaxSensorlessAuthority_Uls_f32	0.20999993
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012
k_MinSensorlessAuthority_Uls_f32	0.119999997
k_TurnsCntrAuthority_Uls_f32	0.119999997
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	377.299988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487

AbsHwPos_Per2



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	s 0		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_InstAp_AbsHwPosReducedPertarget_$	erf_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32$	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_function = 0.0000000000000000000000000000000000$	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc$	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32$	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	-
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.119999997	0.119999997 ± 0.004	→
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	→
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	→
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	→
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	→
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	→
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	•

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

65535

65535 ± 0.09

Test Step 2.15 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_Uls_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.319999993

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32





Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_Uls_f32	0.109999999		
k_TurnsCntrAuthority_UIs_f32	0.109999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	V

	tongo = 0 = 1 o o o o		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Name	Input Value	
Name	input value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1958.08997	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006	
AbsHwPos_HwPosSource_Cnt_M_u16	192	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997	
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785	
AbsHwPos SrlComHwPos HwDeg M f32	269.209991	

AbsHwPos Per2

2017-02-06, 14:29:50+0530



Input Value AbsHwPos TargetHwAuthority Uls M f32 0.150000006 301.100006 AbsHwPos TempHwPos HwDeg M f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 784.900024 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos VehCntrValid Cnt M Igc Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus Rte Inst Ap AbsHwPos target_Rte_Inst_Ap_AbsHwPos $k_HwPosAuthorityStep_Uls_f32$ 0.140000001 k_HwPosOutputLPFError_HwDeg_f32 793.099976 k_I2CHwAuthority_Uls_f32 0.140000001 k_MaxSensorlessAuthority_Uls_f32 0.230000004 k_MaxVehCntrOffDiff_HwDeg_f32 496 100006 0.140000001 k_MinSensorlessAuthority_Uls_f32 k_TurnsCntrAuthority_Uls_f32 0.140000001 k_UseTurnsCntr_Cnt_lgc 436.700012 k_VehCntrOffValidLimit_HwDeg_f32 target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value 1 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value 461.899994 target_AbsHwPos_Per2_ManufMode_Cnt_enum.value target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value 0.230000004 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value -1600 target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value 68 target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 482.899994 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 1735 $target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$ target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target AbsHwPos Per2 HandwheelPosition HwDeg f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum target AbsHwPos Per2 ManufMode Cnt enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$ target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32$ target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08$ target AbsHwPos Per2 TurnsCntrValidity Cnt u08 target_Pim_EOLVehCntrOffset $target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset$ **Actual Value Expected Value** AbsHwPos AlignedHwPos HwDeg M f32 126.699997 126.699997 ± 0.0009 AbsHwPos_HandwheelAuthority_Uls_M_f32 0.150000006 0.150000006 ± 0.004 AbsHwPos HandwheelPositionLPF M str.SV Uls f32 301.100006 301.100006 ± 0.1 AbsHwPos_HwPosSource_Cnt_M_u16 192 192 AbsHwPos_HwPosState_Cnt_M_enum HWPOS_STATE_SENSORINVALID HWPOS_STATE_SENSORINVALID 301.100006 ± 0.1 $AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32$ 301.100006 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 65534 65534 269 209991 269 209991 + 0 1 AbsHwPos SrlComHwPos HwDeg M f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 0.150000006 0.150000006 ± 0.004 **~** 301 100006 301 100006 + 0 1 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 784.900024 784.900024 ± 0.0009 • AbsHwPos VehCntrOfstLearn Cnt M Igc 1 1 **~** AbsHwPos VehCntrValid Cnt M Igc 0 0 **v** $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$ 504 504 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)$ Λ 0 Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) 0 0 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value 0.150000006 0.150000006 ± 0.004 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value 301.100006 301.100006 ± 0.1

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

192

65534

269.209991

482.899994

192

65534

269.209991 ± 0.1

482.899994 ± 0.0009

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32



Test Step 2.17 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-868.820007		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.159999996		
AbsHwPos_HwPosSource_Cnt_M_u16	5200		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	139.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	139.100006 22857		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	-409.950012		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.159999996		
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.150000006		
k_HwPosOutputLPFError_HwDeg_f32	822.799988		
k_I2CHwAuthority_UIs_f32	0.150000006		
k_MaxSensorlessAuthority_Uls_f32	0.239999995 525.799988		
k_MaxVehCntrOffDiff_HwDeg_f32 k MinSensorlessAuthority Uls f32	0.150000006		
k TurnsCntrAuthority Uls f32	0.150000006		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	466.399994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.239999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	74		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	513.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1859		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3 (target AbelliuPee Per? DiegStatusHiuP	as Badusad Borf Cat Jan	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_ target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	_(target_AbsHwPos_Per2_DiagStatusHwPos_target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_(
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_I		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	139.100006	139.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	
AbsHwPos_HunderPositionLPF_M_str.SV_Uls_f32	-473.808807	-473.808807 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	3	3	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos PrevHandwheelPosition HwDeg M f32	HWPOS_STATE_SENSORINVALID -473.808807	HWPOS_STATE_SENSORINVALID -473.808807 ± 0.1	
AbsHwPos_PrevHandwneeiPosition_HwDeg_M_132 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	-473.808807 21845	-473.808807 ± 0.1	
AbshwPos SrlComHwPos HwDeg M f32	1600	1600 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	1600	1600 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	815.099976	815.099976 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Tio_call_rp_rbs ivi 0s_ivitblagivigi_setivi 0status(ivi 0_citt_i_enulii)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0 0.23999995	0.239999995 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 0.239999995 -473.808807	0.239999995 ± 0.004 -473.808807 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	0 0.239999995 -473.808807 3	0.239999995 ± 0.004 -473.808807 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0 0.239999995 -473.808807	0.239999995 ± 0.004 -473.808807 ± 0.1	





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	513.099976	513.099976 ± 0.0009	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedRelHwPos HwDeg M f32	-2239.57007		
AbsHwPos HandwheelAuthority Uls M f32	0.170000002		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
AbsHwPos HwPosSource Cnt M u16	208		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORINVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	151.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5		
AbsHwPos SrlComHwPosStatus Cnt M u16	22929		
AbsHwPos SrlComHwPos HwDeg M f32	550.690002		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.170000002		
AbsHwPos TempHwPos HwDeq M f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCnt	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.159999996		
k HwPosOutputLPFError HwDeg f32	852.5		
k I2CHwAuthority Uls f32	0.159999996		
k MaxSensorlessAuthority Uls f32	0.25		
k_MaxVehCntrOffDiff_HwDeg_f32	555.5		
k MinSensorlessAuthority Uls f32	0.159999996		
k_TurnsCntrAuthority_UIs_f32	0.159999996		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	496.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.25		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	543.299988		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1983		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (educedParf Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensoriessAuthority_0is_132 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensoriessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu	·	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	in_uoo	
	Actual Value	Expected Value	Result
Name	151.5	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	131.3	151.5 ± 0.0009	_

- 3					
Name	Actual Value	Expected Value	Result		
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	~		
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	✓		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~		
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	✓		

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AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.19	(Repeat	m
Name		

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	163.899994	163.899994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.180000007	0.180000007 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1934.14343	1934.14343 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	216	216	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1934.14343	1934.14343 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	691.429993	691.429993 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.180000007	0.180000007 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	875.5	875.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.180000007	0.180000007 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	216	216	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	691.429993	691.429993 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.20 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_UIs_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996
k_TurnsCntrAuthority_UIs_f32	0.159999996
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-500.600006
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$





Name	Input Value			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-500.600006	-500.600006 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-500.600006	-500.600006 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	-	

0.25

21845

65535

-500.600006

-500.600006

0.25 ± 0.004 -500.600006 ± 0.1

-500.600006 ± 0.1

65535 ± 0.09

21845

Test Step 2.21 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001





Name	Input Value		
k MaxVehCntrOffDiff HwDeg f32	644.599976		
k MinSensorlessAuthority Uls f32	0.189999998		
k TurnsCntrAuthority Uls f32	0.189999998		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeq f32	585,200012		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-900		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.280000001		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	461.899994		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	98		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	633.900024		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2355		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target AbsHwPos Per2 DiagStatusHwPosF	ReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuthorit	y Uls f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	 I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.189999998	0.189999998 ± 0.004	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.189999998	0.189999998 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-900	-900 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-900	-900 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	633.900024	633.900024 ± 0.0009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2802.53003	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20999993	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	970.219971	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.20999993	
AbsHwPos_HwPosSource_Cnt_M_u16	240	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5	
AbsHwPos_RelHwPos_HwDeg_M_f32	-921.5	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23217	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1113.65002	

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993		
AbsHwPos TempHwPos HwDeg M f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	971.299988		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.289999992		
k_MaxVehCntrOffDiff_HwDeg_f32	674.299988		
k_MinSensorlessAuthority_Uls_f32	0.200000003		
k_TurnsCntrAuthority_Uls_f32	0.200000003		
k_UseTurnsCntr_Cnt_Igc	0		
k VehCntrOffValidLimit HwDeg f32	614.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.289999992		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	104		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	664.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2479		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009	ixesuit
	0.200000003	0.200000003 ± 0.004	-
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPosition! PF_M_str_SV_Llls_f32	955.473755	955.473816 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos HwPosSource Cnt M u16	955.473755	955.473616 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum			
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos PrevHandwheelPosition HwDeq M f32	HWPOS_STATE_SENSORVALID 955.473755	HWPOS_STATE_SENSORVALID 955.473816 ± 0.1	
AbsHwPos_PrevnandwheerPostdon_nwDeg_m_isz AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
	900	21845 900 ± 0.1	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos TargetHwAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	
	900	900 ± 0.1	-
AbsHwPos_TempHwPos_HwDog_M_f32 AbsHwPos_VehCatrOffest_HwDog_M_f32			
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	966.099976	966.099976 ± 0.0009	-
	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc Rts_Call_Ap_AbsHwPos_NytrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	955.473755	955.473816 ± 0.1	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	24945	· ·
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	V
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	900	900 ± 0.1	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

664.099976

664.099976 ± 0.0009

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$



Test Step 2.23 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos HandwheelAuthority Uls M f32	0.219999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-990.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.219999999		
AbsHwPos_HwPosSource_Cnt_M_u16	248		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	324.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23289		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1254.39001		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.219999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	996.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.209999993		
k HwPosOutputLPFError HwDeg f32	1001		
k I2CHwAuthority Uls f32	0.209999993		
k_MaxSensorlessAuthority_UIs_f32	0.300000012		
k_maxSensonessAuthority_ors_rsz k_maxVehCntrOffDiff_HwDeg_f32	704		
k_MaxvenChtrollolli_nwbeg_isz k_MinSensorlessAuthority_Uls_f32	0.209999993		
	0.209999993		
k_TurnsCntrAuthority_Uls_f32 k_UseTurnsCntr_Cnt_lgc	0.209999993		
k_VehCntrOffValidLimit_HwDeg_f32	644.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.300000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	110		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	694.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2603		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	2 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Ci	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset	-	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	324.5	324.5 ± 0.0009	11000
AbsHwPos HandwheelAuthority Uls M f32	0.209999993	0.209999993 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0.209999993 ± 0.004	
	2	2	
AbsHwPos_HwPosSource_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993	0.209999993 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	996.299988	996.299988 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20999993	0.209999993 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
	1	04045	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	

AbsHwPos_Per2

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

Test Step 2.24 (Repeat Count = 1) Name	Input Value		
	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.230000004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1010.78003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.230000004 256		
AbsHwPos_HwPosStote_Cnt_M_u16			
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_SENSORINVALID 471.600006		
AbsHwPos_RelHwPos_HwDeg_M_f32	471.600006		
AbsHwPos SrlComHwPosStatus Cnt M u16	23361		
AbsHwPos SrlComHwPos HwDeg M f32	1395.13		
AbsHwPos TargetHwAuthority Uls M f32	0.230000004		
AbsHwPos TempHwPos HwDeg M f32	7306.20996		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1026.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	CntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	one one of the order of the ord	
k_HwPosAuthorityStep_Uls_f32	0.219999999		
k_HwPosOutputLPFError_HwDeg_f32	1030.69995		
k I2CHwAuthority Uls f32	0.219999999		
k MaxSensorlessAuthority Uls f32	0.310000002		
k_MaxVehCntrOffDiff_HwDeg_f32	733.700012		
k MinSensorlessAuthority Uls f32	0.219999999		
k_TurnsCntrAuthority_UIs_f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.310000002		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	522.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	724.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2727		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	prity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	471.600006	471.600006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.219999999	0.219999999 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	100.300003	100.300003 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	100.300003	100.300003 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos SrlComHwPos HwDog M f32	100 300003	100 300003 + 0 1	

100.300003

0.219999999

100.300003

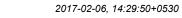
AbsHwPos_SrlComHwPos_HwDeg_M_f32

AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 100.300003 ± 0.1

100.300003 ± 0.1

0.219999999 ± 0.004

AbsHwPos_Per2





Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1026.5	1026.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.219999999	0.219999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	100.300003	100.300003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	100.300003	100.300003 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.25 (Repeat Count = 1)	
lame	Input Value
.bsHwPos_AlignedRelHwPos_HwDeg_M_f32	3224.75
.bsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1031.06006
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.239999995
.bsHwPos_HwPosSource_Cnt_M_u16	264
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-488.100006
.bsHwPos_ReIHwPos_HwDeg_M_f32	-488.100006
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	23433
.bsHwPos_SrlComHwPos_HwDeg_M_f32	1535.87
.bsHwPos TargetHwAuthority Uls M f32	0.239999995
bsHwPos_TempHwPos_HwDeg_M_f32	-7446.9502
bsHwPos VehCntrOffset HwDeg M f32	1056.69995
bsHwPos VehCntrOfstLearn Cnt M Igc	0
bsHwPos VehCntrValid Cnt M Igc	0
tte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
te Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.230000004
HwPosOutputLPFError_HwDeg_f32	1060.40002
I2CHwAuthority Uls f32	0.230000004
MaxSensorlessAuthority Uls f32	0.319999993
MaxVehCntrOffDiff HwDeg f32	763.400024
MinSensorlessAuthority_Uls_f32	0.230000004
TurnsCntrAuthority Uls f32	0.230000004
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	704
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
riget_bs\twos_reiz_biagstatus\twosi\cudceurei_cii_gc.value	1
	-350.200012
irget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	
irget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.319999993
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	542.299988
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	122
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	754.700012
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2851
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32

@ Report created by TESSY V3.1.13, report template V2.1





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-488.100006	-488.100006 ± 0.0009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-350.200012	-350.200012 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.230000004	0.230000004 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1056.69995	1056.69995 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.230000004	0.230000004 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-350.200012	-350.200012 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	754.700012	754.700012 ± 0.0009	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedReIHwPos_HwDeg_M_f32	3365.48999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1051.33997
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.25
AbsHwPos_HwPosSource_Cnt_M_u16	272
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	765.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	765.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23505
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25
AbsHwPos_TempHwPos_HwDeg_M_f32	7587.68994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.23999995
k_HwPosOutputLPFError_HwDeg_f32	1090.09998
k_I2CHwAuthority_Uls_f32	0.23999995
k_MaxSensorlessAuthority_Uls_f32	0.330000013
<pre>MaxVehCntrOffDiff_HwDeg_f32</pre>	793.099976
K MinSensorlessAuthority Uls f32	0.239999995
C TurnsCntrAuthority Uls f32	0.239999995
UseTurnsCntr_Cnt_lgc	0
<pre> C</pre>	733.700012
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5
rarget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.330000013
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	562.400024
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	784.900024
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2975
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

AbsHwPos_Per2



Name	Input Value		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedParticles (Ap_AbsHwPosAbsHwP$	Perf_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_HandwheelAu	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_HandwheelPosition_Hand$	_f32 target_AbsHwPos_Per2_HandwheelPo	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_IgcChromatical States and the states are also also also also also also also also$	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32$	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f3222222222222222222222222222222222222$	2 target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_t	f32 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u$	16 target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	765.799988	765.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	705.880005	705.880005 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	705.880005	705.880005 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.239999995	0.239999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-330.5	-330.5 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1086.90002	1086.90002 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.239999995	0.239999995 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	705.880005	705.880005 ± 0.1	~
target AbsHwPos Per2 HwPosSource Cnt u16.value	2	2	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

21845

-330.5

65535

21845

-330.5 ± 0.1

65535 ± 0.09

T4 04 0 07 (P4 04)	
Test Step 2.27 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_UIs_f32	0.159999996

AbsHwPos_Per2



Name	Input Value		
k_TurnsCntrAuthority_UIs_f32	0.159999996		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	496.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	us 2		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Others$	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	151.5	151.5 ± 0.0009	

target_rtte_init_rtp_rtbsrtwr oo: ini_EoEvenonitioniset	target_r ini_Lot venontronoct		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988	543.299988 ± 0.0009	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value	
AbsHwPos AlignedRelHwPos HwDeg M f32	3646,96997	
AbsHwPos HandwheelAuthority Uls M f32	0.270000011	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1091.90002	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.270000011	
AbsHwPos_HwPosSource_Cnt_M_u16	2288	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1060	
AbsHwPos_RelHwPos_HwDeg_M_f32	1060	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23649	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1958.08997	

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AbsHwPos_Per2

Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.270000011		
AbsHwPos TempHwPos HwDeg M f32	-7869.16992		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	9238.54004		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.25999999		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k_I2CHwAuthority_Uls_f32	0.25999999		
k_MaxSensorlessAuthority_Uls_f32	0		
k_MaxVehCntrOffDiff_HwDeg_f32	852.5		
k_MinSensorlessAuthority_Uls_f32	0.25999999		
k_TurnsCntrAuthority_Uls_f32	0.25999999		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	793.099976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.349999994		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	845.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3223		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target AbsHwPos Per2 DiagStatusHwPosR	ReducedPerf Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1060	1060 ± 0.0009	\ ✓
	0.25999999	0.25999999 ± 0.004	-
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPosition PF_M_str_SV_Llls_f32	-712.636963	-712.636902 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos HwPosSource Cnt M u16	1	-712.030902 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID		
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos PrevHandwheelPosition HwDeg M f32	-712.636963	HWPOS_STATE_SENSORVALID -712.636902 ± 0.1	-
AbsHwPos_PrevnandwheelPostdon_nwDeg_in_isz AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5591.57031	-5591.56982 ± 0.1	-
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25999999	0.25999999 ± 0.004	
	-5591.57031	-5591.56982 ± 0.1	-
AbsHwPos_TempHwPos_HwDog_M_f32 AbsHwPos_VehCatrOffest_HwDog_M_f32			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	9238.54004	9238.54004 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	*
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25999999	0.25999999 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-712.636963	-712.636902 ± 0.1	V
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	V
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	V
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

845.299988 ± 0.0009



Test Step 2.29 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	3787.70996		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001		
AbsHwPos_HwPosSource_Cnt_M_u16	296		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc		abCatrOffeet CatErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos target_Rte_Inst_Ap_AbsHwPos	ehCntrOffset_GetErrorStatus_ErrorStatus	
k_HwPosAuthorityStep_UIs_f32	0.270000011		
k_HwPosOutputLPFError HwDeg f32	1179.19995		
k_nwrosoutputtrenoi_nwbeg_isz k_l2CHwAuthority_Uls_f32			
k_I2CHWAuthority_Uis_i32 k_MaxSensorlessAuthority_Uis_f32	0.270000011		
k_MaxVehCntrOffDiff HwDeq f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_minsensoriessAuthority_ois_is2 k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Ois_is2 k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf		PosReducedPerf Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAut		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.0009	Roou
AbsHwPos HandwheelAuthority Uls M f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORVALID	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	622.700012	622.700012 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos SrlComHwPos HwDeg M f32	2098.83008	2098.83008 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.360000014	0.360000014 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	622.700012	622.700012 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target AbsHwPos Per2 HandwheelAuthority Uls f32.value	0.360000014	0.360000014 ± 0.004	
	2.0000000.7		
· · · · · · · · · · · · · · · · · · ·	622.700012	622.700012 ± 0.1	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	
	622.700012 3 21845	622.700012 ± 0.1 3 21845	

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5	875.5 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.30 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1676.60999		
AbsHwPos HandwheelAuthority Uls M f32	0.129999995		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995		
AbsHwPos_HwPosSource_Cnt_M_u16	176		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-901.799988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988		
AbsHwPos SrlComHwPosStatus Cnt M u16	22641		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498		
AbsHwPos TargetHwAuthority Uls M f32	0.129999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.119999997		
k HwPosOutputLPFError HwDeg f32	733.700012		
k I2CHwAuthority Uls f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_UIs_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (educedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	_	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32		•	Result
ADSI IWE US_AIIGHEURWEUS_RWDEU_IVI_ISZ	-901.799988	-901.799988 ± 0.0009	

0.119999997

-1015.08685

-1015.08685

-2401.10986

0.119999997

21845

HWPOS_STATE_SENSORVALID

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_SrlComHwPos_HwDeg_M_f32

AbsHwPos_TargetHwAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

 $AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32$

0.119999997 ± 0.004

HWPOS_STATE_SENSORVALID

-1015.08691 ± 0.1

-1015.08691 ± 0.1

-2401.11011 ± 0.1

0.119999997 ± 0.004

21845

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOI VehCntrOffset EOI VehCntrOffset HwDeg f32	422 5	422 5 + 0 0009	✓

T ·					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓	

Test Step 2.31 (Repeat Count = 1)	
	Input Value
Name Abelly Dec Aligned Dell by Dec Hy Dec M 422	Input Value 4069.18994
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.30000012
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1152.73999
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.30000012
AbsHwPos_HwPosSource_Cnt_M_u16	4312
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1501.30005
AbsHwPos_RelHwPos_HwDeg_M_f32	1501.30005
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23865
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2380.31006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012
AbsHwPos_TempHwPos_HwDeg_M_f32	8920.66016
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8920.66016
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
C_HwPosAuthorityStep_Uls_f32	0.100000001
C_HwPosOutputLPFError_HwDeg_f32	1238.59998
c_I2CHwAuthority_Uls_f32	0.100000001
<pre>x_MaxSensorlessAuthority_Uls_f32</pre>	0.379999995
_MaxVehCntrOffDiff_HwDeg_f32	941.599976
_MinSensorlessAuthority_Uls_f32	0
_TurnsCntrAuthority_Uls_f32	0.100000001
_UseTurnsCntr_Cnt_lgc	0
<pre><_VehCntrOffValidLimit_HwDeg_f32</pre>	882.200012
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-232
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.379999995
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	662.900024
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	158
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	935.900024
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3595
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	1
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwrieeiPosition_HwDeg_i32 arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1501.30005	1501.30005 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.379999995	0.379999995 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	662.900024	662.900024 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	662.900024	662.900024 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.379999995	0.379999995 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	662.900024	662.900024 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8920.66016	8920.66016 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.379999995	0.379999995 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	662.900024	662.900024 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	662.900024	662.900024 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	935.900024	935.900024 ± 0.0009	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

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Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.310000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1173.02002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.310000002
AbsHwPos_HwPosSource_Cnt_M_u16	320
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-448.700012
AbsHwPos_RelHwPos_HwDeg_M_f32	-448.700012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.310000002
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.200000003
C_HwPosOutputLPFError_HwDeg_f32	1268.30005
c_I2CHwAuthority_Uls_f32	0.200000003
_MaxSensorlessAuthority_Uls_f32	0.389999986
c_MaxVehCntrOffDiff_HwDeg_f32	1060.40002
_MinSensorlessAuthority_Uls_f32	1
c_TurnsCntrAuthority_UIs_f32	0.200000003
 UseTurnsCntr_Cnt_lgc	1
 <pre><_VehCntrOffValidLimit_HwDeg_f32</pre>	911.900024
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-212.300003
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.38999986
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	683
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	966,099976
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	3719
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedI	Perf_(target_AbsHwPos_Per2_DiagStatusH	-lwPosReducedPerf_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3$	target_AbsHwPos_Per2_Handwheel	Authority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg	g_f32 target_AbsHwPos_Per2_Handwheelf	Position_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSour	ce_Cnt_u16	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igentation{ $	c target_AbsHwPos_Per2_I2CHwAbsP	PosValid_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32$	target_AbsHwPos_Per2_I2CHwAbsP	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode	_Cnt_enum	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f3$	target_AbsHwPos_Per2_SensorlessA	Authority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_Results = 100000000000000000000000000000000000$	_f32	HwPos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_transfer (a) and transfer (b) and transfer$	u16 target_AbsHwPos_Per2_SrlComHwF	PosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwF	Pos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08$	target_AbsHwPos_Per2_TurnsCntrVa	alidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	0.109999999 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	320	320	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3541.94092	3541.94092 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002	8814.7002 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.109999999	0.109999999 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target AbsHwPos Per2 HwPosSource Cnt u16.value	320	320	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

65534

1600

65535

65534

1600 ± 0.1

65535 ± 0.09

Test Step 2.33 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-4350.66992
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.319999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1193.30005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.319999993
AbsHwPos_HwPosSource_Cnt_M_u16	328
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-429
AbsHwPos_RelHwPos_HwDeg_M_f32	-429
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24009
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.319999993
AbsHwPos_TempHwPos_HwDeg_M_f32	-8708.74023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.310000002
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_UIs_f32	0.310000002
k_MaxSensorlessAuthority_Uls_f32	0.40000006
k_MaxVehCntrOffDiff_HwDeg_f32	1090.09998





710071111 00_1 012			
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.310000002		
k_TurnsCntrAuthority_UIs_f32	0.310000002		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	941.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.400000006		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	170		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	996.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3843		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-429	-429 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006	0.40000006 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	703.099976	703.099976 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	~

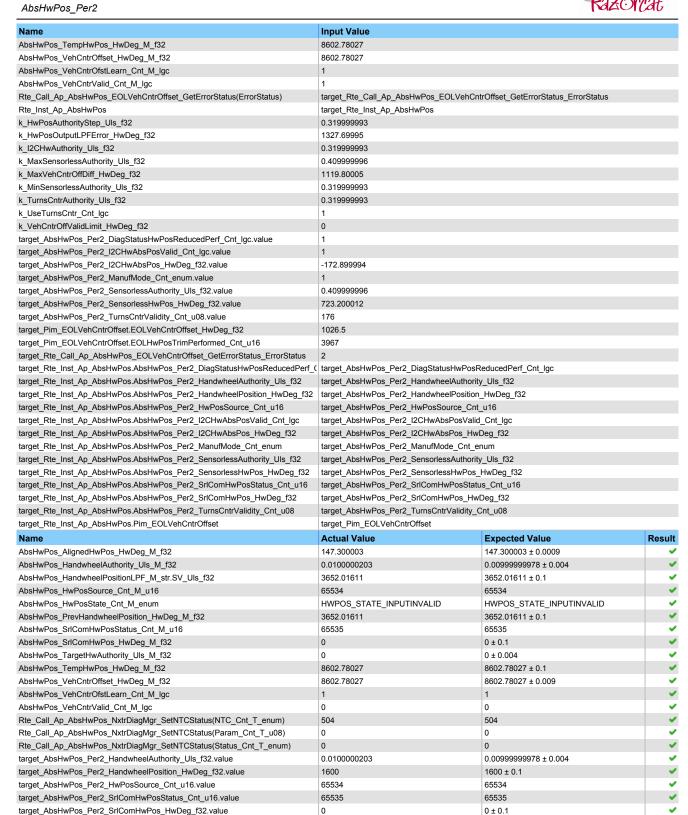
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-429	-429 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006	0.400000006 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	703.099976	703.099976 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1208.73999	-1208.73999 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.40000006	0.400000006 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	703.099976	703.099976 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	703.099976	703.099976 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	996.299988	996.299988 ± 0.0009	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.34 (Repeat Count = 1)		<u>✓</u>
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	4491.41016	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.330000013	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1213.57996	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.330000013	
AbsHwPos_HwPosSource_Cnt_M_u16	336	
AbsHwPos_HwPosState_Cnt_M_enum	7	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	147.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24081	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2802.53003	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.330000013	

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T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

 1026.5 ± 0.009

1026.5

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$



Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	4632.1499		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.340000004		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1233.85999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.340000004		
AbsHwPos_HwPosSource_Cnt_M_u16	344		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	156.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	156.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24153		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.340000004		
AbsHwPos_TempHwPos_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8496.82031		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.330000013		
k HwPosOutputLPFError HwDeg f32	1357.40002		
k I2CHwAuthority Uls f32	0.33000013		
k_MaxSensorlessAuthority_Uls_f32	0.419999987		
k_MaxVehCntrOffDiff HwDeg f32	1149.5		
k_maxvenchitolibiii_nwbeg_isz k_MinSensorlessAuthority_Uls_f32	0.330000013		
k_minsensoriessauthority_Uis_132 k_TurnsCntrAuthority_Uls_132	0.330000013		
	0.330000013		
k_UseTurnsCntr_Cnt_Igc			
k_VehCntrOffValidLimit_HwDeg_f32	1600		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.419999987		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	182		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1056.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4091		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuth	nority Uls f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwF		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosS	Status Cnt u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	,,,_	
Name	Actual Value	Expected Value	Resu
		156.899994 ± 0.0009	IXesu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	156.899994		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3703.2666	3703.26636 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3703.2666	3703.26636 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	- '
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	8496.82031	8496.82031 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8496.82031	8496.82031 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	
	0.00999999046	0.00999999978 ± 0.004	
target AbsHwPos Per2 HandwheelAuthority Uls f32.value	The state of the s		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target AbsHwPos Per2 HandwheelPosition HwDeg f32.value	1600	1600 ± 0.1	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value			
	1600 65534 65534	1600 ± 0.1 65534 65534	

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T .			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.36 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-4772.89014		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.349999994		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1254.14001		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.349999994		
AbsHwPos HwPosSource Cnt M u16	352		
AbsHwPos HwPosState Cnt M enum	9		
AbsHwPos PrevHandwheelPosition HwDeg M f32	166.5		
AbsHwPos RelHwPos HwDeg M f32	166.5		
AbsHwPos SrlComHwPosStatus Cnt M u16	24225		
AbsHwPos SrlComHwPos HwDeg M f32	-3084.01001		
AbsHwPos TargetHwAuthority Uls M f32	0.349999994		
AbsHwPos TempHwPos HwDeg M f32	-8390.86035		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.340000004		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_Uls_f32	0.340000004		
k_MaxSensorlessAuthority_Uls_f32	0.430000007		
k_MaxVehCntrOffDiff_HwDeg_f32	1179.19995		
k_MinSensorlessAuthority_Uls_f32	0.340000004		
k_TurnsCntrAuthority_Uls_f32	0.340000004		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1030.69995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.430000007		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	763.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1086.90002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4215		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	166.5	166.5 ± 0.0009	~
	0.00000000010	0.0000000000000000000000000000000000000	

0.00999999046

HWPOS_STATE_INPUTINVALID

-2121.60986

-2121.60986

65534

65535

0

AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_SrlComHwPos_HwDeg_M_f32

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

 $AbsHwPos_HandwheelAuthority_Uls_M_f32$

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum

0.00999999978 ± 0.004

HWPOS_STATE_INPUTINVALID

-2121.61011 ± 0.1

-2121.61011 ± 0.1

65534

65535

0 ± 0.1

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1390.85999	-1390.85999 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.37 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	4913.62988
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1274.42004
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.360000014
AbsHwPos_HwPosSource_Cnt_M_u16	360
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	176.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	176.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24297
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75
.bsHwPos TargetHwAuthority Uls M f32	0.360000014
AbsHwPos_TempHwPos_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
HwPosAuthorityStep Uls f32	0.100000001
HwPosOutputLPFError HwDeg f32	1416.80005
I I2CHwAuthority Uls f32	0.100000001
	0.43999998
MaxVehCntrOffDiff HwDeg f32	1208.90002
MinSensorlessAuthority Uls f32	0.100000001
TurnsCntrAuthority Uls f32	0.100000001
UseTurnsCntr Cnt Igc	1
vehCntrOffValidLimit HwDeg f32	1060.40002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.43999998
<u> </u>	783.5
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	194
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4339
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthority Uls f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	176.100006	176.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	783.5	783.5 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	783.5	783.5 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.43999998	0.439999998 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.439999998	0.439999998 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	783.5	783.5 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	783.5	783.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

T			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	5054.37012
AbsHwPos HandwheelAuthority Uls M f32	0.370000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1294.69995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.370000005
AbsHwPos_HwPosSource_Cnt_M_u16	368
AbsHwPos_HwPosState_Cnt_M_enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	185.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	185.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24369
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3365.48999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.370000005
AbsHwPos_TempHwPos_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.300000012
_HwPosOutputLPFError_HwDeg_f32	1446.5
c_I2CHwAuthority_Uls_f32	0.300000012
C_MaxSensorlessAuthority_Uls_f32	0.449999988
C_MaxVehCntrOffDiff_HwDeg_f32	1238.59998
_MinSensorlessAuthority_Uls_f32	0.300000012
C_TurnsCntrAuthority_Uls_f32	0.300000012
_UseTurnsCntr_Cnt_lgc	1
<_VehCntrOffValidLimit_HwDeg_f32	1090.09998
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2





Name	Input Value		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.449999988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	803.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4463		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPe	rf(target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f	32 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHwl	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPos	Status Cnt u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	185.699997	185.699997 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.0699999928	0.0700000003 ± 0.004	~
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	3841.86523	3841.86499 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	~
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3841.86523	3841.86499 ± 0.1	✓
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	•
AbsHwPos TempHwPos HwDeg M f32	8178.93018	8178.93018 ± 0.1	✓
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018	8178.93018 ± 0.009	✓
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	✓
AbsHwPos VehCntrValid Cnt M Igc	1	1	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	504	~
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	1	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	1	•
target AbsHwPos Per2 HandwheelAuthority Uls f32.value	0.0699999928	0.0700000003 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
		65535	•
target AbsHwPos Per2 SrlComHwPosStatus Cnt u16.value	65535	00000	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	65535 0	0±0.1	

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.39 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5195.10986
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.379999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1314.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.379999995
AbsHwPos_HwPosSource_Cnt_M_u16	376
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	195.300003
AbsHwPos_RelHwPos_HwDeg_M_f32	195.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24441
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3506.22998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.379999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-8072.97021
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.370000005





Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	1476.19995		
k_I2CHwAuthority_Uls_f32	0.370000005		
k_MaxSensorlessAuthority_Uls_f32	0.460000008		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.370000005		
k_TurnsCntrAuthority_UIs_f32	0.370000005		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.460000008		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	206		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4587		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	195.300003	195.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	~
Abolity Dec. Harriston and Decision I DE Marte OV I Har 100	000 700040	000 700040 + 0.4	

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	195.300003	195.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.460000008	0.460000008 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000008	0.460000008 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	823.700012	823.700012 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	823.700012	823.700012 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-5335.8501	
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.389999986	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1487	
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.389999986	

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AbsHwPos_Per2

Name	Input Value		
	· ·		
AbshwPos_HwPosSource_Cnt_M_u16	6384		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	204.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	204.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24513		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3646.96997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.38999986		
AbsHwPos_TempHwPos_HwDeg_M_f32	7967.00977		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLV	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.379999995		
k_HwPosOutputLPFError_HwDeg_f32	1505.90002		
k_I2CHwAuthority_Uls_f32	0.379999995		
k_MaxSensorlessAuthority_Uls_f32	0.469999999		
k_MaxVehCntrOffDiff_HwDeg_f32	1298		
k_MinSensorlessAuthority_Uls_f32	0.379999995		
k_TurnsCntrAuthority_Uls_f32	0.379999995		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	1149.5		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	562.400024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.46999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	212		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1000.59998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4711		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	2		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPersUnited StatusHwPosReducedPersUnited StatusHwPosReducedPersUnit$	rf_(target_AbsHwPos_Per2_DiagStatusHw	vPosReducedPerf_Cnt_lgc	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32$	target_AbsHwPos_Per2_HandwheelAu	uthority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f	32 target_AbsHwPos_Per2_HandwheelPo	osition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	e_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	sValid Cnt Igc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3		,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrVali	lalty_Crit_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	204.899994	204.899994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4014.20361	4014.20386 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	_ ·
AbsHwPos PrevHandwheelPosition HwDeg M f32	4014.20361	4014.20386 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	٠,
AbsHwPos TempHwPos HwDeg M f32	7967.00977	7967.00977 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	7967.00977	7967.00977 ± 0.009	
AbshwPos_VenChtrOfstLearn Cnt M lgc	1	1	
	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	,
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
	65534	65534	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	00004		
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	



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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.41 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5476.58984		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.40000006		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1507.09998		
ubsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.40000006		
.bsHwPos_HwPosSource_Cnt_M_u16	392		
lbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
lbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	214.5		
lbsHwPos_RelHwPos_HwDeg_M_f32	214.5		
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	24585		
.bsHwPos_SrlComHwPos_HwDeg_M_f32	3787.70996		
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
.bsHwPos_TempHwPos_HwDeg_M_f32	7861.0498		
.bsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498		
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
bsHwPos_VehCntrValid_Cnt_M_lgc	0		
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	nCntrOffset_GetErrorStatus_ErrorStatus	
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
_HwPosAuthorityStep_Uls_f32	0.389999986		
_HwPosOutputLPFError_HwDeg_f32	1535.59998		
_I2CHwAuthority_Uls_f32	0.389999986		
_MaxSensorlessAuthority_Uls_f32	0.479999989		
_MaxVehCntrOffDiff_HwDeg_f32	1327.69995		
_MinSensorlessAuthority_Uls_f32	0.389999986		
_TurnsCntrAuthority_Uls_f32	0.389999986		
_UseTurnsCntr_Cnt_lgc	1		
_VehCntrOffValidLimit_HwDeg_f32	1179.19995		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.479999989		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	863.900024		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	218		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-500.899994		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4835		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_(target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	target_AbsHwPos_Per2_HandwheelPosit	tion_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_0	Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	/alid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3:	2 target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
lame	Actual Value	Expected Value	Resu
bsHwPos AlignedHwPos HwDeg M f32	214.5	214.5 ± 0.0009	
bsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	
bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	863.900024	863.900024 ± 0.1	
bsHwPos_HwPosSource_Cnt_M_u16	3	3	
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
	863.900024	863.900024 ± 0.1	
heHwPoe PreyHandwheelPosition HwDog M f32		000.800024 I U. I	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32			

863.900024

AbsHwPos_TempHwPos_HwDeg_M_f32

863.900024 ± 0.1

AbsHwPos_Per2

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	863.900024	863.900024 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	863.900024	863.900024 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

lame	Input Value
	Input Value 1617.32996
bsHwPos_AlignedRelHwPos_HwDeg_M_f32	
bsHwPos_HandwheelAuthority_Uls_M_f32	0.40999996
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1527.19995
bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.409999996
bsHwPos_HwPosSource_Cnt_M_u16	400
bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	224.100006
bsHwPos_RelHwPos_HwDeg_M_f32	224.100006
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	24657
bsHwPos_SrlComHwPos_HwDeg_M_f32	-3928.44995
bsHwPos_TargetHwAuthority_UIs_M_f32	0.40999996
bsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984
bsHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
bsHwPos_VehCntrValid_Cnt_M_lgc	1
te_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.40000006
_HwPosOutputLPFError_HwDeg_f32	1565.30005
_I2CHwAuthority_Uls_f32	0.40000006
_MaxSensorlessAuthority_Uls_f32	0.49000001
_MaxVehCntrOffDiff_HwDeg_f32	0
_MinSensorlessAuthority_Uls_f32	0.40000006
_TurnsCntrAuthority_Uls_f32	0.40000006
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1208.90002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.49000001
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	884
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1268.09998
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4959
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16

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AbsHwPos_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	224.100006	224.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-2278.53882	-2278.53882 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-2278.53882	-2278.53882 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984	-7755.08984 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	733.329956	733.330017 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	733.329956	733.330017 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	-558.070007
AbsHwPos HandwheelAuthority Uls M f32	0.419999987
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	157.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.41999987
AbsHwPos_HwPosSource_Cnt_M_u16	408
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-389.600006
AbsHwPos_RelHwPos_HwDeg_M_f32	-389.600006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24729
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.419999987
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.409999996
k_HwPosOutputLPFError_HwDeg_f32	1595
k_I2CHwAuthority_Uls_f32	0.409999996
k_MaxSensorlessAuthority_Uls_f32	0.5
k_MaxVehCntrOffDiff_HwDeg_f32	1600
k_MinSensorlessAuthority_Uls_f32	0.409999996
k_TurnsCntrAuthority_Uls_f32	0.409999996
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1500.34998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.5
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	904.099976

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



AUSHWPOS_Per2		MAC	ICHU	
Name	Input Value			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	230			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1298.30005			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5083			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3;	2 target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	vDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStar			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos Hv			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity	Cnt u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	-		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-389.600006	-389.600006 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.419999987	0.419999987 ± 0.004	•	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	269.209991	269.209991 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	408	408	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	269.209991	269.209991 ± 0.1	✓	
AbsHwPos SrlComHwPosStatus Cnt M u16	24729	24729		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4069.18994	4069.18994 ± 0.1	✓	
AbsHwPos TargetHwAuthority Uls M f32	0.419999987	0.419999987 ± 0.004		
AbsHwPos_TempHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1462.16992	-1462.17004 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.419999987	0.419999987 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	269.209991	269.209991 ± 0.1	•	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	408	408	•	

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

-1462.16992

24729

1600

24729

1600 ± 0.1

-1462.17004 ± 0.009

Test Step 2.44 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	898.809998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007
AbsHwPos_HwPosSource_Cnt_M_u16	416
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003
AbsHwPos_ReIHwPos_HwDeg_M_f32	243.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.419999987
k_HwPosOutputLPFError_HwDeg_f32	50.5999985

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value \\ target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$



AbsHwPos_Per2

Name	Input Value		
k_I2CHwAuthority_Uls_f32	0.419999987		
k_MaxSensorlessAuthority_Uls_f32	0.50999999		
k_MaxVehCntrOffDiff_HwDeg_f32	5		
k_MinSensorlessAuthority_Uls_f32	0.419999987		
k_TurnsCntrAuthority_Uls_f32	0.419999987		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1268.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.50999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	924.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1328.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5207		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C	target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

- 3·- ·- ·- ·- · · · · · · · · · · ·			
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	243.300003	243.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000203	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.139496	147.139496 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	147.139496	147.139496 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012	-409.950012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-25.3900146	-25.3899994 ± 0.00009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000203	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	147.139496	147.139496 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-25.3900146	-25.3900146 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.45 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6039.5498	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1587.5	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.439999998	
AbsHwPos_HwPosSource_Cnt_M_u16	424	

AbsHwPos_Per2





Name	Input Value		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	252.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	252.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24873		
AbsHwPos SrlComHwPos HwDeg M f32	-4350.66992		
AbsHwPos TargetHwAuthority UIs M f32	0.43999998		
AbsHwPos TempHwPos HwDeg M f32	550.690002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.430000007		
k HwPosOutputLPFError HwDeg f32	0		
k_I2CHwAuthority_Uls_f32	0.430000007		
k MaxSensorlessAuthority Uls f32	0.519999981		
k_MaxVehCntrOffDiff_HwDeg_f32	1446.5		
k_MinSensorlessAuthority_Uls_f32	0.430000007		
k_TurnsCntrAuthority_UIs_f32	0.430000007		
k_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	1298		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	662.900024		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.519999981		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	944.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	242		
	1358.69995		
target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32	5331		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1	DesDadward Darf Cat Jan	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum			
	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	0	1	1
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	252.899994	252.899994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1131.30359	1131.30359 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	424	424	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1131.30359	1131.30359 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24873	24873	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-4350.66992	-4350.66992 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.439999998	0.439999998 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	550.690002	550.690002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997	-1437.21997 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504		1
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 1 0.439999998	*none* 0.439999998 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1 1 0.439999998 1131.30359	*none* 0.439999998 ± 0.004 1131.30359 ± 0.1 424 24873	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 1 0.439999998 1131.30359 424	*none* 0.439999998 ± 0.004 1131.30359 ± 0.1 424	





Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.449999988		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1000.29999		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.449999988		
AbsHwPos_HwPosSource_Cnt_M_u16	432		
AbsHwPos_HwPosState_Cnt_M_enum	6		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	262.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	262.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24945		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4491.41016		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.449999988		
AbsHwPos_TempHwPos_HwDeg_M_f32	691.429993		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.43999998		
k_HwPosOutputLPFError_HwDeg_f32	1620		
k_I2CHwAuthority_Uls_f32	0.43999998		
k_MaxSensorlessAuthority_Uls_f32	0.529999971		
k_MaxVehCntrOffDiff_HwDeg_f32	1476.19995		
k_MinSensorlessAuthority_Uls_f32	0.43999998		
k_TurnsCntrAuthority_Uls_f32	0.43999998		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1327.69995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.529999971		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	964.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1388.90002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5455		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		Dee Deedward Deef Ook Jan	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_I2CHwAbsPos_ target_AbsHwPos_Per2_ManufMode_C		
	target AbsHwPos Per2 SensorlessAuti		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f3		,	
target_Rte_linst_Ap_AbsHwPos.AbsHwPos_Fet2_SetIsoriessHwPos_HwDeg_i3 target_Rte_linst_Ap_AbsHwPos.AbsHwPos_Pet2_SrlComHwPosStatus_Cnt_u10			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Fer2_SilComHwPosStatus_Citt_utive target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	- · · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	ny_ont_uoo	
		Fyrna ata d Value	Dagu
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	262.5	262.5 ± 0.0009	
- 7	0.00999999046 691.429993	0.00999999978 ± 0.004 691.429993 ± 0.1	
AbsHwPos HandwheelPosition DE M. etr SV 111s 122	031.423330	691.429993 ± 0.1	
	65534		
AbsHwPos_HwPosSource_Cnt_M_u16	65534		
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_INPUTINVALID 691.429993	HWPOS_STATE_INPUTINVALID 691.429993 ± 0.1	
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	HWPOS_STATE_INPUTINVALID 691.429993 65535	HWPOS_STATE_INPUTINVALID 691.429993 ± 0.1 65535	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	HWPOS_STATE_INPUTINVALID 691.429993 65535 0	HWPOS_STATE_INPUTINVALID 691.429993 ± 0.1 65535 0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	HWPOS_STATE_INPUTINVALID 691.429993 65535 0	HWPOS_STATE_INPUTINVALID 691.429993 ± 0.1 65535 0 ± 0.1 0 ± 0.004	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	HWPOS_STATE_INPUTINVALID 691.429993 65535 0	HWPOS_STATE_INPUTINVALID 691.429993 ± 0.1 65535 0 ± 0.1	

AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc

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AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	691.429993	691.429993 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1388.90002	1388.90002 ± 0.009	✓

T .				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.47 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos HandwheelAuthority Uls M f32	0.460000008		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-980.599976		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.460000008		
AbsHwPos HwPosSource Cnt M u16	6440		
AbsHwPos HwPosState Cnt M enum	7		
AbsHwPos PrevHandwheelPosition HwDeg M f32	272.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	272.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25017		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4632.1499		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.460000008		
AbsHwPos_TempHwPos_HwDeg_M_f32	-832.169983		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.449999988		
k_HwPosOutputLPFError_HwDeg_f32	5		
k_I2CHwAuthority_Uls_f32	0.44999988		
k_MaxSensorlessAuthority_Uls_f32	0.540000021		
k_MaxVehCntrOffDiff_HwDeg_f32	1505.90002		
k_MinSensorlessAuthority_Uls_f32	0.44999988		
k_TurnsCntrAuthority_Uls_f32	0.44999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1357.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.540000021		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	984.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	254		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1419.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5579		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	_	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	,	
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	status_Cnt_u16	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32			
	target_AbsHwPos_Per2_SrlComHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset	ty_Cnt_u08	-
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value	ty_Cnt_u08 Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006	Expected Value 272.100006 ± 0.0009	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440	ty_Cnt_u08 Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValidi target_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Learn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Alig_C Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none*	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOffst_BruDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none* *none*	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffste_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504 0	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none* *none* *none*	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrVofstLearn_Cnt_M_lgc AbsHwPos_VehCntrVolid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504 0 0 0.460000008	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none* *none* *none* *none* 0.460000008 ± 0.004	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504 0 0 0.460000008 -912.322144	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none* *none* *none* *none* 0.460000008 ± 0.004 -912.322205 ± 0.1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrVofstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_TurnsCntrValiditarget_Pim_EOLVehCntrOffset Actual Value 272.100006 0.460000008 -912.322144 6440 HWPOS_STATE_INPUTINVALID -912.322144 25017 4632.1499 0.460000008 -832.169983 7225.2998 0 0 504 0 0 0.460000008	Expected Value 272.100006 ± 0.0009 0.460000008 ± 0.004 -912.322205 ± 0.1 6440 HWPOS_STATE_INPUTINVALID -912.322205 ± 0.1 25017 4632.1499 ± 0.1 0.460000008 ± 0.004 -832.169983 ± 0.1 7225.2998 ± 0.009 0 *none* *none* *none* *none* 0.460000008 ± 0.004	Resu

1600

1600 ± 0.1

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

AbsHwPos_Per2



Name	Actual Value	Expected Value	Result
target Pim FOI VehCntrOffset FOI VehCntrOffset HwDeg f32	1419 09998	1419 09998 + 0 009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.469999999	0.469999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-52.0092773	-52.0093002 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.49 (Repeat Count = 1)	
	Inmust Value
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.47999989
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1547.30005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.479999989
AbsHwPos_HwPosSource_Cnt_M_u16	456
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-350.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-350.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25161
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4913.62988
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.47999989
AbsHwPos_TempHwPos_HwDeg_M_f32	1113.65002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	1
k_HwPosOutputLPFError_HwDeg_f32	199.100006
k_I2CHwAuthority_Uls_f32	0.469999999
k_MaxSensorlessAuthority_Uls_f32	0.560000002
k_MaxVehCntrOffDiff_HwDeg_f32	1565.30005
k_MinSensorlessAuthority_Uls_f32	0.469999999
k_TurnsCntrAuthority_Uls_f32	0.469999999
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1416.80005
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	743.299988
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.560000002
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1024.69995
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	16
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-960.900024
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5827
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Corrections and the property of the pro$	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-350.200012	-350.200012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1339.14807	1339.14795 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	456	456	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1339.14807	1339.14795 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25161	25161	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	1113.65002	1113.65002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988	7013.37988 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1339.14807	1339.14795 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	456	456	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	25161	25161	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-960.900024	-960.900024 ± 0.0009	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.50 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.49000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1567.40002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.49000001
AbsHwPos_HwPosSource_Cnt_M_u16	464
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	300.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	300.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25233
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5054.37012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.49000001
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	228.800003
k_I2CHwAuthority_Uls_f32	0.479999989
k_MaxSensorlessAuthority_Uls_f32	0.569999993
k_MaxVehCntrOffDiff_HwDeg_f32	1595
k_MinSensorlessAuthority_Uls_f32	0.479999989
k_TurnsCntrAuthority_Uls_f32	0.479999989
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	1446.5
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	763.400024
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.569999993
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1044.80005
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-941.200012
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5951

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target AbsHwPos Per2 HwPosSource Cnt u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

AbsHwPos_Per2



Name	Input Value			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0			
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_AbsAbsHwPosReducedPerf_Inst_AbsAbsHwPosReducedPerf_Inst_AbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsA$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	300.899994	300.899994 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.290000021	0.290000021 ± 0.004	✓	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	184.7229	184.7229 ± 0.1	✓	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	184.7229	184.7229 ± 0.1	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓	
AbsHwPos_TempHwPos_HwDeg_M_f32	-1254.39001	-1254.39001 ± 0.1	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992	6907.41992 ± 0.009	•	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	•

0.290000021

184.7229

65534

65535

65535

1

65534

65535

0 ± 0.1

65535 ± 0.09

0.290000021 ± 0.004 184.7229 ± 0.1

Test Step 2.51 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
AbsHwPos_HwPosSource_Cnt_M_u16	472
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	310.5
AbsHwPos_RelHwPos_HwDeg_M_f32	310.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25305
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5195.10986
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13
AbsHwPos_VehCntrOffset_HwDeg_M_f32	269.209991
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.49000001
k_HwPosOutputLPFError_HwDeg_f32	258.5
k_I2CHwAuthority_Uls_f32	0
k_MaxSensorlessAuthority_Uls_f32	0.579999983





Name	Input Value		
k MaxVehCntrOffDiff HwDeg f32	50.5999985		
k MinSensorlessAuthority Uls f32	0.49000001		
k_TurnsCntrAuthority_Uls_f32	0.4900001		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.579999983		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1064.90002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	24		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-921.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6075		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	310.5	310.5 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	✓

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Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	310.5	310.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-59.1499634	-59.1500015 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-59.1499634	-59.1500015 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	783.5	783.5 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	269.209991	269.209991 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-59.1499634	-59.1500015 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	783.5	783.5 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-921.5	-921.5 ± 0.0009	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7024.72998	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.50999999	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.50999999	
AbsHwPos_HwPosSource_Cnt_M_u16	480	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	320.100006	
AbsHwPos_RelHwPos_HwDeg_M_f32	320.100006	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25377	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501	





Name	Input Value			
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.50999999			
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87			
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012			
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
AbsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	ntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
k_HwPosAuthorityStep_Uls_f32	0.5			
k_HwPosOutputLPFError_HwDeg_f32	288.200012			
k_I2CHwAuthority_Uls_f32	1			
k_MaxSensorlessAuthority_Uls_f32	0.589999974			
k_MaxVehCntrOffDiff_HwDeg_f32	80.3000031			
k_MinSensorlessAuthority_Uls_f32	0.5			
k_TurnsCntrAuthority_Uls_f32	0.5			
k_UseTurnsCntr_Cnt_lgc	0			
k_VehCntrOffValidLimit_HwDeg_f32	1505.90002			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0			
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.589999974			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085			
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	28			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-901.799988			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6199			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0				
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValie			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthorit	,		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	CIIL_008		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1=		
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	Y	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	Y	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	V	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	· ·	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	Y	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	V	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	V	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	Y.	
AbsHwPos_TargetHwAuthority_UIs_M_f32	1	1 ± 0.004	V	
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1 0	1 0		
	504		· ·	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1	*none*		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	465.843018	1 ± 0.004 465.842987 ± 0.1		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	405.843018	405.842987 ± 0.1		
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	-	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	803.599976	21845 803.599976 ± 0.1	-	
target_AbshwPos_Per2_SricornnwPos_nwDeg_i32.value target_Pim_EOLVehCntrOffset_HwDeg_f32	-901.799988	-901.799988 ± 0.0009	-	
target_Fill_LOLVerionitiOnset.EOLVerionitiOnset_Hwbeg_i32	-301.733300	-301.733300 I 0.0003		

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	-



Test Step 2.53 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7165.47021		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.519999981		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	126.699997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.519999981		
AbsHwPos_HwPosSource_Cnt_M_u16	488		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-507.799988		
AbsHwPos_RelHwPos_HwDeg_M_f32	-507.799988		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25449		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5476.58984		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.519999981		
AbsHwPos_TempHwPos_HwDeg_M_f32	-1676.60999		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	550.690002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.50999999		
k_HwPosOutputLPFError_HwDeg_f32	317.899994		
k_I2CHwAuthority_Uls_f32	0.479999989		
k_MaxSensorlessAuthority_Uls_f32 k_MaxVehCntrOffDiff_HwDeg_f32	0.600000024		
k_maxvenontrollolli_nwbeg_is2 k_MinSensorlessAuthority_Uls_f32	0.50999999		
k_minsensoriessautnority_dis_f32 k_TurnsCntrAuthority_Uls_f32	0.50999999		
k UseTurnsCntr Cnt lgc	0		
k VehCntrOffValidLimit HwDeq f32	1535.59998		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.600000024		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1105.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	32		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-882.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6323		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuti	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		1_
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-507.799988	-507.799988 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.47999989 ± 0.004	'
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	489.139954	489.140015 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	489.139954 21845	489.140015 ± 0.1 21845	
AbsHwPos_SriComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	
AbsHwPos_TargethwAuthority_bis_M_132 AbsHwPos_TempHwPos_HwDeg_M_f32	823.700012	823.700012 ± 0.1	
AbsHwPos_rempHwPos_HwDeg_M_132 AbsHwPos_VehCntrOffset HwDeg_M_f32	550.690002	550.690002 ± 0.0009	
AbsHwPos VehCntrOfistLearn Cnt M lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	1	*none*	
	0.47999989	0.479999989 ± 0.004	
target AbsHwPos Per2 HandwheelAuthority Uls f32 value			
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	489.139954	489.140015 ± 0.1	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	489.139954	489.140015 ± 0.1	
	489.139954 2 21845	489.140015 ± 0.1 2 21845	

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Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-882.099976	-882.099976 ± 0.0009	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.54 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-7306.20996		
AbsHwPos HandwheelAuthority Uls M f32	0.529999971		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	139.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.529999971		
AbsHwPos_HwPosSource_Cnt_M_u16	9496		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	389.200012		
AbsHwPos_ReiHwPos_HwDeg_M_f32	389.200012		
AbsHwPos SrlComHwPosStatus Cnt M u16	25521		
AbsHwPos SrlComHwPos HwDeg M f32	5617.33008		
AbsHwPos TargetHwAuthority Uls M f32	0.529999971		
AbsHwPos TempHwPos HwDeg M f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.519999981		
k_HwPosOutputLPFError_HwDeg_f32	347.600006		
k_I2CHwAuthority_UIs_f32	0.519999981		
k_MaxSensorlessAuthority_Uls_f32	0.610000014		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.519999981		
k_TurnsCntrAuthority_UIs_f32	0		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(educedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI	 -	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDog f32	target AbsHwPos Per2 SensorlessHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatu	·-	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
	389.200012	389.200012 ± 0.0009	iveaul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	303.200012	303.200012 I 0.0008	

0.00999999046

HWPOS_STATE_SENSORVALID

-4173.37207

-4173.37207

-7997.64014

-7997.64014

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

 $AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32$

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32

AbsHwPos_TargetHwAuthority_Uls_M_f32

AbsHwPos_TempHwPos_HwDeg_M_f32

0.00999999978 ± 0.004

HWPOS_STATE_SENSORVALID

-4173.37207 ± 0.1

-4173.37207 ± 0.1

-7997.64014 ± 0.1

-7997.64014 ± 0.1

0 ± 0.004





Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.0099999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

lame	Input Value
bsHwPos AlignedRelHwPos HwDeg M f32	7446.9502
bsHwPos HandwheelAuthority Uls M f32	0.540000021
bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	151.5
bshwPos HandwheelPositionLPF M str.K Uls f32	0.540000021
bsHwPos HwPosSource Cnt M u16	504
bsHwPos HwPosState Cnt M enum	HWPOS STATE INIT
bsHwPos PrevHandwheelPosition HwDeg M f32	-468,399994
bsHwPos RelHwPos HwDeg M f32	-468.399994
	25593
bsHwPos_SrlComHwPosStatus_Cnt_M_u16 bsHwPos_SrlComHwPos_HwDeq_M_f32	-5758.06982
	0.540000021
bsHwPos_TargetHwAuthority_Uls_M_f32	
bsHwPos_TempHwPos_HwDeg_M_f32	1958.08997
bsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
bsHwPos_VehCntrValid_Cnt_M_lgc	1
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.529999971
_HwPosOutputLPFError_HwDeg_f32	377.299988
_I2CHwAuthority_Uls_f32	0.529999971
_MaxSensorlessAuthority_Uls_f32	0.620000005
_MaxVehCntrOffDiff_HwDeg_f32	169.399994
_MinSensorlessAuthority_Uls_f32	0.529999971
_TurnsCntrAuthority_Uls_f32	1
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1595
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.620000005
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1145.30005
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-842.700012
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6571
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
urget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-468.399994	-468.399994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4540.41504	4540.41504 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4540.41504	4540.41504 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	8279.12012	8279.12012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-832.169983	-832.169983 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-842.700012	-842.700012 ± 0.0009	•

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Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓	

Took Ston 2.50 (Bonock Count = 4)	
Test Step 2.56 (Repeat Count = 1)	Innut Value
	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7587.68994
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.550000012
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	163.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.550000012
AbsHwPos_HwPosSource_Cnt_M_u16	512
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	784.099976
AbsHwPos_RelHwPos_HwDeg_M_f32	7844.1001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25665
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.550000012
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.540000021
k_HwPosOutputLPFError_HwDeg_f32	50.5999985
k_I2CHwAuthority_Uls_f32	0.540000021
k_MaxSensorlessAuthority_Uls_f32	0.629999995
k_MaxVehCntrOffDiff_HwDeg_f32	199.100006
k_MinSensorlessAuthority_Uls_f32	0.540000021
k_TurnsCntrAuthority_Uls_f32	0.479999989
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	884
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.62999995
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1165.40002
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-823

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



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Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6695		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7844.1001	7844.1001 ± 0.0009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2860.40674	2860.40698 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2860.40674	2860.40698 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.479999989	0.479999989 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	5066.63965	5066.64014 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2521.05005	2521.05005 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.479999989	0.479999989 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
	1		

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 ChecknointReached	1	

1600

65535

1600 ± 0.1

65535 ± 0.09

Test Step 2.57 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-7728.43018
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.560000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	176.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.560000002
AbsHwPos_HwPosSource_Cnt_M_u16	520
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	966.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	9665.2002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25737
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6039.5498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.560000002
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.550000012
k_HwPosOutputLPFError_HwDeg_f32	80.3000031
k_I2CHwAuthority_Uls_f32	0.550000012

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$





Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.63999986		
k_MaxVehCntrOffDiff_HwDeg_f32	228.800003		
k_MinSensorlessAuthority_Uls_f32	0.550000012		
k_TurnsCntrAuthority_Uls_f32	0.550000012		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.639999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1185.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	48		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-803.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	9665.2002	9665.2002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	✓

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	9665.2002	9665.2002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	741.452026	741.452026 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	741.452026	741.452026 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1185.5	1185.5 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	1185.5	1185.5 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1261.79004	-1261.79004 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.639999986	0.639999986 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	741.452026	741.452026 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1185.5	1185.5 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-803.299988	-803.299988 ± 0.0009	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.58 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7869.16992	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.569999993	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-941.200012	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.569999993	
AbsHwPos_HwPosSource_Cnt_M_u16	528	
AbsHwPos_HwPosState_Cnt_M_enum	5	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-409.299988	
AbsHwPos_RelHwPos_HwDeg_M_f32	-409.299988	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25809	

2017-02-06, 14:29:50+0530





Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006	
AbsHwPos VehCntrOffset HwDeg M f32	2802.53003		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.560000002		
k_HwPosOutputLPFError_HwDeg_f32	110		
k I2CHwAuthority Uls f32	0.560000002		
k MaxSensorlessAuthority Uls f32	0.649999976		
k MaxVehCntrOffDiff HwDeg f32	258.5		
k_MinSensorlessAuthority_Uls_f32	0.560000002		
k TurnsCntrAuthority Uls f32	0.560000002		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	110		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.649999976		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1205.59998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	52		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-783.599976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f		target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	2 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1000000000000000000000000000000000000$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	•
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	952.06073	952.06073 ± 0.1	٠,
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-
AbsHwPos PrevHandwheelPosition HwDeg M f32	952.06073	952.06073 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
AbsHwPos TempHwPos HwDeg M f32	2380.31006	2380.31006 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	2802.53003	2802.53003 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbshwPos_venCntrOisiteam_Cnt_M_igc AbshwPos_VenCntrValid Cnt_M_igc	0	0	
	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	-	-	

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	-

0

0

0.00999999046

952.06073

-783.599976

65534

65535

0

65534

65535

0 ± 0.1

0.00999999978 ± 0.004

-783.599976 ± 0.0009

952.06073 ± 0.1

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



Test Step 2.59 (Repeat Count = 1)	Innut Volus		
Name Abelly Dec Aligned Bell by Dec Lly Dec M #22	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.579999983		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-921.5 0.579999983		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32			
AbsHwPos_HwPosSource_Cnt_M_u16	536		
AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_ProvHendwhoolPosition_HwPog_M_f22	213		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213		
AbsHwPos_RelHwPos_HwDeg_M_f32	25881		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos TargetHwAuthority Uls M f32	0.579999983		
AbsHwPos TempHwPos HwDeq M f32	2521.05005		
AbsHwPos VehCntrOffset HwDeg M f32	2943.27002		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		VehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	venonii onset_oetEnorotatas_Enorotatas	
KIC_ITISE_AP_ADST WIT 03 KIC_ITISE_AP_ADST WIT 03 KIC_ITISE_AP_ADST WIT 03	0.569999993		
<pre>C_INM GSAdmontyGttp_Gis_id2</pre> <pre>C_HwPosOutputLPFError_HwDeg_f32</pre>	139.699997		
C_nwrosoutputtretroi_nwbeg_is2	0.569999993		
K_MaxSensorlessAuthority_Uls_f32	0.660000026		
C. MaxVehCntrOffDiff HwDeg f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.569999993		
C_IVIIII/OETISOTIESSAUTIONITY_OIS_132 K_TurnsCntrAuthority_UIs_f32	0.569999993		
<_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	139.699997		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.660000026		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1225.69995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	56		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-763.900024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7067		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStat			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedI		wPosReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSourc		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igo			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f3		· -	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg		·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrVal		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target Pim EOLVehCntrOffset		
	·	Expected Value	
Namo			Docul
	Actual Value	•	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32	213 0.579999983	213 ± 0.0009 0.579999983 ± 0.004	•
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	213 0.579999983 1075.17896	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1	•
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	213 0.579999983 1075.17896 536	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536	•
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID	•
AbshwPos_AlignedHwPos_HwDeg_M_f32 AbshwPos_HandwheelAuthority_Uls_M_f32 AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1	•
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881	
AbshwPos_AlignedHwPos_HwDeg_M_f32 AbshwPos_HandwheelAuthority_Uls_M_f32 AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32 AbshwPos_SrlComHwPosStatus_Cnt_M_u16 AbshwPos_SrlComHwPosStatus_Cnt_M_u16 AbshwPos_SrlComHwPosStatus_Cnt_M_u16	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1	,
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1	,
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009	,
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_earn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504 0	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 *none* *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504 0	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 *none* *none* *none*	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) larget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504 0 0 0.579999983	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 *none* *none* *none* 0.579999983 ± 0.004	
Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 2581 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504 0 0 0.579999983 1075.17896	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 0 *none* *none* *none* 0.579999983 ± 0.004 1075.17896 ± 0.1	
AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) larget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	213 0.579999983 1075.17896 536 HWPOS_STATE_INPUTINVALID 1075.17896 25881 -6321.02979 0.579999983 2521.05005 2943.27002 0 0 504 0 0 0.579999983	213 ± 0.0009 0.579999983 ± 0.004 1075.17896 ± 0.1 536 HWPOS_STATE_INPUTINVALID 1075.17896 ± 0.1 25881 -6321.02979 ± 0.1 0.579999983 ± 0.004 2521.05005 ± 0.1 2943.27002 ± 0.009 0 *none* *none* *none* 0.579999983 ± 0.004	Resul

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target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

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AbsHwPos_Per2



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AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001	-1084.01001 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т			✓	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.61 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.60000024
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.60000024
AbsHwPos_HwPosSource_Cnt_M_u16	7552
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26025
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
<_HwPosAuthorityStep_Uls_f32	0.589999974
C_HwPosOutputLPFError_HwDeg_f32	199.100006
C I2CHwAuthority Uls f32	0.589999974
MaxSensorlessAuthority Uls f32	0.680000007
MaxVehCntrOffDiff HwDeg f32	347.600006
MinSensorlessAuthority Uls f32	0.589999974
C TurnsCntrAuthority Uls f32	0.589999974
C UseTurnsCntr Cnt Igc	0
 VehCntrOffValidLimit HwDeg f32 	199.100006
rarget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.680000007
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1265.90002
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	64
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-724.5
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	7315
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	
arget_Rte_inst_Ap_AbshwFos.AbshwFos_Fer2_handwireerFosition_hwbeg_isz arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt u16
arget_Rte_Inst_Ap_AbshwPos.AbshwPos_Per2_nwPosSource_Crit_u16 arget_Rte_Inst_Ap_AbshwPos.AbshwPos Per2_I2ChwAbsPosValid_Cnt_Igc	target AbshwPos Per2 I2CHwAbsPosValid Cnt Igc
arget_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_i2CHwAbsPosValid_Cnt_igc arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_i2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_ManufMode_Cnt_enum target AbsHwPos Per2 SensorlessAuthority Uls f32
	7 7
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75	3224.75 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

au				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.62 (Papert Count = 1)	<u> </u>
Test Step 2.62 (Repeat Count = 1) Name	Input Value
	0
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	0.610000014
	-488.100006
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.610000014 560
AbsHwPos_HwPosSource_Cnt_M_u16	
AbsHwPos_HwPosState_Cnt_M_enum	9 140.300003
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	
AbsHwPos_RelHwPos_HwDeg_M_f32	140.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26097
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.610000014
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.600000024
k_HwPosOutputLPFError_HwDeg_f32	228.800003
k_I2CHwAuthority_Uls_f32	0.600000024
k_MaxSensorlessAuthority_Uls_f32	0.68999998
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988
k_MinSensorlessAuthority_Uls_f32	0.600000024
k_TurnsCntrAuthority_UIs_f32	0.600000024
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	228.800003
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	200.600006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.68999998
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1286

2017-02-06, 14:29:50+0530





ADSTWPOS_Per2			TIMO
Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1872.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7439		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_AbsHwPosReduced$	_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	2 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1605.03577	1605.03564 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1605.03577	1605.03564 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3365.48999	3365.48999 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
	05504	05504	

T				
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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

65534

65535

65535

65534

65535

0 ± 0.1

65535 ± 0.09

Test Step 2.63 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8572.87012
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	765.799988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.620000005
AbsHwPos_HwPosSource_Cnt_M_u16	568
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	160.399994
AbsHwPos_RelHwPos_HwDeg_M_f32	160.399994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26169
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.620000005
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1506.22998
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.610000014

 $target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value$

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$





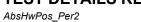
Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	258.5		
k_I2CHwAuthority_Uls_f32	0.610000014		
k_MaxSensorlessAuthority_Uls_f32	0.69999988		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_Uls_f32	0.610000014		
k_TurnsCntrAuthority_UIs_f32	0.610000014		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	258.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.69999988		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1306.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	72		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1902.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7563		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Order = 0.0000000000000000000000000000000000$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005	0.620000005 ± 0.004	•

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.620000005	0.620000005 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1621.08228	-1621.08215 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	568	568	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1621.08228	-1621.08215 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26169	26169	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.620000005	0.620000005 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1506.22998	-1506.22998 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.620000005	0.620000005 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	568	568	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26169	26169	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	✓
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

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Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.64 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-8713.61035	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.629999995	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	912.900024	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.629999995	
AbsHwPos_HwPosSource_Cnt_M_u16	576	

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Name	Input Value		
AbsHwPos_HwPosState_Cnt_M_enum	11		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	180.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	180.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26241		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7024.72998		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.629999995		
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	0-1-0#1 0-1	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh	Chtromset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.620000005		
k_HwPosOutputLPFError_HwDeg_f32	288.200012 0.620000005		
k_I2CHwAuthority_Uls_f32 k_MaxSensorlessAuthority_Uls_f32	0.709999979		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.620000005		
	0.620000005		
k_TurnsCntrAuthority_UIs_f32 k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	288.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_l2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.709999979		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1326.19995		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	76		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1932.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	7687		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	(target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	prity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	7		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_h	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosSt	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_I	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	/_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	180.5	180.5 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2369.36572	2369.36548 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2369.36572	2369.36548 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3646.96997	3646.96997 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	•		-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	504 0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	504 0 0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	504 0 0 0 0.00999999046	0 0 0.00999999978 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	504 0 0 0.00999999046 1600	0 0 0.00999999978 ± 0.004 1600 ± 0.1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	504 0 0 0.00999999046 1600 65534	0 0 0.00999999978 ± 0.004 1600 ± 0.1 65534	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	504 0 0 0.00999999046 1600 65534 65535	0 0 0.00999999978 ± 0.004 1600 ± 0.1 65534 65535	0
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	504 0 0 0.00999999046 1600 65534	0 0 0.00999999978 ± 0.004 1600 ± 0.1 65534	•



T	ullet			
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.65 (Repeat Count = 1)				
Name	Input Value			
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9558.0498			
AbsHwPos_HandwheelAuthority_Uls_M_f32	0			
.bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-429			
hbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.68999998			
.bsHwPos_HwPosSource_Cnt_M_u16		624		
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID			
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006			
lbsHwPos_RelHwPos_HwDeg_M_f32	301.100006			
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	26313			
bsHwPos_SrlComHwPos_HwDeg_M_f32	1889.09998			
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.68999998			
.bsHwPos_TempHwPos_HwDeg_M_f32	4069.18994			
.bsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016			
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1			
.bsHwPos_VehCntrValid_Cnt_M_lgc	1			
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVer	nCntrOffset_GetErrorStatus_ErrorStatus		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos			
_HwPosAuthorityStep_Uls_f32	0.680000007			
_HwPosOutputLPFError_HwDeg_f32	466.399994			
_I2CHwAuthority_Uls_f32	0.680000007			
_MaxSensorlessAuthority_Uls_f32	0.769999981			
_MaxVehCntrOffDiff_HwDeg_f32	614.900024			
_MinSensorlessAuthority_Uls_f32	0.680000007			
_TurnsCntrAuthority_Uls_f32	0.680000007			
_UseTurnsCntr_Cnt_lgc	1			
_VehCntrOffValidLimit_HwDeg_f32	466.399994			
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1			
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1			
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994			
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1			
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.769999981			
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1446.80005			
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100			
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995			
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8431			
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32				
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	'alid_Cnt_lgc		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwPos HwDeg f32			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	status_Cnt_u16		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	ty_Cnt_u08		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Resu	
bsHwPos_AlignedHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.0009		
bsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004		
bsHwPos HandwheelPositionLPF M str.SV Uls f32	2674.75098	2674.75122 ± 0.1		
bsHwPos HwPosSource Cnt M u16	65534	65534		
bsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INPUTINVALID	HWPOS_STATE_INPUTINVALID		
ubsHwPos PrevHandwheelPosition HwDeg M f32	2674.75098	2674.75122 ± 0.1		
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535		
	0	0 + 0 1		
AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.1 0 ± 0.004		

4069.18994

AbsHwPos_TempHwPos_HwDeg_M_f32

4069.18994 ± 0.1

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Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4491.41016	4491.41016 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2113.69995	2113.69995 ± 0.009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

Test Step 2.66 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9698.79004
AbsHwPos_HandwheelAuthority_Uls_M_f32	1
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	147.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	632
AbsHwPos_HwPosState_Cnt_M_enum	5
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	321.26001
bsHwPos_RelHwPos_HwDeg_M_f32	3421.26001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26385
.bsHwPos_SrlComHwPos_HwDeg_M_f32	909.380005
AbsHwPos TargetHwAuthority Uls M f32	0.69999988
bsHwPos_TempHwPos_HwDeg_M_f32	4209.93018
.bsHwPos VehCntrOffset HwDeg M f32	4632.1499
ubsHwPos VehCntrOfstLearn Cnt M Igc	0
.bsHwPos VehCntrValid Cnt M Igc	0
tte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
tte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.689999998
_HwPosOutputLPFError_HwDeg_f32	496.100006
I2CHwAuthority Uls f32	0.689999998
MaxSensorlessAuthority Uls f32	0.779999971
MaxVehCntrOffDiff HwDeg f32	644.599976
_MinSensorlessAuthority_UIs_f32	0.68999998
TurnsCntrAuthority Uls f32	0.68999998
UseTurnsCntr Cnt Igc	0
VehCntrOffValidLimit HwDeg f32	496.100006
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.779999971
·	1466.90002
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	104
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2143.8999 8555
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1111
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
irget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32

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Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	3421.26001	3421.26001 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2991.14111	2991.14111 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	632	632	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2991.14111	2991.14111 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26385	26385	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4209.93018	4209.93018 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4632.1499	4632.1499 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.69999988	0.699999988 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	632	632	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26385	26385	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	909.380005	909.380005 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2143.8999	2143.8999 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

N	Invest Value
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9839.53027
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.709999979
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	156.899994
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.709999979
AbsHwPos_HwPosSource_Cnt_M_u16	640
AbsHwPos_HwPosState_Cnt_M_enum	6
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	341.540009
AbsHwPos_RelHwPos_HwDeg_M_f32	341.540009
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26457
AbsHwPos_SrlComHwPos_HwDeg_M_f32	929.659973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.709999979
AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	0.69999988
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	525.799988
c_I2CHwAuthority_Uls_f32	0.69999988
<pre><_MaxSensorlessAuthority_Uls_f32</pre>	0.790000021
x_MaxVehCntrOffDiff_HwDeg_f32	674.299988
K_MinSensorlessAuthority_Uls_f32	0.69999988
x_TurnsCntrAuthority_UIs_f32	0.69999988
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	1
<pre> C</pre>	525.799988
rarget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
rarget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.790000021
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1487
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	108
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2174.1001
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	8679
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4



AbsHwPos_VehCntrValid_Cnt_M_lgc

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) \\ Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) \\$

 $target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value$

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	341.540009	341.540009 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3043.47461	-3043.47461 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-3043.47461	-3043.47461 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-4350.66992	-4350.66992 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1572.89001	-1572.89001 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

504

-1600 65534

65535

65535

0

0.00999999046

504

0.00999999978 ± 0.004

-1600 ± 0.1

65535 ± 0.09

65534

65535

0 ± 0.1

Test Step 2.68 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9980.26953
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.720000029
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	166.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.720000029
AbsHwPos_HwPosSource_Cnt_M_u16	1
AbsHwPos_HwPosState_Cnt_M_enum	7
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	361.820007
AbsHwPos_RelHwPos_HwDeg_M_f32	361.820007
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26529
AbsHwPos_SrlComHwPos_HwDeg_M_f32	949.940002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.720000029
AbsHwPos_TempHwPos_HwDeg_M_f32	4491.41016
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4913.62988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.709999979
k_HwPosOutputLPFError_HwDeg_f32	555.5
k_I2CHwAuthority_Uls_f32	0.709999979
k_MaxSensorlessAuthority_Uls_f32	0.800000012
k_MaxVehCntrOffDiff_HwDeg_f32	704

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AbsHwPos Per2

ADSHWPOS_Per2		I WIE	CIGO
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.709999979		
k_TurnsCntrAuthority_UIs_f32	0.709999979		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	555.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	522.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.800000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1507.09998		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	112		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2204.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8803		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	361.820007	361.820007 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.720000029	0.720000029 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3280.43555	3280.4353 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3280.43555	3280.4353 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26529	26529	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.720000029	0.720000029 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4491.41016	4491.41016 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4913.62988	4913.62988 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

504

1600

26529

949.940002

2204.30005

0.720000029

none

none

none

26529

1600 ± 0.1

0.720000029 ± 0.004

949.940002 ± 0.1

2204.30005 ± 0.009

Test Step 2.69 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9874.30957	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.730000019	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	176.100006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.730000019	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	382.100006	
AbsHwPos_RelHwPos_HwDeg_M_f32	382.100006	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26601	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	970.219971	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.730000019	

AbsHwPos_VehCntrValid_Cnt_M_lgc

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

2017-02-06, 14:29:50+0530



AbsHwPos Per2

AbsHwPos_Per2		MACH	al
Name	Input Value		
AbsHwPos TempHwPos HwDeg M f32	4632.1499		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5054.37012		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	_ <u>-</u>	
k HwPosAuthorityStep Uls f32	0.720000029		
k_HwPosOutputLPFError_HwDeg_f32	585.200012		
k_I2CHwAuthority_Uls_f32	0.720000029		
k_MaxSensorlessAuthority_Uls_f32	0.810000002		
k_MaxVehCntrOffDiff_HwDeg_f32	733.700012		
k_MinSensorlessAuthority_Uls_f32	0.720000029		
k_TurnsCntrAuthority_UIs_f32	0.720000029		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	585.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	542.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.810000002		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1527.19995		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	116		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2234.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	8927		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	382.100006	382.100006 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.0099999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	3429.0166	3429.0166 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3429.0166	3429.0166 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4632.1499	4632.1499 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5054.37012	5054.37012 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~

AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3429.0166	3429.0166 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3429.0166	3429.0166 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4632.1499	4632.1499 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5054.37012	5054.37012 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•





Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9768.34961		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.74000001		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	185.699997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.74000001		
AbsHwPos_HwPosSource_Cnt_M_u16	1024		
AbsHwPos_HwPosState_Cnt_M_enum	9		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	402.380005		
AbsHwPos_RelHwPos_HwDeg_M_f32	402.380005		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26673		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-990.5		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.74000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.730000019		
k_HwPosOutputLPFError_HwDeg_f32	614.900024		
k_I2CHwAuthority_Uls_f32	0.730000019		
k_MaxSensorlessAuthority_Uls_f32	0.819999993		
k_MaxVehCntrOffDiff_HwDeg_f32	763.400024		
k_MinSensorlessAuthority_Uls_f32	0.730000019		
k_TurnsCntrAuthority_Uls_f32	0.730000019		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	614.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-330.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.819999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1547.30005		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2264.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9051		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		PosReducedPerf Cnt lac	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3:			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAut		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	any_oni_doo	
Name	Actual Value	Expected Value	Beau
		Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	402.380005	402.380005 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.74000001	0.74000001 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-3483.65698	-3483.65649 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1024	1024	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-3483.65698	-3483.65649 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26673	26673	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.7400001	0.74000001 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	-4772.89014	-4772.89014 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	5195.10986	5195.10986 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.74000001	0.74000001 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1024	1024	
	26673	26673	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	20073		





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2264.69995	2264.69995 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.71 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedRelHwPos HwDeg M f32	9662.38965		
AbsHwPos HandwheelAuthority Uls M f32	0.75		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	195.300003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75		
AbsHwPos HwPosSource Cnt M u16	1236		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	422.660004		
AbsHwPos_RelHwPos_HwDeg_M_f32	422.660004		
AbsHwPos SrlComHwPosStatus Cnt M u16	26745		
AbsHwPos SrlComHwPos HwDeg M f32	1010.78003		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.75		
AbsHwPos TempHwPos HwDeq M f32	4913.62988		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCni	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos		
k_HwPosAuthorityStep_UIs_f32	0.74000001		
k HwPosOutputLPFError HwDeg f32	644.599976		
k I2CHwAuthority Uls f32	0.74000001		
k MaxSensorlessAuthority Uls f32	0.829999983		
k_MaxVehCntrOffDiff_HwDeg_f32	793.099976		
k MinSensorlessAuthority Uls f32	0.74000001		
k_TurnsCntrAuthority_UIs_f32	0.74000001		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	644.599976		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-310.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.82999983		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1567.40002		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	124		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2294.8999		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9175		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (educedParf Cnt Inc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	target AbsHwPos Per2 HandwheelPosition		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_ei		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuthority		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target AbsHwPos Per2 SensorlessHwPos		
target_Rte_Inst_Ap_AbshwPos.AbshwPos_Per2_SrlComHwPosStatus_Cnt_u16	target AbsHwPos Per2 SrlComHwPosStatu	·	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos Hwl		
target_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	in_uoo	
		Evacated Value	Docult
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	422.660004	422.660004 ± 0.0009	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	422.660004	422.660004 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75	0.75 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3734.04761	3734.04761 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1236	1236	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3734.04761	3734.04761 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75	0.75 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988	4913.62988 ± 0.1	~

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AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965	8284.88965 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75	0.75 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1236	1236	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2294.8999	2294.8999 ± 0.009	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

lame	Input Value
	9556.42969
lbsHwPos_AlignedRelHwPos_HwDeg_M_f32	
lbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999
bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	204.899994
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75999999
.bsHwPos_HwPosSource_Cnt_M_u16	1348
lbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	442.940002
lbsHwPos_RelHwPos_HwDeg_M_f32	442.940002
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817
.bsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999
.bsHwPos_TempHwPos_HwDeg_M_f32	5054.37012
.bsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.75
_HwPosOutputLPFError_HwDeg_f32	674.299988
_I2CHwAuthority_Uls_f32	0.75
_MaxSensorlessAuthority_Uls_f32	0.83999974
_MaxVehCntrOffDiff_HwDeg_f32	822.799988
_MinSensorlessAuthority_Uls_f32	0.75
_TurnsCntrAuthority_Uls_f32	0.75
_UseTurnsCntr_Cnt_lgc	0
VehCntrOffValidLimit HwDeg f32	674.299988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.839999974
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1587.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	128
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2325.1001
arget_Pim_EOLVerichtionset.EOLVerichtionset_Hwbeg_isz	9299
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	
arget_Rte_Call_Ap_AbsHwPos_EOEvenChilOnset_GetEnbi3tatus_Enbi3tatus arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget_kte_inst_Ap_absHwPos.AbsHwPos_Per2_HandwheelAuthority_Uis_132 arget_kte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_t	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_srlComHwPosStatus_Cnt_u1 arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_srlComHwPos_HwDeg_f32	6 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	123.400002	123.400002 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	~

T ·						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~		
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•		
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-		

Test Step 2.73 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9450.46973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	214.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.769999981
AbsHwPos_HwPosSource_Cnt_M_u16	1460
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	463.220001
AbsHwPos_RelHwPos_HwDeg_M_f32	463.220001
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26889
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.76999981
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.75999999
<hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/> <hr/>	704
C_I2CHwAuthority_Uls_f32	0.75999999
<pre><_MaxSensorlessAuthority_Uls_f32</pre>	0.850000024
<pre><_MaxVehCntrOffDiff_HwDeg_f32</pre>	852.5
_MinSensorlessAuthority_Uls_f32	0.75999999
<_TurnsCntrAuthority_Uls_f32	0.75999999
<_UseTurnsCntr_Cnt_lgc	1
<_VehCntrOffValidLimit_HwDeg_f32	704
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	132
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value\\ target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	(target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	463.220001	463.220001 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4049.56958	4049.56958 ± 0.1	•	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4049.56958	4049.56958 ± 0.1	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	✓	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	5195.10986	5195.10986 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1072.96997	-1072.96997 ± 0.009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~	
Abolto Dee Deed Heady beeld of the 111 - 600 color	0.00000000000	0.00000000070 + 0.004		

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

0.00999999046

1600

65534

65534

65535

-248.300003

0.00999999978 ± 0.004

-248.300003 ± 0.1

65535 ± 0.09

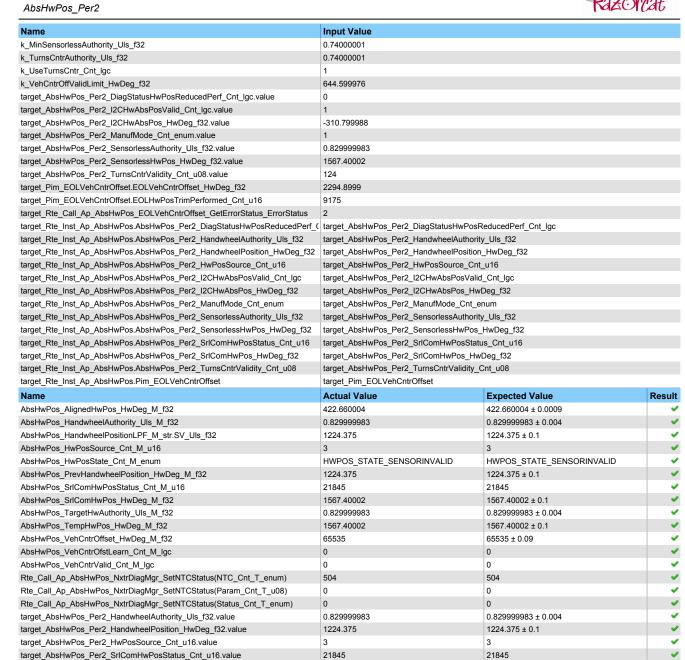
1600 ± 0.1

65534

Test Step 2.74 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9662.38965
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	195.300003
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.75
AbsHwPos_HwPosSource_Cnt_M_u16	1236
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	422.660004
AbsHwPos_RelHwPos_HwDeg_M_f32	422.660004
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26745
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1010.78003
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75
AbsHwPos_TempHwPos_HwDeg_M_f32	4913.62988
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8284.88965
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.74000001
k_HwPosOutputLPFError_HwDeg_f32	644.599976
k_I2CHwAuthority_Uls_f32	0.74000001
k_MaxSensorlessAuthority_Uls_f32	0.829999983
k_MaxVehCntrOffDiff_HwDeg_f32	793.099976

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Т					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

1567.40002

2294 8999

1567.40002 ± 0.1

2294 8999 + 0 009

Test Step 2.75 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9344.5	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	224.100006	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.779999971	
AbsHwPos_HwPosSource_Cnt_M_u16	1572	
AbsHwPos_HwPosState_Cnt_M_enum	13	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11725	
AbsHwPos_RelHwPos_HwDeg_M_f32	-600	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26961	

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32

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AbsHwPos_Per2

		(Signal	
Name	Input Value		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.779999971		
AbsHwPos_TempHwPos_HwDeg_M_f32	-5335.8501		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVeh0	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.769999981		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.769999981		
k_MaxSensorlessAuthority_Uls_f32	0.860000014		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.769999981		
k_TurnsCntrAuthority_Uls_f32	0.769999981		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	733.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	136		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_AbsHwPosReducedPerf_Inst_AbsHwPosReducedPerf_Inst_AbsHwPosReducedPerf_Inst$	(target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-600	-600 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4112.66113	-4112.66113 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4112.66113	-4112.66113 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26961	26961	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.779999971	0.779999971 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	-5335.8501	-5335.8501 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7967.00977	7967.00977 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	,
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.779999971	0.779999971 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	

Т					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~	

26961

1395.13

2385.5

26961

1395.13 ± 0.1

2385.5 ± 0.009

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



Test Step 2.76 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9238.54004		
AbsHwPos HandwheelAuthority Uls M f32	0.790000021		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-389.600006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.790000021		
AbsHwPos_HwPosSource_Cnt_M_u16	1684		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725		
AbsHwPos_RelHwPos_HwDeg_M_f32	6008		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27033		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1535.87		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.790000021		
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984		
AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	7861.0498 1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	cholicoloci_cottinolotatao_tholotatao	
K HwPosAuthorityStep Uls f32	0.779999971		
k_HwPosOutputLPFError_HwDeg_f32	763.400024		
C_I2CHwAuthority_Uls_f32	0.779999971		
<pre>x_MaxSensorlessAuthority_Uls_f32</pre>	0.870000005		
<pre><_MaxVehCntrOffDiff_HwDeg_f32</pre>	911.900024		
<_MinSensorlessAuthority_Uls_f32	0.779999971		
C_TurnsCntrAuthority_Uls_f32	0.779999971		
<_UseTurnsCntr_Cnt_lgc	1		
<_VehCntrOffValidLimit_HwDeg_f32	763.400024		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	-232		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.870000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	thority_UIs_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_I2CHwAbsPos target AbsHwPos Per2 ManufMode C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAut	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	6008	6008 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	4244.68994	4244.68994 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	4244.68994	4244.68994 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	5476.58984 7861.0408	5476.58984 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7861.0498 1	7861.0498 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AhsHwPos VehCntrValid Cnt M Ido	•	504	
	504		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) carget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1	
AbsHwPos_VehCntrValid_Cnt_M_igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 1 0.0100000501	1 1 0.00999999978 ± 0.004	

65535

0

65535

0 ± 0.1

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.77 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-9132.58008		
AbsHwPos HandwheelAuthority Uls M f32	0.800000012		
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	243.300003		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.800000012		
AbsHwPos_HwPosSource_Cnt_M_u16	1796		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID		
AbsHwPos PrevHandwheelPosition HwDeg M f32	0		
AbsHwPos_RelHwPos_HwDeg_M_f32	7150		
AbsHwPos SrlComHwPosStatus Cnt M u16	27105		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999		
AbsHwPos TargetHwAuthority Uls M f32	0.800000012		
AbsHwPos TempHwPos HwDeg M f32	5617.33008		
AbsHwPos VehCntrOffset HwDeg M f32	-1455.08997		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCnt	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	a onoci_ociEnorolalas_Enorolalas	
k HwPosAuthorityStep Uls f32	0.790000021		
k HwPosOutputLPFError HwDeg f32	793.099976		
k I2CHwAuthority Uls f32	0.790000021		
k_MaxSensorlessAuthority_Uls_f32	0.879999995		
k MaxVehCntrOffDiff HwDeg f32	941.599976		
k MinSensorlessAuthority Uls f32	0.790000021		
k_TurnsCntrAuthority_UIs_f32	0.790000021		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	793.099976		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.879999995		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-941.200012		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	144		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2445.8999		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9795		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	7150	7150 ± 0.0009	~

0.800000012

HWPOS_STATE_SENSORINVALID

4542.52393

4542.52393

65534

 $AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32$

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

 $AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32$

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum

0.800000012 ± 0.004

HWPOS_STATE_SENSORINVALID

4542.52393 ± 0.1

4542.52393 ± 0.1

AbsHwPos_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1676.60999	-1676.60999 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.800000012	0.800000012 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	5617.33008	5617.33008 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1455.08997	-1455.08997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.800000012	0.800000012 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1796	1796	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2445.8999	2445.8999 ± 0.009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

lame	Input Value
	9026.62012
bsHwPos_AlignedRelHwPos_HwDeg_M_f32	0.810000002
bsHwPos_HandwheelAuthority_Uls_M_f32	
bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	252.899994
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.810000002
.bsHwPos_HwPosSource_Cnt_M_u16	1908
.bsHwPos_HwPosState_Cnt_M_enum	3
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	544.340027
.bsHwPos_RelHwPos_HwDeg_M_f32	544.340027
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	27177
.bsHwPos_SrlComHwPos_HwDeg_M_f32	1817.34998
bsHwPos_TargetHwAuthority_Uls_M_f32	0.810000002
.bsHwPos_TempHwPos_HwDeg_M_f32	-5758.06982
bsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	1
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.800000012
_HwPosOutputLPFError_HwDeg_f32	822.799988
_I2CHwAuthority_Uls_f32	0.800000012
_MaxSensorlessAuthority_Uls_f32	0.889999986
_MaxVehCntrOffDiff_HwDeg_f32	971.299988
_MinSensorlessAuthority_Uls_f32	0.800000012
_TurnsCntrAuthority_Uls_f32	0.800000012
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	822.799988
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-192.600006
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.889999986
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-921.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	148
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2476.1001
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9919
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	2 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08			
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	544.340027	544.340027 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-4615.98535	-4615.98584 ± 0.1	✓	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-4615.98535	-4615.98584 ± 0.1	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•	
AbsHwPos_TempHwPos_HwDeg_M_f32	-5758.06982	-5758.06982 ± 0.1	•	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7649.14014	7649.14014 ± 0.009	•	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2476.1001	2476.1001 ± 0.009	✓	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.79 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8920.66016
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.819999993
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	262.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.819999993
AbsHwPos_HwPosSource_Cnt_M_u16	2020
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-564.619995
AbsHwPos_RelHwPos_HwDeg_M_f32	-564.619995
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27249
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.819999993
AbsHwPos_TempHwPos_HwDeg_M_f32	5898.81006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.810000002
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_UIs_f32	0.810000002
k_MaxSensorlessAuthority_Uls_f32	0.89999976
k_MaxVehCntrOffDiff_HwDeg_f32	1001
k_MinSensorlessAuthority_Uls_f32	0.810000002
k_TurnsCntrAuthority_UIs_f32	0.810000002
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	852.5
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-172.899994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.89999976
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-901.799988
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	152

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2506.30005		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10043		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value Expected Value		Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	-901.799988 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~

target_rite_inst_Ap_Absrivir os.i ini_EoEvenontronset	target_1 iii_LOEVerioniiiOliset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-564.619995	-564.619995 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	0.899999976 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-901.799988	-901.799988 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	✓
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.899999976	0.89999976 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018	7543.18018 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.899999976	0.89999976 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-901.799988	-901.799988 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2506.30005	2506.30005 ± 0.009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

Test Step 2.80 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8814.7002
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.829999983
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	272.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.829999983
AbsHwPos_HwPosSource_Cnt_M_u16	2132
AbsHwPos_HwPosState_Cnt_M_enum	5
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	584.900024
AbsHwPos_RelHwPos_HwDeg_M_f32	-10125
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27321
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.829999983
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.819999993
k_HwPosOutputLPFError_HwDeg_f32	882.200012
k_I2CHwAuthority_Uls_f32	0.81999993
k_MaxSensorlessAuthority_Uls_f32	0.910000026
k_MaxVehCntrOffDiff_HwDeg_f32	1030.69995





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Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.819999993		
k_TurnsCntrAuthority_Uls_f32	0.819999993		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	882.200012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.910000026		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-882.099976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	156		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2536.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10167		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_v	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5059.0835	5059.0835 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5059.0835	5059.0835 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498	6039.5498 ± 0.1	~
AbolityDee Veh Catroffeet His Dee M 522	1407.01007	1427 24007 + 0 000	

AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5059.0835	5059.0835 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6039.5498	6039.5498 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1437.21997	-1437.21997 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.81 (Repeat Count = 1)		<u> </u>
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-8708.74023	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.839999974	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281.700012	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.839999974	
AbsHwPos_HwPosSource_Cnt_M_u16	2244	
AbsHwPos_HwPosState_Cnt_M_enum	6	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	605.179993	
AbsHwPos_RelHwPos_HwDeg_M_f32	10125	



AbsHwPos_Per2	06, 14:29:50+0530	RAZO	Orcat
Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27393		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2239.57007		
AbsHwPos TargetHwAuthority Uls M f32	0.839999974		
AbsHwPos TempHwPos HwDeg M f32	6180.29004		
AbsHwPos VehCntrOffset HwDeg M f32	7331.25977		
AbsHwPos VehCntrOfstLearn Cnt M Igc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	ehCntrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target Rte Inst Ap AbsHwPos		
k HwPosAuthorityStep Uls f32	0.829999983		
k_HwPosOutputLPFError_HwDeg_f32	911.900024		
k_I2CHwAuthority_Uls_f32	0.829999983		
k_MaxSensorlessAuthority_Uls_f32	0.920000017		
k MaxVehCntrOffDiff HwDeg f32	1060.40002		
k MinSensorlessAuthority Uls f32	0.829999983		
k TurnsCntrAuthority Uls f32	0.829999983		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	911.900024		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.920000017		
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-862.400024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160		
target_nbs1wFos_Fe12_1df1sc1id validity_cfit_ubd.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2566.69995		
target_Pim_EOLVenOntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10291		
target_rim_colvenontionset.Eoch words rimine informed_cit_uno target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
		Doo Dody ood Dorf Cat Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	·	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuti		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.839999974	0.839999974 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5236.51563	5236.51563 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2244	2244	✓
AbeHwPoe HwPoeState Cnt M enum	HWPOS STATE INPLITINIVALID	HWPOS STATE INPLITINIVALID	✓

- 31 - 11 - 12 - 12 - 13 - 14 - 15 - 15 - 15 - 15 - 15 - 15 - 15			
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.839999974	0.839999974 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5236.51563	5236.51563 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	2244	2244	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5236.51563	5236.51563 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27393	27393	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2239.57007	-2239.57007 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.839999974	0.839999974 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	6180.29004	6180.29004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7331.25977	7331.25977 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.839999974	0.839999974 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2244	2244	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27393	27393	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2566.69995	2566.69995 ± 0.009	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-



Test Step 2.82 (Repeat Count = 1)	Imput Value		
Name Abelly Dec Aligned Belly Dec Hydron M (9)	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 AbsHwPos HandwheelAuthority Uls M f32	8602.78027		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0.850000024 -350.200012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.850000024		
AbsHwPos_HwPosSource_Cnt_M_u16	2356		
AbsHwPos_HwPosState_Cnt_M_enum	7		
AbsHwPos PrevHandwheelPosition HwDeg M f32	625.460022		
AbsHwPos_RelHwPos_HwDeg_M_f32	0		
AbsHwPos SrlComHwPosStatus Cnt M u16	27465		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2380.31006		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.850000024		
AbsHwPos_TempHwPos_HwDeg_M_f32	-6321.02979		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7225.2998		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
	0.83999974		
C_HwPosOutputLPFError_HwDeg_f32	941.599976		
c_I2CHwAuthority_Uls_f32	0.839999974		
C_MaxSensorlessAuthority_Uls_f32	0.930000007		
C_MaxVehCntrOffDiff_HwDeg_f32	1090.09998		
<pre></pre>	0.839999974		
c_TurnsCntrAuthority_UIs_f32	0.839999974		
	1		
_VehCntrOffValidLimit_HwDeg_f32	941.599976		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.930000007		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-842.700012		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	164		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2596.8999		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10415		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	(target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	2 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	0	0 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-5425.40527	-5425.40527 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
		-5425.40527 ± 0.1	
	-5425.40527	-3423.40327 I U.I	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-5425.40527 65535	65535	
hbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 hbsHwPos_SrlComHwPosStatus_Cnt_M_u16			
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	65535	65535	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	65535 0	65535 0 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	65535 0 0	65535 0 ± 0.1 0 ± 0.004	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535 0 0 -6321.02979	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	65535 0 0 -6321.02979 7225.2998	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	65535 0 0 -6321.02979 7225.2998	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	65535 0 0 -6321.02979 7225.2998 1	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009 1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	65535 0 0 -6321.02979 7225.2998 1 0 504	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009 1 0 504	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	65535 0 0 -6321.02979 7225.2998 1 0 504 1	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009 1 0 504 1	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) arget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	65535 0 0 -6321.02979 7225.2998 1 0 504 1 1 0.0100000501	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009 1 0 504 1 1 0.00999999978 ± 0.004	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65535 0 0 -6321.02979 7225.2998 1 0 504 1	65535 0 ± 0.1 0 ± 0.004 -6321.02979 ± 0.1 7225.2998 ± 0.009 1 0 504 1	

65535

0

65535

0 ± 0.1

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

AbsHwPos_Per2

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Name	Actual Value	Expected Value	Result
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.83 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8496.82031		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.860000014		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	300.899994		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.860000014		
AbsHwPos_HwPosSource_Cnt_M_u16	2468		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	645.73999		
AbsHwPos_RelHwPos_HwDeg_M_f32	645.73999		
AbsHwPos SrlComHwPosStatus Cnt M u16	27537		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.860000014		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
		ntrOffset GetErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	IIII OII361_GEILITOTGIAIUS_ETTOTGIAIUS	
Rte_Inst_Ap_AbsHwPos k_HwPosAuthorityStep_Uls_f32	target_Rte_Inst_Ap_AbsHwPos 0.850000024		
k_HwPosOutputLPFError_HwDeg_f32	1060.40002		
k I2CHwAuthority Uls f32	0.850000024		
, – , – , –	0.93999998		
k_MaxSensorlessAuthority_Uls_f32	1119.80005		
k_MaxVehCntrOffDiff_HwDeg_f32 k MinSensorlessAuthority UIs f32	0.850000024		
k_TurnsCntrAuthority_UIs_f32 k_UseTurnsCntr_Cnt_lgc	0.850000024		
	971.299988		
k_VehCntrOffValidLimit_HwDeg_f32			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	522.200012		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0.93999998		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	-823		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	168		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	-488.100006		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32			
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10539		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		Deduced Devi Cot Jac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_UIs_f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDos_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1-	1-
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	645.73999	645.73999 ± 0.0009	,
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5599.24854	5599.24805 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	•
AbsHwPos PrevHandwheelPosition HwDea M f32	5599 24854	5599 24805 + 0 1	

5599.24854

65534

AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

5599.24805 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1119.32996	-1119.32996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-488.100006	-488.100006 ± 0.0009	~

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

lame	Input Value
NosHwPos AlignedRelHwPos HwDeg M f32	-8390.86035
AbsHwPos HandwheelAuthority Uls M f32	0.870000005
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	310.5
AbsHwPos HandwheelPositionLPF M str.K UIs f32	0.870000005
AbsHwPos HwPosSource Cnt M u16	2580
NosHwPos HwPosState Cnt M enum	9
NbsHwPos PrevHandwheelPosition HwDeg M f32	-666.02002
AbsHwPos_RelHwPos_HwDeg_M_f32	-666.02002
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	27609
ubsHwPos SrlComHwPos HwDeg M f32	-2661.79004
AbsHwPos TargetHwAuthority Uls M f32	0.870000005
bsHwPos TempHwPos HwDeg M f32	6602.50977
ubsHwPos VehCntrOffset HwDeg M f32	7013.37988
NosHwPos VehCntrOfstLearn Cnt M lqc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.860000014
_HwPosOutputLPFError_HwDeg_f32	1090.09998
I2CHwAuthority Uls f32	0.860000014
MaxSensorlessAuthority Uls f32	0.94999988
MaxVehCntrOffDiff HwDeg f32	1149.5
_MinSensorlessAuthority_Uls_f32	0.860000014
_TurnsCntrAuthority_Uls_f32	0.860000014
_UseTurnsCntr_Cnt_lgc	1
VehCntrOffValidLimit HwDeg f32	1001
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	542.299988
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.949999988
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-803,299988
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	172
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-468.399994
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	10663
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	
irget_rite_irist_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16



AbsHwPos_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-666.02002	-666.02002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5784.54834	5784.54883 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5784.54834	5784.54883 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	6602.50977	6602.50977 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7013.37988	7013.37988 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-468.399994	-468.399994 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.85 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8284.88965
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.87999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	320.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.87999995
AbsHwPos_HwPosSource_Cnt_M_u16	2692
AbsHwPos_HwPosState_Cnt_M_enum	10
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	686.299988
AbsHwPos_RelHwPos_HwDeg_M_f32	686.299988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27681
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.87999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6907.41992
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.870000005
k_HwPosOutputLPFError_HwDeg_f32	1119.80005
<_I2CHwAuthority_Uls_f32	0.870000005
_MaxSensorlessAuthority_Uls_f32	0.959999979
k_MaxVehCntrOffDiff_HwDeg_f32	1179.19995
k_MinSensorlessAuthority_Uls_f32	0.870000005
k_TurnsCntrAuthority_UIs_f32	0.870000005
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1030.69995
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	562.400024
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.95999979
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-783.599976
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	176

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

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Input Value

AbsHwPos_Per2

Name



-448.700012		
10787		
3		
target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_AbsHwPos_Per2_HandwheelAuthority	/_Uls_f32	
target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_AbsHwPos_Per2_I2CHwAbsPos_HwI	Deg_f32	
target_AbsHwPos_Per2_ManufMode_Cnt_el	num	
target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_AbsHwPos_Per2_SrlComHwPosStatu	ıs_Cnt_u16	
target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Pim_EOLVehCntrOffset		
Actual Value	Expected Value	Result
686.299988	686.299988 ± 0.0009	~
686.299988 0.879999995	686.299988 ± 0.0009 0.879999995 ± 0.004	*
		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
0.879999995	0.879999995 ± 0.004	-
0.879999995 -5895.64795	0.879999995 ± 0.004 -5895.64795 ± 0.1	~
0.879999995 -5895.64795 2692	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692	~
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1	· · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681	*
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1	*
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004	· · · · · · · · · · · · · · · · · · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1	· · · · · · · · · · · · · · · · · · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25 6907.41992	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1 6907.41992 ± 0.009	· · · · · · · · · · · · · · · · · · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25 6907.41992 0	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1 6907.41992 ± 0.009 0	· · · · · · · · · · · · · · · · · · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25 6907.41992 0	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1 6907.41992 ± 0.009 0	***************************************
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25 6907.41992 0 0 504	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1 6907.41992 ± 0.009 0 *none*	· · · · · · · · · · · · · · · · · · ·
0.879999995 -5895.64795 2692 HWPOS_STATE_INPUTINVALID -5895.64795 27681 -11725 0.879999995 -6743.25 6907.41992 0 0 504	0.879999995 ± 0.004 -5895.64795 ± 0.1 2692 HWPOS_STATE_INPUTINVALID -5895.64795 ± 0.1 27681 -11725 ± 0.1 0.879999995 ± 0.004 -6743.25 ± 0.1 6907.41992 ± 0.009 0 *none* *none*	· · · · · · · · · · · · · · · · · · ·
	a target_AbsHwPos_Per2_DiagStatusHwPosR target_AbsHwPos_Per2_HandwheelAuthority target_AbsHwPos_Per2_HandwheelPosition target_AbsHwPos_Per2_HwPosSource_Cnt_target_AbsHwPos_Per2_I2CHwAbsPosValid target_AbsHwPos_Per2_I2CHwAbsPos_Hwltarget_AbsHwPos_Per2_ManufMode_Cnt_etarget_AbsHwPos_Per2_SensorlessAuthority target_AbsHwPos_Per2_SensorlessAuthority target_AbsHwPos_Per2_SrlComHwPosStatutarget_AbsHwPos_Per2_SrlComHwPos_Hwtarget_AbsHwPos_Per2_TurnsCntrValidity_Carget_Pim_EOLVehCntrOffset	10787 3 target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_ManufMode_Cnt_enum target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Pim_EOLVehCntrOffset

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

-448.700012

2692

27681

-1600

-1600 ± 0.1

-448.700012 ± 0.0009

2692

Test Step 2.86 (Repeat Count = 1)	<u> </u>
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	8178.93018
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.889999986
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-507.799988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.889999986
AbsHwPos_HwPosSource_Cnt_M_u16	2804
AbsHwPos_HwPosState_Cnt_M_enum	11
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	706.580017
AbsHwPos_RelHwPos_HwDeg_M_f32	706.580017
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27753
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.889999986
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6801.45996
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.879999995
k_HwPosOutputLPFError_HwDeg_f32	1149.5
k_I2CHwAuthority_Uls_f32	0.879999995
k_MaxSensorlessAuthority_Uls_f32	0.970000029
k_MaxVehCntrOffDiff_HwDeg_f32	1208.90002

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Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.879999995		
k TurnsCntrAuthority Uls f32	0.879999995		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	1060.40002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	582.5		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.970000029		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-763.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	180		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-429		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	10911		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	706.580017	706.580017 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6070.89307	6070.89307 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	6070.89307	6070.89307 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6801.45996	6801.45996 ± 0.009	-
AL II D. WIGHT COLAR		4	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

0

504

1600

65534

65535

65535

0.00999999046

0

504

0.00999999978 ± 0.004

1600 ± 0.1

65535 ± 0.09

65534

65535

 0 ± 0.1

Test Step 2.87 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-8072.97021	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.899999976	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	389.200012	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.899999976	
AbsHwPos_HwPosSource_Cnt_M_u16	2916	
AbsHwPos_HwPosState_Cnt_M_enum	12	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-726.859985	
AbsHwPos_RelHwPos_HwDeg_M_f32	-726.859985	

AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

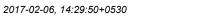
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

AbsHwPos_Per2





Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27825		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.89999976		
AbsHwPos_TempHwPos_HwDeg_M_f32	7024.72998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6695.5		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.889999986		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_Uls_f32	0.88999986		
k_MaxSensorlessAuthority_UIs_f32	0.980000019		
k_MaxVehCntrOffDiff_HwDeg_f32	1238.59998		
k_MinSensorlessAuthority_Uls_f32	0.88999986		
k_TurnsCntrAuthority_Uls_f32	0.889999986		
k_UseTurnsCntr_Cnt_lgc	0 1090.09998		
k_VehCntrOffValidLimit_HwDeg_f32	0		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.980000019		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-744.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	184		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-409.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11035		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf		PosReducedPerf Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3.			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	hority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3222222222222222222222222222222222222$	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-726.859985	-726.859985 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.89999976	0.899999976 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6361.17676	6361.17676 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2916	2916	*
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	6361.17676	6361.17676 ± 0.1	*
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27825	27825	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.89999976	0.899999976 ± 0.004 7024.72998 ± 0.1	-
AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	7024.72998 6695.5	7024.72996 ± 0.1 6695.5 ± 0.009	
AbsHwPos_VenCntrOfstLearn Cnt M lgc	0	0	
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	504	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	1	*none*	
	1	*none*	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)		0.899999976 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target AbsHwPos Per2 HandwheelAuthority Uls f32.value	0.899999976		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.899999976 1600	1600 ± 0.1	✓
	0.899999976 1600 2916	1600 ± 0.1 2916	· ·
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600		
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1600 2916	2916	

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.88 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7967.00977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.910000026		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-468.399994		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.910000026		
AbsHwPos_HwPosSource_Cnt_M_u16	3028		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	747.140015		
AbsHwPos_RelHwPos_HwDeg_M_f32	747.140015		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	27897		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.910000026		
AbsHwPos_TempHwPos_HwDeg_M_f32	7165.47021 -1589.54004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Call_Ap_Abshwros_EoLvenChiroliset_GetErrorStatus(ErrorStatus) Rte_Inst_Ap_Abshwros	target Rte Inst Ap AbsHwPos	enontroliset_GetErrorStatus_ErrorStatus	
k_HwPosAuthorityStep_Uls_f32	0.89999976		
k_nwPosAuthontyStep_ois_is2 k_HwPosOutputLPFError_HwDeg_f32	1208.90002		
k_I2CHwAuthority_Uls_f32	0.899999976		
k_MaxSensorlessAuthority_Uls_f32	0.9900001		
k_MaxVehCntrOffDiff_HwDeg_f32	1268.30005		
k_MinSensorlessAuthority_Uls_f32	0.899999976		
k TurnsCntrAuthority Uls f32	0.899999976		
k_UseTurnsCntr_Cnt_lgc	1		
k VehCntrOffValidLimit HwDeg f32	1119.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.99000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-724.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	188		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-389.600006		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11159		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	747.140015	747.140015 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.0100000501	0.00999999978 ± 0.004	
		6470 40400 + 0.4	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	6478.42188	6478.42188 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	65534 HWPOS_STATE_INPUTINVALID	65534 HWPOS_STATE_INPUTINVALID	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1	
AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32 AbshwPos_SrlComHwPosStatus_Cnt_M_u16	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535	
AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32 AbshwPos_SrlComHwPosStatus_Cnt_M_u16 AbshwPos_SrlComHwPos_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1	
AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32 AbshwPos_SrlComHwPosStatus_Cnt_M_u16 AbshwPos_SrlComHwPos_HwDeg_M_f32 AbshwPos_TargetHwAuthority_Uls_M_f32 AbshwPos_TempHwPos_HwDeg_M_f32 AbshwPos_TempHwPos_HwDeg_M_f32 AbshwPos_VehCntrOffset_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504 1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504 1 1 0.0100000501	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504 1 1 0.0100000501 1600	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 1600 ± 0.1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosSource_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504 1 1 0.0100000501 1600 65534	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 1600 ± 0.1 65534	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534 HWPOS_STATE_INPUTINVALID 6478.42188 65535 0 0 7165.47021 -1589.54004 1 0 504 1 1 0.0100000501 1600	65534 HWPOS_STATE_INPUTINVALID 6478.42188 ± 0.1 65535 0 ± 0.1 0 ± 0.004 7165.47021 ± 0.1 -1589.54004 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 1600 ± 0.1	

0

0 ± 0.1

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

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Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Name	Input Value		
AbsHwPos AlignedRelHwPos HwDeg M f32	7861.0498		
AbsHwPos HandwheelAuthority Uls M f32	0.920000017		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	784.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.920000017		
AbsHwPos HwPosSource Cnt M u16	3140		
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID		
AbsHwPos_revHandwheelPosition_HwDeg_M_f32	767.419983		
AbsHwPos RelHwPos HwDeg M f32	767.419983		
AbsHwPos SrlComHwPosStatus Cnt M u16	27969		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001		
	0.920000017		
AbsHwPos_TargetHwAuthority_UIs_M_f32			
AbsHwPos_TempHwPos_HwDeg_M_f32	-7306.20996		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6483.58008		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0, 10, 15, 01, 15	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	trOπset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.910000026		
<hr/> k_HwPosOutputLPFError_HwDeg_f32	1238.59998		
c_I2CHwAuthority_UIs_f32	0.910000026		
x_MaxSensorlessAuthority_Uls_f32	0.100000001		
x_MaxVehCntrOffDiff_HwDeg_f32	1298		
_MinSensorlessAuthority_Uls_f32	0.910000026		
c_TurnsCntrAuthority_Uls_f32	0.910000026		
c_UseTurnsCntr_Cnt_lgc	0		
<_VehCntrOffValidLimit_HwDeg_f32	1149.5		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	642.799988		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-704.799988		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	192		
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-369.899994		
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	11283		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPosR	educedPerf Cnt lac	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum			
	target_AbsHwPos_Per2_ManufMode_Cnt_ei		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	.nt_นบช	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resi
AbsHwPos_AlignedHwPos_HwDeg_M_f32	767.419983	767.419983 ± 0.0009	
AbeHwPoe HandwhoolAuthority IIIe M f32	0.02000017	0.020000017 ± 0.004	

0.920000017

-6658.98535

-6658.98535

27969

HWPOS_STATE_INIT

AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum 0.920000017 ± 0.004

HWPOS_STATE_INIT

 -6658.98535 ± 0.1

-6658.98535 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3084.01001	-3084.01001 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.920000017	0.920000017 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-7306.20996	-7306.20996 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6483.58008	6483.58008 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.920000017	0.920000017 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3140	3140	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	27969	27969	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-369.899994	-369.899994 ± 0.0009	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

ame	Input Value
bsHwPos AlignedRelHwPos HwDeg M f32	-7755.08984
bsHwPos HandwheelAuthority Uls M f32	0.93000007
psHwPos HandwheelPositionLPF M str.SV Uls f32	966.200012
bsHwPos HandwheelPositionLPF M str.K Uls f32	0.93000007
osHwPos HwPosSource Cnt M u16	3252
osHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
osHwPos_PrevHandwheelPosition_HwDeg_M_f32	787.700012
osHwPos_RelHwPos_HwDeg_M_f32	787.700012
sHwPos_SrlComHwPosStatus_Cnt_M_u16	21845
sHwPos SrlComHwPos HwDeg M f32	2802.53003
psHwPos TargetHwAuthority Uls M f32	0.93000007
sHwPos TempHwPos HwDeg M f32	5828.25977
sHwPos VehCntrOffset HwDeg M f32	6377.62012
sHwPos VehCntrOfstLearn Cnt M Igc	1
psHwPos VehCntrValid Cnt M lqc	1
e Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
e_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep_Uls_f32	0.920000017
HwPosOutputLPFError_HwDeg_f32	1268.30005
I2CHwAuthority Uls f32	0.920000017
MaxSensorlessAuthority Uls f32	0.109999999
MaxVehCntrOffDiff HwDeg f32	1327.69995
MinSensorlessAuthority_Uls_f32	0.920000017
TurnsCntrAuthority_UIs_f32	0.920000017
UseTurnsCntr_Cnt_lgc	1
VehCntrOffValidLimit HwDeg f32	1179.19995
get_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
get AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
get AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	662.900024
get AbsHwPos Per2 ManufMode Cnt enum.value	1
get AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.109999999
get AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-685.099976
get AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	196
get_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-350.200012
get_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11407
get Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
get Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
get_Rte_inst_Ap_absHwPos.AbsHwPos_Per2_HandwheelAuthonty_bis_is2 get_Rte_inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3;	
get_Rte_inst_Ap_AbsHwPos.AbsHwPos_Pet2_HandwheeiPosition_HwDeg_ts. get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Pet2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
get_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	787.700012	787.700012 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	5487.91553	5487.91602 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	5487.91553	5487.91602 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	5828.25977	5828.25977 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6377.62012	6377.62012 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-350.200012	-350.200012 ± 0.0009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.91 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7649.14014
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-409.299988
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.93999998
AbsHwPos_HwPosSource_Cnt_M_u16	3364
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-807.97998
AbsHwPos_RelHwPos_HwDeg_M_f32	-807.97998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.93999998
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.930000007
k_HwPosOutputLPFError_HwDeg_f32	1298
k_I2CHwAuthority_UIs_f32	0.930000007
k_MaxSensorlessAuthority_Uls_f32	0.119999997
k_MaxVehCntrOffDiff_HwDeg_f32	1357.40002
k_MinSensorlessAuthority_Uls_f32	0.930000007
k_TurnsCntrAuthority_UIs_f32	0.930000007
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	683
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-665.400024
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	200

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-330.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11531		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Inst_Ap_AbsHwPosReducedPerf_Inst_AbsAbsHwPosReducedPerf_Inst_AbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsAbsA$	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-807.97998	-807.97998 ± 0.0009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	773.069519	773.06958 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3364	3364	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	773.069519	773.06958 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.93999998	0.939999998 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1271.65002	-1271.65002 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
	F0.4	*none*	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	none	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	-

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

0.93999998

773.069519

202.529999

3364

65535

-330.5

0.939999998 ± 0.004

773.06958 ± 0.1

202.529999 ± 0.1

-330.5 ± 0.0009

3364

Test Step 2.92 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7543.18018
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.94999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	213
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.94999988
AbsHwPos_HwPosSource_Cnt_M_u16	3476
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	828.26001
AbsHwPos_RelHwPos_HwDeg_M_f32	5828.25977
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	28969
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9802.53027
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.94999988
AbsHwPos_TempHwPos_HwDeg_M_f32	-868.820007
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6165.7002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.93999998
k_HwPosOutputLPFError_HwDeg_f32	1327.69995
k_I2CHwAuthority_Uls_f32	0.93999998
k_MaxSensorlessAuthority_Uls_f32	0.129999995
k_MaxVehCntrOffDiff_HwDeg_f32	1387.09998

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AbsHwPos Per2

ADSHWPOS_Per2		TO A	CILAB
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.93999998		
k_TurnsCntrAuthority_UIs_f32	0.939999998		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1238.59998		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	703.099976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.129999995		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-645.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	204		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-310.799988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11655		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Order = 0.0000000000000000000000000000000000$	target_AbsHwPos_Per2_DiagStatusHwPosf	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	ty_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	/Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	5828.25977	5828.25977 ± 0.0009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-814.729004	-814.729004 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-814.729004	-814.729004 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-868.820007	-868.820007 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6165.7002	6165.7002 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

0.00999999046

-814.729004

65534

65535

65535

0

504

0

1

504

65534

65535

 0 ± 0.1

65535 ± 0.09

0.00999999978 ± 0.004

-814.729004 ± 0.1

Test Step 2.93 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-7437.22021	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.959999979	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-369.899994	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.959999979	
AbsHwPos_HwPosSource_Cnt_M_u16	3588	
AbsHwPos_HwPosState_Cnt_M_enum	5	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	848.539978	
AbsHwPos_RelHwPos_HwDeg_M_f32	848.539978	

AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

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AbsHwPos Per2 Input Value AbsHwPos SrlComHwPosStatus Cnt M u16 29467 3224.75 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos TargetHwAuthority Uls M f32 0 AbsHwPos_TempHwPos_HwDeg_M_f32 1889.09998 AbsHwPos VehCntrOffset HwDeg M f32 6059.74023 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc 0 AbsHwPos VehCntrValid Cnt M Igc $target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$ $Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)$ Rte_Inst_Ap_AbsHwPos target_Rte_Inst_Ap_AbsHwPos $k_HwPosAuthorityStep_Uls_f32$ 0.949999988 k_HwPosOutputLPFError_HwDeg_f32 1357.40002 k_I2CHwAuthority_Uls_f32 0.949999988 k_MaxSensorlessAuthority_Uls_f32 0.140000001 k MaxVehCntrOffDiff HwDeg f32 1416 80005 0.949999988 k_MinSensorlessAuthority_Uls_f32 k_TurnsCntrAuthority_Uls_f32 0.949999988 k_UseTurnsCntr_Cnt_lgc k_VehCntrOffValidLimit_HwDeg_f32 1268.30005 $target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value$ 0 target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value 723.200012 target_AbsHwPos_Per2_ManufMode_Cnt_enum.value target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value 0.140000001 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value -626 target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value 208 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$ -291.100006 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 11779 target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc$ target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target AbsHwPos Per2 HandwheelPosition HwDeg f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum$ target_AbsHwPos_Per2_ManufMode_Cnt_enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$ target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32$ target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08$ target AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	848.539978	848.539978 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1889.09998	1889.09998 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3588	3588	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1889.09998	1889.09998 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29467	29467	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	3224.75	3224.75 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1889.09998	1889.09998 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	6059.74023	6059.74023 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3588	3588	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	29467	29467	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-291.100006	-291.100006 ± 0.0009	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.94 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7331.25977		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.970000029		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	120.199997		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.970000029		
AbsHwPos_HwPosSource_Cnt_M_u16	3700		
AbsHwPos_HwPosState_Cnt_M_enum	6		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-868.820007		
AbsHwPos_RelHwPos_HwDeg_M_f32	-868.820007		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	29965		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	120.199997		
AbsHwPos_TargetHwAuthority_UIs_M_f32	1		
AbsHwPos_TempHwPos_HwDeg_M_f32	909.380005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1446.94995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.959999979		
k_HwPosOutputLPFError_HwDeg_f32	1387.09998		
k_I2CHwAuthority_Uls_f32	0.959999979		
k_MaxSensorlessAuthority_Uls_f32	0.150000006		
k_MaxVehCntrOffDiff_HwDeg_f32	1446.5		
k_MinSensorlessAuthority_Uls_f32	0.959999979		
k_TurnsCntrAuthority_Uls_f32	0.959999979		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1298		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	743.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.150000006		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-606.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	212		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-271.399994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	11903		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	(target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuti	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority Uls f32	
toward Discharge And Abellioner At 11 B		.o()_0.002	
target_kte_inst_ap_adshwpos.Adshwpos_per2_SensorlessHwpos_hwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
	target_AbsHwPos_Per2_SrlComHwPos	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		Pos_HwDeg_f32 Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_SrlComHwPostarget_AbsHwPos_Per2_SrlComHwPos_	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_AbsHwPos_Per2_SrlComHwPostarget_AbsHwPos_Per2_SrlComHwPostarget_AbsHwPos_Per2_TurnsCntrValid	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name	target_AbsHwPos_Per2_SrlComHwPostarget_AbsHwPos_Per2_SrlComHwPostarget_AbsHwPos_Per2_TurnsCntrValidtarget_Pim_EOLVehCntrOffset	Pos_HwDeg_f32 Status_Cnt_u16 HwDeg_f32 ity_Cnt_u08	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos_ target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value	Pos_HwDeg_f32 Status_Cnt_u16 HwDeg_f32 ity_Cnt_u08 Expected Value	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos. target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value868.820007 ± 0.0009	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos_ target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.00999999978 ± 0.004 885.70459 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos_ target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 65534	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos. target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID	Resi
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosStatus_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos. target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651	Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 685.70459 ± 0.1	Resi
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosStatus_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos. target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535	Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosStatus_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0	Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1	Resi
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosStatus_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0	Expected Value -868.82007 ± 0.009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 909.380005	Expected Value -868.82007 ± 0.009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 909.380005 -1446.94995	Expected Value -868.82007 ± 0.009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosStatus_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	target_AbsHwPos_Per2_SrlComHwPos: target_AbsHwPos_Per2_SrlComHwPos. target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 909.380005 -1446.94995 1	Expected Value -868.82007 ± 0.0009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009 1	Resi
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SrlComHwPos_HwDeg_I32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset Actual Value -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 909.380005 -1446.94995 1 0	Expected Value -868.82007 ± 0.0009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009 1 0	Rest
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Dat_M_f32 AbsHwPos_	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value868.820007 ± 0.0009 _0.00999999978 ± 0.004 _885.70459 ± 0.1 _65534 _HWPOS_STATE_INPUTINVALID _885.70459 ± 0.1 _65535 _0 ± 0.1 _0 ± 0.004 _999.380005 ± 0.11446.94995 ± 0.009 _1 _0 _504 _1	Resu
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_Satus_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value868.820007 ± 0.00090.00999999978 ± 0.004885.70459 ± 0.165534HWPOS_STATE_INPUTINVALID885.70459 ± 0.1655350 ± 0.10 ± 0.004909.380005 ± 0.11446.94995 ± 0.00910504111111111	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504 1 1 0.0100000501	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value868.820007 ± 0.00090.00999999978 ± 0.004885.70459 ± 0.165534HWPOS_STATE_INPUTINVALID885.70459 ± 0.1655350 ± 0.10 ± 0.004909.380005 ± 0.11446.94995 ± 0.009105041110.00999999978 ± 0.004	Rest
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_Holog_M_f32 AbsHwPos_VehCntrOffset_Holog_M_f32 AbsHwPos_VehCntrOffset_Holog_M_f32 AbsHwPos_VehCntrOffset_Pd_Gas_M_f32 AbsHwPos_VehCntrOffset_Sem_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504 1 1 0.0100000501 885.704651	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 885.70459 ± 0.1	Resi
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_HwDeg_M_f32 AbsHwPos_VehCntrOffst_By_G_f32 AbsHwPos_VehCntrOffst_By_G_f32 AbsHwPos_VehCntrOffst_SyltrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504 1 1 0.0100000501 885.704651 65534	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.009999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 885.70459 ± 0.1 65534	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset Name AbsHwPos_AlignedHwPos_HwDeg_M_f32 AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_TermpHwPos_HwDeg_M_f32 AbsHwPos_TermpHwPos_HwDeg_M_f32 AbsHwPos_TermpHwPos_HwDeg_M_f32 AbsHwPos_TermpHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_Hord_Gn_M_igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) arget_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value arget_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	target_AbsHwPos_Per2_SrlComHwPost target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_SrlComHwPos target_AbsHwPos_Per2_TurnsCntrValid target_Pim_EOLVehCntrOffset **Actual Value** -868.820007 0.0100000501 885.704651 65534 HWPOS_STATE_INPUTINVALID 885.704651 65535 0 0 999.380005 -1446.94995 1 0 504 1 1 0.0100000501 885.704651	Pos_HwDeg_f32 Status_Cnt_u16 _HwDeg_f32 status_Cnt_u16 _HwDeg_f32 sty_Cnt_u08 Expected Value -868.820007 ± 0.0009 0.00999999978 ± 0.004 885.70459 ± 0.1 65534 HWPOS_STATE_INPUTINVALID 885.70459 ± 0.1 65535 0 ± 0.1 0 ± 0.004 909.380005 ± 0.1 -1446.94995 ± 0.009 1 0 504 1 1 0.009999999978 ± 0.004 885.70459 ± 0.1	Resu

0

0 ± 0.1

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.95 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos AlignedRelHwPos HwDeg M f32	7225.2998		
AbsHwPos HandwheelAuthority Uls M f32	0.980000019		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	140.300003		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.980000019		
AbsHwPos_HwPosSource_Cnt_M_u16	3812		
AbsHwPos HwPosState Cnt M enum	7		
AbsHwPos PrevHandwheelPosition HwDeg M f32	889.099976		
AbsHwPos RelHwPos HwDeg M f32	1889.09998		
AbsHwPos SrlComHwPosStatus Cnt M u16	30463		
AbsHwPos SrlComHwPos HwDeg M f32	140.300003		
AbsHwPos TargetHwAuthority Uls M f32	0.980000019		
AbsHwPos_TempHwPos_HwDeg_M_f32	929.659973		
AbsHwPos VehCntrOffset HwDeg M f32	7587.68994		
AbsHwPos VehCntrOfstLearn Cnt M Iqc	0		
AbsHwPos VehCntrValid Cnt M Igc	0		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.970000029		
k HwPosOutputLPFError HwDeg f32	1416.80005		
k_I2CHwAuthority_Uls_f32	0.970000029		
k MaxSensorlessAuthority Uls f32	0.159999996		
k MaxVehCntrOffDiff HwDeg f32	1476.19995		
k_MinSensorlessAuthority_UIs_f32	0.970000029		
k TurnsCntrAuthority Uls f32	0.970000029		
k UseTurnsCntr Cnt Igc	0		
k VehCntrOffValidLimit HwDeg f32	1327.69995		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	763.400024		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-586.599976		
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	216		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	-251.699997		
0	12027		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus_	1		
·		PoducodPorf Ont Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_ei		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	,_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Snt_uo8	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result

1889.09998

0.980000019

929.659973

929.659973

30463

HWPOS_STATE_INPUTINVALID

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

AbsHwPos_AlignedHwPos_HwDeg_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_HandwheelAuthority_Uls_M_f32

1889.09998 ± 0.0009

0.980000019 ± 0.004

HWPOS_STATE_INPUTINVALID

929.659973 ± 0.1

929.659973 ± 0.1

AbsHwPos_Per2



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	140.300003	140.300003 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.980000019	0.980000019 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	929.659973	929.659973 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7587.68994	7587.68994 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.980000019	0.980000019 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	929.659973	929.659973 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3812	3812	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	30463	30463	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	140.300003	140.300003 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-251.699997	-251.699997 ± 0.0009	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

lame	Input Value
	-7119.33008
bsHwPos_AlignedRelHwPos_HwDeg_M_f32 bsHwPos HandwheelAuthority Uls M f32	0.99000001
_ /	160.399994
bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	
bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.99000001
bsHwPos_HwPosSource_Cnt_M_u16	3924
bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	909.380005
bsHwPos_RelHwPos_HwDeg_M_f32	909.380005
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	30961
bsHwPos_SrlComHwPos_HwDeg_M_f32	160.399994
bsHwPos_TargetHwAuthority_Uls_M_f32	0.99000001
bsHwPos_TempHwPos_HwDeg_M_f32	-11725
bsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018
bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
bsHwPos_VehCntrValid_Cnt_M_lgc	1
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.980000019
_HwPosOutputLPFError_HwDeg_f32	1446.5
_I2CHwAuthority_Uls_f32	0.980000019
_MaxSensorlessAuthority_Uls_f32	0.170000002
_MaxVehCntrOffDiff_HwDeg_f32	1505.90002
_MinSensorlessAuthority_Uls_f32	0.980000019
_TurnsCntrAuthority_Uls_f32	0.980000019
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1357.40002
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	783.5
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.170000002
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-566.900024
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	220
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5000.6001
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12151
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	/Deg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	909.380005	909.380005 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11606.1465	-11606.1465 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-11606.1465	-11606.1465 ± 0.1	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7728.43018	7728.43018 ± 0.009	✓	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	✓	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5000.6001	5000.6001 ± 0.009	✓	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.97 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	7013.37988
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	180.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.100000001
AbsHwPos_HwPosSource_Cnt_M_u16	4036
AbsHwPos_HwPosState_Cnt_M_enum	9
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	929.659973
AbsHwPos_RelHwPos_HwDeg_M_f32	929.659973
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	31459
AbsHwPos_SrlComHwPos_HwDeg_M_f32	180.5
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.100000001
AbsHwPos_TempHwPos_HwDeg_M_f32	11725
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1369.17004
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.99000001
k_HwPosOutputLPFError_HwDeg_f32	1476.19995
k_I2CHwAuthority_Uls_f32	0.99000001
k_MaxSensorlessAuthority_Uls_f32	0.180000007
k_MaxVehCntrOffDiff_HwDeg_f32	1535.59998
k_MinSensorlessAuthority_Uls_f32	0.99000001
k_TurnsCntrAuthority_UIs_f32	0.99000001
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1387.09998
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.180000007
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-547.200012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224

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Razorcat

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

ADSHWPOS_Per2		10	ACITATO
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_Uls_f32	0.100000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1416.80005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	823.700012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-527.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	228		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5240.2002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12399		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos SrlComHwPosStatus Cnt M u16	65535	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32		65535 ± 0.09		~
T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

2804

0

504

0

65534

65535

0.100000001

2804 ± 0.009

0.100000001 ± 0.004

0

1

504

0 ± 0.1

65534

65535

0 ± 0.1

Test Step 2.99 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6801.45996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.300000012	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	220.699997	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.300000012	
AbsHwPos_HwPosSource_Cnt_M_u16	4260	
AbsHwPos_HwPosState_Cnt_M_enum	11	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	970.219971	
AbsHwPos_RelHwPos_HwDeg_M_f32	970.219971	

AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)$

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value \\ target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

AbsHwPos_Per2





Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998 0.109999999		
k_I2CHwAuthority_UIs_f32 k_MaxSensorlessAuthority_UIs_f32	0.200000003		
k MaxVehCntrOffDiff HwDeg f32	1595		
k MinSensorlessAuthority Uls f32	0.109999999		
k TurnsCntrAuthority Uls f32	0.109999999		
k_UseTurnsCntr_Cnt_Igc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1446.5		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	843.799988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-507.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	232		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5360		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12523		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_ManufMode_Cnt_e target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStati		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target AbsHwPos Per2 SrlComHwPos Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	-	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	970.219971	970.219971 ± 0.0009	· 1000.11
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.300000012	0.300000012 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1037.47107	1037.47095 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	4260	4260	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1037.47107	1037.47095 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32455	32455	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	220.699997	220.699997 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012	0.300000012 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2943.27002	2943.27002 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2916	2916 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	*
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	V
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	V
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.30000012	0.300000012 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1037.47107	1037.47095 ± 0.1	· ·
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4260	4260	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	32455 220.699997	32455 220.699997 ± 0.1	
target Abelly/Doc Par2 SrlComHy/Doc Hy/Doc f22 value			
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	5360	5360 ± 0.009	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 2.100 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6695.5		
	0.40000006		
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos HandwheelPositionLPF M str.SV Uls f32	240.800003		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.400000006		
AbshwPos HwPosSource Cnt M u16	4372		
AbsHwPos HwPosState Cnt M enum	12		
	-990.5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-990.5		
AbsHwPos_RelHwPos_HwDeg_M_f32			
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	32953		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	240.800003		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.40000006		
AbsHwPos_TempHwPos_HwDeg_M_f32	-3084.01001		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3028		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.119999997		
<_HwPosOutputLPFError_HwDeg_f32	1565.30005		
<_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	50.5999985		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_Uls_f32	0.119999997		
k UseTurnsCntr Cnt Igc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1476.19995		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	863.900024		
	2		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.20999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-488.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	236		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5479.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12647		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Ci		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuth	nority Uls f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	, <u> </u>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ny_Grit_u00	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	w.	-
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.0009	
		0.280000001 ± 0.004	
	0.280000001	0.26000001 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32	-1089.12402	-1089.12402 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32			
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16	-1089.12402	-1089.12402 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	-1089.12402 65534	-1089.12402 ± 0.1 65534	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004	
AbshwPos_HandwheelAuthority_Uls_M_f32 AbshwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbshwPos_HwPosSource_Cnt_M_u16 AbshwPos_HwPosState_Cnt_M_enum AbshwPos_PrevHandwheelPosition_HwDeg_M_f32 AbshwPos_SrlComHwPosStatus_Cnt_M_u16 AbshwPos_SrlComHwPos_HwDeg_M_f32 AbshwPos_TargetHwAuthority_Uls_M_f32 AbshwPos_TempHwPos_HwDeg_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504 1	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504 1 1	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504 1 1 0.2800000001 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) atarget_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504 1 1 0.2800000001 -1089.12402	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504 1 1 0.280000001 ± 0.004 -1089.12402 ± 0.1	
AbsHwPos_HandwheelAuthority_UIs_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffst_Deg_M_f32 AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_UIs_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504 1 1	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504 1 1 0.2800000001 ± 0.004	
AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1089.12402 65534 HWPOS_STATE_INPUTINVALID -1089.12402 65535 0 0 -3084.01001 3028 1 0 504 1 1 0.2800000001 -1089.12402	-1089.12402 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -1089.12402 ± 0.1 65535 0 ± 0.1 0 ± 0.004 -3084.01001 ± 0.1 3028 ± 0.009 1 0 504 1 1 0.280000001 ± 0.004 -1089.12402 ± 0.1	

0

0 ± 0.1

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

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Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.101 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	200		
AbsHwPos HandwheelAuthority Uls M f32	0.5		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	260.899994		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.5		
AbsHwPos_HwPosSource_Cnt_M_u16	4484		
AbsHwPos_HwPosState_Cnt_M_enum	13		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1010.78003		
AbsHwPos RelHwPos HwDeg M f32	1010.78003		
AbsHwPos SrlComHwPosStatus Cnt M u16	33451		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5		
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600		
AbsHwPos_vehCntrOfstLearn_Cnt_M_lgc	1		
	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc Rta_Call_An_AbsHwPos_EOI_VabCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	iii Oliset_GetEllorstatus_Ellorstatus	
k_HwPosAuthorityStep_Uls_f32 k_HwPosOutputLPFError_HwDeg_f32	0.129999995 1595		
k_12CHwAuthority_Uls_f32	0.129999995		
	0.219999999		
k_MaxSensorlessAuthority_Uls_f32	80.3000031		
k_MaxVehCntrOffDiff_HwDeg_f32	0.129999995		
k_MinSensorlessAuthority_Uls_f32			
k_TurnsCntrAuthority_UIs_f32	0.129999995		
k_UseTurnsCntr_Cnt_lgc	1505.90002		
k_VehCntrOffValidLimit_HwDeg_f32			
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	884		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.219999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-468.399994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	240		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5599.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12771		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1	D (0)	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	·-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	- -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1010.78003	1010.78003 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-248.300003	-248.300003 ± 0.1	•
AbeHwPos HwPosSource Cnt M u16	4484	1181	-

HWPOS_STATE_INPUTINVALID

-248.300003

33451

AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum

HWPOS_STATE_INPUTINVALID

-248.300003 ± 0.1

33451





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	260.899994	260.899994 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	668.400024	668.400024 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-248.300003	-248.300003 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	4484	4484	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	33451	33451	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	260.899994	260.899994 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	668.400024	668.400024 ± 0.0009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.102 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1000.25
.bsHwPos_HandwheelAuthority_Uls_M_f32	0.600000024
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
.bsHwPos_HwPosSource_Cnt_M_u16	4596
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006
bsHwPos_RelHwPos_HwDeg_M_f32	123.400002
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949
.bsHwPos_SrlComHwPos_HwDeg_M_f32	281
.bsHwPos_TargetHwAuthority_UIs_M_f32	0.600000024
bsHwPos_TempHwPos_HwDeg_M_f32	1395.13
bsHwPos_VehCntrOffset_HwDeg_M_f32	65535
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
HwPosAuthorityStep Uls f32	0.140000001
HwPosOutputLPFError HwDeg f32	1149.5
I2CHwAuthority Uls f32	0.140000001
MaxSensorlessAuthority Uls f32	0.230000004
MaxVehCntrOffDiff HwDeq f32	110
_MinSensorlessAuthority_Uls_f32	0.140000001
TurnsCntrAuthority Uls f32	0.140000001
_UseTurnsCntr_Cnt_lgc	1
VehCntrOffValidLimit HwDeg f32	1535.59998
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	12895
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32



AbsHwPos_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000038	0.460000008 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1395.13	1395.13 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1448.94995	1448.94995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000038	0.460000008 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1395.13	1395.13 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999	5719.3999 ± 0.009	~

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

Test Step 2.103 (Repeat Count = 1)	<u></u>
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	524.210022
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	4708
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1051.33997
AbsHwPos_RelHwPos_HwDeg_M_f32	-248.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	34447
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.150000006
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_UIs_f32	0.150000006
k_MaxSensorlessAuthority_Uls_f32	0.239999995
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997
k_MinSensorlessAuthority_Uls_f32	0.150000006
k_TurnsCntrAuthority_UIs_f32	0.150000006
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.239999995

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

2017-02-06, 14:29:50+0530





Name	Input Value		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-429		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	248		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5839.2002		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13019		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-248.300003	-248.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1535.87	1535.87 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	4708	4708	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1535.87	1535.87 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988	0.699999988 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1535.87	1535.87 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	953.210022	953.210022 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	

0

0.699999988

301.100006

953.210022

1535.87

4708

65534

none

4708

65534

0.699999988 ± 0.004

1535.87 ± 0.1

301.100006 ± 0.1

953.210022 ± 0.0009

Test Step 2.104 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-324.119995
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.800000012
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	321.26001
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.800000012
AbsHwPos_HwPosSource_Cnt_M_u16	4820
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1071.62
AbsHwPos_RelHwPos_HwDeg_M_f32	147.300003
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	34945
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.800000012
AbsHwPos_TempHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1000.59998
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Innut Value		
	Input Value 1208.90002		
k_HwPosOutputLPFError_HwDeg_f32	0.159999996		
k_I2CHwAuthority_UIs_f32			
k_MaxSensorlessAuthority_Uls_f32	0.25		
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994		
k_MinSensorlessAuthority_Uls_f32	0.159999996		
k_TurnsCntrAuthority_Uls_f32	0.159999996		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1595		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-409.299988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	252		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5959		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13143		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	147.300003	147.300003 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0.639999986	0.639999986 ± 0.004	~
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-1277.03601	-1277.03601 ± 0.1	~

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	147.300003	147.300003 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.639999986	0.639999986 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1277.03601	-1277.03601 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1277.03601	-1277.03601 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-1676.60999	-1676.60999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	85.1799927	85.1800003 ± 0.00009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.639999986	0.639999986 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1277.03601	-1277.03601 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	85.1799927	85.1800003 ± 0.00009	✓

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	-
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.105 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-400.209991	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.89999976	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	341.540009	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.899999976	

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AbsHwPos Per2 Input Value AbsHwPos_HwPosSource_Cnt_M_u16 4932 AbsHwPos_HwPosState_Cnt_M_enum HWPOS_STATE_SENSORINVALID AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 1091.90002 AbsHwPos_RelHwPos_HwDeg_M_f32 -991.700012 AbsHwPos SrlComHwPosStatus Cnt M u16 35443 AbsHwPos_SrlComHwPos_HwDeg_M_f32 6461.77002 AbsHwPos TargetHwAuthority Uls M f32 0.899999976 AbsHwPos_TempHwPos_HwDeg_M_f32 1817.34998 AbsHwPos_VehCntrOffset_HwDeg_M_f32 -500.899994 AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc $target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$ $Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)$ Rte_Inst_Ap_AbsHwPos target_Rte_Inst_Ap_AbsHwPos 0.170000002 k_HwPosAuthorityStep_Uls_f32 k_HwPosOutputLPFError_HwDeg_f32 1238.59998 0.170000002 k I2CHwAuthority Uls f32 k_MaxSensorlessAuthority_Uls_f32 0.25999999 k_MaxVehCntrOffDiff_HwDeg_f32 199.100006 k_MinSensorlessAuthority_Uls_f32 0.170000002 k_TurnsCntrAuthority_Uls_f32 0.170000002 k_UseTurnsCntr_Cnt_lgc Λ k_VehCntrOffValidLimit_HwDeg_f32 50.5999985 $target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value$ Λ target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value 0 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value 160.399994 target_AbsHwPos_Per2_ManufMode_Cnt_enum.value target AbsHwPos Per2 SensorlessAuthority Uls f32.value 0.25999999 target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value -389.600006 target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value 10 target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 6078.7998 target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16 13267 target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$ target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16$ target_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc$ target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum$ target_AbsHwPos_Per2_ManufMode_Cnt_enum target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$ target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 $target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32$ target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32

target_Rte_Inst_Ap_AbsHwPos.Pim_EOL	VehCntrOffset		target_Pin	_EOLVehC	ntrOffset			
Name			Actual V	alue		E	Expected Value	Result

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Τ						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~		
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•		
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~		

Test Step 2.106 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	6059.74023		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	361.820007		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999		
AbsHwPos_HwPosSource_Cnt_M_u16	5044		
AbsHwPos_HwPosState_Cnt_M_enum	5		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1112.18005		
AbsHwPos_RelHwPos_HwDeg_M_f32	894.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	35941		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos_TempHwPos_HwDeg_M_f32	1958.08997		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3700		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
C_HwPosAuthorityStep_Uls_f32	0.180000007		
k_HwPosOutputLPFError_HwDeg_f32	1268.30005		
c_I2CHwAuthority_Uls_f32	0.180000007		
<_MaxSensorlessAuthority_Uls_f32	0.270000011		
c_MaxVehCntrOffDiff_HwDeg_f32	228.800003		
_MinSensorlessAuthority_Uls_f32	0.180000007		
c_TurnsCntrAuthority_Uls_f32	0.180000007		
c_UseTurnsCntr_Cnt_lgc	1		
<_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	180.5		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.270000011		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-369.899994		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	20		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6198.6001		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13391		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	_(target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3:	target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target AbsHwPos Per2 ManufMode Cr		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target AbsHwPos Per2 SensorlessAuth	ority Uls f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		,	
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	894.5	894.5 ± 0.0009	11001
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1958.08997	1958.08997 ± 0.1	
	65534	65534	
		00004	
AbsHwPos_HwPosSource_Cnt_M_u16		HIMPOR STATE INDUSTRIVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	HWPOS_STATE_INPUTINVALID 1958.08997	1958.08997 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	HWPOS_STATE_INPUTINVALID 1958.08997 65535	1958.08997 ± 0.1 65535	
AbsHwPos_Handwheelr-OstionLFP_w_su.3v_Ois_i32 AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SriComHwPosStatus_Cnt_M_u16 AbsHwPos_SriComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uis_M_f32	HWPOS_STATE_INPUTINVALID 1958.08997	1958.08997 ± 0.1	

1958.08997

AbsHwPos_TempHwPos_HwDeg_M_f32

1958.08997 ± 0.1





Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3700	3700 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6198.6001	6198.6001 ± 0.009	•

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.107 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-5953.77881
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	382.100006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	5156
AbsHwPos_HwPosState_Cnt_M_enum	6
bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1132.45996
AbsHwPos_ReIHwPos_HwDeg_M_f32	5641
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36439
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
ubsHwPos TempHwPos HwDeg M f32	2098.83008
AbsHwPos VehCntrOffset HwDeg M f32	3812
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.18999998
HwPosOutputLPFError HwDeg f32	1298
I2CHwAuthority_Uls_f32	0.189999998
MaxSensorlessAuthority Uls f32	0.280000001
MaxVehCntrOffDiff HwDeg f32	258.5
MinSensorlessAuthority Uls f32	0.189999998
TurnsCntrAuthority Uls f32	0.18999998
UseTurnsCntr Cnt Iqc	0
: VehCntrOffValidLimit HwDeg f32	110
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	200.600006
·	0
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	
arget_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.280000001
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-350.200012
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	30
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6318.3999
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13515
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	5641	5641 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	588.107605	588.107605 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	5156	5156	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	588.107605	588.107605 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36439	36439	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6743.25	-6743.25 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3812	3812 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	588.107605	588.107605 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5156	5156	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	36439	36439	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

T					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-	

Test Step 2.108 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	5847.81982
AbsHwPos HandwheelAuthority Uls M f32	0.129999995
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	402.380005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	5268
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1152.73999
AbsHwPos_RelHwPos_HwDeg_M_f32	150.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	36937
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3924
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.200000003
k_HwPosOutputLPFError_HwDeg_f32	1327.69995
k_I2CHwAuthority_Uls_f32	0.200000003
k_MaxSensorlessAuthority_Uls_f32	0.289999992
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012
k_MinSensorlessAuthority_Uls_f32	0.200000003
k_TurnsCntrAuthority_Uls_f32	0.200000003
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	139.699997
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.289999992
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-330.5
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6438.2002
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13639

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

AbsHwPos_Per2



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_v	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	:_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	150.5	150.5 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	-
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	58.9264832	58.9264984 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	-
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	58.9264832	58.9264984 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6883.99023	6883.99023 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos TempHwPos HwDeg M f32	-2239.57007	-2239.57007 ± 0.1	-
AbsHwPos VehCntrOffset HwDeg M f32	3924	3924 ± 0.009	-
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	58.9264832	58.9264984 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	-
_ ·			

Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	~

65534

1600

6438.2002

65534

1600 ± 0.1

6438.2002 ± 0.009

Test Step 2.109 (Repeat Count = 1)	🗸
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5741.85986
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.140000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	422.660004
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.140000001
AbsHwPos_HwPosSource_Cnt_M_u16	5380
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1173.02002
AbsHwPos_RelHwPos_HwDeg_M_f32	-620
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	37435
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7024.72998
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.140000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4036
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_UIs_f32	0.20999993
k_HwPosOutputLPFError_HwDeg_f32	1357.40002
k_I2CHwAuthority_Uls_f32	0.20999993
k_MaxSensorlessAuthority_Uls_f32	0.300000012
k_MaxVehCntrOffDiff_HwDeg_f32	317.899994

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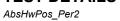
AbsHwPos_Per2

		• • • • • • • • • • • • • • • • • • • •	
Name	Input Value		
k_MinSensorlessAuthority_Uls_f32	0.209999993		
k_TurnsCntrAuthority_Uls_f32	0.209999993		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	169.399994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	240.800003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.300000012		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-310.799988		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6558		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13763		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-620	-620 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20999993	0.209999993 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1705.85986	1705.85999 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1705.85986	1705.85999 ± 0.1	•
Aballus Pag Sci Complus Page Status Cot M v.16	21845	21945	

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-620	-620 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.209999993	0.209999993 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1705.85986	1705.85999 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1705.85986	1705.85999 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	1705.85986	1705.85999 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993	0.209999993 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	1705.85986	1705.85999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4036	4036 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.209999993	0.209999993 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target Pim FOI VehCntrOffset FOI VehCntrOffset HwDeg f32	65535	65535 + 0.09	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.110 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5635.8999	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-11725	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006	
AbsHwPos_HwPosSource_Cnt_M_u16	5492	
AbsHwPos_HwPosState_Cnt_M_enum	9	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1193.30005	
AbsHwPos_RelHwPos_HwDeg_M_f32	201.199997	





Name	Input Value		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	37933		
AbshwPos SrlComHwPos HwDeg M f32	7165.47021		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006		
AbsHwPos TempHwPos HwDeg M f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVel	hCntrOffeet GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	nontronact_octEnorotates_Enorotates	
k_HwPosAuthorityStep_Uls_f32	0.219999999		
k HwPosOutputLPFError HwDeg f32	1387.09998		
k I2CHwAuthority UIs f32	0.219999999		
k_MaxSensorlessAuthority_Uls_f32	0.310000002		
k_MaxVehCntrOffDiff_HwDeg_f32	347.600006		
k_MinSensorlessAuthority_Uls_f32	0.219999999		
k TurnsCntrAuthority UIs f32	0.219999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	199.100006		
	1		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	260,899994		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value			
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0 0.310000002		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value			
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	60		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6677.7998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	13887		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4	and the state of t	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos\		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuth		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	201.199997	201.199997 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4148	4148 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	l .	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1	
	1		
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 0	1 0 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1 1 0 1600	1 0 ± 0.004 1600 ± 0.1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1 1 0 1600 65534	1 0 ± 0.004 1600 ± 0.1 65534	



Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.111 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-5529.93018		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.159999996		
AbsHwPos_HwPosSource_Cnt_M_u16	5604		
AbsHwPos_HwPosState_Cnt_M_enum	10		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1213.57996		
AbsHwPos_RelHwPos_HwDeg_M_f32	64.9800034		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38431		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7306.20996		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.159999996		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4260		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.230000004		
k_HwPosOutputLPFError_HwDeg_f32	1416.80005		
k I2CHwAuthority Uls f32	0.230000004		
k_MaxSensorlessAuthority_Uls_f32	0.319999993		
k_MaxVehCntrOffDiff_HwDeg_f32	377.299988		
k MinSensorlessAuthority Uls f32	0.23000004		
	0.23000004		
<_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.319999993		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	70		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14011		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos AlignedHwPos HwDeg M f32	64.9800034	64.9800034 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.159999996	0.159999996 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	9423.11328	9423.11328 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	5604	5604	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	9423.11328	9423.11328 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	38431	38431	
ALONIAN OS_CHOOMINIM OSCIALUS_ONE_W_U10			
AbsHwPos SrlComHwPos HwDeg M f32	7306.20996	7306.20996 ± 0.1	

0.159999996

AbsHwPos_TargetHwAuthority_Uls_M_f32

0.159999996 ± 0.004



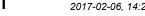


Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4260	4260 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.159999996	0.159999996 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	5604	5604	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	38431	38431	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6797.6001	6797.6001 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

lame	Input Value
bsHwPos AlignedRelHwPos HwDeg M f32	5423.97998
bsHwPos HandwheelAuthority Uls M f32	0.170000002
psHwPos HandwheelPositionLPF M str.SV Uls f32	0
osHwPos HandwheelPositionLPF M str.K Uls f32	0.170000002
psHwPos_HwPosSource_Cnt_M_u16	5716
bsHwPos HwPosState Cnt M enum	11
bsHwPos PrevHandwheelPosition HwDeg M f32	1233.85999
bsHwPos RelHwPos HwDeg M f32	8888
bsHwPos_SrlComHwPosStatus_Cnt_M_u16	38929
bsHwPos SrlComHwPos HwDeg M f32	-7446.9502
bsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
bsHwPos TempHwPos HwDeg M f32	949.940002
psHwPos VehCntrOffset HwDeg M f32	4372
psHwPos VehCntrOfstLearn Cnt M Igc	1
psHwPos_VehCntrValid_Cnt_M_lgc	1
te Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
te Inst Ap AbsHwPos	target_Rie_Cail_Ap_AbsHwPos_EOL verichit/Oilset_GetErforstatus_Errorstatus target_Rie_Inst_Ap_AbsHwPos
HwPosAuthorityStep UIs f32	0.239999995
HwPosOutputLPFError HwDeg f32	1446.5
I2CHwAuthority Uls f32	0.239999995
	0.33000013
MaxSensorlessAuthority_UIs_f32	
_MaxVehCntrOffDiff_HwDeg_f32	407
MinSensorlessAuthority_Uls_f32	0.239999995
_TurnsCntrAuthority_Uls_f32	0.239999995
_UseTurnsCntr_Cnt_Igc	1
_VehCntrOffValidLimit_HwDeg_f32	258.5
rget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	
rget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
rget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006
rget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
rget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.330000013
rget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-251.699997
rget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80
rget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	6917.3999
rget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14135
rget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
rget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32
rget Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08

AbsHwPos_Per2





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	8888	8888 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	949.940002	949.940002 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	949.940002	949.940002 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	949.940002	949.940002 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4372	4372 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	949.940002	949.940002 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.113 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	3787.70996
AbsHwPos HandwheelAuthority Uls M f32	0.280000001
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	1112.18005
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.280000001
AbsHwPos HwPosSource Cnt M u16	296
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.270000011
k_HwPosOutputLPFError_HwDeg_f32	1179.19995
k_I2CHwAuthority_Uls_f32	0.270000011
k_MaxSensorlessAuthority_Uls_f32	0.34000004
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.270000011
k_TurnsCntrAuthority_UIs_f32	0.270000011
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	822.799988
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5

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AbsHwPos_Per2



Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer	f_(target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	nority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3$	target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutl	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	622.700012	622.700012 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Pte Call AhsHwPos Per? CP1 ChecknointReached	1	Pte Call AbsHwPos Per2 CP1 CheckpointReached	1	-

1600

65535

1600 ± 0.1

65535 ± 0.09

Test Step 2.114 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-5212.05908
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-564.619995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.189999998
AbsHwPos_HwPosSource_Cnt_M_u16	5940
AbsHwPos_HwPosState_Cnt_M_enum	13
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1274.42004
AbsHwPos_RelHwPos_HwDeg_M_f32	-471
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	39925
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7728.43018
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998
AbsHwPos_TempHwPos_HwDeg_M_f32	-990.5
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4596
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.25999999
k_HwPosOutputLPFError_HwDeg_f32	1505.90002
k_I2CHwAuthority_Uls_f32	0.25999999

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$





Name	Input Value		
	•		
k_MaxSensorlessAuthority_UIs_f32	0.34999994		
k_MaxVehCntrOffDiff_HwDeg_f32	466.399994		
k_MinSensorlessAuthority_Uls_f32	0.25999999		
k_TurnsCntrAuthority_Uls_f32	0.25999999		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	341.299988		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.34999994		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-212.300003		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7157		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14383		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosR	leducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	/_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	is_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-471	-471 ± 0.0009	~
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	~

target_rate_met_rp_rberran een mi_Leelvenena eneet	targot_1 mi_LotTronoma onlock		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-471	-471 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-645.53717	-645.53717 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-645.53717	-645.53717 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	-990.5	-990.5 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4596	4596 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-645.53717	-645.53717 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7157	7157 ± 0.009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.115 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5106.08984
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	484
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0
AbsHwPos_HwPosSource_Cnt_M_u16	6052
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1294.69995

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Name	Input Value		
AbsHwPos_RelHwPos_HwDeg_M_f32	4649		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	40423		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-7869.16992		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	1010.78003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1535.59998		
k_I2CHwAuthority_Uls_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	0.360000014		
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-192.600006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	110 7276.7998		
	14507		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		PeducedParf Cnt Igo	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		, - -	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	,	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPos HwDeg f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	4649	4649 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	·
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	1010.78003	1010.78003 ± 0.1	-
AbsHwPos HwPosSource Cnt M u16	6052	6052	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE INIT	HWPOS STATE INIT	-
AbsHwPos PrevHandwheelPosition HwDeg M f32	1010.78003	1010.78003 ± 0.1	•
AbsHwPos SrlComHwPosStatus Cnt M u16	40423	40423	_
AbsHwPos SrlComHwPos HwDeg M f32	-7869.16992	-7869.16992 ± 0.1	•
AbsHwPos TargetHwAuthority Uls M f32	0.200000003	0.200000003 ± 0.004	-
AbsHwPos TempHwPos HwDeg M f32	1010.78003	1010.78003 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	4708	4708 ± 0.009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	-
AbsHwPos VehCntrValid Cnt M Igc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1010.78003	1010.78003 ± 0.1	~
	6053	6052	

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

6052

40423

-1600

7276.7998

6052

40423

-1600 ± 0.1

7276.7998 ± 0.009

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



Test Step 2.116 (Repeat Count = 1) Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	5000.12988		
AbsHwPos HandwheelAuthority Uls M f32	0.20999993		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-564.299988		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1		
AbsHwPos_HwPosSource_Cnt_M_u16	6164		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1314.97998		
AbsHwPos_RelHwPos_HwDeg_M_f32	234		
AbsHwPos SrlComHwPosStatus Cnt M u16	40921		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.209999993		
AbsHwPos_TempHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1687		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLV	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.280000001		
k_HwPosOutputLPFError_HwDeg_f32	1565.30005		
k_I2CHwAuthority_Uls_f32	0.280000001		
k_MaxSensorlessAuthority_Uls_f32	0.370000005		
k_MaxVehCntrOffDiff_HwDeg_f32	525.799988		
k_MinSensorlessAuthority_Uls_f32	0.280000001		
k_TurnsCntrAuthority_UIs_f32	0.280000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	482		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.370000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-172.899994		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	120		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	7396.6001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	14631		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	thority_UIs_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPo	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPos	 Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	:_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	Cnt_enum	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAut	thority Uls f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target AbsHwPos Per2 SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	dity Cnt u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos AlignedHwPos HwDeg M f32	234	234 ± 0.0009	
AbsHwPos_HandwheelAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	123.400002	123.400002 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	123.400002	123.400002 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos SrlComHwPos HwDeg M f32	0	0 ± 0.1	
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1687	1687 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbsHwPos VehCntrValid Cnt M Igc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cht_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_nxtrDiagMgr_SetNTCStatus(Param_Cnt_1_u0o) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
ALC CALL AP ADSTRUCTOS INALIDIAGINIST SELIVICOLALIS(SIALIS CITE I ETIUM)		0 ± 0.004	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0		
	0 123.400002 65534	123.400002 ± 0.1 65534	

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T .			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.117 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	9556.42969		
AbsHwPos HandwheelAuthority Uls M f32	0.75999999		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-941.200012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.569999993		
AbsHwPos HwPosSource Cnt M u16	1348		
AbsHwPos HwPosState Cnt M enum	13		
AbsHwPos PrevHandwheelPosition HwDeg M f32	-409.299988		
AbsHwPos RelHwPos HwDeg M f32	-409.299988		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41419		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993		
AbsHwPos TempHwPos HwDeg M f32	2380.31006		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	-	
k_HwPosAuthorityStep_Uls_f32	0.75		
k_HwPosOutputLPFError_HwDeg_f32	674.299988		
k_I2CHwAuthority_Uls_f32	0.75		
k_MaxSensorlessAuthority_Uls_f32	0.839999974		
k_MaxVehCntrOffDiff_HwDeg_f32	822.799988		
k_MinSensorlessAuthority_Uls_f32	0.75		
k_TurnsCntrAuthority_UIs_f32	0.75		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	674.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.839999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1587.5		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	130		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Other and the property of the property $	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	6 target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-409.299988	-409.299988 ± 0.0009	•
AL II D. II I I I I I I I I I I I I I I I	0.500000000	0.5000000000000000000000000000000000000	

0.569999993

HWPOS_STATE_INPUTINVALID

952.06073

952.06073

1348

AbsHwPos_HandwheelAuthority_Uls_M_f32 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum

0.569999993 ± 0.004

HWPOS_STATE_INPUTINVALID

952.06073 ± 0.1

952.06073 ± 0.1

1348

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41419	41419	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6180.29004	6180.29004 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.569999993	0.569999993 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2380.31006	2380.31006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2802.53003	2802.53003 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.569999993	0.569999993 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	952.06073	952.06073 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	•

41419

1600

65535

41419

1600 ± 0.1

65535 ± 0.09

Name	Input Value
	·
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-514.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	-921.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.579999983
AbsHwPos_HwPosSource_Cnt_M_u16	1460
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213
AbsHwPos_RelHwPos_HwDeg_M_f32	213
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41917
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-6321.02979
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.579999983
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
C_HwPosAuthorityStep_Uls_f32	0.75999999
C_HwPosOutputLPFError_HwDeg_f32	704
C_I2CHwAuthority_Uls_f32	0.75999999
C_MaxSensorlessAuthority_Uls_f32	0.850000024
C_MaxVehCntrOffDiff_HwDeg_f32	852.5
_MinSensorlessAuthority_Uls_f32	0.75999999
_TurnsCntrAuthority_Uls_f32	0.75999999
c_UseTurnsCntr_Cnt_lgc	1
c_VehCntrOffValidLimit_HwDeg_f32	704
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	-271.399994
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.850000024
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	140
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2355.30005
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9423
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	0
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	
arget_Rie_ilist_Ap_AbsHwPos.AbsHwPos_Fer2_blagstatusHwFosReducedFeri_ arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbshwPos Per2 HandwheelAuthority Uls f32
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_HandwheelAuthority_0is_132 arget_Rte_Inst_Ap_AbshwPos.AbshwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_HandwrieeiPosition_hwDeg_isz arget Rte_Inst_Ap_AbshwPos.AbshwPos_Per2_HwPosSource_Cnt_u16	
	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 target AbsHwPos Per2 ManufMode Cnt enum





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	2 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1075.17896	1075.17896 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1075.17896	1075.17896 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1075.17896	1075.17896 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	485.97998	485.980011 ± 0.0009	~

Τ			~	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	•
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 2.119 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-874.22998
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.5
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974
AbsHwPos_HwPosSource_Cnt_M_u16	1572
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.769999981
k_HwPosOutputLPFError_HwDeg_f32	733.700012
k_I2CHwAuthority_Uls_f32	0.769999981
k_MaxSensorlessAuthority_Uls_f32	0.860000014
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012
k_MinSensorlessAuthority_Uls_f32	0.769999981
k_TurnsCntrAuthority_Uls_f32	0.769999981
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	733.700012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997





Name	Input Value		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2385.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	s 1		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	erf_(target_AbsHwPos_Per2_DiagStatusHwP	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f	f32 target_AbsHwPos_Per2_HandwheelPosi	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosV	alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cn	t_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	target_AbsHwPos_Per2_SensorlessHwP	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHwPosS	tatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidit	y_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	-
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	-
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	-
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	-
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	106.369995	106.370003 ± 0.0009	✓

T			V	
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 2.120 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-325.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
AbsHwPos_HwPosSource_Cnt_M_u16	1684
AbsHwPos_HwPosState_Cnt_M_enum	3
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
AbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus





Name	Input Value		
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.779999971		
k_HwPosOutputLPFError_HwDeg_f32	763.400024		
k_I2CHwAuthority_Uls_f32	0.779999971		
k_MaxSensorlessAuthority_Uls_f32	0.870000005		
k_MaxVehCntrOffDiff_HwDeg_f32	911.900024		
k_MinSensorlessAuthority_Uls_f32	0.779999971		
k_TurnsCntrAuthority_Uls_f32	0.779999971		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	763.400024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.870000005		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-960.900024		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9671		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	-
AbeHwPos HandwheelAuthority IIIs M f32	0.0100000501	0.00000000078 ± 0.004	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2415.69995	2415.69995 ± 0.009	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Test Step 2.121 (Repeat Count = 1)	→
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-514.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.769999981
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-921.5

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AbsHwPos_Per2		MAC	SICAL
Name	Input Value		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.579999983		
AbsHwPos_HwPosSource_Cnt_M_u16	1460		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	213		
AbsHwPos_RelHwPos_HwDeg_M_f32	213		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	41917		
AbsHwPos SrlComHwPos HwDeg M f32	-6321.02979		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.579999983		
AbsHwPos_TempHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	2943.27002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	HICHIONSEL_GELLIOISIAIUS_EITOISIAIUS	
	0.75999999		
(_HwPosAuthorityStep_UIs_f32	704		
< HwPosOutputLPFError_HwDeg_f32 < 1300 http://doi.org/10.1016/332			
c_I2CHwAuthority_Uls_f32	0.75999999		
<_MaxSensorlessAuthority_Uls_f32 <_MaxVebCatrOffDiff_HurDog_f32	0.850000024		
<_MaxVehCntrOffDiff_HwDeg_f32 <_MinCapagalogs Nutberity Up_f32	852.5		
	0.75999999		
c_TurnsCntrAuthority_Uls_f32	0.75999999		
x_UseTurnsCntr_Cnt_lgc	1		
C_VehCntrOffValidLimit_HwDeg_f32	704		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.850000024		
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1000.29999		
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	140		
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2355.30005		
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9423		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (target_AbsHwPos_Per2_DiagStatusHwl	PosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	HwDeg f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_C		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
arget Rte Inst Ap AbsHwPos.Pim EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
		Pour ant al Malon	D
Name	Actual Value	Expected Value	Resu
.bsHwPos_AlignedHwPos_HwDeg_M_f32	213	213 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999046	0.00999999978 ± 0.004	
lbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1075.17896	1075.17896 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	
bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1075.17896	1075.17896 ± 0.1	
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
bsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
bsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	
bsHwPos_TempHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	
NbsHwPos_VehCntrOffset_HwDeg_M_f32	485.97998	485.980011 ± 0.0009	
sbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	
bsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target AbeHwPos Per? HandwheelAuthority I lle f32 value	0.0000000046	0.0000000078 + 0.004	

0.00999999046

1075.17896

485.97998

65534

65535

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

0.00999999978 ± 0.004

1075.17896 ± 0.1

485.980011 ± 0.0009

65534

65535

0 ± 0.1



Т				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

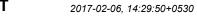
Test Step 2.122 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-874.22998		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.779999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	324.5		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.589999974		
AbsHwPos_HwPosSource_Cnt_M_u16	1572		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-369.899994		
AbsHwPos_RelHwPos_HwDeg_M_f32	-369.899994		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	42415		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.58999974		
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1084.01001		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.769999981		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.769999981		
k_MaxSensorlessAuthority_Uls_f32	0.860000014		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.769999981		
k_TurnsCntrAuthority_Uls_f32	0.769999981		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	733.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-251.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.860000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-980.599976		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2385.5		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9547		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0		sReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Ci		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	_	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32			
	target_AbsHwPos_Per2_I2CHwAbsPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	<i>′</i>	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-369.899994	-369.899994 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.589999974	0.589999974 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1437.41101	-1437.41113 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	1572	1572	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1437.41101	-1437.41113 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	6461.77002	6461.77002 ± 0.1	
AbaHurDoo TorgotHurAuthority IIIo M f22	0.590000074	0.590000074 + 0.004	

0.589999974

AbsHwPos_TargetHwAuthority_Uls_M_f32

0.589999974 ± 0.004

AbsHwPos_Per2





Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-2661.79004	-2661.79004 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	106.369995	106.370003 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.589999974	0.589999974 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1437.41101	-1437.41113 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1572	1572	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	106.369995	106.370003 ± 0.0009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

Test Step 2.123 (Repeat Count = 1)	
Name	Input Value
hbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-325.320007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.790000021
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	471.600006
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024
AbsHwPos_HwPosSource_Cnt_M_u16	1684
AbsHwPos_HwPosState_Cnt_M_enum	3
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997
NbsHwPos_RelHwPos_HwDeg_M_f32	120.199997
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	42913
.bsHwPos_SrlComHwPos_HwDeg_M_f32	6602.50977
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024
.bsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	3224.75
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.779999971
_HwPosOutputLPFError_HwDeg_f32	763.400024
I2CHwAuthority_Uls_f32	0.779999971
_MaxSensorlessAuthority_Uls_f32	0.870000005
MaxVehCntrOffDiff HwDeg f32	911.900024
MinSensorlessAuthority Uls f32	0.779999971
TurnsCntrAuthority Uls f32	0.779999971
UseTurnsCntr Cnt lgc	1
VehCntrOffValidLimit HwDeg f32	763.400024
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-232
arget AbsHwPos Per2 ManufMode Cnt enum.value	2
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.870000005
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	-960.900024
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	160
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2415.69995
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	9671
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelPosition HwDeg f32	1
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	target AbsHwPos Per2 SrlComHwPosStatus Cnt u16

2017-02-06, 14:29:50+0530



AbsHwPos_Per2

Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_F	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.0100000501	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1870.15796	1870.15796 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1870.15796	1870.15796 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	635.580017	635.580017 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.0100000501	0.00999999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	635.580017	635.580017 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

AbsHwPos_Per2

2017-02-06, 14:29:50+0530



Test Case 3: Path Test

2017-02-06, 14:29:50+0530

AbsHwPos_Per2



Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

CPU Cycles:

TS3.1 1467.00 Cycles
TS3.2 2915.00 Cycles
TS3.3 1421.00 Cycles
TS3.3 1421.00 Cycles
TS3.4 1965.00 Cycles
TS3.5 2827.00 Cycles
TS3.5 2827.00 Cycles
TS3.6 2340.00 Cycles
TS3.7 2783.00 Cycles
TS3.10 1374.00 Cycles
TS3.10 1374.00 Cycles
TS3.11 1438.00 Cycles
TS3.11 18860.00 Cycles
TS3.12 1850.00 Cycles
TS3.13 1896.00 Cycles
TS3.14 1860.00 Cycles
TS3.15 1365.00 Cycles
TS3.15 1365.00 Cycles
TS3.17 1844.00 Cycles
TS3.18 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.19 1915.00 Cycles
TS3.20 1426.00 Cycles
TS3.21 1904.00 Cycles
TS3.22 1861.00 Cycles
TS3.23 1844.00 Cycles
TS3.24 1904.00 Cycles
TS3.25 1904.00 Cycles
TS3.27 1373.00 Cycles
TS3.28 1910.00 Cycles
TS3.28 1910.00 Cycles
TS3.29 1438.00 Cycles
TS3.29 1438.00 Cycles
TS3.29 1438.00 Cycles
TS3.30 1904.00 Cycles



```
Description Vector Description:
```

```
if ( (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) || (AbsHwPos_HwPosState_Cnt_M_enum ==
#WPOS_STATE_INPUTINVALID) )=>False
if (AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc == TRUE)=>False
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >=
k_HwPosOutputLPFError_HwDeg_f32)=>True
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>True
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>True
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False'
TS3.2"
 if (Diagoslation Wrosineduceur ein [__in_c__in_c] = 1100_j=1100 if (AbsHwPos_VehCntrOfstLearn_Cnt_M_igc == TRUE)=>True if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>True
if( NvMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>True"
if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) =>False||
(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID)=>True)
if (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)=>False"
TS3 4
163.4 if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True if (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>True"
if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NyMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
 if( (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) >
(NvMStatus_Cnt_T_enum!= NVM_REQ_PENDING) )=>False"
TS3 7"
if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>False else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False"
if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)=>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=>False
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=>False"
TS3.9"
else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=>True if (k_UseTurnsCntr_Cnt_Igc == TRUE)=>True if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)=>True"
TS3.10'
if (k_UseTurnsCntr_Cnt_lgc == TRUE)=>False
if (l2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False"
TS3.11
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.12"
else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False"
TS3.13"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True"
if (k_UseTurnsCntr_Cnt_lgc == FALSE)=>False
if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>True
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
 TS3.15"
if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True"
TS3.16"
 if (TurnsCntrValidity\_Cnt\_T\_u08 == D\_TCVVALID\_CNT\_U08) => False"
if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=>False
if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>False"
if (Sensorle
              essAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>False'
TS3 19"
else if (AbsHwPos HwPosState Cnt M enum == HWPOS STATE SENSORINVALID)=>True
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE) )=>True"
TS3.21"
TS3 22"
if (SensorlessAuthority Uls T f32 >= k MaxSensorlessAuthority Uls f32)=>False"
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>True"
```



TS3.26" $if (Sensorless Authority_Uls_T_f32 >= k_MinSensorless Authority_Uls_f32) => False"$ TS3 27" if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>False"

if(NvMStatus_Cnt_T_enum != NVM_REQ_PENDING)=>False"

TS3.29"

(AbsHwPos_HandwheelAuthority_Uls_M_f32>=D_MAXAUTHORITY_ULS_F32)=>True"
TS3.30"(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=False,(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=True,

(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=True,
(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=True,
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=False,(k_UseTurnsCntr_Cnt_lgc == TRUE)=True,
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=False,(k_UseTurnsCntr_Cnt_lgc == TRUE)=True,
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=False,(k_UseTurnsCntr_Cnt_lgc == TRUE)=True,
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=False,(k_UseTurnsCntr_Cnt_lgc == TRUE)=False)
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=False,(k_UseTurnsCnt_Lgc == TRUE)=False)
(I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=True,(k_UseTurnsCnt_Lgc == TRUE)=True,(k_UseTurnsCnt_Lgc

Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-10125	
AbsHwPos HandwheelAuthority Uls M f32	0	
AbsHwPos HandwheelPositionLPF M str.SV UIs f32	-1600	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0	
AbsHwPos HwPosSource Cnt M u16	1	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-1600	
AbsHwPos RelHwPos HwDeg M f32	-10125	
NbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	
NosHwPos SrlComHwPos HwDeg M f32	-11725	
	0	
NbSHwPos_TargetHwAuthority_UIs_M_f32	-	
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	O	
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	
_HwPosAuthorityStep_Uls_f32	0	
_HwPosOutputLPFError_HwDeg_f32	0	
_I2CHwAuthority_Uls_f32	0	
_MaxSensorlessAuthority_Uls_f32	0	
_MaxVehCntrOffDiff_HwDeg_f32	0	
_MinSensorlessAuthority_Uls_f32	0	
_TurnsCntrAuthority_Uls_f32	0	
_UseTurnsCntr_Cnt_lgc	0	
_VehCntrOffValidLimit_HwDeg_f32	0	
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0	
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0	
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900	
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0	
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0	
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600	
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0	
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0	
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	
Name	Actual Value Expected Value	Res

Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-10125	-10125 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1600	-1600 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1600	-1600 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-11725	-11725 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1600	-1600 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600	-1600 ± 0.009	✓

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	-

lame	Input Value
	10125
bsHwPos_AlignedRelHwPos_HwDeg_M_f32	
bsHwPos_HandwheelAuthority_Uls_M_f32	1 1600
.bsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32	1777
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1
.bsHwPos_HwPosSource_Cnt_M_u16	65534
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600
.bsHwPos_RelHwPos_HwDeg_M_f32	10125
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535
.bsHwPos_SrlComHwPos_HwDeg_M_f32	11725
.bsHwPos_TargetHwAuthority_Uls_M_f32	1
.bsHwPos_TempHwPos_HwDeg_M_f32	11725
.bsHwPos_VehCntrOffset_HwDeg_M_f32	65535
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
.bsHwPos_VehCntrValid_Cnt_M_lgc	1
tte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
tte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	1
_HwPosOutputLPFError_HwDeg_f32	1620
_I2CHwAuthority_Uls_f32	1
_MaxSensorlessAuthority_Uls_f32	1
_MaxVehCntrOffDiff_HwDeg_f32	1600
_MinSensorlessAuthority_Uls_f32	1
_TurnsCntrAuthority_Uls_f32	1
_UseTurnsCntr_Cnt_lgc	1
_VehCntrOffValidLimit_HwDeg_f32	1600
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1
arget_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900
arget_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2
arget_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf	f_(target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3	2 target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16	



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	11725	11725 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	11725 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65535	65535	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	•
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	9556.42969
AbsHwPos HandwheelAuthority Uls M f32	0.75999999
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	204.899994
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.75999999
AbsHwPos HwPosSource Cnt M u16	1348
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID
AbsHwPos PrevHandwheelPosition HwDeg M f32	442.940002
AbsHwPos RelHwPos HwDeg M f32	442.940002
AbsHwPos SrlComHwPosStatus Cnt M u16	26817
AbsHwPos SrlComHwPos HwDeg M f32	123.400002
AbsHwPos TargetHwAuthority Uls M f32	0.75999999
AbsHwPos TempHwPos HwDeq M f32	5054.37012
AbsHwPos VehCntrOffset HwDeg M f32	8178.93018
AbsHwPos VehCntrOfstLearn Cnt M Igc	0
AbsHwPos VehCntrValid Cnt M Igc	0
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte Inst Ap AbsHwPos	target Rte Inst Ap AbsHwPos
k HwPosAuthorityStep Uls f32	0.75
k HwPosOutputLPFError HwDeg f32	674.299988
k I2CHwAuthority Uls f32	0.75
k MaxSensorlessAuthority Uls f32	0.839999974
k MaxVehCntrOffDiff HwDeg f32	822.799988
k MinSensorlessAuthority Uls f32	0.75
k TurnsCntrAuthority Uls f32	0.75
k UseTurnsCntr Cnt Igc	0
k VehCntrOffValidLimit HwDeg f32	674.299988
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-291.100006
target AbsHwPos Per2 ManufMode Cnt enum.value	2
target AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.839999974
target AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	1587.5
target AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	128
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	2325.1001
angon in	2020.1001





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	9299		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_	(target_AbsHwPos_Per2_DiagStatusHwPos	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor	ity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVal	id_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor	ity_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwPos	s_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	442.940002	442.940002 ± 0.0009	✓
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3890.49731	3890.49731 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1348	1348	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3890.49731	3890.49731 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	26817	26817	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	123.400002	123.400002 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.75999999	0.75999999 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	5054.37012	5054.37012 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8178.93018	8178.93018 ± 0.009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.75999999	0.75999999 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1348	1348	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	26817	26817	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	123.400002	123.400002 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	2325.1001	2325.1001 ± 0.009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

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Test Step 3.4 (Repeat Count = 1)	V
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1617.32996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.409999996
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1527.19995
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.409999996
AbsHwPos_HwPosSource_Cnt_M_u16	400
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	224.100006
AbsHwPos_RelHwPos_HwDeg_M_f32	224.100006
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24657
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-3928.44995
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.40999996
AbsHwPos_TempHwPos_HwDeg_M_f32	-7755.08984
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.08997
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.40000006
k_HwPosOutputLPFError_HwDeg_f32	1565.30005
k_I2CHwAuthority_Uls_f32	0.40000006
k_MaxSensorlessAuthority_Uls_f32	0.49000001
k_MaxVehCntrOffDiff_HwDeg_f32	0
k_MinSensorlessAuthority_Uls_f32	0.400000006

2017-02-06, 14:29:50+0530



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Name	Input Value		
k_TurnsCntrAuthority_UIs_f32	0.40000006		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1208.90002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	602.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.49000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	884		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	224		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1268.09998		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4959		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	us 4		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	erf_(target_AbsHwPos_Per2_DiagStatusHw	/PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_	f32 target_AbsHwPos_Per2_HandwheelPo	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	e_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	sValid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_0	Cnt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAu	thority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f	f32 target_AbsHwPos_Per2_SensorlessHw	/Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	16 target_AbsHwPos_Per2_SrlComHwPos	sStatus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrVali	dity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	224.100006	224.100006 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0.00999999046	0.00999999978 ± 0.004	
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	-2278.53882	-2278.53882 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE INPUTINVALID	HWPOS STATE INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	-2278 53882	-2278 53882 + 0.1	

target_Filli_EOLVERCHILORSet		
Actual Value	Expected Value	Result
224.100006	224.100006 ± 0.0009	~
0.00999999046	0.00999999978 ± 0.004	•
-2278.53882	-2278.53882 ± 0.1	•
65534	65534	•
HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
-2278.53882	-2278.53882 ± 0.1	•
65535	65535	✓
0	0 ± 0.1	•
0	0 ± 0.004	✓
-7755.08984	-7755.08984 ± 0.1	✓
733.329956	733.330017 ± 0.0009	•
1	1	•
1	1	•
504	504	✓
1	1	✓
1	1	•
0.00999999046	0.00999999978 ± 0.004	✓
-1600	-1600 ± 0.1	✓
65534	65534	✓
65535	65535	✓
0	0 ± 0.1	~
733.329956	733.330017 ± 0.0009	•
	224.100006 0.00999999046 -2278.53882 65534 HWPOS_STATE_INPUTINVALID -2278.53882 65535 0 0 -7755.08984 733.329956 1 1 1 0.00999999046 -1600 65534 65535 0	Actual Value Expected Value 224.100006 224.100006 ± 0.0009 0.00999999046 0.00999999978 ± 0.004 -2278.53882 -2278.53882 ± 0.1 65534 HWPOS_STATE_INPUTINVALID -2278.53882 -2278.53882 ± 0.1 65535 65535 0 0 ± 0.1 0 0 ± 0.004 -7755.08984 -7755.08984 ± 0.1 733.329956 733.330017 ± 0.0009 1 1 504 504 1 1 0.00999999046 0.00999999978 ± 0.004 -1600 -1600 ± 0.1 65535 65535 0 0 ± 0.1

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 3.5 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	898.809998	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.430000007	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	567.400024	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.430000007	
AbsHwPos_HwPosSource_Cnt_M_u16	416	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	243.300003	
AbsHwPos_RelHwPos_HwDeg_M_f32	243.300003	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	24801	

2017-02-06, 14:29:50+0530



Name	Input Value
AbsHwPos_SrlComHwPos_HwDeg_M_f32	4209.93018
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.430000007
AbsHwPos_TempHwPos_HwDeg_M_f32	-409.950012
AbsHwPos_VehCntrOffset_HwDeg_M_f32	7543.18018
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos



Test Step 3.6 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1000.25		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.600000024		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	281		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.600000024		
AbsHwPos_HwPosSource_Cnt_M_u16	4596		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1031.06006		
AbsHwPos_RelHwPos_HwDeg_M_f32	123.400002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	33949		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.600000024		
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	enontronset_GetEnorstatus_Enorstatus	
k_HwPosAuthorityStep_Uls_f32	0.140000001		
k_HwPosOutputLPFError_HwDeg_f32	1149.5		
k I2CHwAuthority Uls f32	0.140000001		
k MaxSensorlessAuthority Uls f32	0.230000001		
k MaxVehCntrOffDiff HwDeq f32	110		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_Uls_f32	0.14000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1535.59998		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998		
target AbsHwPos Per2 ManufMode Cnt enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-448.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	244		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	12895		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPer		PosReducedPerf Cnt lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target AbsHwPos Per2 HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	2 target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	123.400002	123.400002 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.460000038	0.460000008 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1395.13	1395.13 ± 0.1	-
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1395.13	1395.13 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	-
AbsHwPos_TempHwPos_HwDeg_M_f32	1395.13	1395.13 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1448.94995	1448.94995 ± 0.009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.460000038	0.460000008 ± 0.004	-
	1395.13	1395.13 ± 0.1	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1000.10		
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	-

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	5719.3999	5719.3999 ± 0.009	✓

T .				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.7 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	269.209991		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.20000003		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	605.179993		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003		
AbsHwPos_HwPosSource_Cnt_M_u16	96		
AbsHwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1507.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1507.09998		
AbsHwPos SrlComHwPosStatus Cnt M u16	21921		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003		
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCnt	rOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	436.700012		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.109999999		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k MinSensorlessAuthority Uls f32	0.20000003		
k_TurnsCntrAuthority_UIs_f32	0.200000003		
k UseTurnsCntr Cnt Igc	1		
k_VehCntrOffValidLimit_HwDeg_f32	80.3000031		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.109999999		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	120.199997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	247		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(educedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwD		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target AbsHwPos Per2 SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target AbsHwPos Per2 TurnsCntrValidity C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	_	
Name	Actual Value	Expected Value	Result
	1507.09998	1507.09998 ± 0.0009	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1001.1001	1007.0050 ± 0.0005	

524.650024

524.650024

HWPOS_STATE_SENSORINVALID

65534

65534

AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum

0 ± 0.004

65534

524.650024 ± 0.1

524.650024 ± 0.1

HWPOS_STATE_SENSORINVALID





Name	Actual Value	Expected Value	Result
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8920.66016	8920.66016 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	202.529999	202.529999 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1358.69995	1358.69995 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	524.650024	524.650024 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	120.5	120.5 ± 0.0009	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.8 (Repeat Count = 1)	
lame	Input Value
.bsHwPos_AlignedRelHwPos_HwDeg_M_f32	691.429993
.bsHwPos_HandwheelAuthority_Uls_M_f32	0.5
.bsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-666.02002
.bsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.5
.bsHwPos_HwPosSource_Cnt_M_u16	120
.bsHwPos_HwPosState_Cnt_M_enum	6
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	567.400024
.bsHwPos_RelHwPos_HwDeg_M_f32	567.400024
.bsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137
.bsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027
.bsHwPos_TargetHwAuthority_Uls_M_f32	0.5
.bsHwPos_TempHwPos_HwDeg_M_f32	120.199997
.bsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976
.bsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
.bsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
_HwPosAuthorityStep_Uls_f32	0.5
_HwPosOutputLPFError_HwDeg_f32	525.799988
_I2CHwAuthority_Uls_f32	0.5
_MaxSensorlessAuthority_Uls_f32	0.140000001
_MaxVehCntrOffDiff_HwDeg_f32	228.800003
_MinSensorlessAuthority_Uls_f32	0.5
_TurnsCntrAuthority_Uls_f32	0.5
_UseTurnsCntr_Cnt_lgc	0
VehCntrOffValidLimit_HwDeg_f32	169.399994
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	0
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	180.5
arget AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.140000001
arget AbsHwPos Per2 SensorlessHwPos HwDeg f32.value	180.5
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	14
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	211.100006
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	619
arget Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	5
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HandwheelAuthority Uls f32	target AbsHwPos Per2 HandwheelAuthority Uls f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	7 7
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc	target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 I2CHwAbsPos HwDeg f32	target AbsHwPos Per2 I2CHwAbsPos HwDeg f32
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32	target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_h	lwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	567.400024	567.400024 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.5	0.5 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	120	120	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	HWPOS_STATE_INPUTINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	120.199997	120.199997 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22137	22137	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8602.78027	8602.78027 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.5	0.5 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	120.199997	120.199997 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-980.599976	-980.599976 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.5	0.5 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	120.199997	120.199997 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	120	120	✓
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	22137	22137	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	211.100006	211.100006 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_

Test Step 3.9 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	128.470001
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.10000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	584.900024
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.10000001
AbsHwPos_HwPosSource_Cnt_M_u16	88
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1487
AbsHwPos_RelHwPos_HwDeg_M_f32	1487
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21849
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.10000001
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.100000001
k_HwPosOutputLPFError_HwDeg_f32	407
k_I2CHwAuthority_Uls_f32	0.100000001
k_MaxSensorlessAuthority_Uls_f32	0.10000001
k_MaxVehCntrOffDiff_HwDeg_f32	110
k_MinSensorlessAuthority_Uls_f32	0.100000001
k_TurnsCntrAuthority_Uls_f32	0.100000001
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	50.5999985
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	100.099998
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	0





Name	Input Value		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	90.3000031		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	123		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Others$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1487	1487 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	806.663025	806.663025 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	806.663025	806.663025 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	9026.62012	9026.62012 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	2802.53003	2802.53003 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1328.5	1328.5 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	806.663025	806.663025 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	Rte Call AhsHwPos Per2 CP1 ChecknointReached	1	-

1600

90.3000031

1600 ± 0.1

90.3000031 ± 0.00009

Test Step 3.10 (Repeat Count = 1)	v v
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	972.909973
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.69999988
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	706.580017
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.69999988
AbsHwPos_HwPosSource_Cnt_M_u16	136
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1000.29999
AbsHwPos_RelHwPos_HwDeg_M_f32	-1000.29999
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22281
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.69999988
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.69999988
k_HwPosOutputLPFError_HwDeg_f32	585.200012
k_I2CHwAuthority_Uls_f32	0.69999988
k_MaxSensorlessAuthority_Uls_f32	0.159999996

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$





Name	Input Value		
k_MaxVehCntrOffDiff_HwDeg_f32	288.200012		
k_MinSensorlessAuthority_Uls_f32	0.69999988		
k_TurnsCntrAuthority_UIs_f32	0.69999988		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	228.800003		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.159999996		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	220.699997		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	26		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	867		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.0009	~
AbsHwPos HandwheelAuthority Llls M f32	0	0 + 0 004	-

	10. g		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-1000.29999	-1000.29999 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	324.253998	324.253998 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	324.253998	324.253998 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-8390.86035	-8390.86035 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	160.399994	160.399994 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	324.253998	324.253998 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	65534	65534	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	271.5	271.5 ± 0.0009	✓

T .					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓	
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	_	

Name	Input Value	
	•	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1395.13	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.109999999	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	767.419983	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.109999999	
AbsHwPos_HwPosSource_Cnt_M_u16	160	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1547.30005	
AbsHwPos_RelHwPos_HwDeg_M_f32	1547.30005	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22497	
AbsHwPos SrlComHwPos HwDeq M f32	-8072.97021	

2017-02-06, 14:29:50+0530



Name	Input Value		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.109999999		
AbsHwPos TempHwPos HwDeg M f32	220.699997		
AbsHwPos VehCntrOffset HwDeg M f32	664.099976		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.100000001		
k HwPosOutputLPFError HwDeg f32	674.299988		
k I2CHwAuthority Uls f32	0.100000001		
k_MaxSensorlessAuthority_Uls_f32	0.189999998		
k MaxVehCntrOffDiff HwDeg f32	377.299988		
k_MinSensorlessAuthority_Uls_f32	0.100000001		
k_TurnsCntrAuthority_Uls_f32	0.100000001		
k_UseTurnsCntr_Cnt_lgc	0		
VehCntrOffValidLimit HwDeg f32	317.899994		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	281		
target AbsHwPos Per2 ManufMode Cnt enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.189999998		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	281		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	44		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	362.100006		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1239		
target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus	4		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf		PosReducedPerf Cnt Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAut		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHw	,	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid		
		inty_crit_uoo	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resul
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1547.30005	1547.30005 ± 0.0009	,
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	713.913757	713.913818 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	,
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	713.913757	713.913818 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	281	281 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.100000001	0.100000001 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	281	281 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	664.099976	664.099976 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.100000001	0.100000001 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	713.913757	713.913818 ± 0.1	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	281	281 ± 0.1	٠ ا

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~



Test Step 3.12 (Repeat Count = 1) Name	Input Value		
	·		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-409.950012 0.300000012		
AbsHwPos_HandwheelAuthority_Uls_M_f32	625.460022		
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32			
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.300000012		
AbsHwPos_HwPosSource_Cnt_M_u16	104		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1527.19995		
AbsHwPos_RelHwPos_HwDeg_M_f32	1527.19995		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21993		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.300000012		
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.300000012		
k_HwPosOutputLPFError_HwDeg_f32	466.399994		
k_I2CHwAuthority_Uls_f32	0.300000012		
k_MaxSensorlessAuthority_Uls_f32	0.119999997		
k_MaxVehCntrOffDiff_HwDeg_f32	169.399994		
k_MinSensorlessAuthority_Uls_f32	0.300000012		
k TurnsCntrAuthority Uls f32	0.30000012		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	110		
target AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	140.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.119999997		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	140.300003		
	100		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	150.699997		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	371		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16			
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3	D 1 1D 1 0 1 1	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthori		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cn	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosVali	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv	-	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthori	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta	tus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hv	vDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos AlignedHwPos HwDeg M f32	1527.19995	1527.19995 ± 0.0009	
AbsHwPos HandwheelAuthority Uls M f32	0	0 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos HwPosSource Cnt M u16	65534	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	3378.58105	3378.58105 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	
	0 0 14.7002		
AbsHwPos_TargetHwAuthority_Uls_M_f32		0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	9802.53027	9802.53027 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1388.90002	1388.90002 ± 0.009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	
arget_Abstrive os_retz_trandwheelAdthority_Ois_152.value			

2017-02-06, 14:29:50+0530





Name	Actual Value	Expected Value	Result
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	150.699997	150.699997 ± 0.0009	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.13 (Repeat Count = 1)			V
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-7306.20996		
AbsHwPos HandwheelAuthority Uls M f32	0.529999971		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	139.100006		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.529999971		
AbsHwPos_HwPosSource_Cnt_M_u16	9496		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT		
AbsHwPos PrevHandwheelPosition HwDeg M f32	389.200012		
AbsHwPos_RelHwPos_HwDeg_M_f32	389.200012		
AbsHwPos SrlComHwPosStatus Cnt M u16	25521		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	5617.33008		
AbsHwPos TargetHwAuthority Uls M f32	0.529999971		
AbsHwPos_TempHwPos_HwDeg_M_f32	1817.34998		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCni	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos	a onsot_oct_noroutus_thoroutus	
k_HwPosAuthorityStep_Uls_f32	0.519999981		
k HwPosOutputLPFError HwDeg f32	347.600006		
k I2CHwAuthority Uls f32	0.519999981		
k_MaxSensorlessAuthority_Uls_f32	0.610000014		
k_MaxVehCntrOffDiff_HwDeg_f32	139.699997		
k_MinSensorlessAuthority_Uls_f32	0.519999981		
k_TurnsCntrAuthority_UIs_f32	0		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1565.30005		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
	843.799988		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.610000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1125.19995		
	100		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value			
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-862.400024		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6447		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4	and and Dark Oak Inc.	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf (
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_UIs_f32	target_AbsHwPos_Per2_HandwheelAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwI		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_er		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_C	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	389.200012	389.200012 ± 0.0009	•

0.00999999046

HWPOS_STATE_SENSORVALID

-4173.37207

-4173.37207

-7997.64014

21845

0

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_SrlComHwPosStatus_Cnt_M_u16

AbsHwPos_SrlComHwPos_HwDeg_M_f32

AbsHwPos_TargetHwAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32

 $AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32$

0.00999999978 ± 0.004

HWPOS_STATE_SENSORVALID

-4173.37207 ± 0.1

-4173.37207 ± 0.1

21845 -7997.64014 ± 0.1

0 ± 0.004

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	-7997.64014	-7997.64014 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	691.429993	691.429993 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.00999999046	0.00999999978 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1600	-1600 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

Τ				V
Actual Function C	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached 1		Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus 1		Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock 1		Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag 1		TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus 1		Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached 1		Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•





Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStat	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.15 (Repeat Count = 1)	√
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2661,79004
AbsHwPos HandwheelAuthority Uls M f32	0.200000003
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	949.940002
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.200000003
AbsHwPos_HwPosSource_Cnt_M_u16	232
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-941.200012
AbsHwPos_RelHwPos_HwDeg_M_f32	-941.200012
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23145
AbsHwPos_SrlComHwPos_HwDeg_M_f32	972.909973
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003
AbsHwPos_TempHwPos_HwDeg_M_f32	6883.99023
AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.189999998
k_HwPosOutputLPFError_HwDeg_f32	941.599976
k_I2CHwAuthority_Uls_f32	0.189999998
k_MaxSensorlessAuthority_Uls_f32	0.280000001
k_MaxVehCntrOffDiff_HwDeg_f32	644.599976
k_MinSensorlessAuthority_Uls_f32	0.189999998
k_TurnsCntrAuthority_UIs_f32	0.189999998
k_UseTurnsCntr_Cnt_lgc	0
k_VehCntrOffValidLimit_HwDeg_f32	585.200012
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-900
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.280000001

2017-02-06, 14:29:50+0530



AbsHwPos_VehCntrValid_Cnt_M_lgc

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value



target_Pim_EOLVehont/Offset_EOLVehont/Offset_HwDeg_f32 target_Pim_EOLVehont/Offset_EOLVehont/Offset_HwDeg_f32 target_Pim_EOLVehont/Offset_EOLVehont/Offset_MwDeg_f32 target_Ric_Call_Ap_AbsHwPos_EOLVehont/Offset_GetErrorStatus_ErrorStatus target_Ric_Inst_Ap_AbsHwPos_EOLVehont/Offset_GetErrorStatus_ErrorStatus target_Ric_Inst_Ap_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cit_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LandwheelPosition_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_LandwheelPosition_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwDesSource_Crit_uf6 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Valid_Crit_Igc target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Valid_Crit_Igc target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Valid_Crit_Igc target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Valid_Crit_Igc target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_I2CHwAbsPos_Valid_Crit_Igc target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uis_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uis_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Crit_uf6 target_AbsHwPos_Per2_SensorlessAuthority_Uis_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Crit_uf6 target_AbsHwPos_Per2_SifComHwPosStatus_Crit_uf6 target_AbsHwPos_Per2_SifComHwPosHwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SifComHwPosStatus_Crit_uf6 target_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPosStatus_Crit_uf6 target_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_SifComHwPos_HwDeg_f32 target_Ric_Inst_Ap_AbsHwPos_Per2_Mid_f32 0.189999998 0.189999998 0.004 0.189999998 0.004 0.1900 0.10 0.189	Name	Input Value		
target_Pim_EOLVehCntrOffset_EOLt-whostrimPerformed_cnt_u16	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	461.899994		
target_Pim_EOLVehCntrOffset_EOLHwPos_TrimPerformed_Cnt_u16 2355 target_Rite_Oall_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus 1 target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cat_get_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132 target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132 target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132 target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_132 target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosVaid_Cnt_igc target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosVaid_Cnt_igc target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosVaid_Cnt_igc target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosVaid_Cnt_igc target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsPosVaid_Cnt_igc target_Rite_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IZCHwAbsHwPos_Per2_IZCHwAbsHwPos_Per	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	98		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus_ target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_DalgStatusHwPosReducedPerf_Cataget_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPostion_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPostion_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ICHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ICHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ICHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_ICHwAbsPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	633.900024		
target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_DiagStatusHwPosReducedPerf_Ctarget_Rte_Inst_Ap_AbshwPos_Per2_HandwheelPosition_HwDeg_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_HandwheelPosition_HwDeg_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_HandwheelPosition_HwDeg_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_HwPosSource_Cnt_u16_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_I2CHwAbsPosValid_Cnt_lgc_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_I2CHwAbsPosValid_Cnt_lgc_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_I2CHwAbsPosValid_Cnt_lgc_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_I2CHwAbsPos_RwDeg_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_Rte_Inst_Ap_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_SensorlessAtuhority_Uls_ris2_target_AbshwPos_Per2_S	target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2355		
target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelAuthority_UIs_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_HwPosSource_Cnt_u16 target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos_AbsHwPos_Per2_IndHwAbsPos_Per2_IndHw	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	1		
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target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_Pin_EOLVehCntrOffset Name Actual Value Expected Value Result AbsHwPos_AlignedHwPos_HwDeg_M_f32 -941.200012 -941.200012 ± 0.0009 ✓ AbsHwPos_HandwheelAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_HwPosSource_Cnt_M_u16 2 2 ✓ AbsHwPos_HwPosState_Cnt_M_enum HWPOS_STATE_SENSORVALID HWPOS_STATE_SENSORVALID ✓ AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TargetHwAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ <	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 Name Actual Value Expected Value Result AbsHwPos_AlignedHwPos_HwDeg_M_f32 -941.200012 -941.200012 ± 0.0009 ✓ AbsHwPos_HandwheelAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_HwPosSource_Cnt_M_u16 2 2 ✓ AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_SflComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_TargetHwAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
Name Actual Value Expected Value Result AbsHwPos_AlignedHwPos_HwDeg_M_f32 -941.200012 -941.200012 ± 0.0009 ✓ AbsHwPos_HandwheelAuthority_Uis_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_HandwheelPositionLPF_M_str.SV_Uis_f32 -900 -900 ± 0.1 ✓ AbsHwPos_HwPosSource_Cnt_M_u16 2 2 2 AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TargetHwAuthority_Uis_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
Name Actual Value Expected Value Result AbsHwPos_AlignedHwPos_HwDeg_M_f32 -941.200012 -941.200012 ± 0.0009 ✓ AbsHwPos_HandwheelAuthority_Uis_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 -900 -900 ± 0.1 ✓ AbsHwPos_HwPosSource_Cnt_M_u16 2 2 2 AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TargetHwAuthority_Uis_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
AbsHwPos_HandwheelAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 -900 -900 ± 0.1 AbsHwPos_HwPosSource_Cnt_M_u16 2 2 AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 AbsHwPos_TargetHwAuthority_Uls_M_f32 -900 -900 ± 0.1 AbsHwPos_TargetHwAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_TempHwPos_HwDeg_M_f32 990 -900 ± 0.1 AbsHwPos_TempHwPos_HwDeg_M_f32 990 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	Name	Actual Value	Expected Value	Result
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 -900 -900 ± 0.1 ✓ AbsHwPos_HwPosSource_Cnt_M_u16 2 2 ✓ AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_TargetHwAuthority_Uls_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TargetHwAuthority_Uls_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	AbsHwPos_AlignedHwPos_HwDeg_M_f32	-941.200012	-941.200012 ± 0.0009	•
AbsHwPos HwPosSource_Cnt_M_u16 2 2 AbsHwPos_HwPosState_Cnt_M_enum HWPOS_STATE_SENSORVALID +WPOS_STATE_SENSORVALID AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 AbsHwPos_TargetHwAuthority_UIs_M_f32 -900 -900 ± 0.1 AbsHwPos_TargetHwAuthority_UIs_M_f32 0.189999998 0.18999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	AbsHwPos_HandwheelAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	✓
AbsHwPos HwPosState Cnt_M_enum HWPOS_STATE_SENSORVALID HWPOS_STATE_SENSORVALID AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_TargetHwAuthority_UIs_M_f32 0.189999998 0.18999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-900	-900 ± 0.1	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_TargetHwAuthority_UIs_M_f32 0.189999998 0.189999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	AbsHwPos_HwPosSource_Cnt_M_u16	2	2	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 21845 21845 ✓ AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_TargetHwAuthority_UIs_M_f32 0.189999998 0.189999998 ± 0.004 ✓ AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 ✓ AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_TargetHwAuthority_Uls_M_f32 0.189999998 0.18999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-900	-900 ± 0.1	•
AbsHwPos_TargetHwAuthority_UIs_M_f32 0.189999998 0.18999998 ± 0.004 AbsHwPos_TempHwPos_HwDeg_M_f32 -900 -900 ± 0.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009	AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_TempHwPos_HwDeg_M_f32	AbsHwPos_SrlComHwPos_HwDeg_M_f32	-900	-900 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32 935.900024 935.900024 ± 0.0009 ✓	AbsHwPos_TargetHwAuthority_Uls_M_f32	0.189999998	0.189999998 ± 0.004	~
	AbsHwPos_TempHwPos_HwDeg_M_f32	-900	-900 ± 0.1	~
AbsHwPos VehCntrOfstl earn Cnt M Inc 0	AbsHwPos_VehCntrOffset_HwDeg_M_f32	935.900024	935.900024 ± 0.0009	~
Addition 50_venerial electronic mage	AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

633.900024

0.189999998

504

-900

21845

-900

0

2

none

none

none

21845

-900 ± 0.1

-900 ± 0.1

0.189999998 ± 0.004

633.900024 ± 0.0009

Test Step 3.16 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1958.08997
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	848.539978
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.150000006
AbsHwPos_HwPosSource_Cnt_M_u16	192
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	126.699997
AbsHwPos_RelHwPos_HwDeg_M_f32	126.699997
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22785
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.140000001
k_HwPosOutputLPFError_HwDeg_f32	793.099976
k_I2CHwAuthority_Uls_f32	0.140000001





Name	Input Value		
k_MaxSensorlessAuthority_Uls_f32	0.230000004		
k_MaxVehCntrOffDiff_HwDeg_f32	496.100006		
k_MinSensorlessAuthority_Uls_f32	0.140000001		
k_TurnsCntrAuthority_UIs_f32	0.140000001		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	436.700012		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	461.899994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.230000004		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	-1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	68		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1735		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Output Ap_AbsHwPosAbsHw$	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32$	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	~
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	~
Aballupas HupasSauras Cat M u16	102	102	

target_re_met_rp_mer.mr een mi_Letvenenden	targot_:EoEvolionaronoot		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	126.699997	126.699997 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	301.100006	301.100006 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	192	192	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	269.209991	269.209991 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.150000006	0.150000006 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	784.900024	784.900024 ± 0.0009	•
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.150000006	0.150000006 ± 0.004	✓
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	301.100006	301.100006 ± 0.1	✓
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	192	192	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	269.209991	269.209991 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	482.899994	482.899994 ± 0.0009	~

τ -				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

Test Step 3.17 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	3787.70996
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001
AbsHwPos_HwPosSource_Cnt_M_u16	296
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998

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Name	Input Value		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_Uls_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	1		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_Uls_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	3	Andread Dark Out Inc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_(
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority	· - -	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_ target_AbsHwPos_Per2_I2CHwAbsPosValid		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hwl		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_el		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hwl		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 TurnsCntrValidity Cnt u08	target AbsHwPos Per2 TurnsCntrValidity C		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target Pim EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32	1207.09998	1207.09998 + 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	•
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	-
AbsHwPos HwPosSource Cnt M u16	3	3	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos PrevHandwheelPosition HwDeg M f32	622.700012	622.700012 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008	2098.83008 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	622.700012	622.700012 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996	-1132.57996 ± 0.009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.360000014	0.360000014 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	622.700012	622.700012 ± 0.1	•
	I -	I -	1 .

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

21845

1600

875.5

21845

1600 ± 0.1

875.5 ± 0.0009

 $target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value\\ target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32$



Test Step 3.18 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	4209.93018		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.310000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1173.02002		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.310000002		
AbsHwPos_HwPosSource_Cnt_M_u16	320		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-448.700012		
AbsHwPos_RelHwPos_HwDeg_M_f32	-448.700012		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23937		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.310000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.200000003		
k_HwPosOutputLPFError_HwDeg_f32	1268.30005		
k_I2CHwAuthority_Uls_f32	0.200000003		
k_MaxSensorlessAuthority_Uls_f32	0.38999986		
k_MaxVehCntrOffDiff_HwDeg_f32	1060.40002		
k_MinSensorlessAuthority_Uls_f32	1		
k_TurnsCntrAuthority_Uls_f32	0.200000003		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	911.900024		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-212.300003		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.38999986		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	683		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	966.099976		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3719		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_Erro$	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPe	rf_(target_AbsHwPos_Per2_DiagStatusHwF	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutl	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_i	32 target_AbsHwPos_Per2_HandwheelPos	ition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	_HwDegf32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Ci	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	nority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3	target_AbsHwPos_Per2_SensorlessHwF	Pos_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u1	6 target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Res
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-448.700012	-448.700012 ± 0.0009	
AbsHwPos_HandwheelAuthority_UIs_M_f32	0.10999999	0.109999999 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	3541.94092	3541.94092 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	320	320	
AbsHwPos HwPosState Cnt M enum	HWPOS STATE SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	3541.94092	3541.94092 ± 0.1	
AbsHwPos SrlComHwPosStatus Cnt M u16	65534	65534	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2521.05005	2521.05005 ± 0.1	
AbsHwPos_TargetHwAuthority_UIs_M_f32	0	0 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	8814.7002	8814.7002 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	8814.7002	8814.7002 ± 0.009	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	
AbsHwPos_venCntrOistLearn_Cnt_w_igc AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	
	504	504	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	
		1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	•	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.109999999	0.109999999 ± 0.004	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		•	

320

320

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Name	Actual Value	Expected Value	Result
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	1600	1600 ± 0.1	✓
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535	65535 ± 0.09	✓

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.19 (Repeat Count = 1)			✓
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1535.87		
AbsHwPos HandwheelAuthority Uls M f32	0.119999997		
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	787.700012		
AbsHwPos HandwheelPositionLPF M str.K Uls f32	0.119999997		
AbsHwPos_HwPosSource_Cnt_M_u16	168		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002		
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977		
AbsHwPos_TargetHwAuthority_UIs_M_f32	0.119999997		
AbsHwPos TempHwPos HwDeg M f32	240.800003		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCr	htrOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep Uls f32	0.109999999		
k_HwPosOutputLPFError_HwDeg_f32	704		
k I2CHwAuthority Uls f32	0.109999999		
k MaxSensorlessAuthority Uls f32	0.20000003		
k_MaxVehCntrOffDiff_HwDeg_f32	407		
k_MinSensorlessAuthority_UIs_f32	0.109999999		
k_TurnsCntrAuthority_UIs_f32	0.109999999		
k UseTurnsCntr Cnt Igc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_UIs_f32.value	0.20000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target AbsHwPos Per2 HandwheelPosition		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 HwPosSource Cnt u16	target_AbsHwPos_Per2_HwPosSource_Cnt		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target AbsHwPos Per2 I2CHwAbsPosValid	_	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_UIs_f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDog f32			
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	<u>uuu</u>	
Name	Actual Value	Expected Value	Result
AbsHwPos AlignedHwPos HwDeg M f32		Expected Value	Nesuit
AbsHwPos_HandwheelAuthority_Llls_M_f32	1567.40002	1567.40002 ± 0.0009	

0.200000003

729.307983

HWPOS_STATE_SENSORINVALID

AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32

AbsHwPos_HandwheelAuthority_Uls_M_f32

AbsHwPos_HwPosSource_Cnt_M_u16

AbsHwPos_HwPosState_Cnt_M_enum

0.200000003 ± 0.004 729.307983 ± 0.1

HWPOS_STATE_SENSORINVALID





Name	Actual Value	Expected Value	Result
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.20000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	301.100006	301.100006 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988	392.299988 ± 0.0009	~

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	2802.53003
AbsHwPos HandwheelAuthority Uls M f32	0.20999993
AbsHwPos HandwheelPositionLPF M str.SV Uls f32	970.219971
bsHwPos HandwheelPositionLPF M str.K Uls f32	0.20999993
AbsHwPos HwPosSource Cnt M u16	240
.bsHwPos_HwPosState_Cnt_M_enum	HWPOS STATE SENSORINVALID
.bsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-921.5
AbsHwPos RelHwPos HwDeg M f32	-921.5
bsHwPos SrlComHwPosStatus Cnt M u16	23217
ubsHwPos SrlComHwPos HwDeg M f32	1113.65002
.bsHwPos TargetHwAuthority Uls M f32	0.20999993
ubsHwPos TempHwPos HwDeg M f32	7024.72998
AbsHwPos VehCntrOffset HwDeg M f32	966.099976
AbsHwPos VehCntrOfstLearn Cnt M Igc	1
AbsHwPos VehCntrValid Cnt M Igc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target Rte Call Ap AbsHwPos EOLVehCntrOffset GetErrorStatus ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
HwPosAuthorityStep Uls f32	0.20000003
HwPosOutputLPFError HwDeg f32	971.299988
: I2CHwAuthority Uls f32	0.20000003
MaxSensorlessAuthority Uls f32	0.289999992
MaxVehCntrOffDiff HwDeg f32	674.299988
MinSensorlessAuthority Uls f32	0.20000003
TurnsCntrAuthority Uls f32	0.200000003
UseTurnsCntr Cnt lgc	0
: VehCntrOffValidLimit HwDeq f32	614.900024
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
arget AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	900
arget_AbsHwPos Per2 ManufMode Cnt enum.value	1
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.289999992
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	482
arget AbsHwPos Per2 TurnsCntrValidity Cnt u08.value	104
arget Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	664.099976
arget Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	2479
arget_Fill_LOCVenCriticinset.EOENWF0sTrilliFellorifleu_Crit_u10 arget_Fill_LOCVenCriticinset.EOENWF0sTrilliFellorifleu_Crit_u10	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReduce	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_	
arget_Rte_inst_ap_abshwPos.AbshwPos_Per2_handwheelPosition_ty_bis_ arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_handwheelPosition_HwDe	
arget_Rte_inst_Ap_AbshwPos.AbshwPos_Per2_handwrieeiPosition_hwDe arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cnt u16
·	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_I	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f3	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority UIs t	target_AbsHwPos_Per2_ManufMode_Cnt_enum f32 target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32





Name	Input Value			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset			
Name	Actual Value	Expected Value	Result	
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-921.5	-921.5 ± 0.0009	~	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	955.473755	955.473816 ± 0.1	~	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	955.473755	955.473816 ± 0.1	~	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	900	900 ± 0.1	~	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~	
AbsHwPos_TempHwPos_HwDeg_M_f32	900	900 ± 0.1	~	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	966.099976	966.099976 ± 0.0009	~	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	~	
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~	
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	955.473755	955.473816 ± 0.1	~	
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	2	2	~	
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~	
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	900	900 ± 0.1	~	
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	664.099976	664.099976 ± 0.0009	~	

Т				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.21 (Repeat Count = 1)	✓
Name	Input Value
AbsHwPos AlignedRelHwPos HwDeg M f32	-2239.57007
AbsHwPos HandwheelAuthority Uls M f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.159999996
k_HwPosOutputLPFError_HwDeg_f32	852.5
k_I2CHwAuthority_Uls_f32	0.159999996
k_MaxSensorlessAuthority_Uls_f32	0.25
k_MaxVehCntrOffDiff_HwDeg_f32	555.5
k_MinSensorlessAuthority_Uls_f32	0.159999996
k_TurnsCntrAuthority_UIs_f32	0.159999996
k_UseTurnsCntr_Cnt_lgc	1
k_VehCntrOffValidLimit_HwDeg_f32	496.100006
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0

 $target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value$

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$

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AbsHwPos Per2



AbsHwPos_Per2		MAC	1606
Name	Input Value		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	4		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_Inst_Ap_AbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbsHwPosReducedPertarget_Rte_InstApAAbs$	rf_(target_AbsHwPos_Per2_DiagStatusHwF	osReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuth	ority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3000000000000000000000000000000000000$	32 target_AbsHwPos_Per2_HandwheelPos	tion_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_	Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos\	/alid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cr	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuth	ority_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f3.	2 target_AbsHwPos_Per2_SensorlessHwF	os_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosS	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidi	ty_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓

T				
				~
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	•
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	-
Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	Rte Call AbsHwPos Per2 CP1 CheckpointReached	1	✓

0.25

0

3

0

21845

65535

0.25 ± 0.004

65535 ± 0.09

0 ± 0.1

0 ± 0.1

3 21845

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-1676.60999
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.12999995
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-807.97998
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.129999995
AbsHwPos_HwPosSource_Cnt_M_u16	176
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-901.799988
AbsHwPos_RelHwPos_HwDeg_M_f32	-901.799988
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22641
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7861.0498
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.129999995
AbsHwPos_TempHwPos_HwDeg_M_f32	260.899994
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.119999997





Name	Input Value		
k_HwPosOutputLPFError_HwDeg_f32	733.700012		
k_I2CHwAuthority_Uls_f32	0.119999997		
k_MaxSensorlessAuthority_Uls_f32	0.209999993		
k_MaxVehCntrOffDiff_HwDeg_f32	436.700012		
k_MinSensorlessAuthority_Uls_f32	0.119999997		
k_TurnsCntrAuthority_UIs_f32	0.119999997		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	377.299988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.100000001		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	321.200012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1487		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	0		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	ty_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	n_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	t_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	d_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosState	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	•
AbsHwPos HandwheelAuthority Uls M f32	0 119999997	0 119999997 + 0 004	

target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	-901.799988	-901.799988 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	-1015.08685	-1015.08691 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	1	1	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	-1015.08685	-1015.08691 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	~
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997	0.119999997 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	-2401.10986	-2401.11011 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	724.5	724.5 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.119999997	0.119999997 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	-1015.08685	-1015.08691 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	1	1	•
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	•
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	-1600	-1600 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	422.5	422.5 ± 0.0009	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 3.23 (Repeat Count = 1)		~
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	3787.70996	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.280000001	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1112.18005	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.280000001	
AbsHwPos_HwPosSource_Cnt_M_u16	296	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	





Name	Input Value		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1207.09998		
AbsHwPos_RelHwPos_HwDeg_M_f32	1207.09998		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	23721		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	2098.83008		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.280000001		
AbsHwPos_TempHwPos_HwDeg_M_f32	8009.91016		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1132.57996		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0		
AbsHwPos_VehCntrValid_Cnt_M_lgc	0		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVe	ehCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.270000011		
k_HwPosOutputLPFError_HwDeg_f32	1179.19995		
k_I2CHwAuthority_Uls_f32	0.270000011		
k_MaxSensorlessAuthority_Uls_f32	0.340000004		
k_MaxVehCntrOffDiff_HwDeg_f32	882.200012		
k_MinSensorlessAuthority_Uls_f32	0.270000011		
k_TurnsCntrAuthority_UIs_f32	0.270000011		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	822.799988		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	-271.399994		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.360000014		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	622.700012		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	100		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	875.5		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3347		
$target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus$	3		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerfunction{ $	_(target_AbsHwPos_Per2_DiagStatusHw	PosReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAut	hority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f3222222222222222222222222222222222222$	2 target_AbsHwPos_Per2_HandwheelPos	sition_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource	_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPos	Valid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C	nt_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAut	hority_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHw	Pos_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPos	Status_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	lity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1207.09998	1207.09998 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.360000014	0.360000014 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	622.700012	622.700012 ± 0.1	-
AbsHwPos_HandwheelPositionLPF_M_str.SV_UIs_f32 AbsHwPos_HwPosSource_Cnt_M_u16	622.700012	622.700012 ± 0.1	-
			•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	•
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	3 HWPOS_STATE_SENSORVALID	3 HWPOS_STATE_SENSORVALID	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012 21845	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845	0
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004	0
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_UIs_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_Igc AbsHwPos_VehCntrValid_Cnt_M_Igc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504 1	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504 1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504 1 1 0.360000014	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504 1 1 0.360000014 ± 0.004	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504 1 1 0.360000014 622.700012	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504 1 1 0.360000014 ± 0.004 622.700012 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504 1 1 0.360000014 622.700012 3	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504 1 1 0.360000014 ± 0.004 622.700012 ± 0.1 3	
AbsHwPos_HwPosSource_Cnt_M_u16 AbsHwPos_HwPosState_Cnt_M_enum AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 AbsHwPos_SrlComHwPos_HwDeg_M_f32 AbsHwPos_TargetHwAuthority_Uls_M_f32 AbsHwPos_TempHwPos_HwDeg_M_f32 AbsHwPos_VehCntrOffset_HwDeg_M_f32 AbsHwPos_VehCntrOffstLearn_Cnt_M_lgc AbsHwPos_VehCntrValid_Cnt_M_lgc Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	3 HWPOS_STATE_SENSORVALID 622.700012 21845 2098.83008 0.360000014 622.700012 -1132.57996 1 0 504 1 1 0.360000014 622.700012	3 HWPOS_STATE_SENSORVALID 622.700012 ± 0.1 21845 2098.83008 ± 0.1 0.360000014 ± 0.004 622.700012 ± 0.1 -1132.57996 ± 0.009 1 0 504 1 1 0.360000014 ± 0.004 622.700012 ± 0.1	



Τ				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 3.24 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
AbsHwPos_HwPosSource_Cnt_M_u16	208		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	ntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.159999996		
k_HwPosOutputLPFError_HwDeg_f32	852.5		
k_I2CHwAuthority_Uls_f32	0.159999996		
k_MaxSensorlessAuthority_Uls_f32	0.25		
k_MaxVehCntrOffDiff_HwDeg_f32	555.5		
_MinSensorlessAuthority_Uls_f32	0.159999996		
<_TurnsCntrAuthority_Uls_f32	0.159999996		
k_UseTurnsCntr_Cnt_lgc	1		
x_VehCntrOffValidLimit_HwDeg_f32	496.100006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32	65535		
target Pim EOLVehCntrOffset.EOLHwPosTrimPerformed Cnt u16	1983		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	4		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_u		ReducedPerf Cnt lac	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthor		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target AbsHwPos Per2 HwPosSource Cr		
rarget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVali	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hv		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 ManufMode Cnt enum	target AbsHwPos Per2 ManufMode Cnt		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessAuthority Uls f32	target AbsHwPos Per2 SensorlessAuthori		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SensorlessHwPos HwDeg f32	target_AbsHwPos_Per2_SensorlessHwPos	<i>'</i>	
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 SrlComHwPosStatus Cnt u16			
	target_AbsHwPos_Per2_SrlComHwPosSta target_AbsHwPos_Per2_SrlComHwPos_Hv		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_	CIII_uvo	
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	l=	
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
Abolly Dog Torget Liv Authority I II M 122	0.25	0.25 + 0.004	

0.25

AbsHwPos_TargetHwAuthority_Uls_M_f32

0.25 ± 0.004

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	✓
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535	65535 ± 0.09	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2239.57007
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002
AbsHwPos_HwPosSource_Cnt_M_u16	208
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1
AbsHwPos_VehCntrValid_Cnt_M_lgc	1
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
K HwPosAuthorityStep Uls f32	0.159999996
K HwPosOutputLPFError HwDeg f32	852.5
C I2CHwAuthority Uls f32	0.159999996
	0.25
MaxVehCntrOffDiff HwDeg f32	555.5
MinSensorlessAuthority Uls f32	0.159999996
C TurnsCntrAuthority UIs f32	0.159999996
 UseTurnsCntr Cnt Igc 	1
Cocramocha_she_go VehCntrOffValidLimit HwDeq f32	496.100006
arget AbsHwPos Per2 DiagStatusHwPosReducedPerf Cnt Igc.value	0
arget_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1
arget AbsHwPos Per2 I2CHwAbsPos HwDeg f32.value	502.100006
arget AbsHwPos Per2 ManufMode Cnt enum.value	0
arget AbsHwPos Per2 SensorlessAuthority Uls f32.value	0.25
arget_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0
	80
arget_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	11
arget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535
arget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_enum
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32

2017-02-06, 14:29:50+0530



Name	Input Value		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	~
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	•
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	•
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value			

target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value$

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

AbsHwPos_Per2



Name	Input Value		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_0	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthorit	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc	target_AbsHwPos_Per2_I2CHwAbsPosValid	_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e	num	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthority	y_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStatu	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw	Deg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0	Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.0009	-
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.00999999791	0.00999999978 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	722.072021	722.072021 ± 0.1	✓
AbsHwPos_HwPosSource_Cnt_M_u16	168	168	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	✓
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	722.072021	722.072021 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977	7967.00977 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003	240.800003 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	✓
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

0.00999999791

722.072021

392.299988

168

65534

1600

0.00999999978 ± 0.004

722.072021 ± 0.1

392.299988 ± 0.0009

168

65534

1600 ± 0.1

Took Ston 2.27 (Bonnet Count = 4)	
Test Step 3.27 (Repeat Count = 1)	*
Name	Input Value
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	1535.87
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.119999997
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	787.700012
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.119999997
AbsHwPos_HwPosSource_Cnt_M_u16	168
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1567.40002
AbsHwPos_RelHwPos_HwDeg_M_f32	1567.40002
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22569
AbsHwPos_SrlComHwPos_HwDeg_M_f32	7967.00977
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.119999997
AbsHwPos_TempHwPos_HwDeg_M_f32	240.800003
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0
AbsHwPos_VehCntrValid_Cnt_M_lgc	0
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos
k_HwPosAuthorityStep_Uls_f32	0.109999999
k_HwPosOutputLPFError_HwDeg_f32	704
k_I2CHwAuthority_UIs_f32	0.109999999
k_MaxSensorlessAuthority_Uls_f32	0.31999993
k_MaxVehCntrOffDiff_HwDeg_f32	407
k_MinSensorlessAuthority_Uls_f32	0.10999999
k_TurnsCntrAuthority_UIs_f32	0.109999999

2017-02-06, 14:29:50+0530





Name	Input Value		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	347.600006		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	1		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.200000003		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	301.100006		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	50		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	392.299988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1363		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	5		
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Diag$	target_AbsHwPos_Per2_DiagStatusHwPo	sReducedPerf_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAutho	rity_Uls_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	on_HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_C	nt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosVa	lid_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt	_enum	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAutho	rity_Uls_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32$	target_AbsHwPos_Per2_SensorlessHwPo	s_HwDeg_f32	
$target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16$	target_AbsHwPos_Per2_SrlComHwPosSta	atus_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H	wDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity	_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	1567.40002	1567.40002 ± 0.0009	•
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	729.307983	729.307983 ± 0.1	~
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	~
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	729.307983	729.307983 ± 0.1	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	•
AbsHwPos_SrlComHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.200000003	0.200000003 ± 0.004	~
AbsHwPos_TempHwPos_HwDeg_M_f32	301.100006	301.100006 ± 0.1	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	694.299988	694.299988 ± 0.0009	~
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.200000003	0.200000003 ± 0.004	~
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	729.307983	729.307983 ± 0.1	~
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

301.100006

392.299988

301.100006 ± 0.1

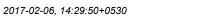
392.299988 ± 0.0009

Test Step 3.28 (Repeat Count = 1)		✓
Name	Input Value	
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	10125	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	1600	
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	1	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INPUTINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	1600	
AbsHwPos_RelHwPos_HwDeg_M_f32	10125	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	11725	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	
AbsHwPos_TempHwPos_HwDeg_M_f32	11725	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

 $target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32$

AbsHwPos_Per2





ADSHWP0S_Per2		(dec	1000
Name	Input Value		
AbsHwPos VehCntrOfstLearn Cnt M lgc	1		
AbsHwPos VehCntrValid Cnt M Igc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehCn	trOffset GetErrorStatus ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k HwPosAuthorityStep UIs f32	1		
k_HwPosOutputLPFError_HwDeg_f32	1620		
k_I2CHwAuthority_Uls_f32	1		
k_MaxSensorlessAuthority_Uls_f32	1		
k MaxVehCntrOffDiff HwDeg f32	1600		
k_MinSensorlessAuthority_Uls_f32	1		
k TurnsCntrAuthority UIs f32	1		
k UseTurnsCntr Cnt Igc	1		
k VehCntrOffValidLimit HwDeg f32	1600		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	900		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	2		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1600		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	255		
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
target Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf	target_AbsHwPos_Per2_DiagStatusHwPosF	ReducedPerf Cnt Igc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target AbsHwPos Per2 HandwheelAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPosition	HwDeg_f32	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt	_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid	I_Cnt_lgc	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_e		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthorit		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosStati	us_Cnt_u16	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_Hw		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_0		
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_AlignedHwPos_HwDeg_M_f32	10125	10125 ± 0.0009	_
AbsHwPos_HandwheelAuthority_Uls_M_f32	0	0 ± 0.004	✓
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	11725	11725 ± 0.1	_
AbsHwPos HwPosSource Cnt M u16	65534	65534	✓
AbsHwPos HwPosState_Cnt_M_enum	HWPOS STATE INPUTINVALID	HWPOS_STATE_INPUTINVALID	
AbsHwPos PrevHandwheelPosition HwDeg M f32	11725	11725 ± 0.1	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65535	65535	
AbsHwPos SrlComHwPos HwDeq M f32	0	0 ± 0.1	✓
AbsHwPos TargetHwAuthority Uls M f32	0	0 ± 0.004	_
AbsHwPos TempHwPos HwDeg M f32	11725	11725 ± 0.1	•
AbsHwPos VehCntrOffset HwDeg M f32	65535	65535 ± 0.09	
AbsHwPos VehCntrOfstLearn Cnt M Igc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	0	0	-
Rte Call Ap AbsHwPos NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	0	0	-
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0	0 ± 0.004	
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	1600	1600 ± 0.1	
target AbsHwPos Per2 HwPosSource Cnt u16.value	65534	65534	
target_AboliviDec_Der2_CrlComliviDecCtetus_Crt_vi16_value	65535	65555	-

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
TrimNotPerfDiag	1	TrimNotPerfDiag	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	•

65535

65535

0

65535

0 ± 0.1

65535 ± 0.09

✓
Input Value
7024.72998

 $target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value$

 $target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32$

target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value

AbsHwPos_Per2

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ADSHWPOS_Per2		IMAC	Mad
Name	Input Value		
AbsHwPos HandwheelAuthority Uls M f32	0.50999999		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	114.300003		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.50999999		
AbsHwPos_HwPosSource_Cnt_M_u16	480		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	320.100006		
AbsHwPos_RelHwPos_HwDeg_M_f32	320.100006		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	25377		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	-5335.8501		
AbsHwPos TargetHwAuthority Uls M f32	0.50999999		
AbsHwPos TempHwPos HwDeg M f32	1535.87		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012		
AbsHwPos VehCntrOfstLearn Cnt M Igc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)		hCntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.5		
k_HwPosOutputLPFError_HwDeg_f32	288.200012		
k_I2CHwAuthority_Uls_f32	1		
k MaxSensorlessAuthority Uls f32	0.589999974		
k MaxVehCntrOffDiff HwDeg f32	80.3000031		
k MinSensorlessAuthority Uls f32	0.5		
k_TurnsCntrAuthority_Uls_f32	0.5		
k_UseTurnsCntr_Cnt_lgc	0		
k VehCntrOffValidLimit HwDeg f32	1505.90002		
target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target AbsHwPos Per2 I2CHwAbsPosValid Cnt Igc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	803.599976		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value			
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	1 0.58999974		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	1085		
	28		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value target Pim EOLVehCntrOffset.EOLVehCntrOffset HwDeg f32			
- ·	-901.799988		
target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6199		
target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus		PooPoduoodPorf Cot Igo	
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_t			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32			
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cnt_u16		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_C		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuti		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwF		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValid	ity_Cnt_u08	
target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	320.100006	320.100006 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	465.843018	465.842987 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	2	2	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORVALID	HWPOS_STATE_SENSORVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	465.843018	465.842987 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	1	1 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	803.599976	803.599976 ± 0.1	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-409.950012	-409.950012 ± 0.0009	
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1	1	•
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	•
Pte Call An AhsHwPos NytrDiagMar SetNTCStatus(NTC Cnt T enum)	504	*none*	

504

0

0

465.843018

803.599976

-901.799988

21845

none

none

none 1 ± 0.004

2

21845

465.842987 ± 0.1

803.599976 ± 0.1

-901.799988 ± 0.0009

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 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)$

Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)

 $target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value$ target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value

target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value

 $target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value$ target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32

target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value

 $Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)$



T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	-
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	✓

Test Step 3.30 (Repeat Count = 1)			
Name	Input Value		
AbsHwPos_AlignedRelHwPos_HwDeg_M_f32	-2239.57007		
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.170000002		
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	889.099976		
AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32	0.170000002		
AbsHwPos_HwPosSource_Cnt_M_u16	208		
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID		
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	151.5		
AbsHwPos_RelHwPos_HwDeg_M_f32	151.5		
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	22929		
AbsHwPos_SrlComHwPos_HwDeg_M_f32	550.690002		
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.170000002		
AbsHwPos_TempHwPos_HwDeg_M_f32	6461.77002		
AbsHwPos_VehCntrOffset_HwDeg_M_f32	845.299988		
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	1		
AbsHwPos_VehCntrValid_Cnt_M_lgc	1		
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus)	target_Rte_Call_Ap_AbsHwPos_EOLVehC	CntrOffset_GetErrorStatus_ErrorStatus	
Rte_Inst_Ap_AbsHwPos	target_Rte_Inst_Ap_AbsHwPos		
k_HwPosAuthorityStep_Uls_f32	0.159999996		
k_HwPosOutputLPFError_HwDeg_f32	852.5		
k_I2CHwAuthority_UIs_f32	0.159999996		
x_MaxSensorlessAuthority_Uls_f32	0.25		
<_MaxVehCntrOffDiff_HwDeg_f32	555.5		
<_MinSensorlessAuthority_Uls_f32	0.159999996		
x_TurnsCntrAuthority_Uls_f32	0.159999996		
<pre>c_UseTurnsCntr_Cnt_lgc</pre>	1		
c_VehCntrOffValidLimit_HwDeg_f32	496.100006		
arget_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value	0		
target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value	1		
target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value	502.100006		
target_AbsHwPos_Per2_ManufMode_Cnt_enum.value	0		
target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value	0.25		
target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value	0		
target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value	80		
rarget_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988		
rarget_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1983		
arget_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus	2		
arget Rte Inst Ap AbsHwPos.AbsHwPos Per2 DiagStatusHwPosReducedPerf			
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32	target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32	target_AbsHwPos_Per2_HandwheelPositio		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16	target_AbsHwPos_Per2_HwPosSource_Cr	_	
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc	target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32	target_AbsHwPos_Per2_I2CHwAbsPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum	target_AbsHwPos_Per2_ManufMode_Cnt_		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32	target_AbsHwPos_Per2_SensorlessAuthor		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32	target_AbsHwPos_Per2_SensorlessHwPos		
target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16	target_AbsHwPos_Per2_SrlComHwPosSta		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32	target_AbsHwPos_Per2_SrlComHwPos_H		
arget_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08	target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08		
arget_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	target_Pim_EOLVehCntrOffset	1=	12
Name	Actual Value	Expected Value	Resu
AbsHwPos_AlignedHwPos_HwDeg_M_f32	151.5	151.5 ± 0.0009	
AbsHwPos_HandwheelAuthority_Uls_M_f32	0.25	0.25 ± 0.004	
AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32	0	0 ± 0.1	
AbsHwPos_HwPosSource_Cnt_M_u16	3	3	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_SENSORINVALID	HWPOS_STATE_SENSORINVALID	
AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	21845	21845	
AbsHwPos_SrlComHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos_TargetHwAuthority_Uls_M_f32	0.25	0.25 ± 0.004	
AbsHwPos_TempHwPos_HwDeg_M_f32	0	0 ± 0.1	
AbsHwPos VehCntrOffset HwDeg M f32	65535	65535 + 0.00	

65535

AbsHwPos_VehCntrOffset_HwDeg_M_f32

65535 ± 0.09

2017-02-06, 14:29:50+0530



Name	Actual Value	Expected Value	Result
AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc	0	0	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value	0.25	0.25 ± 0.004	•
target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value	0	0 ± 0.1	•
target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value	3	3	~
target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value	21845	21845	~
target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value	0	0 ± 0.1	~
target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	543.299988	543.299988 ± 0.0009	•

T				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached	1	~
Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus	1	✓
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓
Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached	1	~

2017-02-06, 14:23:37+0530





Project AbsHwPos

ModuleAbsHwPos_Tcl2IVdTest ObjectAbsHwPos_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G
Configuration File	D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(SOURCEROOT)\Ap_AbsHwPos.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\NxtrLib\include

Name	Text
Module 'AbsHwPos_Tcl2IVd'	Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 2: "CBD_Sandbox_dbg,map" file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	Cycles
Timer Enabled	False

2017-02-06, 14:23:37+0530



Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metrics Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS1.1 1113.00 Cycles TS1.2 1347.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

 $If (Abs_m(Rte_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) => False + k_VehCntrOffValidLimit_HwDeg_f32) => False$

TS1.2 Longest Execution Path:

 $If (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) >> True (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLvehCntrOffset.EOLve$

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	า	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Т				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt	_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_e	num	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatu	s_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓

65534

65534





T					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.1 (Repeat Count = 1)			~
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	0		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	0		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~

T ·				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

65534

65534



Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	500		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	43690		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•

Τ					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cn	t_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStar	tus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	-
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	·
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	-
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	-
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	-
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
· -			

T				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	_

65534

65534

Test Step 2.4 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	25.18166721	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	436.9878821	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0	

2017-02-06, 14:23:37+0530



Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	65535		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	589		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	า	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

Τ				
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039.01001		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	ce_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	_Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwP	osStatus_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039.01001	1039.01001 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	



Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	379.2717962		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	1201.851037		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	0		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1519		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enui	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	-
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	·
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	0	0 ± 0.000009	·
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	·
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	•
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	-

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	401.1355943		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	436.9807251		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-700.02		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1984		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum	1	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

65534

65534

Test Step 2.8 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	162.9279281	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	0	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2	

2017-02-06, 14:23:37+0530



Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	879.9430598		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2449		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enun	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_0	Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	•
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	487.3372427		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1600		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1555.129529		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	2914		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enu	ım	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-1555.12952	-1555.129529 ± 0.009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	•
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~



Test Step 2.10 (Repeat Count = 1)			✓	
Name	Input Value			
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos			
k_HwPosOutputLPFCoeffFc_Hz_f32	402.9378512			
k_UseTurnsCntr_Cnt_lgc	1			
k_VehCntrOffValidLimit_HwDeg_f32	1434.02			
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0			
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	835.0762143			
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3379			
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	e_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPo	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result	
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~	
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓	
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓	
AbsHwPos_VehCntrOffset_HwDeg_M_f32	835.076233	835.0762143 ± 0.0009	✓	
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	✓	
oo oo oo oooo				
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓	

T				V
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~

Test Step 2.11 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	49.55192		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1007.182555		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	3844		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSourc	e_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt AbsHwPos Init1 HwPosSource Cnt u16.value	65534	65534	✓
9			

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.12 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	500	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	516.6709843	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1	

2017-02-06, 14:23:37+0530



Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	669.6303178		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4309		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enu	m	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_	_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	0.212779999		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	1264.996348		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1454.825825		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	4774		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource	e_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_	Cnt_enum	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	✓
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	✓
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	✓
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~



Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	433.7		
k_UseTurnsCntr_Cnt_lgc	1		
k_VehCntrOffValidLimit_HwDeg_f32	678		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	0		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	564.5		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5239		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	564.5	564.5 ± 0.0009	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

Τ					
Actual Function	Count	Expected Function	Count	Result	
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~	
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	~	

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	78		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	984		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-255		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	5704		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	✓
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	-255	-255 ± 0.0009	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	*none*	✓
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	*none*	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~

Test Step 2.16 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos	
k_HwPosOutputLPFCoeffFc_Hz_f32	64	
k_UseTurnsCntr_Cnt_lgc	1	
k_VehCntrOffValidLimit_HwDeg_f32	127.6	
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1	

2017-02-06, 14:23:37+0530



AbsHwPos_Init1

Name	Input Value		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	987		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	6169		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u1	6	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enur	n	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16		
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	✓
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	0	✓
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	✓

T				✓
Actual Function	Count	Expected Function	Count	Result
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	✓

Test Case 3: Path Test

Specification

"Performance Metrics (With "None" Instrumentation and WithPS Environment"

TS3.1 1113.00 Cycles TS3.2 1347.00 Cycles

Description

Vector Description:

 $TS3.1 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) = False \\ TS3.2 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) = True \\ TS3.2 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) = True \\ TS3.2 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) = True \\ TS3.2 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) = True \\ TS3.2 \quad (Abs_m(Rte_Pim_EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32) <= k_VehCntrOffSet_HwDeg_f32) <= k_VehCntrOffS$

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos		
k_HwPosOutputLPFCoeffFc_Hz_f32	393.0287978		
k_UseTurnsCntr_Cnt_lgc	0		
k_VehCntrOffValidLimit_HwDeg_f32	710.623859		
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	1		
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	-1600		
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	124		
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_	u16	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_er	ium	
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatu	s_Cnt_u16	
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset		
Name	Actual Value	Expected Value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	•
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	•
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	•

Name	Actual value	Expected value	Result
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	~
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~
AbsHwPos_VehCntrOffset_HwDeg_M_f32	65535	65535 ± 0.09	~
AbsHwPos_VehCntrValid_Cnt_M_lgc	0	0	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	*none*	•
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0	*none*	~
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0	*none*	~
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	~

T							
Actual Function	Count	Expected Function	Count	Result			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~			





Test Step 3.2 (Repeat Count = 1) ✓								
Name	Input Value							
Rte_Inst_Ap_AbsHwPos	tgt_Rte_Inst_Ap_AbsHwPos							
k_HwPosOutputLPFCoeffFc_Hz_f32	259.5259765							
k_UseTurnsCntr_Cnt_lgc	1							
k_VehCntrOffValidLimit_HwDeg_f32	1396.947509							
tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value	2							
tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32	1039							
tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16	1054							
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16	tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16							
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum	tgt_AbsHwPos_Init1_ManufMode_Cnt_enum							
tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16	tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16							
tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset	tgt_Pim_EOLVehCntrOffset							
Name	Actual Value	Expected Value	Result					
AbsHwPos_HwPosSource_Cnt_M_u16	65534	65534	~					
AbsHwPos_HwPosState_Cnt_M_enum	HWPOS_STATE_INIT	HWPOS_STATE_INIT	•					
AbsHwPos_SrlComHwPosStatus_Cnt_M_u16	65534	65534	~					
AbsHwPos_VehCntrOffset_HwDeg_M_f32	1039	1039 ± 0.009	•					
AbsHwPos_VehCntrValid_Cnt_M_lgc	1	1	~					
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	504	504	~					
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1	1	~					
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1	1	~					
tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value	65534	65534	~					
tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value	65534	65534	•					

Τ							
Actual Function	Count	Expected Function	Count	Result			
TrimNotPerfDiag	1	TrimNotPerfDiag	1	~			
Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus	1	•			