Summary

Total Test Objects:	1
Successful:	1
Failed:	0
Not Executed:	0
Date:	2016-01-10
Time:	13:08:00+0530

Overall Test Object Results (including Coverage)

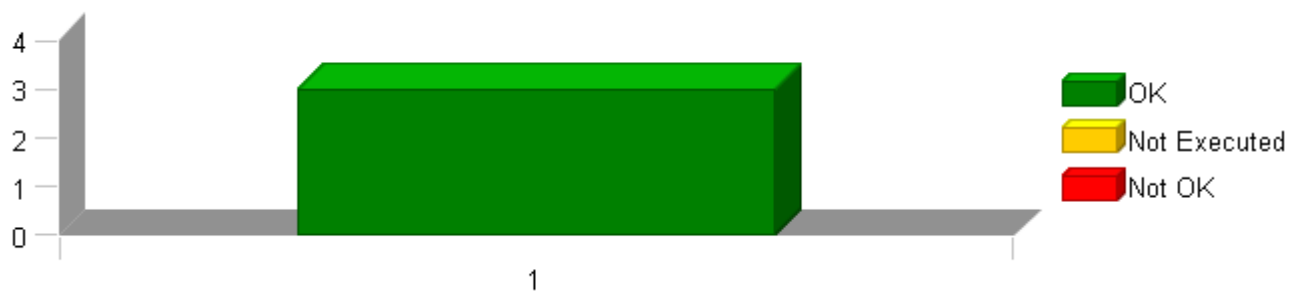

- OK
- Not Executed
- Not OK
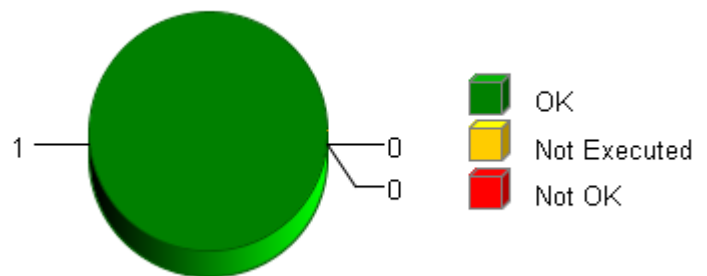
Selected Project Items

Test Object "CBD_UnitTest/MtrCtrl_CM_QuadDet/QuadDet_Per1"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)

- OK
- Not Executed
- Not OK

The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object
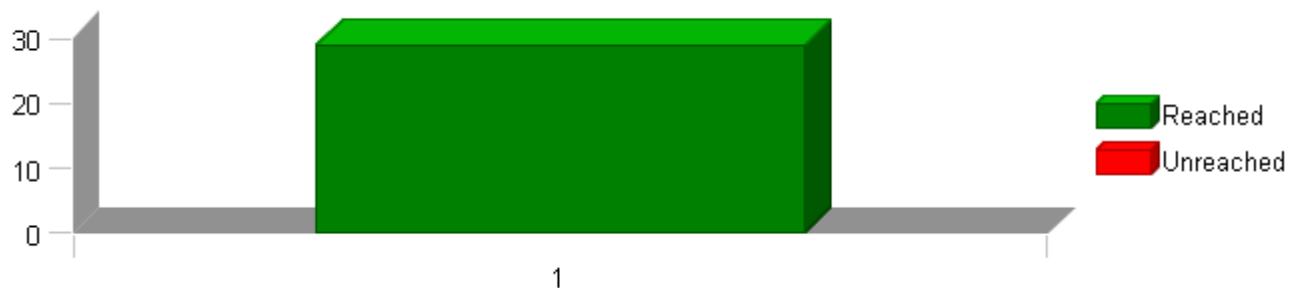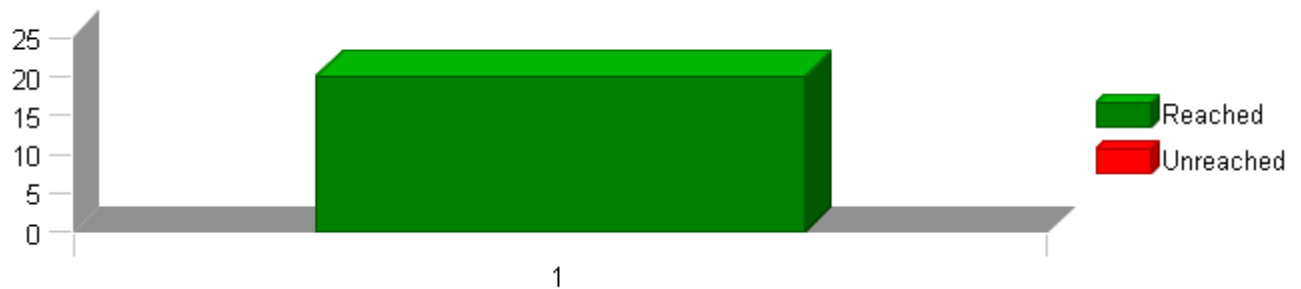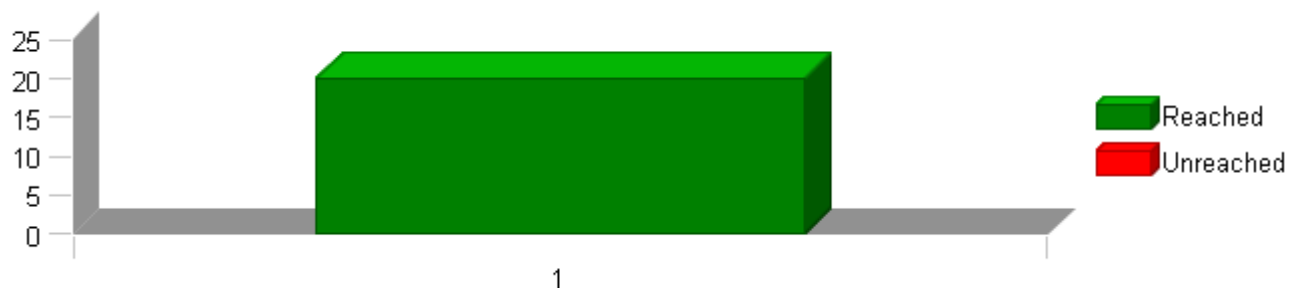

- Reached
- Unreached

The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.
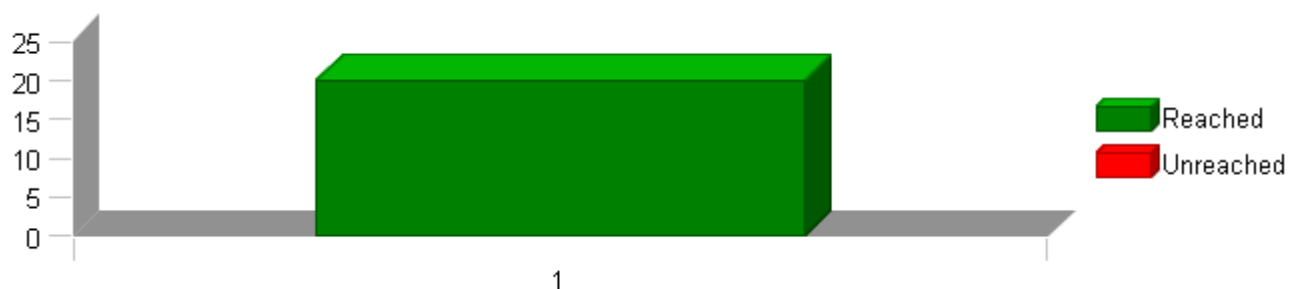
Decision Coverage: Total Decision Outcomes for Each Test Object

The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.
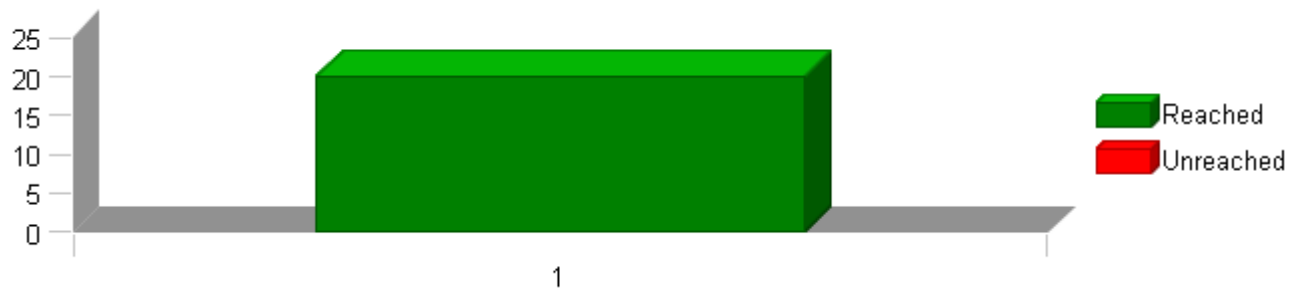
MCC Coverage: Total Condition Combinations for Each Test Object

The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	MtrCtrl	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
	MtrCtrl_CM_QuadDet	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔
1	QuadDet_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	✔

Project	MtrCtrl
Module	MtrCtrl_CM_QuadDet
Test Object	QuadDet_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3
Successful	3 ✔
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	$(PROJECTROOT)\MtrCtrl_CM\src\Ap_QuadDet.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I$(PROJECTROOT)\NxtrLib\include -I$(PROJECTROOT)\StdDef\include -I$(PROJECTROOT)\MtrCtrl_CM\include -I$(PROJECTROOT)\MtrCtrl_CM\utp\contract -I$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_QuadDet -I$(Compiler Install Path)\include

Comments/Description/Specification

Name	Text
Module 'MtrCtrl_CM_QuadDet'	**Unit Test Information** Name of Tester: Priti Mangalekar Code File(s) Under Test: Ap_QuadDet.c Code File(s) Version: 9 Module Design Document: Quadrant_Detection_MDD.docx Module Design Document Version:2 Data Dictionary Version: 13 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):376 Total RAM Used (Bytes):6 Total CALS Used (Bytes):1026 Special Test Requirements: Test Date:01/10/2016 Comments:"Note1: Inline function defined in Global Macro.h are not unit tested. Note2:"CBD_Sandbox_dbg.map" map file is embedded for reference. **

Attributes

Name	Value
Compiler Install Path	$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1

Attributes	
Name	**Value**
UDE Config File	$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

Test Case 1: Metrics Test ✔

Specification

```
Performance Metrics:
(With "None" Instrumentation and "WithPS"
Environment)

CPU Cycles:

TS 1.1  585.00 Cycles   Shortest Execution Path
TS 1.2  611.00 Cycles   Longest Execution Path
```

Description

Vector Description:

```
TS 1.1  "Shortest Execution Path:
if( DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F))=>True
SignDeltaMotPos_Cnt_T_s08 = (sint8)Sign_f32_m(DeltaMotPos_Deg_T_f32);=>False
if( PrevInstMtrDir_Cnt_M_s08 == SignDeltaMotPos_Cnt_T_s08 )=>True
if( Abs_f32_m(MtrTrqCmdScl_MtrNm_T_f32) > D_MTRTRQCMDTOL_MTRNM_F32 )=>False
if( MtrTrqCmdSign_Cnt_D_s08 == PrevInstMtrDir_Cnt_M_s08 )=>True
if( 1 == MtrTrqCmdSign_Cnt_D_s08 )=>False"
TS 1.2  "Longest Execution Path:
if( DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F))=>False
else if( DeltaMotPos_Deg_T_f32 < -(CumMtrPosLmt_Deg_T_f32/2.0F) )=>True
SignDeltaMotPos_Cnt_T_s08 = (sint8)Sign_f32_m(DeltaMotPos_Deg_T_f32);=>True
if( PrevInstMtrDir_Cnt_M_s08 == SignDeltaMotPos_Cnt_T_s08 )=>False
else if( AbsDeltaMotPos_Deg_T_f32 >= k_InstMtrDirHyst_Deg_f32 )=>True
if( Abs_f32_m(MtrTrqCmdScl_MtrNm_T_f32) > D_MTRTRQCMDTOL_MTRNM_F32 )=>True
MtrTrqCmdSign_Cnt_D_s08 = (sint8)Sign_f32_m(MtrTrqCmdScl_MtrNm_T_f32);=>True
if( MtrTrqCmdSign_Cnt_D_s08 == PrevInstMtrDir_Cnt_M_s08 )=>True
if( 1 == MtrTrqCmdSign_Cnt_D_s08 )=>True"
```

Test Step 1.1 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	32
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	5
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0.0001
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	11797200	11797200	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 1.2 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	20
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	0
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T					✔
Actual Function	**Count**	**Expected Function**		**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached		1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached		1	✔

Test Case 2: Boundary Test ✔

Specification
```
Performance Metrics:
(With "None" Instrumentation and "WithPS"
Environment)

CPU Cycles:

TS 2.1   595.00 Cycles
TS 2.2   607.00 Cycles
TS 2.3   598.00 Cycles
TS 2.4   594.00 Cycles
TS 2.5   606.00 Cycles
TS 2.6   592.00 Cycles
TS 2.7   606.00 Cycles
TS 2.8   605.00 Cycles
TS 2.9   607.00 Cycles
TS 2.10   607.00 Cycles
TS 2.11   608.00 Cycles
TS 2.12   593.00 Cycles
TS 2.13   606.00 Cycles
TS 2.14   607.00 Cycles
TS 2.15   606.00 Cycles
TS 2.16   592.00 Cycles
TS 2.17   606.00 Cycles
TS 2.18   605.00 Cycles
TS 2.19   606.00 Cycles
TS 2.20   607.00 Cycles
TS 2.21   603.00 Cycles
TS 2.22   607.00 Cycles
TS 2.23   606.00 Cycles
TS 2.24   605.00 Cycles
TS 2.25   594.00 Cycles
TS 2.26   605.00 Cycles
TS 2.27   606.00 Cycles
TS 2.28   608.00 Cycles
TS 2.29   609.00 Cycles
TS 2.30   606.00 Cycles
TS 2.31   592.00 Cycles
```

Description
```
Vector Description:

TS 2.1MRFMtrTrqCmdScl_MtrNm_f32=Min
TS 2.2MRFMtrTrqCmdScl_MtrNm_f32=Max
TS 2.3MRFMtrTrqCmdScl_MtrNm_f32=Zero
TS 2.4MRFMtrTrqCmdScl_MtrNm_f32=Pos
TS 2.5MRFMtrTrqCmdScl_MtrNm_f32=Neg
TS 2.6 PrevCumMtrPos=Min
TS 2.7PrevCumMtrPos=Max
TS 2.8 PrevCumMtrPos=Zero
TS 2.9 PrevCumMtrPos=Neg
TS 2.10PrevCumMtrPos=Pos
TS 2.11MRFCumMtrPos=Min
TS 2.12MRFCumMtrPos=Max
TS 2.13 MRFCumMtrPos=Zero
TS 2.14MRFCumMtrPos=Neg
TS 2.15MRFCumMtrPos=Pos
TS 2.16MtrTrqCmdSign_Cnt_D_s08=Min
TS 2.17MtrTrqCmdSign_Cnt_D_s08=Max
TS 2.18MtrTrqCmdSign_Cnt_D_s08=Zero
TS 2.19PrevInstMtrDir_Cnt_M_s08=Min
TS 2.20PrevInstMtrDir_Cnt_M_s08=Max
TS 2.21PrevInstMtrDir_Cnt_M_s08= Zero
TS 2.22k_InstMtrDirHyst_Deg_f32 =min
TS 2.23k_InstMtrDirHyst_Deg_f32 =max
TS 2.24k_InstMtrDirHyst_Deg_f32 =middle/Default
TS 2.25MRFMtrVel_MtrRadpS_f32=min
TS 2.26MRFMtrVel_MtrRadpS_f32=max
TS 2.27MRFMtrVel_MtrRadpS_f32=zero
TS 2.28MRFMtrVel_MtrRadpS_f32=pos
TS 2.29MRFMtrVel_MtrRadpS_f32=neg
TS 2.30All Min
TS 2.31All Max
```

Test Step 2.1 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	1
PrevCumMtrPos_Deg_M_f32	5556.02
PrevInstMtrDir_Cnt_M_s08	1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	2
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	34876.3
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32

Name	Input Value			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08			

Name	Actual Value	Expected Value		Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1		✔
PrevCumMtrPos_Deg_M_f32	34876.3008	34876.3		✔
PrevInstMtrDir_Cnt_M_s08	1	1		✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1		✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4		✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.2 (Repeat Count = 1) ✔

Name	Input Value			
MtrTrqCmdSign_Cnt_D_s08	0			
PrevCumMtrPos_Deg_M_f32	26372.3			
PrevInstMtrDir_Cnt_M_s08	1			
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet			
k_InstMtrDirHyst_Deg_f32	1			
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2347.4			
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	8.8			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08			

Name	Actual Value	Expected Value		Result
MtrTrqCmdSign_Cnt_D_s08	1	1		✔
PrevCumMtrPos_Deg_M_f32	2347.3999	2347.4		✔
PrevInstMtrDir_Cnt_M_s08	-1	-1		✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1		✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2		✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.3 (Repeat Count = 1) ✔

Name	Input Value			
MtrTrqCmdSign_Cnt_D_s08	-1			
PrevCumMtrPos_Deg_M_f32	4377.4			
PrevInstMtrDir_Cnt_M_s08	1			
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet			
k_InstMtrDirHyst_Deg_f32	3			
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3828.21			
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08			

Name	Actual Value	Expected Value		Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1		✔
PrevCumMtrPos_Deg_M_f32	3828.20996	3828.21		✔
PrevInstMtrDir_Cnt_M_s08	-1	-1		✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1		✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3		✔

T				✔

Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.4 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	11828.1
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	4
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	8547.1
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5.6
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	8547.09961	8547.1	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.5 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	3848.2
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	2
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	28382.02
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.3
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	28382.0195	28382.02	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.6 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	-11797200
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	2.3
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	1000.3
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	2.3
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	1000.29999	1000.3	✔

Name	Actual Value	Expected Value	Result
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.7 (Repeat Count = 1)   ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	11797200
PrevInstMtrDir_Cnt_M_s08	0
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	5
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	10000.14
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	10000.1396	10000.14	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.8 (Repeat Count = 1)   ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	0
PrevInstMtrDir_Cnt_M_s08	0
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	6
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	6565.32
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	6565.31982	6565.32	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.9 (Repeat Count = 1)   ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	-3728.21

Name	Input Value		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4336.41		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4336.41016	4336.41	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.10 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	4837.32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2.4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4326.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7.4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4326.2998	4326.3	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.11 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	12000.14		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.12 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	-12000.21		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3.5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	11797200	11797200	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.13 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	3728.3		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	0		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	0	0	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.14 (Repeat Count = 1)		✔
Name	**Input Value**	
MtrTrqCmdSign_Cnt_D_s08	0	
PrevCumMtrPos_Deg_M_f32	9938.7	
PrevInstMtrDir_Cnt_M_s08	0	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	3	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-27382.21	

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	-27382.2109	-27382.21	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.15 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	2837.08		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	37478.1		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	37478.1016	37478.1	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.16 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	3848.09		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	38283.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	38283.3008	38283.3	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.17 (Repeat Count = 1)   ✔

Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	9858.21		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1.5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.21		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	5483.20996	5483.21	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.18 (Repeat Count = 1)   ✔

Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	258.32		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293.32		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	293.320007	293.32	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.19 (Repeat Count = 1)   ✔

Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	2932.12		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3.3		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.32		

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	3929.32007	3929.32	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.20 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	5848.03		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4358.5		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4358.5	4358.5	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.21 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	14587.21		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	14587.6		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5.4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	14587.21	14587.21	✔
PrevInstMtrDir_Cnt_M_s08	0	0	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	0	0	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.22 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	9858.32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.1		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	5483.1001	5483.1	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.23 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	258.12		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.25		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293.2		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	293.200012	293.2	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.24 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	1
PrevCumMtrPos_Deg_M_f32	2932.2
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	2.5
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.03

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	3929.03003	3929.03	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.25 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	9858.02
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	1.2
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.8
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	3929.80005	3929.8	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T			✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.26 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	258.3
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	3.5
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4358.9
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7.4
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4358.8999	4358.9	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.27 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	2932.4		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	14587.4		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	14587.4004	14587.4	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.28 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	5848		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.03		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	5483.02979	5483.03	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.29 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	14587.04
PrevInstMtrDir_Cnt_M_s08	0
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	4
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	293	293	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.30 (Repeat Count = 1)   ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	-11797200		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 2.31 (Repeat Count = 1)   ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	11797200		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.25		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	11797200	11797200	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Case 3: Path Test ✔

Specification

```
Performance Metrics:
(With "None" Instrumentation and "WithPS"
Environment)

CPU Cycles:

TS 3.1    592.00 Cycles
TS 3.2    601.00 Cycles
TS 3.3    607.00 Cycles
TS 3.4    590.00 Cycles
TS 3.5    592.00 Cycles
TS 3.6    601.00 Cycles
TS 3.7    592.00 Cycles
TS 3.8    605.00 Cycles
TS 3.9    604.00 Cycles
TS 3.10   599.00 Cycles
TS 3.11   604.00 Cycles
TS 3.12   596.00 Cycles
TS 3.13   607.00 Cycles
TS 3.14   605.00 Cycles
```

Description

Vector Description:

TS 3.1"if(DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F)) = False
"
TS 3.2 if(PrevInstMtrDir_Cnt_M_s08 == SignDeltaMotPos_Cnt_T_s08) = False
TS 3.3(DeltaMotPos_Deg_T_f32 >= 0.0) = False
TS 3.4if(1 == MtrTrqCmdSign_Cnt_D_s08) = FALSE
TS 3.5 If (PrevInstMtrDir_Cnt_M_s08 = SignDeltaMotPos_Cnt_T_s08) =TRUE
TS 3.6(AbsDeltaMotPos_Deg_T_f32 >= k_InstMtrDirHyst_Deg_f32) =True
TS 3.7If (MtrTrqCmdSign_Cnt_D_s08 = PrevInstMtrDir_Cnt_M_s08) = TRUE
TS 3.8if(AbsDeltaMotPos_Deg_T_f32 >= k_InstMtrDirHyst_Deg_f32)= FALSE
TS 3.9if(DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F)) = TRUE
TS 3.10else if(DeltaMotPos_Deg_T_f32 < -(CumMtrPosLmt_Deg_T_f32/2.0F)) = TRUE
TS 3.11else if(AbsDeltaMotPos_Deg_T_f32 >= k_InstMtrDirHyst_Deg_f32) = FALSE
TS 3.12if(1 == MtrTrqCmdSign_Cnt_D_s08) = FALSE
TS 3.13(1 == MtrTrqCmdSign_Cnt_M_s08)=False
TS 3.14(1 == MtrTrqCmdSign_Cnt_M_s08)=False

Test Step 3.1 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4000		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4000	4000	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.2 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	75
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet

Name	Input Value		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	87		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	87	87	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.3 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	10		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	2	2	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.4 (Repeat Count = 1)			✔
Name	**Input Value**		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	47		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	38		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	38	38	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.5 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	19		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	19		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	19	19	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.6 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	22		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	43		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	0	0	✔
PrevCumMtrPos_Deg_M_f32	43	43	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.7 (Repeat Count = 1) ✔

Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	1
PrevCumMtrPos_Deg_M_f32	3
PrevInstMtrDir_Cnt_M_s08	1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet
k_InstMtrDirHyst_Deg_f32	2
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4	4	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.8 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	32		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4000		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	4000	4000	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.9 (Repeat Count = 1) ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	-20		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✔
PrevCumMtrPos_Deg_M_f32	11797200	11797200	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✔

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✓
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 3.10 (Repeat Count = 1) ✓

Name	**Input Value**	
MtrTrqCmdSign_Cnt_D_s08	1	
PrevCumMtrPos_Deg_M_f32	20	
PrevInstMtrDir_Cnt_M_s08	1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	0	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200	
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08	

Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✓
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✓
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1) ✓

Name	**Input Value**	
MtrTrqCmdSign_Cnt_D_s08	-1	
PrevCumMtrPos_Deg_M_f32	2	
PrevInstMtrDir_Cnt_M_s08	-1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	2	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2	
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08	

Name	**Actual Value**	**Expected Value**	**Result**
MtrTrqCmdSign_Cnt_D_s08	1	1	✓
PrevCumMtrPos_Deg_M_f32	2	2	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

T				✓
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✓
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 3.12 (Repeat Count = 1) ✓

Name	**Input Value**	
MtrTrqCmdSign_Cnt_D_s08	-1	
PrevCumMtrPos_Deg_M_f32	32	
PrevInstMtrDir_Cnt_M_s08	-1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	0	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	20	

Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-0.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	20	20	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.13 (Repeat Count = 1)   ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	14587.04		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	293	293	✔
PrevInstMtrDir_Cnt_M_s08	-1	-1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔

Test Step 3.14 (Repeat Count = 1)   ✔

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	5556.02		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	34876.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		

Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✔
PrevCumMtrPos_Deg_M_f32	34876.3008	34876.3	✔
PrevInstMtrDir_Cnt_M_s08	1	1	✔
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✔
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✔

T				✔
Actual Function	**Count**	**Expected Function**	**Count**	**Result**
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	✔
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✔