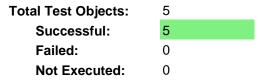
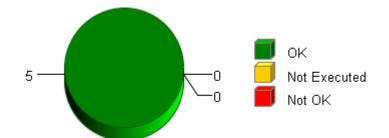


### Summary

# **Overall Test Object Results (including Coverage)**



**Date:** 2014-09-05 **Time:** 18:13:34+0530



## **Selected Project Items**

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Init"

Test Object "CBD UnitTest/AstLmt/AstLmt Per1"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_GetSteeringAssistDefeat"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_ManualTrqCmd"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_SetSteeringAssistDefeat"

#### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

### **Batch Operation Settings**

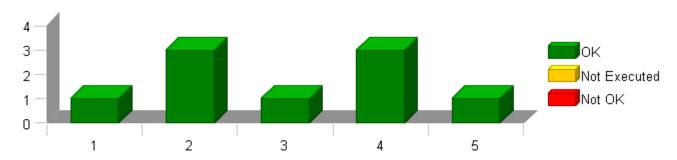
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

**Instrumentation:** Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

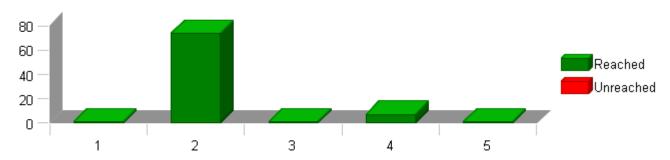
### Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

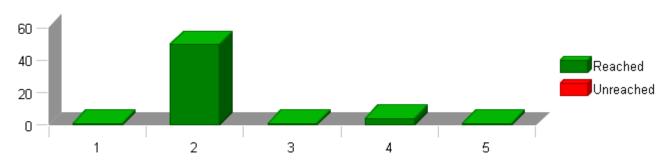


# Statement (C0) Coverage: Total Statements for Each Test Object



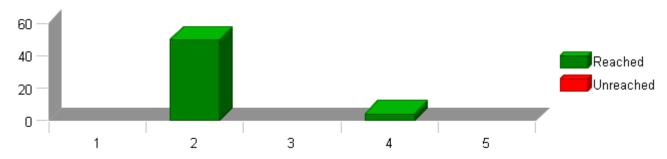
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

# Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

# **Decision Coverage: Total Decision Outcomes for Each Test Object**

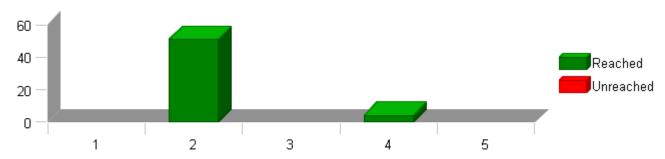


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



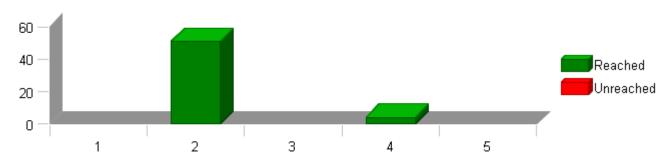
# MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



# **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Result
	AstLmt_CM	100 %	100 %	100 %	100 %	100 %	9 of 9 passed
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	9 of 9 passed
	AstLmt	100 %	100 %	100 %	100 %	100 %	9 of 9 passed
1	AstLmt_Init	100 %	100 %	-	-	-	1 of 1 passed
2	AstLmt_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
3	AstLmt Scom GetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed
4	AstLmt Scom ManualTrqCmd	100 %	100 %	100 %	100 %	100 %	3 of 3 passed
5	AstLmt Scom SetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed

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AstLmt\_Scom\_GetSteeringAssistDefeat

 Project
 AstLmt\_CM

 Module
 AstLmt

Test Object AstLmt\_Scom\_GetSteeringAssistDefeat

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Comments/Description/Specification	
Name	Text
Module 'AstLmt'	Name of Tester Aif Shrivestava
	Name of Tester-Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942
	Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	

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AstLmt\_Scom\_GetSteeringAssistDefeat

Attributes		
Name	Value	
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 20.00 Cycles TS1.2 20.00 Cycles

**Description** Vector Description:

TS1.1 Rte\_Pim\_SteerAsstDefeat = True TS1.2 Rte\_Pim\_SteerAsstDefeat = False

Test Step 1.1 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc	
tgt_Pim_SteerAsstDefeat	1	
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat	
Name	Actual Value Expected Value	Result
tgt_SteeringAsstDefeat_Cnt_lgc	1	~

Test Step 1.2 (Repeat Count = 1)		<b>✓</b>
Name	Input Value	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc	
tgt_Pim_SteerAsstDefeat	0	
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat	
Name	Actual Value Expect	ed Value Result
tgt_SteeringAsstDefeat_Cnt_lgc	0 0	<b>✓</b>

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AstLmt\_Init

 Project
 AstLmt\_CM

 Module
 AstLmt

 Test Object
 AstLmt\_Init

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes			
Name	Value		
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5		
Float Precision	9		
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj		
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src		
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>		
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl		
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>		
Time Unit	Cycles		
Timer Enabled	false		
Timer Prescale	0		
Timer Resolution	1		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		

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AstLmt\_Init

Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

Environment

CPU Cycles:

TS1.1 25.00 Cycles TS1.2 25.00 Cycles

**Description** Vector Description:

TS1.1 SteeringAsstDefeat1\_Cnt\_lgc = True TS1.2 SteeringAsstDefeat1\_Cnt\_lgc = False

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	1		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
gt_Pim_SteerAsstDefeat 0			
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat tgt_Pim_SteerAsstDefeat			
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	0	~

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AstLmt\_Scom\_ManualTrqCmd

 Project
 AstLmt\_CM

 Module
 AstLmt

Test Object AstLmt\_Scom\_ManualTrqCmd

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM		
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml		
Target Environment	TI TMS 570 PLS UDE (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c		
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include		

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava
	Code File(s) Under Test:Ap_AstLmt.c
	Code File(s) Version:9
	Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13
	Data Dictionary Version: 9
	Unit Test Plan Version:6
	Optimization Level:Level 2
	Compiler (CodeGen) Version:TMS470_4.9.5
	Model Type:Excel Macro
	Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942
	Total RAM Used (Bytes):6
	Total CALS Used (Bytes):8
	Special Test Requirements:
	Test Date:09-05-2014
	Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested.  NOTE 2 : ""CBD Sandbox dbg.map"" map file is embedded for reference."

Attributes				
Name Value				
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>			
Float Precision	9			
InitObjDir	ObjDir \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	inker File \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2			
Time Unit	Cycles			
Timer Enabled	false			

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Attributes		
Name	Value	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



#### Test Case 1: Matrix Test

AstLmt\_Scom\_ManualTrqCmd

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS1.1 269.00 Cycles Shortest Execution Path TS1.2 274.00 Cycles Longest Execution Path

Description Vector Description:

TS1.1 "Shortest Execution Path:
( TRUE == EnableManualCtrl ) =False"
TS1.2 "Longest Execution Path:
( TRUE == EnableManualCtrl ) = True
( VehSpd\_Kph\_T\_f32 < FLT\_EPSILON ) = True"

Test Step 1.1 (Repeat Count = 1)			✓	
Name		Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc		0		
AstLmt_ManualTrqCmd_MtrNm_M_f32		1		
EnableManualCtrl		0		
MtrTrqCmd_MtrNm_f32		3		
R	V_mt_Manupd_Kph_T_f3< F		R	V_mt_Ma



#### AstLmt\_Scom\_ManualTrqCmd

### Test Case 2: Boundary Test

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS2.1 TS2.2 TS2.3 TS2.4

269.00 Cycles 272.00 Cycles 275.00 Cycles 275.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 272.00 Cycles 272.00 Cycles 269.00 Cycles 272.00 Cycles TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11

#### Vector Description: Description

TS2.1 EnableManualCtrl = min TS2.2 EnableManualCtrl = max

TS2.2 EnableManualCtrl = max
TS2.3 MtrTrqCmd\_MtrNm\_f32 = min
TS2.4 MtrTrqCmd\_MtrNm\_f32 = zero
TS2.5 MtrTrqCmd\_MtrNm\_f32 = zero
TS2.6 MtrTrqCmd\_MtrNm\_f32 = neg
TS2.7 MtrTrqCmd\_MtrNm\_f32 = pos
TS2.8 VehSpd\_Kph\_f32 = min
TS2.9 VehSpd\_Kph\_f32 = max
TS2.10 VehSpd\_Kph\_f32 = pos
TS2.11 All min
TS2.12 All max

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-15.9949999		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3.4000001		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Veh	nSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54.6699982		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	~
AstLmt Scom ManualTrgCmd()	1	1	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6.19999981		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29.9200001		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999	-12.9949999 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	34	34	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.3 (Repeat Count = 1)		•
Name	Input Value	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-9.99499989	
EnableManualCtrl	1	

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Name	Input Value	Input Value		
MtrTrqCmd_MtrNm_f32	-16	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008	3.45999993e-008		
Name	Actual Value	Expected Value	Result	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	<b>✓</b>	
AstLmt_Scom_ManualTrqCmd()	1	1	~	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	6.38000017e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	<b>✓</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	0		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	7.27999989e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	✓

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-0.995000005		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-2.5		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	5.28000008e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



Test Step 2.7 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	4.55999994		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Veh	nSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	8.38000034e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.55999994	4.55999994 ± 0.0009765625	<b>~</b>
AstLmt_Scom_ManualTrqCmd()	1	1	~

Τ				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.1199999		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	<b>✓</b>
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.1199999	15.1199999 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-3.67000008		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011	8.00500011 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	34	34	~

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	9.82999992		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Vel	nSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	123.260002		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001	11.0050001 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	34	34	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	1	1	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	34	34	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



#### Test Case 3: Path Coverage Test

**Specification** Performance Metrics:

AstLmt\_Scom\_ManualTrqCmd

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS3.1 269.00 Cycles TS3.2 272.00 Cycles TS3.3 275.00 Cycles

**Description** Vector Description:

TS3.1 (TRUE == EnableManualCtrl) = False
TS3.2 "(TRUE == EnableManualCtrl) = True
and(VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)=False"
TS3.3 (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)=True

Test Step 3.1 (Repeat Count = 1)	✓
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	1
EnableManualCtrl	0
MtrTrqCmd_MtrNm_f32	3
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54

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Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Comments/Descrip	otion/Specification
Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

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AstLmt\_Per1

Attributes		
Name	Value	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



### **Test Case 1: Matrix Test**

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

TS1.1 1848.00 cycles Shortest Execution Path TS1.2 1948.00 cycles Longest Execution Path

Description Vector Description:

TS1.1:"Shortest execution Path:

(PosServCCDisable\_Cnt\_T\_lgc = True) = False

(LmPnCtrCCDisable\_Cnt\_T\_lgc = True = False

((AstLmt\_ManualTrqCmdEn\_Cnt\_M\_lgc = True) And (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)) = True

(DefeatLimitService\_Cnt\_T\_lgc = True) = True

TS1.2"Longest Execution Path:

(PosServCCDisable\_Cnt\_T\_lgc = True) = True

(LmPnCtrCCDisable\_Cnt\_T\_lgc = True) = True

((AstLmt\_ManualTrqCmdEn\_Cnt\_M\_lgc = True) And (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)) = False

(LmPnCtrEnable\_Cnt\_T\_lgc = True) = False

(AstLmt\_SteeringAsstDefeat\_Cnt\_M\_lgc = True) = False

(DefeatLimitService\_Cnt\_T\_lgc = True) = False"

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPICmpLimit_MtrNm_f32	0
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

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Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~

Τ				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 4.2 (Papert Count = 4)	
Test Step 1.2 (Repeat Count = 1)	·
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	8
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Igc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_UIs_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt AstLmt Per1 PowerLimitPerc Uls f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

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AstLmt\_Per1

Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	ltrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.73999977	5.73999977 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	~

Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

AstLmt\_Per1

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Test Case 2: Boundary Test

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AstLmt\_Per1



#### Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

CPU Cycles:

TS2.1 1985.00 cycles TS2.2 1879.00 cycles TS2.3 1821.00 cycles TS2.2 1879.00 cycles
TS2.3 1821.00 cycles
TS2.3 1816.00 cycles
TS2.4 1816.00 cycles
TS2.5 1747.00 cycles
TS2.6 1777.00 cycles
TS2.6 1777.00 cycles
TS2.7 1747.00 cycles
TS2.8 1723.00 cycles
TS2.9 1747.00 cycles
TS2.10 1723.00 cycles
TS2.11 1747.00 cycles
TS2.12 1723.00 cycles
TS2.13 1747.00 cycles
TS2.14 1723.00 cycles
TS2.15 1747.00 cycles
TS2.16 1723.00 cycles
TS2.16 1723.00 cycles
TS2.17 1723.00 cycles
TS2.18 1747.00 cycles
TS2.19 1723.00 cycles
TS2.19 1723.00 cycles
TS2.21 1723.00 cycles
TS2.22 1747.00 cycles
TS2.22 1747.00 cycles
TS2.23 1723.00 cycles
TS2.24 1747.00 cycles
TS2.25 1723.00 cycles
TS2.26 1747.00 cycles
TS2.27 1723.00 cycles
TS2.28 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1740.00 cycles
TS2.29 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.33 1740.00 cycles
TS2.35 1740.00 cycles
TS2.36 1740.00 cycles
TS2.37 1740.00 cycles TS2.34 1742.00 cycles TS2.35 1742.00 cycles TS2.36 1742.00 cycles TS2.37 1742.00 cycles TS2.38 1742.00 cycles TS2.39 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.40 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.43 1758.00 cycles TS2.44 1758.00 cycles TS2.45 1760.00 cycles TS2.46 1767.00 cycles TS2.47 1742.00 cycles TS2.48 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.51 1742.00 cycles TS2.51 1742.00 cycles TS2.53 1742.00 cycles TS2.55 1742.00 cycles TS2.55 1758.00 cycles TS2.55 1758.00 cycles TS2.56 1758.00 cycles TS2.61 1723.00 cycles TS2.63 1723.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.67 1723.00 cycles TS2.68 1767.00 cycles TS2.69 1723.00 cycles TS2.71 1723.00 cycles TS2.71 1723.00 cycles TS2.71 1723.00 cycles TS2.73 1723.00 cycles TS2.74 1729.00 cycles TS2.75 1729.00 cycles TS2.75 1723.00 cycles TS2.76 1729.00 cycles TS2.77 1723.00 cycles TS2.78 1723.00 cycles TS2.78 1723.00 cycles TS2.79 1723.00 cycles TS2.81 1729.00 cycles TS2.81 1758.00 cycles TS2.82 1723.00 cycles TS2.83 1729.00 cycles TS2.84 1758.00 cycles TS2.85 1767.00 cycles TS2.86 1757.00 cycles TS2.87 1829.00 cycles TS2.88 1719.00 cycles TS2.89 1712.00 cycles TS2.90 1728.00 cycles TS2.91 1714.00 cycles TS2.92 1729.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.94 1712.00 cycles TS2.94 1712.00 cycles TS2.95 1731.00 cycles TS2.96 1731.00 cycles TS2.97 1729.00 cycles TS2.97 1729.00 cycles TS2.98 1790.00 cycles TS2.99 1758.00 cycles





#### **Description** Vector Description :

TS2.1AssistCmd MtrNm f32 = Min TS2.2AssistCmd\_MtrNm\_f32 = Max TS2.3AssistCmd\_MtrNm\_f32 = Mid TS2.4AssistCmd\_MtrNm\_f32 = Pos TS2.5AssistCmd\_MtrNm\_f32 = Neg TS2.6AssistEOTDamping MtrNm f32 = Min TS2.7AssistEOTDamping MtrNm f32 = Max TS2.8AssistEOTDamping MtrNm f32 = Mid TS2.9AssistEOTDamping\_MtrNm\_f32 = Mid
TS2.9AssistEOTDamping\_MtrNm\_f32 = Pos
TS2.10AssistEOTDamping\_MtrNm\_f32 = Neg
TS2.11AssistEOTGain\_Uls\_f32 = Min
TS2.12AssistEOTGain\_Uls\_f32 = Mid
TS2.14AssistEOTLimit\_MtrNm\_f32 = Min
TS2.14AssistEOTLimit\_MtrNm\_f32 = Min IS2.14AssistEOTLimit\_MtrNm\_f32 = Min TS2.15AssistEOTLimit\_MtrNm\_f32 = Max TS2.16AssistEOTLimit\_MtrNm\_f32 = Mid TS2.17AssistStallLimit\_MtrNm\_f32 = Min TS2.18AssistStallLimit\_MtrNm\_f32 = Max TS2.19AssistStallLimit\_MtrNm\_f32 = Mid TS2.20AssistVehSpdLimit\_MtrNm\_f32 = Min TS2.21AssistVehSpdLimit\_MtrNm\_f32 = Max TS2.22AssistVehSpdLimit\_MtrNm\_f32 = Mid TS2.23CombinedDamping\_MtrNm\_f32 = Min TS2.24CombinedDamping\_MtrNm\_f32 = Max TS2.25CombinedDamping\_MtrNm\_f32 = Mid TS2.26CombinedDamping\_MtrNm\_f32 = Pos TS2.27CombinedDamping\_MtrNm\_f32 = Neg TS2.28DefeatLimitService\_Cnt\_lgc = Min TS2.29DefeatLimitService\_Cnt\_lgc = Max TS2.30LimitedReturn\_MtrNm\_f32 = Min TS2.31LimitedReturn\_MtrNm\_f32 = Max TS2.32LimitedReturn\_MtrNm\_f32 = Mid TS2.33LimitedReturn\_MtrNm\_f32 = Pos TS2.34LimitedReturn\_MtrNm\_f32 = Neg TS2.35LmPnCtrCCDisable Cnt\_lgc = Min
TS2.36LmPnCtrCCDisable\_Cnt\_lgc = Min
TS2.36LmPnCtrCDisable\_Cnt\_lgc = Max
TS2.38LmPnCtrEnable\_Cnt\_lgc = Min
TS2.38LmPnCtrEnable\_Cnt\_lgc = Max
TS2.39LmPnCtrTCmd\_MtrNm\_f32 = Min
TS2.40LmPnCtrTCmd\_MtrNm\_f32 = Max TS2.41LrnPnCtrTCmd\_MtrNm\_f32 = Mid TS2.42LrnPnCtrTCmd\_MtrNm\_f32 = Pos TS2.43LrnPnCtrTCmd\_MtrNm\_f32 = Neg  $TS2.44OpTrqOvr\_MtrNm\_f32 = Min$ TS2.44OpTrqOvr\_MtrNm\_f32 = Min
TS2.45OpTrqOvr\_MtrNm\_f32 = Max
TS2.46OpTrqOvr\_MtrNm\_f32 = Mid
TS2.47OpTrqOvr\_MtrNm\_f32 = Pos
TS2.48OpTrqOvr\_MtrNm\_f32 = Neg
TS2.49OutputRampMult\_Uls\_f32 = Min
TS2.50OutputRampMult\_Uls\_f32 = Mia
TS2.51OutputRampMult\_Uls\_f32 = Mid
TS2.52PosServCCDisable\_Cnt\_lgc = Min
TS2.53PosServCCDisable\_Cnt\_lgc = Max
TS2.54Powerl\_imitPerr\_Uls\_f32 = Min TS2.54PowerLimitPerc\_UIs\_f32 = Min TS2.55PowerLimitPerc\_UIs\_f32 = Max TS2.56PowerLimitPerc\_UIs\_f32 = Mid TS2.57PrkAssistCmd\_MtrNm\_f32 = Min TS2.58PrkAssistCmd\_MtrNm\_f32 = Max TS2.59PrkAssistCmd\_MtrNm\_f32 = Mid TS2.60PrkAssistCmd\_MtrNm\_f32 = Pos TS2.61PrkAssistCmd\_MtrNm\_f32 = Neg TS2.62PullCompCmd\_MtrNm\_f32 = Min TS2.63PullCompCmd\_MtrNm\_f32 = Max TS2.64PullCompCmd\_MtrNm\_f32 = Mid TS2.65PullCompCmd\_MtrNm\_f32 = Pos TS2.66PullCompCmd\_MtrNm\_f32 = Neg TS2.67ThermalLimitPerc\_Uls\_f32 = Min TS2.68ThermalLimitPerc\_Uls\_f32 = Max TS2.69ThermalLimitPerc\_Uls\_f32 = Mid TS2.70ThermalLimit\_MtrNm\_f32 = Min TS2.71ThermalLimit\_MtrNm\_f32 = Max TS2.72ThermalLimit\_MtrNm\_f32 = Mid TS2.73WheellmbalanceCmd\_MtrNm\_f32 = Min TS2.74WheellmbalanceCmd\_MtrNm\_f32 = Max TS2.75WheellmbalanceCmd\_MtrNm\_f32 = Mid TS2.76WheelImbalanceCmd\_MtrNm\_f32 = Pos TS2.77WheelImbalanceCmd\_MtrNm\_f32 = Neg TS2.778k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min TS2.78k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min
TS2.79k\_SumLimPlCmpLimit\_MtrNm\_f32 = Max
TS2.80k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min
TS2.81VehSpd\_Kph\_f32 = Min
TS2.82VehSpd\_Kph\_f32 = Max
TS2.83VehSpd\_Kph\_f32 = Pos
TS2.84ManualTrqCmdEn\_Cnt\_M\_lgc = Min
TS2.85ManualTrqCmdEn\_Cnt\_M\_lgc = Max
TS2.85ManualTrqCmd\_MtrNm\_M\_f32 = Max
TS2.86ManualTrqCmd\_MtrNm\_M\_f32 = Max
TS2.88ManualTrqCmd\_MtrNm\_M\_f32 = Neg
TS2.89ManualTrqCmd\_MtrNm\_M\_f32 = Neg
TS2.99ManualTrqCmd\_MtrNm\_M\_f32 = Pos
TS2.91SteeringAsstDefeat\_Cnt\_M\_lgc = Min
TS2.92SteeringAsstDefeat\_Cnt\_M\_lgc = Max
TS2.93TSMitCommand\_MtrNm\_f32=Min
TS2.94TSMitCommand\_MtrNm\_f32=Min
TS2.94TSMitCommand\_MtrNm\_f32=Max





TS2.95TSMitCommand\_MtrNm\_f32=Zero TS2.96TSMitCommand\_MtrNm\_f32=Neg TS2.97TSMitCommand\_MtrNm\_f32=Pos TS2.98All Min TS2.99All max

Fest Step 2.1 (Repeat Count = 1)	Input Value		
stLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	8.10000038		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.100000001		
pt AstLmt Per1 AssistStallLimit MtrNm f32.value	1.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_tgc.value	4		
gt_AstLmt_Per1_ChrPriCthTofnid_withNin_i32.value	5		
pt_AstLmt_Per1_OpTrqOvr_MtrNm_i32.value	0.100000001		
pt_AstLmt_Per1_OutputRampMult_Ois_is2.value	1		
gt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
	2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.0999999		
	42.0999985		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	-4.5		
gt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	_ <del>-</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		
lame	Actual Value	Expected Value	Res
	0.10000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		8.80000019 ± 0.0009765625 4.5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.2 (Repeat Count = 1)	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc  AstLmt_ManualTrqCmd MtrNm M f32	5.36999989		
_ · · · - ·	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	tgt Rte Inst Ap AstLmt		
Rte_Inst_Ap_AstLmt	7.19999981		
C_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001 8.80000019		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.20000003		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.60000002		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	38.2000008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
gt_Rte_inst_Ap_AstLint.AstLint_Per1_Op1riqOvi_MitNin_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uis_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_ois_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/trNm_f32	
Name	Actual Value	Expected Value	Resi
at Actions Dard LimitDarconsEilterad Lilla 622 value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
gt_AsiLmt_Per1_LimitPercentFiltered_Uis_i32.value gt_AsiLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019 5	8.80000019 ± 0.0009765625 5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.3 (Repeat Count = 1)	Input Value		
Name	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	6.30000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.300000012		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.70000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7999992		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
		NIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<b>600</b>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
			Pac
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.090000036	0.0900000036 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.30000012	0.300000012 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.4 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3.47000003			
AstLmt_ManualTrqCmd_MtrNm_M_f32				
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
x_SumLimPlCmpLimit_MtrNm_f32		5.4000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.5			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.40000006			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006			
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-6			
gt AstLmt Per1 PullCompCmd MtrNm f32.value	5			
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006			
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	14.5			
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7.19999981			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
		m f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	111_132		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	-		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	gc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	732		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f3.			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	Nm f32		
		T	Pos	
Name	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.40000006 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625		
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.79999995	2.79999995 ± 0.0009765625		
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.40000006	0.400000006 ± 0.0009765625		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value				



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.5 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 4.5		
<_SumLimPICmpLimit_MtrNm_f32 Instruct_Apt mt_Port_TSNitCommand_AttrNm_f32 value	-2.5999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
	0.40000006		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.5		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	2.20000005		
	1.8999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_Lmhitedretatin_withtin_i32.value  gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	1		
	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_cmPnOtrCmd_wtrNm_f32.value gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_Op1rqOvt_intrivin_is2.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_Outputkampinut_ots_tsz.value gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLint_Per1_Posservoodisable_Crit_igt.value	0.5		
gt_AsiLint_Per1_PowerLinit[Per0_0is_i32.value] gt_AsiLint_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt AstLmt Per1 ThermalLimit MtrNm f32.value	2.5		
gt_AstLmt_Per1_ThermalLimit_MthNm_i32.value	38.2999992		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
gt_AsiLint_Fe1_virieeiinibalancecinid_wirvini_i32.value  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt_AstLmt_Per1_AssistEOTDamping_init	_	
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_uis_i32  gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLint_Per1_AssistEOTJain_uis_is tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_DereatLimitGervice_Cir		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_Emr10tr10md_MtrNm f32	_132	
		222	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_OutputRampMult_Uls_t tgt_AstLmt_Per1_PosServCCDisable_Cr		
gt_Rte_inst_Ap_AstLmt.AstLmt_Peri_PosServCcDisable_Cnt_gc gt_Rte_inst_Ap_AstLmt.AstLmt_Peri_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosServCcDisable_Cr	— -	
gt_Rte_inst_ap_astLmt.astLmt_Peri_PowerLimitPerc_uis_f32 gt_Rte_inst_ap_astLmt.astLmt_Peri_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uis_ts		
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PreLimitForStail_MitNin_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_intrinii tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimit.rorque_witnim_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt AstLmt Per1 PreLimitrorque_MtrNm		
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_intinnin_is2  gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_inst_ap_astLmt.astLmt_Per1_Pullcompcind_mtrNm_is2 gt_Rte_inst_ap_astLmt.astLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimfrqCmd_MtiNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mit	_	
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_r5witcommand_witnm_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSWittCommand_wit tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_ThermalLinitPerc_dis_is2 gt_Rte_inst_Ap_AstLint.AstLint_Per1_ThermalLinit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_inst_Ap_AstLmt.AstLmt_Peri_ThermalLimit_MtrNm_132 gt_Rte_inst_Ap_AstLmt.AstLmt_Peri_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrgLimitMin MtrNm f3.		
gt_Rte_inst_Ap_AstLmt.AstLmt_PeriirqLimitwin_witrvm_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_PeriirqLimitwin_witrvm_i32			
	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtrNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_	l Da
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.25 0.5	-0.25 ± 0.0009765625 0.5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

est Step 2.6 (Repeat Count = 1)	Input Value				
	1				
stLmt_ManualTrqCmdEn_Cnt_M_lgc stLmt_ManualTrqCmd_MtrNm_M_f32	1 -1.76999998				
stLmt_SteeringAsstDefeat_Cnt_M_lgc	1				
te_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt				
_SumLimPlCmpLimit_MtrNm_f32	3.5999999				
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5				
	-3				
yt_AstLmt_Per1_AssistCmd_MtrNm_f32.value					
pt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019				
yt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5				
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.600000024				
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.29999995				
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2				
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2				
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1				
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8				
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0				
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1				
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005				
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999				
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024				
yt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0				
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024				
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8				
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7				
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024				
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999				
gt_AstLmt_Per1_VehSpd_Kph_f32.value	36.4000015				
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3	2			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr	1_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	n_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_le	jc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	f32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32				
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cnt				
pt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt lgc	3.			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	2			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32				
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32				
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc					
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosserveeDisable_Cnt_igc		<del>9</del> ~	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitFer0_0is_132 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32				
		32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_1				
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f:	32			
	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3; tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3;	32 ?			
	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3; tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3; tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32 2 2			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	32 2 2 2 732			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN	32 2 2 2 332 m_f32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	32 2 2 2 332 m_f32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	32 2 2 2 332 m_f32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	32 2 2 2 332 m_f32			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	32 2 2 332 m_f32 2			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	32 2 2 332 m_f32 2			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	32 2 2 332 m_f32 2	Resu		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VheelImbalanceCmd_Mtr	32 2 2 332 m_f32 ? Nm_f32	Resu		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VheelImbalanceCmd_Mtr Actual Value	32 2 2 332 m_f32 2 Nm_f32 Expected Value	Resu		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.6000000024	Nm_f32  Expected Value  0.600000024 ± 0.000030517578125	Resu		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 pt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value pt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ft tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.600000024 3	Nm_f32  Expected Value  0.600000024 ± 0.000030517578125  3 ± 0.0009765625	Resu		



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
x_SumLimPlCmpLimit_MtrNm_f32	2.7000005		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.20000005		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	26.6000004		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_0is_is2 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	NtrNm f32	
lame	Actual Value	Expected Value	Res
			Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
at April mt Dor't Droi imit Lorallo MtrNm +27 Volus	-8.80000019	-8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value		0.40000004 - 0.00000000	
gt_AstLmt_Peri_PreLimitForque_MtrNm_r32.value gt_AstLmt_Peri_SumLimTrqCmd_MtrNm_r32.value gt_AstLmt_Peri_TrqLimitMin_MtrNm_r32.value	-0.48999998 0.69999988	-0.49000001 ± 0.0009765625 0.699999988 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.8 (Repeat Count = 1)	Input Value		
Name	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-8.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.79999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4.1999981		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_tmPnCtrEnable_Ont_igc.value	-4.5		
tgt_AstLmt_Per1_cmPnCtr1Cmd_wtrNm_f32.value	-4.5 -6.5		
gt_AstLmt_Per1_Op1rqOv1_mtrNm_isz.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_Outputkampinuit_ois_isz.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Peri_ThermalLimit_etc_ois_132.value	2.7999995		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	34.7000008		
	3		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	<del></del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt I		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1_132	
		f2.2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_OutputRampMult_Uls_ tgt_AstLmt_Per1_PosServCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Crit_igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Crit_igc	tgt_AstLmt_Per1_PosservCcDisable_Cl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_0is_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitPerc_Ois_i.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStail_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit1orque_within1_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimit1orque_initrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PillCompCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_rswitcommand_within1_is2 igt_Rte_inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TswittCommand_w		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_PerC_0is_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ois	_	
tgt_Rte_inst_Ap_AstLmt.AstLmt_PerimermaiLimit_within_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_PerimermaiLimit_within_is2	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitwiin_witviin_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_rrqLimitwiin_witwin_is	4	
		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		D.
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014 0.800000012	-3.5999999 ± 0.0009765625 0.800000012 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	0.89999976		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.19999981		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.89999976		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	65.9000015		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	1103
gt_AstLmt_Per1_LimitPercentFiltered_Ois_isz.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19999981	6.19999981 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForstail_MtrNm_f32.value	-2.66000032	-2.66000009 ± 0.0009765625	
	-0.809999943	-0.810000002 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.899999976	0.899999976 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.10 (Repeat Count = 1) Name	Input Value		
	The state of the s		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32	5.63000011		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	25.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		1_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	£2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	732	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_cimitretcent intered_ors_roz.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitPorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	
	0.330000013	0.330000013 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value			



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.11 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.23999977		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	75.8000031		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLint.AstLint_Per1_PriAssistCinto_MtiNin_i32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_0is_i32	tgt_AstLmt_Per1_ThermalLimit_erc_ois		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		· ·	Nesu
	0.200000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019 -8.80000019	8.80000019 ± 0.0009765625 -8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_FreLimitrorque_MtrNm_f32.value	-0.160000019	-0.159999996 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.12 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.44999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.89999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.20000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.20000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	28.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_i		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	00	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn target AstLmt Per1 TSMitCommand Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32		_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Abobles 622	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

ManualTrqCmdEn_Cnt_M_lgc  ManualTrqCmd_MtrNm_M_f32  SteeringAsstDefeat_Cnt_M_lgc  :_Ap_AstLmt implCmpLimit_MtrNm_f32  sstLmt_Per1_TSMitCommand_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LmPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LmPnCtrTCmd_MtrNm_f32.value  mt_Per1_DpTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PllCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitMtrNm_f32.value  mt_Per1_ThermalLimitMtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_NheellmbalanceCmd_MtrNm_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  Jnst_Ap_AstLmt.AstLmt_Per1_AssistEotDamping_MtrNm_f32  Jnst_Ap_AstLmt.AstLmt_Per1_AssistEotDain_Uls_f32	Input Value  0 6.55999994 0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.799999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.20000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
ManualTrqCmd_MtrNm_M_f32  SteeringAsstDefeat_Cnt_M_lgc t_Ap_AstLmt imPlCmpLimit_MtrNm_f32  sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimpCtrCCDisable_Cnt_lgc.value mt_Per1_LrmPnCtrCDisable_Cnt_lgc.value mt_Per1_LmPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrhamalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	6.55999994 0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.20000005 0 -1 1 0 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
SteeringAsstDefeat_Cnt_M_lgc t_Ap_AstLmt imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCcDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrhermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
t_Ap_AstLmt imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_Igc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrmPnCtrCDisable_Cnt_Igc.value mt_Per1_LrmPnCtrCDisable_Cnt_Igc.value mt_Per1_LrmPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCCDisable_Cnt_Igc.value mt_Per1_PosServCDisable_Cnt_Igc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PremalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_Igc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCcDisable_Cnt_Igc.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_DopTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCcDisable_Cnt_Igc.value mt_Per1_PosServCDisable_Cnt_Igc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
astLmt_Per1_TSMitCommand_MtrNm_f32.value  mt_Per1_AssistCmd_MtrNm_f32.value  mt_Per1_AssistEOTDamping_MtrNm_f32.value  mt_Per1_AssistEOTGain_Uls_f32.value  mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrEnable_Cnt_lgc.value  mt_Per1_DopTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PthAssistCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPers_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistCmd_MtrNm_f32.value  mt_Per1_AssistEOTDamping_MtrNm_f32.value  mt_Per1_AssistEOTGain_Uls_f32.value  mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrEnable_Cnt_lgc.value  mt_Per1_DopTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PthAssistCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCond_mtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	2 4 0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCoDisable_Cnt_lgc.value mt_Per1_LimPnCtrEnable_Cnt_lgc.value mt_Per1_LimPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCoDisable_Cnt_lgc.value mt_Per1_PosServCoDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PillCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	4 0.5 1.29999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimpnCtrCCDisable_Cnt_lgc.value mt_Per1_LimpnCtrCmd_MtrNm_f32.value mt_Per1_LimpnCtrCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PillCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimpnCtrCCDisable_Cnt_lgc.value  mt_Per1_LimpnCtrCmd_MtrNm_f32.value  mt_Per1_LimpnCtrCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PtlCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  linst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32  linst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1.2999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimitPnCtrCCDisable_Cnt_lgc.value  mt_Per1_LimitPnCtrCmd_MtrNm_f32.value  mt_Per1_LimitPnCtrCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PosServCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PillCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  linst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  linst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LmPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LmPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PtllCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_WehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCcDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_TrkermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value  mt_Per1_LrnPnCtrEnable_Cnt_lgc.value  mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_WhSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1 0 -2 -3 0.400000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_LrnPnCtrEnable_Cnt_lgc.value  mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 -2 -3 0.400000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015		
mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.400015 8		
mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015		
mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	5.5999999 0.400000006 3.29999995 85.4000015 8		
mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 3.2999995 85.4000015 8		
mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	3.29999995 85.4000015 8		
mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	85.4000015 8		
mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	8		
mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
	tgt_AstLmt_Per1_AssistEOTDamping_MtrNn	n f32	
	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	2	
Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm		
Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm	_	
Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lge		
Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3		
Inst_Ap_AstLmt.AstLmt_Per1_Limite Greent intered_ois_132	tgt_AstLmt_Per1_Limiterercentrintered_Ois_ic		
Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lg	gc	
Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	2	
Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	2	
Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg	gc	
Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	-	
Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3		
Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3	32	
_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm	m_f32	
_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN	Nm_f32	
	Actual Value	Expected Value	Res
mt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
mt Per1 PreLimitForStall MtrNm f32.value	3.64000058	3.6400001 ± 0.0009765625	
mt_Per1_PreLimitTorque_MtrNm_f32.value	3.6400058	3.6400001 ± 0.0009765625	
mt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.160000011	0.159999996 ± 0.0009765625	
mt_Per1_TrqLimitMin_MtrNm_f32.value	0.400000011	0.400000006 ± 0.0009765625	





Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

M	Inner A Males		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994 1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 6.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.099999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	0.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.5		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	 :Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.099999	3.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.15 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.239999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	425.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	11030
tgt_AstLmt_Per1_LimitPercentFiltered_bis_isz.value	6.63999939	6.63999987 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t32.value	-6.63999939	-6.63999987 ± 0.0009765625	
tgt_AstLmt_Per1_StumLimTrqCmd_MtrNm_f32.value	-0.360000014	-0.360000014 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.600000024	0.600000014 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Fest Step 2.16 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	1.36000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5999999		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	153.100006		
gt AstLmt Per1 WheelImbalanceCmd MtrNm f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	···· <u>-</u> ··-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
		•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_UIs_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f;		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
gt_Rte_inst_Ap_astLmt.AstLmt_Peri_vvneeiimbalanceCmd ivitrinm 132		Expected Value	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Actual value		
Name	Actual Value	0.69999988 + 0.00030517578125	
Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125 5 ± 0.0009765625	
Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0.699999988 5	5 ± 0.0009765625	
Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988		



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.48000002		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.3999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	-8		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_tmPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.599999		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	-7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	12.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_1		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt AstLmt Per1 ThermalLimit MtrNm f		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt AstLmt Per1 TrqLimitMin MtrNm f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	1.20000005	1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	
	0	0 ± 0.0009765625	٠,

## **TEST DETAILS REPORT**

2014-09-05, 18:07:58+0530



AstLmt\_Per1

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.18 (Repeat Count = 1)		<b>~</b>
Name	Input Value	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995	
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.79999995	
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019	
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995	
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5	
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005	
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1	
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0	
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999	
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4r_MtrNm_f32.value	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.19 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd MtrNm M f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
\te_msi_Ap_Asicint <_SumLimPlCmpLimit_MtrNm_f32	4.71999979		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt AstLmt Per1 AssistEOTLimit MtrNm f32.value	1.8999998		
gt_AstLmt_Per1_AssistE01Eimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_Assiststalitimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_CombinedDamping_MithIm_i32.value gt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
	0		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5 -6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value			
	0.400000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.20000005		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	425.700012		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	— <del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
	4.5	4.5 ± 0.0009765625	
gt Astemt Peri PrelimithorStall Mitrium 132.Value			
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	
		-4.5 ± 0.0009765625 -1.79999995 ± 0.0009765625	



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Nama	Innut Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7		
AstLmt_ManualTrqCmd_MtrNm_M_f32			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32	5.84000015		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.3999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.29999995		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	373.299988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	·	Resi
		0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.8499999	6.8499999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-6.8499999	-6.8499999 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0 0	0 ± 0.0009765625 0 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Fest Step 2.21 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	6.96000004		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
pt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.0999999		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.4000001		
t_AstLmt_Per1_VehSpd_Kph_f32.value	324.600006		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
pt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable (	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_		
	tgt_AstLmt_Per1_tmPnCtrTCmd_MtrNi	•	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_UIs_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
A ASILITI FELL LITTILFEICENTFILETEU DIS 132.VAIDE			
	3.29999995	3,29999995 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.29999995 3.29999995	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625	
	3.2999995 3.2999995 1.9800002	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625 1.98000002 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Name	
Section   Sect	
Astlum! Scientinp-Assillation   Display   Disp	
Ris_Inst_Ap_AstLmt	
SumLimPCropLimi Mirkin, 132   gat. Ast.mir, Pert, AssistErOT Mirkin, 132 value   gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value   gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value   gat. Ast.mir, Pert, Defeat. Irmited Review (and in the second seco	
got Ast.ml Pert _ TSMICOmmand_MitNim_152 value	
g. AstLm. Pert _AssistCOT (MYNn_ 152 value   5.599999   g. AstLm. Pert _AssistCOT (Comp. MYNn_ 152 value   2   2   2   2   2   2   2   2   2	
gl. Aslt.mt Pert   AssistEOTDamping   MRNm, 152 value   2   2   2   2   2   2   2   2   2	
Q. AstLim, Perl. AssistEOTGain, Us. 132 value   2 20000005	
Ast.ml. Perl	
Jastimil Pert   AssistiveNpdit imil, Mrkhm, 132 value   5.5	
Ast.mt   Peri   AssistVehSpdLimit   MtrNm   52 value   2	
AstLmt_Per1_CombinedDamping_MirNm_132 value   2   2   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,299999999   3   3,2999999999   3   3,299999999999   3,2999999999999999999999999999999999999	
gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         0           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         3           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         1           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         0           gl AstLIM_Pert_ImitedToChosable_Cnt_ligo_value         5           gl AstLIM_Pert_OptTroOv_MitNim_f32_value         5           gl AstLIM_Pert_OptTroOv_MitNim_f32_value         1           gl AstLIM_Pert_PosSexCCDSable_Cnt_ligo_value         1           gl AstLIM_Pert_PowerLiminPerc_Uis_f32_value         0           gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value         5           gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value         45           gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value         0           gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value         45           gl AstLIM_Pert_VerbSpx_fxb, f32_value         45           gl AstLIM_Pert_NetibilitanceCom_MitNim_f32_value         45           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32         tgt_AstLIM_Pert_AssistCom_MitNim_f32_value           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_         tgt_AstLIM_Pert_AssistCom_MitNim_f32_value           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_         tgt_AstLIM_Pert_AssistCom_MitNim_f32_           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_         tg	
J. AstLmt. Perl _ LimitedRetum_MtrNm_f32.value   3.29999995     J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt.   gevalue   0     J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value   5.599999     J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value   1     J. AstLmt. Perl _ Dovignov_MtrNm_f32.value   0.69999988     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value   5     J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value   4.5     J. AstLmt. Perl _ Posterv. Developed _ J. AstLmt. Perl _ Posterv. Developed _ J. AstLmt. Perl _ ThermalLimil Perl _ Devis _ J. AstLmt. Perl _ ThermalLimil MtrNm_f32.value   4.5     J. AstLmt. Perl _ PosServ. Cornd_MtrNm_f32.value   4.5     J. AstLmt. Perl _ Nesignov. Devis _ J. AstLmt. Perl _ AssistCornd_MtrNm_f32   4.5     J. AstLmt. Perl _ Nesignov. Devis _ J. AstLmt. Perl _ AssistCornd_MtrNm_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Us_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ Devis LimitedRetum_MtrNm_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ LombiceCobamping_MtrNm_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ LombiceCobamping_MtrNm_f32   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ LombiceCobamble_Cn	
1, AstLmt_Perl_LmPnCtrCoDisable_Cnt_lgcvalue   1   0   0   0   0   0   0   0   0   0	
J. AstLmT. Pert   LmPhCritTcnde   Cnt   Igc. value   5.599999     J. AstLmT. Pert   LmPhCritTcnde   Mixhm   132 value   5.5999999     J. AstLmt. Pert   OutpurRampMilt, Uls   132 value   1	
AstLmt Per1 LmPnCtrTCmd MtrNm_f32 value	
J. AstLmt. Pert   OptrqOv_MtrNm_f32 value   0.699999988   1.     J. AstLmt. Pert   OrtputRampMult_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PosservcCDisable_Cnt_Ug. value   1.     J. AstLmt. Pert   PosservcCDisable_Cnt_Ug. value   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   ThermalLimitPerc_Uis_182 value   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.699999988   0.6999999988   0.699999988   0.6999999988   0.6999999988   0.6999999988   0.6999999988   0.699999988   0.6999999988   0.6999999988   0.6999999988   0.6999999988   0.69999999988   0.69999999988   0.69999999988   0.69999999988   0.69999999988   0.699999999988   0.699999999988   0.69999999988   0.6999999999988   0.6999999999999999999999999999999999999	
31, AstLmt, Pert   PosSenvCCDisable, Cnt.   Jgc. value   1	
1, AstLmt_Per1   PosservCCDisable_Cnt_lgc_value   1	
AstLmt   Perf   PowerLimitPerc   Uis	
AstLmt_Pert_PowerLimitPerc_Uis_r32.value   0.699999988       AstLmt_Pert_PrkAssistComd_MirNm_r32.value   5	
31, AstLmt_Per1_ThermalLimitPerc_Uls_f32.value   0.689999988	
AstLmt   Per1_ThermalLimitPerc_UIs_f32.value	
AstLmt_Per1_ThermalLimitPerc_Uis_f32.value   4.5     AstLmt_Per1_ThermalLimit_MtrNm_f32.value   4.5     AstLmt_Per1_VehSpd_Kph_f32.value   4.5     AstLmt_Per1_WehSpd_Kph_f32.value   4.5     AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32   4.5     AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32   4.5     AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32   4.5     AstLmt_Per1_AssistComd_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistSotLimit_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistSotLimit_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igo   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCCDisable_Cnt_Igo   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_Igo   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_Igo   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Ap_AstLmt_AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Per1_DirpCov_MtrNm_f32   4.5     AstLmt_Per1_PreLimitTorque_MtrNm_f32   4.5     AstLmt_Per1_PreLimitTorque_MtrNm_f32   4.5     AstLmt_Per1_PreLimitTorque_MtrNm_f32   4.5     AstLmt_Per1_PreLimit	
11. AstLmt_Perl_VehSpd_Kph_f32.value 12. AstLmt_Perl_VehSpd_Kph_f32.value 13. AstLmt_Perl_WheelImbalanceCmd_MtrNm_f32.value 13. Rte_Inst_Ap_AstLmtAptLmt_Perl_AssistComd_MtrNm_f32 14. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistSomlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_CombinedDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrictrComlable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrictrComl_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrictrComl_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrictrComl_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Comprover_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Comprover_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PrelimitForestLengthrn f32 15	
ti, AstLmt, Perl_VhRopd, Kph_f32.value ti, AstLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Ap. AstLmt, Perl_AssistEOTDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_CombinedDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimitedRetur_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrTCmd_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PosservCObiasble_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PosservCObiasble_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip,	
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tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_Derfection_MtrNm_f32 tgt_AstLmt_Per1_Derfectio	
tgt_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvv_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvv_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvv_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqCvb_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_Per0_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_Mtr	
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tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimi	
target_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	
gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_	
gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	
gt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
	Per
Name Actual Value Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5.5999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -1.01999986 -1.01999998 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -0.713999867 -0.713999987 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 1.60000002 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.23 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.0599999987		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.7000005		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	2.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.80000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	?	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.800000012	-0.800000012 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.24 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988		
	-0.099999966		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-Z -4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	234.199997		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
Name	Actual Value	Expected Value	Res
			Nest
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
		-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-1.61999989 1.79999995	-1.62 ± 0.0009765625 1.79999995 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.25 (Repeat Count = 1)			V
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.74000001		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	0		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	123.800003		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
		irNm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	1
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	,
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.8999998	1.89999998 ± 0.0009765625	



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1)	Invest Welling		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.57999992		
k_SumLimPlCmpLimit_MtrNm_f32	-0.5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	- <del></del> 4 6		
	0.69999988		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5999999		
gt_AstEmt_Per1_AssistEdTellint_WithInt_i32.value	2		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
tgt_AstLmt_Per1_Assistvensput.imit_MitrNm_f32.value	1.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_Lmnledr\edum_withtn_i32.value	1		
	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AsiLint_Fe11_LinFricti*Tema_MitMin_i32.value gt_AsiLint_Pe11_OpTrqOvr_MtrNm_f32.value	-5		
gt_AsiLint_Peri_OptriqOvi_withtin_isz.value	0.20000003		
gt_AstLmt_Per1_Outputkampinult_ols_lsz.value gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PosserveeDisable_Cit_igc.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_dte_dis_i32.value	4.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	500.700012		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U	<u> </u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt le		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrgOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	<del></del>	
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	132	
ggt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	<del></del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt AstLmt Per1 PrkAssistCmd MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd	MtrNm f32	
Name	Actual Value	Expected Value	Resu
		·	Kest
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3 77000073	4 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.77999973	3.77999997 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.400000006 2	0.400000006 ± 0.0009765625 2 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.27 (Repeat Count = 1) Name	Input Value	
	•	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
s_SumLimPlCmpLimit_MtrNm_f32	3.42000008	
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006	
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5	
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7	
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.70000005	
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999	
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5	
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.5999999	
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4	
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5	
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6	
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012	
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012	
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2	
at AstLmt Per1 PullCompCmd MtrNm f32.value	-1.20000005	
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012	
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5	
t_AstLmt_Per1_VehSpd_Kph_f32.value	400.899994	
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
gt_rtte_mst_Ap_Astentt.Astentt_ren_relimit orotan_withtin_isz	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.300000012  0.300000012  Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.300000012  0.300000012  5 \$ ± 0.00009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32  Actual Value  0.300000012  0.300000012  0.300000012  5 5 ± 0.00009765625  -5 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.300000012  0.300000012  5 \$ ± 0.00009765625	Res



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.28 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.26000023		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_UIs_f32.value	0.899999976		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	2.79999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.40000006		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	5.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	300.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	ıt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7.2000029	7.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.880000055	0.879999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	

**TEST DETAILS REPORT** 

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5

AstLmt\_Per1



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.30 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	5.94000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.80000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	58.2000008		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Igc	<u>igo</u>	
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f	22	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32		32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		rNm f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	114111_102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt Actual Value	Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name	<u> </u>	Expected Value	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	Actual Value 0.200000003	Expected Value 0.200000003 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	Actual Value 0.200000003 5	Expected Value 0.200000003 ± 0.000030517578125 5 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	Actual Value 0.200000003	Expected Value 0.200000003 ± 0.000030517578125	Res



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.31 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	6.78000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr1	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	Im_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt AstLmt Per1 PosServCCDisable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f32	-9-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_Fullcomperind_withVin_i		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_i32	tgt_AstLmt_Per1_ThermalLimit_erc_Ois_is		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	trNm f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_M		D.
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	1
	0	0 ± 0.0009765625	1 1
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	

3

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

3 ± 0.0009765625



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.32 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1.75999999		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	-		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.61999989		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.70000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.0999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt AstLmt Per1 DefeatLimitService Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrgOvr MtrNm f32	_	
	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	٠,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.80000007	-1.79999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.0999999	3.0999999 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.33 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.46000004		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.79999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.20000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	T32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	it Igc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	c	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm	f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_0is_i32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitviiiI_initviii1_initviii	tgt_AstLmt_Per1_TrqLimitwiii_witrim_i32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_venSpd_kpn_i32  tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
	0	0 + 0 0000765605	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 3.20000005	0 ± 0.0009765625 3.20000005 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.34 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	0.600000024		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.300000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.9000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.70000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	-7		
at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.19999981		
t_AstLmt_Per1_VehSpd_Kph_f32.value	3.5		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
		1111_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm f32	
		1	l n
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.2999995	3.29999995 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.98000002	1.98000002 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.2999995	3.29999995 ± 0.0009765625	

**TEST DETAILS REPORT** 

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AstLmt\_Per1



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.36 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.32000005		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	1		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	342.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Ci		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	_ ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LmPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	4.5	4.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.37 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
x_SumLimPlCmpLimit_MtrNm_f32	1.67999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	484.799988		
gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	···_·-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
		-	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_l	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
	tgt_AstLmt_Per1_WheelImbalanceCmd_Mti	rNm_f32	
		T	12
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Actual Value	Expected value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name	Actual Value	0.89999976 + 0.00030517578125	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0.899999976 6.5	0.899999976 ± 0.000030517578125 6.5 ± 0.0009765625	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	Res



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.38 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	2.03999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.70000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	155.899994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtri		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32		_102	
	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.5	-0.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.70000005	3.70000005 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Fest Step 2.39 (Repeat Count = 1)	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	2.4000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.4000001		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.69999981		
t_AstLmt_Per1_VehSpd_Kph_f32.value	278.600006		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	I_MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt AstLmt Per1 PreLimitForStall MtrNm f32.value	5.5999999	5.5999999 ± 0.0009765625	
	0	0 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.40 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.75999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	312.399994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	<u> </u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt AstLmt Per1 OutputRampMult Uls	R2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtail_within1_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitPorStail_MtrNr	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PreLimitrorque_withtin	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.900001	3.9000001 ± 0.0009765625	



T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.41 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
	tgt Rte Inst Ap AstLmt		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	3.11999989		
	1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	328.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	Im f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_I	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	<del></del> -	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
	tgt_AstLmt_Per1_PosServCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_UI	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	†32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmo	J_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	•



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.42 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	3.48000002		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
st_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	429.399994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
		f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cr	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	lls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mi		
pt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target AstLmt Per1 TSMitCommand	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
pt_Rte_inst_Ap_AstEmt.AstEmt_Fer1_ThermalLimitFerc_ois_is2 pt_Rte_inst_Ap_AstEmt.AstEmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_	
	tgt_AstLmt_Per1_ThermalLimit_MtrNr tgt_AstLmt_Per1_TrgLimitMin_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32		_192	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	ad Milithian 199	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	
8			



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.43 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.5		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	3.83999991		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	52.7999992		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt_AstLmt_Per1_AssistEOTGain Uls f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimiterelCentrintered_ois_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPriCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCobisable_Cnt_lgc	_igc	
pt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrTCmd MtrNm f32		22	
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	52	
gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_OpTrqOvi_withini_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	<b>6</b> 00	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	
		T	

4.19999981

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

4.19999981 ± 0.0009765625



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.44 (Repeat Count = 1) Name	Input Value		
	· · ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	4.19999981		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.29999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		lm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm f32	
lame	Actual Value	Expected Value	Res
			IVE2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	
	1.44000000	4 44000000 + 0 0000705005	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.44000006 4.80000019	1.44000006 ± 0.0009765625 4.80000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.45 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.55999994		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.3999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1 0.400000006		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value			
tgt AstLmt Per1 VehSpd Kph f32.value	7.80000019 83.9000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	jc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtrNm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.40000006 ± 0.000030517578125	1
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	1
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2	-2 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.46 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	-6		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.92000008		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	73.6999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	102	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_Output\ampiwut_Ois_is		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ftrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.47 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	5.28000021		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.60000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.6999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	500.200012		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_mtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_UIs_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Uis_rs2 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_le		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_I	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
JENE INSTAPLASIEMEN EN L'OUTE EN L'O	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32		m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3;	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	m_f32 2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	m_f32 2 ·Nm_f32	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 st_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	m_f32 2 Nm_f32 Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.6000000024	m_f32 2 Nm_f32 Expected Value 0.600000024 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.600000024 7	m_f32 2 Nm_f32 Expected Value 0.600000024 ± 0.000030517578125 7 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_Tel_imitForStall_MtrNm_f32.value gt_AstLmt_Per1_Prel_imitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.6000000024	m_f32 2 Nm_f32 Expected Value 0.600000024 ± 0.000030517578125	Resi



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Lmt_ManualTrqCmdEn_Cnt_M_lgc Lmt_ManualTrqCmd_MtrNm_M_f32 Lmt_SteeringAsstDefeat_Cnt_M_lgc Lnt_SteeringAsstDefeat_Cnt_M_lgc Lnt_SteeringAsstDefeat_Cnt_M_lgc Lnt_St_Ap_AstLmt Lnt_Werl_TSMitCommand_MtrNm_f32.value AstLmt_Perl_AssistEOTDamping_MtrNm_f32.value AstLmt_Perl_AssistEOTDamping_MtrNm_f32.value AstLmt_Perl_AssistEOTCain_Uls_f32.value AstLmt_Perl_AssistEOTCain_Uls_f32.value AstLmt_Perl_AssistEOTLimit_MtrNm_f32.value AstLmt_Perl_AssistStallLimit_MtrNm_f32.value AstLmt_Perl_AssistStallLimit_MtrNm_f32.value AstLmt_Perl_CombinedDamping_MtrNm_f32.value AstLmt_Perl_CombinedDamping_MtrNm_f32.value AstLmt_Perl_DefeatLimitService_Cnt_lgc.value AstLmt_Perl_LimitedReturn_MtrNm_f32.value AstLmt_Perl_LimPnCtrCcDisable_Cnt_lgc.value AstLmt_Perl_LmPnCtrCcDisable_Cnt_lgc.value AstLmt_Perl_LmPnCtrComplex_Uls_f32.value AstLmt_Perl_OpTrqovr_MtrNm_f32.value AstLmt_Perl_OpTrqovr_MtrNm_f32.value AstLmt_Perl_PoservCCDisable_Cnt_lgc.value AstLmt_Perl_PowerLimitPerc_Uls_f32.value AstLmt_Perl_PowerLimitPerc_Uls_f32.value AstLmt_Perl_PowerLimitPerc_Uls_f32.value AstLmt_Perl_PrhAssistCmd_MtrNm_f32.value AstLmt_Perl_ThremalLimit_MtrNm_f32.value AstLmt_Perl_ThremalLimit_MtrNm_f32.value AstLmt_Perl_ThremalLimit_Derl_Uls_f32.value AstLmt_Perl_PowerLimitPerc_Uls_f32.value AstLmt_Perl_NeellmbalanceCmd_MtrNm_f32.value AstLmt_Perl_VehSpd_Kph_f32_value AstLmt_Perl_ThermalLimit_MtrNm_f32.value AstLmt_Perl_AstLmt_AstLmt_Perl_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Perl_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstL	Input Value  0 -5 -5 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1	2 2 _f32 n_f32 c	
_mt_ManualTrqCmd_MtrNm_M_f32 _mt_SteeringAsstDefeat_Cnt_M_lgc _Inst_Ap_AstLmt _umLimPlCmpLimit_MtrNm_f32 _et_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCTOmd_MtrNm_f32.value AstLmt_Per1_OptrqOv_MtrNm_f32.value AstLmt_Per1_OptrqOv_MtrNm_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerl_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.As	1 tgt_Rte_Inst_Ap_AstLmt 5.63999987 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0 -8 1 0 0 -8 -2.5999999 0.699999988 1 0.699999988 4.5 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3;	2 2 _f32 n_f32 c	
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AstLmt_Per1_Thermall.imitPerc_UIs_f32.value  AstLmt_Per1_Thermall.imit_MtrNm_f32.value  AstLmt_Per1_VehSpd_Kph_f32.value  AstLmt_Per1_VehSpd_Kph_f32.value  AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_UIs_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTDiadle_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotputRampMult_UIs_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotputRampMult_UIs_f32	0.699999988  8.10000038  58.2000008  -6.5  tgt_AstLmt_Per1_AssistCmd_MtrNm_f32  tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32  tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistStallLimit_MtrNm_tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt	2 2 _f32 1_f32 c	
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AstLmt_Per1_VehSpd_Kph_f32.value  AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPctrCCDisable_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32	58.2000008  -6.5  tgt_AstLmt_Per1_AssistCmd_MtrNm_f32  tgt_AstLmt_Per1_AssistEOTDamping_MtrNn  tgt_AstLmt_Per1_AssistEOTGain_Uls_f32  tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm  tgt_AstLmt_Per1_CombinedDamping_MtrNm  tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr  tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	2 2 _f32 1_f32 c	
AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	-6.5  tgt_AstLmt_Per1_AssistCmd_MtrNm_f32  tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32  tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32  tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	2 2 _f32 1_f32 c	
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Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptruputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm, tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgit tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	2 _f32 n_f32 c	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrutRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t	gt_AstLmt_Per1_AssistStallLimit_MtrNm_f3z tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm, tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	2 _f32 n_f32 c	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_DTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	_f32 n_f32 c	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_Igc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrutRampMult_Uls_f32  Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc  t	tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	n_f32 c	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igo tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3	С	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrustRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptruputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t			
Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	igi_AsiLini_Peri_LimiledRetum_withvin_i32		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t			
Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lc	gc	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	2	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg	C	
	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3		
	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3		
Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 t	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	!	
Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3	32	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 t	target_AstLmt_Per1_TSMitCommand_MtrNm	n_f32	
Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 t	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 t	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 t	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 t	tgt_AstLmt_Per1_VehSpd_Kph_f32		
	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN	Nm_f32	
	Actual Value	Expected Value	Resu
	0.69999988	0.699999988 ± 0.000030517578125	
	6	6 ± 0.0009765625	
	n		
AstLmt_Per1_TrqLimitMin_MtrNm_f32.value   4stLmt_Per1_TrqLimitMin_MtrNm_f32.value   5stLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 0	0 ± 0.0009765625 0 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.49 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	5			
·	0			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte Inst Ap AstLmt				
	tgt_Rte_Inst_Ap_AstLmt			
<_SumLimPlCmpLimit_MtrNm_f32 Instant_Apt mt_Part_TSMitCommand_MtrNm_f32 value				
rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.79999995			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.9000001			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.30000019			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.69999981			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012			
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5			
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012			
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.1999981			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	42.2999992			
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN			
		_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc		tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	732		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	trNm f32		
lame	Actual Value	Expected Value	Res	
			Kest	
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.800000012	0.800000012 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625		
		0 . 0 0000707070		
gt_AstLmt_Perl_Fret.mintTorque_Murkin_r32.value gt_AstLmt_Perl_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Perl_TrqLimitMin_MtrNm_f32.value	0 5.30000019	0 ± 0.0009765625 5.30000019 ± 0.0009765625		



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.50 (Repeat Count = 1) Name	Input Value			
	·			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0			
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
s_SumLimPlCmpLimit_MtrNm_f32	6.36000013			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.89999998			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999			
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.4000001			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.80000019			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976			
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.2999995			
gt AstLmt Per1 PullCompCmd MtrNm f32.value	-6			
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.3000019			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	38.4000015			
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	38.4000015 5.5999999			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Im f32		
		MIII_132		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc		tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	rNm f32		
			Pos	
Name	Actual Value	Expected Value	Resi	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125		
	8	8 ± 0.0009765625		
·		0 + 0 0000705005		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value		0 ± 0.0009765625 0 ± 0.0009765625 5.4000001 ± 0.0009765625		



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

### WIND VALUE TO A STATE OF THE PLAN STATE OF T	Test Step 2.51 (Repeat Count = 1)	Input Value			
State   Manual Tracking   Methods					
National Association   Control   Light					
Start ImPriChyphint, Minhins, 122	·				
Samt.mpt   Morthm, 152		-			
1, Ast.m. Perl.   2, Ast.m.   2, A	:-				
Astern Perl   AssistCortan   Jimms   12 value					
Asturn   Perl   AssistEOTORing   1,95   22 value					
Astline   Perl   AssistCOTIAN   Ullis   72 value					
J. AstLim Perl AssistChillim, Mehhm 152 value   5.0000001   1, AstLim Perl AssistVehöpcLim, Mehhm 152 value   5.5   5.5   1, AstLim Perl Children					
Astimpt Perl   AssistStallismi, Minkm, 102 value		0.600000024			
Ast.m. Perl   CommenceTomping   Minim   St2 value	gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.0999999			
Ast.m.   Perl.   CombinedCamping, Mehrh.   122 value		5.5			
J. AstLim. Perl. Limite/Gesturn. Mixthm. 123 value   1	gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.9000001			
	gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value				
J. AstLm. Perl L.	gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
J. AstLim. Perl L. ImProCiff Code Minim. 192 value   1   3   3   3   3   3   3   3   3   3	gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3			
	gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
Astumt Perf   Ostprück Minhm, 132 value   1, Astumt Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf	gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
Jastim   Pert   OutpuRsamMut   Us   52 value   0.5     Jastim   Pert   PosesvcCDisable   Cnt   Us   52 value   0.100000001     Jastim   Pert   ThemalLimide   Themis   Tastim   Tasti	yt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3			
2] AstLmL Pert   PosSeno CODisable, Cn. Igo: value   0   0   0   0   0   0   0   0   0	gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5			
J. Ast.ml. Perl _ PowerLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ PirAssistCmd_MnNm_  S2 value   0.100000001     J. Ast.ml. Perl _ PirAssistCmd_MnNm_  S2 value   0.100000001     J. Ast.ml. Perl _ ThermalLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ ThermalLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ VheSpd_Kph_  S2 value   0.100000001     J. Ast.ml. Perl _ VheSpd_Kph_  S2 value   0.100000001     J. Ast.ml. Perl _ WheelinbalanceCmd_MnNm_  G2 value   0.100000001     J. Ast.ml. Perl _ WheelinbalanceCmd_MnNm_  G2 value   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTDamping_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTDamping_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTLimit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistVehSpd_Limit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistVehSpd_Limit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ LimitPercentFiltered_Uis_ G2   0.10000000000000000000000000000000000	gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5			
	gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
	gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001			
	gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999			
AstLmL Pert   ThermalLimit, MtrNm_f32.value	gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7			
AstLmL Pert   ThermalLimit, MtrNm_f32.value	at AstLmt Per1 ThermalLimitPerc Uls f32.value				
AstLmt_Pert   WebSpd , Kph_ 132 value					
AstLmt Pert   WheelmbalanceCmd MtrNm;					
12, Rte, Inst, Ap, AstLmtAstLmt, Perl, AssistEOTDamping, MirNim, [32]   tg, AstLmt, Perl, AssistEOTGain, Uis, [32]   tg, AstLmt, Perl, LimiteOfecture, MirNim, [32]   tg, AstLmt, Perl, AssistEOTGain, Uis, [32]   tg, AstLmt, Perl, Ass					
Internation					
12   12   13   14   15   15   15   15   15   15   15					
gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStalliLimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStalliLimit_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStAllimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStAllimit_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_DefeatLimitService_Cnt.lgc         tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_DefectColsable_Cnt.lgc         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt. Per1_OpTrqOvr_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Post-InitPerc Uls_f32         tgt_AstLmt. Per1_DotputRampMult_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Post-InitPercs_Uls_f32         tgt_AstLmt. Per1_Post-InitPerc_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt. Per1_Post-InitTorque_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Sulf. Command_MtrNm_f32 <td< td=""><td></td><td colspan="3"></td></td<>					
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistVehSpdLimit   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   Defeat   MtrNm   f32   tg   AstLmt   Pert   Defeat   MtrNm   f32   tg   AstLmt   Pert   Limited   MtrNm   f32   tg   AstLmt   Pert   Doptro   MtrNm   f32   tg   AstLmt   Pert   PowerLimitPerc   Uls   f32   tg   AstLmt   Pert   PreLimitForGall   MtrNm   f32   tg   AstLmt   Pert   Tramillimited   tg   AstLmt   Pert   Tramillimited   tg   AstLmt   Pert   Tramillimited   tg   tg   AstLmt   Pert   Tramillimited   tg   tg   AstL					
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo   Igo   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo   Igo   Italian   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo					
			_		
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmt_AstLmt_Per1_LimiteReturn_MtrNm_i32 tgt_AstLmt_AstLmt_Per1_LimiteReturn_MtrNm_i32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_OutputRempMult_Uls_i32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i33 tgt_AstLmt_Per1_PowerLimitedReturn_i33 tgt_AstLmt_Per1_PowerLimitedReturn_i33 tgt_AstLmt_Per1_PowerLimitedReturn_i33 tgt_AstLmt_Per1_Pre1.imitedReturn_i33 tgt_AstLmt_Per1_Pre1.imitedReturn_i33 tgt_AstLmt_Per1_Pre1.imitedReturn_i33 tgt_AstLmt_Per1_Pre1.imitedReturn_i33 tgt_AstLmt_Per1_Pir_LimitedReturn_i33 tgt_AstLmt_Per1_Pir_LimitedReturn_i33 tgt_AstLmt_Per1_Pir_LimitedReturn_i33 tgt_AstLmt_Per1_Pir_LimitedReturn_i33 tgt_AstLmt_Per1_Pir_LimitedReturn_i33 tgt_AstLmt_Per1_SumLimitedReturn_i33 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 target_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_ThermalLimitedrurn_i34 tgt_AstLmt_Per1_Per1_MtrNm_i33 tgt_AstLmt_Per1_VelSp					
tgt_astLmt_Per1_LimitedReturn_MtrNm_f32   tgt_astLmt_Per1_Directroctorial_MtrNm_f32   tgt_astLmt_Per1_Tirectroctorial_MtrNm_f32   tgt_astLmt_Per1_Directroctorial_MtrNm_f32					
tg   tg   tg   tg   tg   tg   tg   tg					
tgt_AstLmt_Ap_AstLmt_Apt_mt_Per1_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_DosServCCDisable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PreLimitForstall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_PreL					
			f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_TomalLimitTomal_MtrNm_f32   tgt_AstLmt_Per1_TomalLimitPerc_Uls_f32   tgt_AstLmt_Per1_TomalLimitPerc_Uls_f32   tgt_AstLmt_Per1_TomalLimitPerc_Uls_f32   tgt_AstLmt_Per1_TomalLimit_MtrNm_f32   tgt_AstLmt_Per1_VomalLimit_MtrNm_f32   tgt_As					
trace   inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32   tgt_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_SumLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_UshSpd_Kph_f32   tgt_AstL					
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32					
trace   Inst   Ap   AstLmt   AstLmt   Per1   PreLimitTorque   MtrNm   f32   tgt   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   ThermalLimit   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   VehSpd   Kph   f32   tgt   AstLmt   Per1   VehSpd   Kph   f32   tgt   AstLmt   Per1   WheellmbalanceCmd   MtrNm   f32   tgt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   tgt   AstLmt   Per1   PreLimitTorque   MtrNm   f32   tgt   AstLmt   Per1   SumLimTqCmd   MtrNm	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TremalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretimitMin_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForQue_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value target_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value target_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torq	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  target_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_WheelImballanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UnitPerc_Uls_f32  tgt_AstLmt_Per1_UnitPerc_Uls_f32  tgt_AstLmt_Per1_LimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  target_AstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_SumLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Iame       Actual Value       Expected Value       Res         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.100000001       0.100000001 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625		target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Iame       Actual Value       Expected Value       Res         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.100000001       0.100000001 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.100000001 0.100000001 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 3 ± 0.0009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 1.5 ± 0.0009765625		tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3:	2		
tgt_Rtle_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.100000001 0.100000001 0.100000001 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 3 3 ± 0.0009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 1.5 1.5 ± 0.0009765625		tgt_AstLmt_Per1_TrqLimitMin_MtrNm f32			
the content of the					
Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125         0.100000001         0.100000001 ± 0.0000765625         0.0009765625         0			trNm f32		
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value     0.100000001     0.100000001 ± 0.000030517578125       gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value     3     3 ± 0.0009765625       gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value     3     3 ± 0.0009765625       gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value     1.5     1.5 ± 0.0009765625				Pos	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625				11031	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625					
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 1.5 ± 0.0009765625	·				
	gr_Asicht_FetT_PtecimitTorque_withM_t32.value	J			
	at Apt mt Dord SumlimTraCond Machine 500 ustra	1.5			

**TEST DETAILS REPORT** 

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AstLmt\_Per1



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.53 (Repeat Count = 1) Name	Input Value			
	· ·			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3			
AstLmt_ManualTrqCmd_MtrNm_M_f32	1			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc				
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
C_SumLimPICmpLimit_MtrNm_f32	7.4400006			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.20000005			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.30000019			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.69999981			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.0999999			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
at AstLmt Per1 PowerLimitPerc Uls f32.value	0.300000012			
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1			
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.6000038			
yt_AstLmt_Per1_VehSpd_Kph_f32.value	38.7000008			
	-3			
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32		tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	<del>-</del>		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	1_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt AstLmt Per1 PullCompCmd MtrNn	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrI	_		
gt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_N	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls			
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit AtrNm f32	tgt_AstLmt_Per1_ThermalLimit_erc_ois	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f			
gt_Rte_inst_Ap_AstEmt.AstEmt_Fer1_inqEinitiviiin_withViii_is2  gt_Rte_Inst_Ap_AstEmt.AstEmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	<u></u>		
		Mirklim #22		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		1-	
Name	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625		
		4 5 + 0 0000705005		
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625		



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.54 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.29999995		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.6999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	36.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.1999981		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_1		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2.32000017	-2.31999993 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.80000019	5.80000019 ± 0.0009765625	



T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

ListLmt_ManualTrqCmdEn_Cnt_M_lgc ListLmt_ManualTrqCmd_MtrNm_M_f32 ListLmt_SteeringAsstDefeat_Cnt_M_lgc ListLmt_SteeringAsstDefeat_Cnt_M_lgc ListLmt_SteeringAsstDefeat_Cnt_M_lgc ListLmt_Ap_AstLmt _SumLimPlCmpLimit_MtrNm_f32 ListLmt_Per1_AssistCmd_MtrNm_f32.value ListLnt_Per1_AssistCmd_MtrNm_f32.value ListLnt_Per1_AssistEOTDamping_MtrNm_f32.value ListLnt_Per1_AssistEOTGain_Uls_f32.value ListLnt_Per1_AssistEOTLimit_MtrNm_f32.value ListLnt_Per1_AssistStallLimit_MtrNm_f32.value ListLnt_Per1_AssistStallLimit_MtrNm_f32.value ListLnt_Per1_AssistVehSpdLimit_MtrNm_f32.value ListLnt_Per1_Assis	Input Value  1 2 1 tgt_Rte_Inst_Ap_AstLmt 8.15999985 2.4000001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019 -6.5		
astLmt_ManualTrqCmd_MtrNm_M_f32 astLmt_SteeringAsstDefeat_Cnt_M_lgc astLmt_SteeringAsstDefeat_Cnt_M_lgc arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value at_AstLmt_Per1_AssistCmd_MtrNm_f32.value at_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value at_AstLmt_Per1_AssistEOTGain_Uls_f32.value at_AstLmt_Per1_AssistEOTGain_Uls_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2 1 tgt_Rte_Inst_Ap_AstLmt 8.15999985 2.4000001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
astLmt_SteeringAsstDefeat_Cnt_M_lgc  kte_Inst_Ap_AstLmt _SumLimPlCmpLimit_MtrNm_f32  arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value  at_AstLmt_Per1_AssistCmd_MtrNm_f32.value  at_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value  at_AstLmt_Per1_AssistEOTGain_Uls_f32.value  at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value  at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value  at_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1 tgt_Rte_Inst_Ap_AstLmt 8.15999985 2.4000001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
kte_Inst_Ap_AstLmt _SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value at_AstLmt_Per1_AssistCmd_MtrNm_f32.value at_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value at_AstLmt_Per1_AssistEOTGain_Uls_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	tgt_Rte_Inst_Ap_AstLmt 8.15999985 2.4000001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value at_AstLmt_Per1_AssistCmd_MtrNm_f32.value at_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value at_AstLmt_Per1_AssistEOTGain_Uls_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value at_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.15999985 2.4000001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.400001 3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.29999995 -1 0.100000001 6.5 5.9000001 6.30000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	-1 0.100000001 6.5 5.9000001 6.30000019		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.100000001 6.5 5.9000001 6.30000019		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5 5.9000001 6.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001 6.30000019		
	6.30000019		
JC_ASILITIC_F ETT_ASSISTVETISPULITITIC_IVILITYITI_132.Value			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
	0		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_igc.value	7		
gt_AstLmt_Per1_EmPrictr1Cmt_MtrNm_f32.value	-2		
gt_AstLmt_Per1_Op1rqOvi_MtrNm_isz.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
	0.5		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
gt_AstLmt_Fer1_FowerLimitFer2_dis_i32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_HtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	26.8999996		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNr	n f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	11_132	
gt_Rte_inst_Ap_AsiLini.AsiLini_Per1_AssistEOTGain_Uis_i32 gt_Rte_inst_Ap_AsiLmt.AsiLint_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	3	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCOTLmint_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Lmmteuretum_wtwm_i32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmnPnCtrCCDisable_Cnt_l		
pt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	go	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	<b>J</b> C	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3	22	
gt_Rte_inst_Ap_AsiLint.AsiLint_Per1_PreLimitPor3tali_MitMin_i32	tgt_AstLmt_Per1_PreLimitForque_MtrNm_f3		
gt_Rte_inst_Ap_AsiLint.AsiLint_Fei1_FieLinitTorque_withnin_is2 gt_Rte_inst_Ap_AsiLint.AsiLint_Fei1_FieLinitTorque_withnin_is2	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
gt_Rte_inst_Ap_AsiLini.AsiLini_Per1_PriAssistCinia_MitNini_132 gt_Rte_inst_Ap_AsiLmt.AsiLini_Per1_PullCompCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f		
gt_Rte_inst_Ap_AstLint.AstLint_Per1_SumLintTrqCind_withMin_is2 gt_Rte_inst_Ap_AstLint.AstLint_Per1_TSMitCommand_MtrNim_is2	target_AstLmt_Per1_TSMitCommand_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Tswitcommand_wtiNm_132 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TSMitCommand_MtMi tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_InermaiLimitPerc_Uis_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_ThermailLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uis_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_inst_Ap_astLmt.AstLmt_Peri_venSpd_Rpn_r32 gt_Rte_inst_Ap_astLmt.AstLmt_Peri_venSpd_Rpn_r32	tgt_AstLmt_Per1_venSpd_kpn_t32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrI	Nm f32	
lame	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	•
at_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.5 5.9000001	3.5 ± 0.0009765625 5.9000001 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.56 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.52000046		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5		
	5.5999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt lo		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
		22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.81999969	2.81999993 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.69199991	1.69200003 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.57 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.36999989		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	0.40000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.6999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
st_AstLmt_Per1_VehSpd_Kph_f32.value	65.3000031		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
		NIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<b>600</b>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	trNm f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.840000033	-0.839999974 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.0999999	6.0999999 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.58 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	0.829999983		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.70000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.80000019		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.19999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6.69999981		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
st_AstLmt_Per1_VehSpd_Kph_f32.value	25.1000004		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	!	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
			Pos
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.6299988	3.63000011 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.6299988	3.63000011 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.90400004	2.90400004 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.59 (Repeat Count = 1)	In and Males		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 1.25999999		
k_SumLimPICmpLimit_MtrNm_f32	2.79999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_withMin_isz.value	-5 -5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.69999981		
tgt AstLmt Per1 CombinedDamping MtrNm f32.value	-2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75.6999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ tgt_AstLmt_Per1_ThermalLimit_MtrNm_f:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrefmalLimit_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3;		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitwiin_witrviin_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_witvim_is.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_verispd_Rpn_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_venSpd_kpn_is2 tgt_AstLmt_Per1_WheelImbalanceCmd_l	MtrNm f32	
			Page
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5 -4.5	4.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4.5 -4.04999971	-4.5 ± 0.0009765625 -4.05000019 ± 0.0009765625	
tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32.value	6.30000019	6.30000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.60 (Repeat Count = 1)	Input Value		
Name	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	1.6900006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.10000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	1.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	28.8999996		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo	дс	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_l	MtrNm f32	
			Pos
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19000006	6.19000006 ± 0.0009765625	
	6.19000006	6.19000006 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value			
gt_AstLmt_Per1_PreLimitTorque_MtrNm_r32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_r32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_r32.value	0.619000018 6.4000001	0.619000018 ± 0.0009765625 6.4000001 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.61 (Repeat Count = 1)	In AV-II-		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.11999989		
<_SumLimPICmpLimit_MtrNm_f32 Instruct_Apt mt_Port_TSNitCommand_AttrNm_f32 yellus	3		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
	0.69999988		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	6.5		
	6.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	-4		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_Lmnledr\edutr_with\text{virthin_i32.value} gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	0		
	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_DpTrqOvr_MtrNm_f32.value	-8		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	<del>_</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	0_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNn		
gt_Rte_Inst_Ap_AstEmt_AstEmt_Fer1_FeEImitrorique_MitNm_i32	tgt AstLmt Per1 PrkAssistCmd MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_	
gt_Rte_inst_Ap_AstEmt.AstEmt_Fer1_Triefmattinit_MithNit_132 gt_Rte_inst_Ap_AstEmt.AstEmt_Per1_TriefmattMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f3		
gt_Rte_inst_Ap_AstLint.AstLint_PeriirqLinit(win_within_is2 gt_Rte_inst_Ap_AstLmt.AstLint_PeriirqLinit(win_within_is2	tgt_AstLmt_Per1_trqLimitwin_wtrNm_is	_	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_VenSpd_Rpin_is2 gt_Rte_inst_Ap_AstLint.AstLint_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_veriSpd_Kpri_i32 tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
			D.
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.660000026 6.5	0.660000026 ± 0.0009765625 6.5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.62 (Repeat Count = 1)	In the Males		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.54999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	5 5000000		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.05000019	2.04999995 ± 0.0009765625	٠,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.615000069	0.61500001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.5999999	6.5999999 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.63 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-8.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.98000002		
c_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	425.399994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	lls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Ci	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNi	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt AstLmt Per1 PreLimitForStall MtrNm f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.40000006	-0.400000006 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.69999981	6.69999981 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.64 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.41000009		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	363.799988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	Igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLint.AstLint_Fei1_FowerLinitFei0_0is_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLint_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_PreLimitPolStail_witNin_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_FreLimitForstall_MtrNm		
gt_Rte_inist_Ap_AstLint.AstLint_Ferr_FreLinitrorque_withhin_is2 gt_Rte_inist_Ap_AstLint.AstLint_Ferr_FreLinitrorque_withhin_is2	tgt_AstLmt_Per1_FreLimitTorque_MitNin_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PtrAssistCmd_mtrNm_t		
gt_Rte_inst_Ap_AstLint.AstLint_Fei1_FullCompCinta_wtivint_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
gt_Rte_inst_Ap_AstLint.AstLint_Peri_Sunit.infrqCind_intrNin_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_Sunit.infrqCind_intrNin_is2	target AstLmt Per1 TSMitCommand Mtrl	_	
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	v. N	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	_	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.4000001	3.4000001 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.80000019	6.80000019 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.65 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5		
AstLmt_ManualTrqCind_MtNni_M_i32 AstLmt_SteeringAsstDefeat Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_nstEm_ eri_ testerma_wirkin_joz.value	3		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.9000001		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	7.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.10000038		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	246.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_ tgt_AstLmt_Per1_LmPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrCobisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1 132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.8000007	-1.79999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.9000001	6.9000001 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.66 (Repeat Count = 1)	Invest Well		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.26999998		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.300003		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd	MtrNm f32	
Name	Actual Value	Expected Value	Resu
		· ·	Nesu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7	0 ± 0.0009765625 7 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.67 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.69999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	8		
	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	84.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lge		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt AstLmt Per1 PullCompCmd MtrNm 1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
		HrNm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4	-4 ± 0.0009765625	,
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.0999999	7.0999999 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Name	
Askum, Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. A. Askum: A. Shempsoberlorat, Cri, M. ye:  18: Inst. Askum: A. Shempsoberlorat, M. Ye:  18: Inst.	
Astum   Shermi, Astum   Astum   Shermi, Astum   Shermi, Astum   Shermi, Astum   Astum   Shermi, Astum   Astum   Shermi, Astu	
Sim_Inst_Pert_Pert_SMCCommand_Minhim_IS2_value   2,4000001   3,2000011   3,20000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,2000011   3,20000011   3,20000011   3,20000011   3,20000011	
SantamPerl   San	
JAMESTER   Pert   TSMCCommand, Minhthn, 152 value	
Astumer   Perl AssistCort Minhthm (32 value   1	
Astum. Perl   AssisticOTamping, Mirkm, f32 value	
J. Ast.m. Perl AssistFOTIand, Mirkm, JS2 value   7, 50000019   7, 100000019   7, 100000019   7, 100000019   7, 100000019   7, 100000000000000000000000000000000000	
J. AstLim, Perl J. AssistSCHLIm, MithYm, 152 value   7, 50000019   1, AstLim, Perl J. AssistVehSpollimi, MithYm, 152 value   7, 5000009   7, AstLim, Perl J. AssistVehSpollimi, MithYm, 152 value   7, 5000009   7, AstLim, Perl J. AssistVehSpollimi, MithYm, 152 value   7, 50000009   7, AstLim, Perl J. MithPortCCChastbe, Coll, Igo-value   7, AstLim, Perl J. MithCCCChastbe, Coll, Igo-value   7, AstLim, Perl J. MithCCCCChastbe, Igo-value   7, AstLim, Perl J. MithCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	
J. AstLim. Perf. J. Assistive Special Limit. Minns. 122 value   7,5999999   1, AstLim. Perf. Limite Breturn. Minhm. 152 value   6   1, AstLim. Perf. Limite Breturn. Minhm. 152 value   6   1, AstLim. Perf. Limite Breturn. Minhm. 152 value   6   1, AstLim. Perf. Limite Breturn. Minhm. 152 value   7   1, AstLim. Perf. Limite Breturn. Minhm. 152 value   7   1, AstLim. Perf. Limite Driver Limite Breturn. 152 value   7   1, AstLim. Perf. Limite Driver Limite Breturn. 152 value   7   1, AstLim. Perf. Limite Breturn. 152 value   7   1, AstLim. Perf. Limite Breturn. 152 value   7   1, AstLim. Perf. Limite Breturn. 152 value   1   1, AstLim. Perf. Limite Breturn. 152 value   2, 2009999962   1, AstLim. Perf. Limite Breturn. 152 value   2, 200999999999999999999999999999999999	
Ast.ml. Perl CombnedDomping, MiNhm, 122 value	
J. AstLim. Perl _ Defeat LimiSeronce_Cnt_lgo_value   0   0   0   0   0   0   0   0   0	
AstLIME   Pert   Limited Petturn, Mirkim, 132 value	
AstLmt  Pert   LmPhCitCoDisable_Cnt_ gc.value   0   0   0   0   0   0   0   0   0	
Ast.mt   Pert   LmPnCtiTond MtNm   S2 value   0   0   0   0   0   0   0   0   0	
Ast.ml Pert LorPh(ChTCmd_Mithim_152 value   7   7   7   7   7   7   7   7   7	
AstLmt Pert   Optropor, Minhm   S2 value   7   0.89999976   1.   AstLmt Pert   DougleAmpholit   Us     S2 value   1.	
J. AstLmf. Perl CoutputRampMult_Uis_132 value	
AstLmLPert_PosSenoCDisable_Cri_lgo.value   1   1   1   2   2   2   2   2   2   2	
AstLmt Pert   PowerLimitPerc_Uis_732 value	
J. AstLmt. Pert.   PowerLimitPerc. Uis. 132 value   1, 2 to 0000005   1, 2 to 00000005   1, 2 to 00000005   1, 2 to 0000000000000000000000000000000000	
AstLmL Perl_PullCompCmd_MtrNm_f32.value   1	
AstLmt Pert   ThermalLimit  MrNm   52 value   1	
AstLmt Perf   ThermalLimit   MtrNm   f32 value   8.39999962   1, AstLmt   Perf   VehSpd   Kph   f32 value   27.7000008   1, AstLmt   Perf   VehSpd   Kph   f32 value   27.7000008   1, AstLmt   Perf   AssistCord_MtrNm   f32   1, AstLmt   Perf   AssistCord_MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTDamping, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTDamping, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTImit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   Perf   AssistEOTLimit, MtrNm   f32   1, Rie   Inst, Ap   AstLmt   Perf   DefeatLimitService   Cnt   Igc   1, Rie   Inst, Ap   AstLmt   Perf   DefeatLimitService   Cnt   Igc   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   LimitPercentifitered   Uis   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   DefeatLimitService   Cnt   Igc   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   DefeatLimitService   Cnt   Igc   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf   PreclimitForStall   MtrNm   f32   1, Rie   Inst, Ap   AstLmt   AstLmt   Perf	
AstLmt   Pert   ThermalLimit   MirNm	
AstLmt_Per1_VehSpd_Kph_132_value	
AstLmt_Per1_WheelinbalanceCmd_MtrNm_f32.value	
12, Rte_Inst_Ap_AstLmt.AstLmt_Peri_AssistCond_MtrNm_f32   tgt_AstLmt.Peri_AssistCond_MtrNm_f32   tgt_AstLmt.Peri_PoservCoDisable_Cont_igc   tgt_AstLmt.Peri_PoservCoDis	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTDamping   MtrNm   f32   gt   AstLmt   AstLmt   Pert   AssistEOTGain   Uis   f32   gt   AstLmt   AstLmt   Pert   AssistEOTGain   Uis   f32   gt   AstLmt   Pert   AssistEOTTDamping   MtrNm   f32   gt   AstLmt   Pert   AssistStalliLimit   MtrNm   f32   gt   AstLmt   Pert   CombinedDamping   MtrNm   f32   gt   AstLmt   Pert   DefeatLimitService   Cnt   gc   gt   AstLmt   Pert   DefeatLimitService   Cnt   gc   gt   AstLmt   Pert   DefeatLimitService   Cnt   gc   gt   AstLmt   Pert   LimitedReturn   MtrNm   f32   gt   AstLmt   Pert   DefoservCODisable   Cnt   gc   gt   AstLmt   Pert   DefoservCODisable   Cnt   gc   gt   AstLmt   Pert   DefoservCODisable   Cnt   gc   gt   AstLmt   Pert   Pert   MtrNm   f32	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTGain   Uls   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTLimit   Minhm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTLimit   Minhm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistStallilimit   Minhm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistStallilimit   Minhm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   CombinedDamping   Minhm   f32   gt   AstLmt   Pert   LimitedRetur   Minhm   f32   gt   AstLmt   Pert   DesCervCCDisable   Cnt   ge   gt   AstLmt   Pert   DesCervCCDisable   Cnt   ge   gt	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistStalliLimit   MtrNm   f32   gt   AstLmt   Pert   Defaultimit   MtrNm   f32   gt   AstLmt   Pert   Defaultimit   MtrNm   f32   gt   AstLmt   Pert   Defaultimit   Pert	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   AssistStallLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   AssistVehSpdLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   CombinedDamping   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DefeatLimitService   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DefeatLimitService   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPercentFiltered   Uls   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPercentFiltered   Uls   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPercentFiltered   Uls   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPotCrCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPotCrtCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPotCrtCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPotCrtCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPotCrtCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DotTrCDM   MtrNm   f32   gt   AstLmt   Per1   DotTrCDM   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DotTrCDM   MtrNm   f32   gt   AstLmt   Per1   PosServCCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PoservCCDisable   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PrichimitForque   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   PrichimitForque   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   ThermalLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   ThermalLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   ThermalLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   As	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   AssistVehSpdLimit   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   CombinedDamping   MtrNm   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DefeatLimitService   Cnt   Igc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   DefeatLimitService   Cnt   Igc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPercentFiltered   Uls   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Per1   LimitPercentFiltered   Uls   f32   gt   AstLmt   Per1   DefeatLimitPercentFiltered   Uls   f32   gt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   gt   AstLmt   Per1   SumLimitTqCmd   MtrNm   f32   gt   AstLmt   Per1   SumLimitPercentFiltered   Uls   f32 value   gt   AstLmt   Per1   ThermalLimit   MtrN	
tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_CombinedDamping_MtrNmr_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_DefeatLimitService_Cnt_lgc tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_DirtGoVrd_MtrNmr_i32 tgt_AstLmrt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmrt_Per1_DirtGoVrd_MtrNrt_i332 tgt_AstLmrt_Per1_DirtGoVrd_MtrNrt_i332 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_DoverLimitPerc_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_PreLimitForStall_MtrNrt_i32 tgt_AstLmrt_Per1_PreLimitForStall_MtrNrt_i32 tgt_AstLmrt_Per1_PreLimitForStall_MtrNrt_i32 tgt_AstLmrt_Per1_PreLimitForGoverLimitPerc_Uls_i32 tgt_AstLmrt_Per1_PreLimitForGoverLimitPerc_Uls_i32 tgt_AstLmrt_Per1_PreLimitForGoverLimitPerc_Uls_i32 tgt_AstLmrt_Per1_PreLimitForGoverLimitPerc_Uls_i32 tgt_Rte_Inst_Ap_AstLmrt.AstLmrt_Per1_PreLimitTorque_MtrNrt_i32 tgt_AstLmrt_Per1_PreLimitTorque_MtrNrt_i32 tgt_AstLmrt_Per1_SumLimitPerc_Uls_i32 tgt_AstLmrt_Per1_SumLimitPerc_Uls_i32 tgt_AstLmrt_Per1_SumLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TremalLimitPerc_Uls_i32 tgt_AstLmrt_Per1_TreLimitPercontFiltered_Uls_i32.v	
St. Rte   Inst. Ap	
trace_inst_Ap_astLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32   tgt_astLmt_Per1_DptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_OptrqOvr_MtrNm_f32   tgt_astLmt_Per1_DptrqOvr_MtrNm_f32   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Ap_astLmt_AstLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_DosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_PosservCCDisable_Cnt_Igc   tgt_astLmt_Per1_TosservCollisable_Cnt_Igc   tgt_astLmt_Per1_TosservColli	
the linst Ap AstLimit AstLimit Per1 outputRampMult Uls f32 the linst Ap AstLimit AstLimit Per1 posservCCDisable Cnt ligd the linst Ap AstLimit AstLimit Per1 posservCCDisable Cnt ligd the linst Ap AstLimit AstLimit Per1 posservCCDisable Cnt ligd the linst Ap AstLimit AstLimit Per1 prover LimitPerc Uls f32 the linst Ap AstLimit AstLimit Per1 prelimitForStall MtrNm f32 the linst Ap AstLimit AstLimit Per1 prelimitTorque MtrNm f32 the linst Ap AstLimit AstLimit Per1 prelimitTorque MtrNm f32 the linst Ap AstLimit AstLimit Per1 prelimitTorque MtrNm f32 the linst Ap AstLimit AstLimit Per1 prelimitTorque MtrNm f32 the linst Ap AstLimit AstLimit Per1 prelimitTorque MtrNm f32 the linst Ap AstLimit AstLimit Per1 pullCompCmd MtrNm f32 the linst Ap AstLimit AstLimit Per1 pullCompCmd MtrNm f32 the linst Ap AstLimit AstLimit Per1 SumLimit TqCmd MtrNm f32 the linst Ap AstLimit AstLimit Per1 SumLimit TqCmd MtrNm f32 the linst Ap AstLimit AstLimit Per1 ThermalLimit MtrNm f32 the linst Ap AstLimit AstLimit Per1 ThermalLimit MtrNm f32 the linst Ap AstLimit AstLimit Per1 TreptimitMin MtrNm f32 the linst Ap AstLimit AstLimit Per1 TreptimitMin MtrNm f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit Per1 VehSpd Kph f32 the linst Ap AstLimit AstLimit	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForQue_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForQue_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForQue_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UhellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TnermalLimitMtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLm	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TormalLimitPerc_Uls_f32  tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_TnermalLimitMtrNm_f32  tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PreL	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value   1	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_UnitPerc_Uls_f32.value   tgt_AstLmt_Per1_InitPerc_Uls_f32.value   tgt_AstLmt_Per1_InitPerc_Uls_f32.value   tgt_AstLmt_Per1_PreLimitPorque_MtrNm_f32.value   tgt_AstLmt_Per1_PreLimitPorque_MtrNm_f32   tgt_AstLm	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           lame         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         1         1 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32.value         4         4 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         0         0 ± 0.0009765625	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretmalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value         1         1 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         4         4 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         0         0 ± 0.0009765625	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           lame         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         1         1 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         4         4 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         0         0 ± 0.0009765625	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Iame       Actual Value       Expected Value         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       1       1 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       4       4 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       0       0 ± 0.0009765625	
the transmitter of the transmit	
the transmitter of the transmit	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 1	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32     tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32     tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value   1	
Jame         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         1         1 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         4         4 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         0         0 ± 0.0009765625	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value     1     1 ± 0.000030517578125       gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value     4     4 ± 0.0009765625       gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value     0     0 ± 0.0009765625	Res
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       4 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       0         0       0 ± 0.0009765625	1103
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 ± 0.0009765625	
v	
gt_Ast_mt_Per1_SumLim1rqCmd_MtrNm_t32.value	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 7.19999981 7.19999981 2.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.69 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-5.55999994		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.55999994		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	7.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_EmiriouTemu_wurwin_i32.value	8		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988		
tgt_AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	8.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	2.9000001		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	<del>_</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	<del></del>	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	•	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_FowerLimitFerc_ois_i		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit ordinal_within_i32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi	<del>-</del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Towntcommand_withtin_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_Per0_0is_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_PerC_Dis	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	<i>5</i>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VeriSpd_Rpi1_is2  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
			Door
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	1
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.699999988 7.30000019	-0.699999988 ± 0.0009765625 7.30000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.70 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 5.98999977		
k_SumLimPICmpLimit_MtrNm_f32			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	m f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered	·	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	•	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrgOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3;		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
	tgt_AstLmt_Per1_PosServCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc		<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.71 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.42000008		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	8.10000038		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrCobisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_LmPnCtr1Cmd_wtrNm_is2.value	-2.0999999		
tgt_AstLmt_Per1_Op1rqOvi_within_isz.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_OutputRampMuit_Ois_isz.value  tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value	0.300000012		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.89999976		
tgt_AstLmt_Per1_PowerLimitrerc_ois_isz.value	-6.5		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	53.4000015		
	3.2999995		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	<del></del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrgOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_OutputRampMult_Uls_ tgt_AstLmt_Per1_PosServCCDisable_Cl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosservCcDisable_Cl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Ois_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitFer5_dis_i.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitPorStail_MithIn1_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_intrNin_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimit1orque_initrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmt_MtiNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PillCompCmd_MtrNm	_	
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PullCompCrid_withMin_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TStrittCommand_MtrNm_I32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TswittCommand_w		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrefmaiLimit_initrim_is2  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
	tgt_AstLmt_Per1_IrqLimitivlin_MtrNm_rs	6	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.72 (Repeat Count = 1)	Invest Webse		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 6.8499999		
k_SumLimPlCmpLimit_MtrNm_f32	-2		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
	0.89999976		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.19999981		
tgt AstLmt Per1 AssistStallLimit MtrNm f32.value	7.5999999		
	8		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2		
tgt_AstLmt_Per1_Lmnledr\edum_withtn_i32.value	1		
tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_tmPnCtrEnable_Ont_igc.value	2		
tgt_AstLmt_Per1_Emericuremu_within_isz.value	-4.5		
tgt_AstLmt_Per1_Op1rtqOv1_intrini_ioz.value	0.40000006		
tgt_AstLmt_Per1_Outputkampinuit_ois_isz.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.100000001		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
gt AstLmt Per1 ThermalLimit MtrNm f32.value	5.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	12.6000004		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	<del>_</del>	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt AstLmt Per1 LimitPercentFiltered L	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	<del></del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PosServCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Resu
		· ·	Rest
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 5.5	0 ± 0.0009765625 5.5 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.73 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmd_MtrNm_M_f22	-3.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.28000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.8999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	8.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.10000038		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.200000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.599999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	2.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	510.799988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	— <del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.40000006 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	٠,



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.74 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.71000004		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
	2.20000005	2.20000005 ± 0.0009765625	

## **TEST DETAILS REPORT**

AstLmt\_Per1

2014-09-05, 18:07:58+0530



T
Actual Function Count Expected Function Count





Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.76 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	2.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.56999969		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
tgt_AstLmt_Per1_13/inicommand_winvin_i3z.value	-6.5		
	5.5999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.60000038		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	324.200012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		pp.	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.4000001	2.4000001 ± 0.0009765625	





Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.77 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc	-1.5 0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 0.600000024		
<_SumLimPICmpLimit_MtrNm_f32 Instruct_Apt mt_Port_TSNitCommand_AttrNm_f32 value	-1.5		
rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.69999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.10000038		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	456.299988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.5999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	:_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	IC .	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	7.000
gt_AstLmt_Per1_cimitrercent intereu_ors_ros.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimit of Stall_MitNIII_132.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	6.29999971	6.30000019 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.5	2.5 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

est Step 2.78 (Repeat Count = 1) Input Value				
Name Acti mt ManualTraCmdEn Cat M Iga	0			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	1.5			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt		tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.39999998			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999			
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	5			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024			
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999			
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.19999981			
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	8.60000038			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995			
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8			
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988			
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.399994			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	<del></del>		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U			
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCcDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	•		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	·		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNi	_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand N	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			
Name	Actual Value	Expected Value	Resu	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625		
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625		
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.5999999	2.5999999 ± 0.0009765625		



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.79 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.30000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.69999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	234.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_UIs_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_Limiterercentrinered_ois		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmnPnCtrCCDisable_Cn		
gt_Rte_inst_Ap_AstEmt.AstEmt_Fer1_EmFilot(Cobisable_Cnt_igc gt_Rte_inst_Ap_AstEmt.AstEmt_Per1_EmPilot(Enable_Cnt_igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
gt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrTCmd MtrNm f32			
	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3 tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	

2.70000005

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

2.70000005 ± 0.0009765625



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.80 (Repeat Count = 1) Name	Input Value			
	0			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7			
AstLmt_ManualTrqCmd_MtrNm_M_f32		1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc				
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
c_SumLimPlCmpLimit_MtrNm_f32	5.5			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.39999962			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012			
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976			
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.2999995			
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003			
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	123.599998			
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
		lm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	1111_132		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			
		1	Pos	
Name	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625		
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625		
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.79999995	2.79999995 ± 0.0009765625		



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Settler Menschröder, Cirt, M. Lyo  settler Menschröder, Stehn, M. St.2  settler Menschröder, M. M. S	Test Step 2.81 (Repeat Count = 1) Name	Input Value			
Settern Membershort (1)		·			
Substitution   Section					
100, Field J., Add Lett.   10, Sept.   10, Add Lett.   10, A	· _ ·				
Saml.mpfmChplimt, Mnfm, 182 wite		· ·			
Application   Pert   TSMICCommond, Minhin, 192 value   1   1, 40000002   1, 42   41   41   41   42   43   43   44   44   44   44   44	: _				
Astin   Perl   AssistCrota					
Authorn   Perl   Assist©Train   Minhs   122 value					
Astume   Perl   Assistitation   Melvin   122 value					
J. Ast. mp. Perl _ AssistStall in.ml, MrNm_102 value   3,0000001   3,45t.mm   Perl _ CombinedDamping_Mintm_102 value   5,8999998   1,45t.mm   Perl _ CombinedDamping_Mintm_102 value   1,45t.mm   Perl _ LimbirChCCDisside_Crit_igs-value   0   7,45t.mm   Perl _ LimbirChCCDisside_Crit_igs-value   1,40t.mm   Perl _ LimbirChCDisside_Crit_igs-value   1,40t.mm   Perl _ LimbirChCDisside_Crit_igs-value   1,40t.mm   Perl _ LimbirChCDisside_Crit_igs-value   0,500000012   1,40t.mm   Perl _ LimbirChCDisside_Crit_igs-value   0,500000012   1,40t.mm   Perl _ Disposition Minimin_102 value   0,500000012   1,40t.mm   Perl _ Disposition Minimin_102   1,40t.m					
J. Ast. mpt   Port   AssistiveRegion   mil.   Mrkm, 12 value   5 50000019     Ast. mpt   Port   Combined   1	gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value				
AstLim Perl CombreedEamonge, Nethirn   722 value	gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001			
J. AstLim. Perl. Limite/Resturu. Mirkim. 132 vaulus   1	gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019			
	gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999			
Jastim   Pert   LmPnCircCobasie Cnt, govalue   1	gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
	gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005			
AstLmt.Pert   DerPot (Comp. Minhm   52 value   1	gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
	gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1			
	st_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019			
Astumt Pert   PosSenotCDisable, Cnt   govable   0	gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1			
J. AstLmt. Pert   PowerLimitPerc_Uis_F32.value   0.300000012     J. AstLmt. Pert   PriAssistCmd_MNNm_F32.value   5     J. AstLmt. Pert   ThermalLimitPerc_Uis_F32.value   0.300000012     J. AstLmt. Pert   ThermalLimitPerc_Uis_F32.value   0.300000012     J. AstLmt. Pert   ThermalLimitPerc_Uis_F32.value   0.300000019     J. AstLmt. Pert   VehSpd_Kph_F32.value   0.300000019     J. AstLmt. Pert   VehSpd_Kph_F32.value   0.300000019     J. AstLmt. Pert   WebSpd_Kph_F32.value   0.300000019     J. AstLmt. Pert   AssistCotTollamin_MNnm_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_AssistCotTollamin_MNnm_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_AssistCotTollamin_MNnm_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_AssistCotTumi_Mnnm_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_AssistSotTumi_Mnnm_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_LimitPerconfiltered_Uis_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_Pert_Mintroguis_Unitwin_F32   15     J. Relins   Ap_AstLmt. AstLmt. Pert_Pert_Mintroguis_Unitwin_F33   15     J. Relins   Ap_AstLmt. AstLmt. Pert_Pert_Mintroguis_Unitwin_F33   15     J. Relin	gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012			
	gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
	gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012			
J. AstLmL Perl   ThermalLimit Perc_Uis_132 value   0.30000012     J. AstLmL Perl   ThermalLimit Minkin_132 value   0.8000019     J. AstLmL Perl   WheelimbalanceCmd_Minkin_132 value   0     J. AstLmL Perl   WheelimbalanceCmd_Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ AssistCOTDamping_Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ AssistSCOTDamping_Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ AssistScotLimit, Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ AssistScotLimit, Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ AssistScotLimit, Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ DefeatLimitService_Cot. I   1     J. Rel_Inst_Ap_AstLmL AstLmL Perl _ LimitPercentFilered_Uis_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ LimitPercentFilered_Uis_132   1     J. Rel_Inst_Ap_AstLmL AstLmL Perl _ LimitPercentFilered_Uis_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ PosterenCollage_Uis_14   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ PowertLmMPerl _ Uis_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ Prel_ImitForque_Minkin_132   1     J. Rel_Inst_Ap_AstLmLAstLmL Perl _ Tre	gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6			
AstLmt Pert   ThermalLimit MtrNm,	at AstLmt Per1 PullCompCmd MtrNm f32.value	5			
JastLmt_Pert   Thermatumit_MtrNm_f32 value	at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.30000012			
AstLmt_Pert   WebSpd Kph_622 value         AstLmt_Pert   MeelimbalanceCmd_MtnNm_f32 value		6.80000019			
AstLmt Per1   WheelimbalanceCmd MtrNm;					
IR. Inst. Ap AstLmt. AstLmt. Perl _ AssistEOTDamping _MirNm _ 132   tg_ AstLmt. Perl _ AssistStall. Imit_MirNm _ 132   tg_ AstLmt. Perl _ DefeatLimitStarcive _ Cnt. Igc   tg_ AstLmt. Perl _ DefeatLimitStarcive _ Cnt. Igc   tg_ AstLmt. Perl _ LimitPercentFiltered_ Uis_ 132   tg_ AstLmt. Perl _ ImitPercentFiltered_ Uis_ 132   tg_ AstLmt. Perl _ DefeatCiltered_ Uis_ 132   tg_ AstLmt. Perl _ DefeatCiltered_ Uis_ 132   tg_ AstLmt. Perl _ DefeatCiltered_ Uis_ 132   tg_ AstLmt. Perl _ PosecroCDisable_ Cnt. Igc   tg_ AstLmt. Perl _			32		
Ig   Ig   Ig   Ig   Ig   Ig   Ig   Ig					
12   Re   Inst.   Ap   Astl.mt   Astl.mt   Pert   AssistEOTLimit   MtrNm   f32   tg   Astl.mt   Pert   AssistStalli.mit   MtrNm   f32   tg   Astl.mt   Astl.mt   Pert   AssistStalli.mit   MtrNm   f32   tg   Astl.mt   Astl.mt   Astl.mt   Pert   AssistStalli.mit   MtrNm   f32   tg   Astl.mt   Astl.mt   Astl.mt   Astl.mt   Pert   AssistStalli.mit   MtrNm   f32   tg   Astl.mt   Astl.mt   Astl.mt   Pert   DefeatLimitService   Cnt.   tg   tg   Astl.mt   Astl.mt   Pert   LimitePrecentFiltered   Uis   f32   tg   Astl.mt   Pert   DefeatCirclored   Uis   f32   tg   Astl.mt   Pert   Pert   Uis   f32   tg   Astl.mt   Pert   DefeatCirclored   Uis   Uis   Uis   Uis   Uis   Ui			<del>-</del>		
Steelinst Ap_AstLmtAstLmt_Peri_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Peri_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Peri_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Peri_AssistStallLimit_MtrNm_f32   tgt_Rel_inst_Ap_AstLmtAstLmt_Peri_DefeatLimitService_Cnt_lgc   tgt_AstLmt_Peri_DefeatLimitService_Cnt_lgc   tgt_AstLmt_Peri_DefeatLimitServ					
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistVehSpdLimit   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   DefeatLmtillisService   Cnt   Igc   tg   AstLmt   Pert   LimitPercentFiltered   Uls   f32   tg   AstLmt   Pert   DertoRrivation   Uls   f32   tg   AstLmt   Pert   PowerLimitPerc   Uls   f32   tg   AstLmt   Pert   PowerLimitPerc   Uls   f32   tg   AstLmt   Pert   PreLimitForStall   MtrNm   f32   tg   AstLmt   Pert   PreLimitForStall   MtrNm   f32   tg   AstLmt   Pert   PreLimitForStall   MtrNm   f32   tg   AstLmt   Pert   PreLimitForGw   MtrNm   f32   tg   AstLmt   Pert   PreLimitForGw   MtrNm   f32   tg   AstLmt   Pert   Trail Milcommand   MtrNm   f32   tg   AstLmt   Pert   Trail Milcommand   MtrNm   f32   tg   AstLmt   Pert   Trail Milcommand   MtrNm   f32   tg   AstLmt			_		
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   CombinedDamping   MtrNm   f32   tgt   AstLmt   Pert   DefeatLimitService   Cnt   tgc   tgt   AstLmt   Pert   LimitPercentFiltered   Uls   f32   tgt   AstLmt   Pert   DoptrqOvr   MtrNmt   f32   tgt   AstLmt   Pert   DoverLimitPerc   Uls   f32   tgt   AstLmt   Pert   PoseervCoDisable   Cnt   tgc   tgt   AstLmt   Pert   PreLimitForque   MtrNmt   f32   tgt   AstLmt   Pert   Trevession   Trevession   Trevession   T					
			_		
tgl. Rte   inst Ap _ AstLmt. AstLmt   Perl _ LimitPercentFiltered _ Uls _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ Rte   inst Ap _ AstLmt. AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ Rte   inst Ap _ AstLmt. AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ LimitedReturn _ MtrNm _ f32   tgl _ AstLmt   Perl _ Improctrenable _ Cnt _ lgc   tgl _ AstLmt   Perl _ Cnt _ Snt _ AstLmt _ Perl _ Improctrenable _ Cnt _ lgc   tgl _ AstLmt   Perl _ Cnt _ Snt _ AstLmt _ Perl _ Terl _ Snt _ AstLmt _ Perl _ Perl _ Snt _ AstLmt _ Perl _ Perl _ Snt _ AstLmt _ Perl _ Perl _ Snt _ AstLmt _ Perl _ ThermalLimiter _ Luin _ tgl _ tgl _ AstLmt _ Perl _ ThermalLimiter _ Luin _ tgl _ t					
			_ <del>-</del>		
tgt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_DnPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_DnPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_DnPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_DnPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_OptrqOv_MtrNm_f32 tgt_AstLmt_Per1_OptrqOv_MtrNm_f32 tgt_AstLmt_Per1_OptrqOv_DtrNtm_f32 tgt_AstLmt_Per1_OptrqOv_DtrNtm_f32 tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCDDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCDDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCDDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCDDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCDDisable_Cnt_Igc tgt_AstLmt_Per1_ProservImtror_Us_Iff_Table_Us_Table_			_		
Strict   Inst Ap			_		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_Description_Clg_St2 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_Description_Clg_St2 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_TellinitMin_MtrNm_f32 tgt_AstLmt_Per1_TellinitMin_MtrNm_f32 tgt_AstLmt_Per1_TellinitMin_MtrNm_f32 tgt_AstLmt_Per1_TellinitMin_MtrNm_f32 tgt_AstLmt_Per1_UndCompCmd_MtrNm_f32 tgt_AstLmt_Per1_UndCompCmd_MtrNm_f32 tgt_AstLmt_Per1_VenSpd_Kph_f32 tgt_AstLmt_Per1_VenSpd_Kph_f32 tgt_AstLmt_Per1_VenSpd_Kph_f32 tgt_AstLmt_Per1_VenSpd_Kph_f32 tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value  3.000000012 3.000000012 3.000000000000000000000000000000000000					
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32  tgt_AstLmt_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosErvEntilePer2_Uls_f32  tgt_AstLmt_Per1_PosImitTorque_MtrNm_f32  tgt_AstLmt_Per1_Intermited_MtrNm_f32  tgt_AstLmt_Per1_PosIm_Intermited_MtrNm_f32  tgt_AstLmt_Per1_PosIm_Intermited_MtrNm_f32  tgt_AstLmt_Per1_SumLimt_AstLmt_PosT_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TretimitMin_MtrNm_f32  tgt_AstLmt_Per1_TretimitMin_MtrNm_f32  tgt_AstLmt_Per1_TretimitMin_MtrNm_f32  tgt_AstLmt_Per1_TypeImitMin_MtrNm_f32  tgt_AstLmt_Per1_TypeImitMin_MtrNm_f32  tgt_AstLmt_Per1_TypeImitMin_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_DeclimitTorque_MtrNm_f32.value  tgt_AstLmt_Per1_DoublimTrqCmd_MtrNm_f32.value  tgt_AstLmt_Per1_DoublimTrqCmd_MtrNm_f32.value  tgt_AstLmt_P					
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimiTrqCmd_MtrNm_f32   target_AstLmt_Per1_TSMitCompand_MtrNm_f32   target_AstLmt_Per1_TSMitCompand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCompand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32   tgt_AstLmt_Per1_TorqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TorqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_UnitPerc_Uls_f32.value   tgt_AstLmt_Per1_UnitPerc_Uls_f32.value   tgt_AstLmt_Per1_UnitPerc_Uls_f32.value   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value   tgt_AstLmt_Per1_PreLimitTorque					
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitPerc_Md_MtrNm_f32   tgt_AstLmt_Per1_PreLimitPerc_Md_MtrNm_f32   tgt_AstLmt_Per1_PreLimitPerc_Md_MtrNm_f32   tgt_AstLmt_Per1_PreLimitPerc_Md_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMtrNm_f32   tgt_AstLmt_Per1_TotemalLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value   0.300000012					
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_UnitNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_UnitNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_UnitNm_f32.val			·		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_PerLimitTorque_MtrNm_f32.value tg	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32           lame         Actual Value         Expected Value         Res           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         8.80000019         8.80000019 ± 0.00009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         2.6400001         2.6400001 ± 0.00009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	lm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         0.300000012         0.300000012 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         8.80000019         8.80000019 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         2.6400001         2.6400001 ± 0.00009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32		
tgt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  ttg_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TSMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMitCommand_MtrNm_f32  ttarget_AstLmt_Per1_TremalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_TremalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_TremalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_TremalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_ThermalLimitPerc_Uls_f32  ttarget_AstLmt_Per1_TremalLimitMtrNm_f32  ttarget_AstLmt_Per1_TremalLimitMtrNm_f32  ttarget_AstLmt_Per1_TremalLimitMtrNm_f32  ttarget_AstLmt_Per1_TremalLimitMtrNm_f32  ttarget_AstLmt_Per1_Uls_f32  ttarget_AstLmt_Per1_SumLimitTrqCmd_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCommand_MtrNm_f32  ttarget_AstLmt_Per1_TsMittCo	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	1_f32		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimi	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	n_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           dame         Actual Value         Expected Value         Res           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.300000012         0.300000012 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         8.80000019         8.80000019 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         2.6400001         2.6400001 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	Nm_f32		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value 0.300000012 0.300000012 0.300000012 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.6400001 2.6400001 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	MtrNm_f32		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value 0.300000012 0.300000012 0.300000012 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.6400001 2.6400001 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	s_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.300000012 0.300000012 0.300000012 0.300000012 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 0.30000019 0.80000019 ± 0.0009765625 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0.26400001 0.26400001 0.26400001 ± 0.0009765625			_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.300000012 0.300000012 0.300000012 0.300000012 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 0.30000019 0.30000019 0.30000019 ± 0.0009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0.30000019 0.30000019 0.30000019 ± 0.0009765625 0.30000019 0.30000019 ± 0.0009765625 0.30000019 0.30000019 ± 0.0009765625 0.30000019 0.30000019 ± 0.0009765625					
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitForQue_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_LimbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_LimbalanceCmd_MtrNm_f32  tgt_					
Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.300000012         0.300000012 ± 0.000030517578125         0.300000012 ± 0.00009765625           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         8.80000019         8.80000019 ± 0.0009765625         8.80000019 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         2.6400001         2.6400001 ± 0.0009765625			MtrNm f32		
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value       0.300000012       0.300000012 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       8.80000019       8.80000019 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       8.80000019       8.80000019 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.6400001       2.6400001 ± 0.0009765625				Pos	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       8.80000019       8.80000019 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       8.80000019       8.80000019 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.6400001       2.6400001 ± 0.0009765625				I/C2	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       8.80000019       8.80000019 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.6400001       2.6400001 ± 0.0009765625					
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.6400001 2.6400001 ± 0.0009765625					
	wt Anti-mat Doug Duni impitTorous Matching 100		LA 80000019 + 0.0009765625	1	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.82 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-3		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 0.579999983		
k_SumLimPlCmpLimit_MtrNm_f32	-1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	m f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_	_ <del>-</del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
	tgt_AstLmt_Per1_PosServCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosservCCDisable_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.11999989	5.11999989 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.11999989	-5.11999989 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.60000002	-1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	٠,



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.83 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	0.860000014		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.899999976		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
yt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
yt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
t AstLmt Per1 ThermalLimitPerc Uls f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
t_AstLmt_Per1_VehSpd_Kph_f32.value	125.260002		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrI	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	\\\\_\_\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Ois_i32	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistOtaliLmit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	2	
yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
at Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	trNm f32	
lame	Actual Value	Expected Value	Res
			Kes
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
at Astumi Peri Prelimitioralie MtrNm 132 value	1	1 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0.5	0.5 : 0.0000705555	
gt_AstLmt_Per1_FreLimitrOrque_witrMn_r32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.5 4.0999999	0.5 ± 0.0009765625 4.0999999 ± 0.0009765625	

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AstLmt\_Per1



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.85 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.41999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.69999988		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.30000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.19999981		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2.2999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.4000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.4000001		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_l	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	ftrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C	·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_	Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
	0.69999988	0.699999988 ± 0.000030517578125	
tgt AstLmt Per1 LimitPercentFiltered Uls f32.value			
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value		5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625 3.29999995 ± 0.0009765625	
		5±0.0009765625 3.29999995±0.0009765625 2.30999994±0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.86 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.70000005		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5.30000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U	<del>_</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
ggt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt_Rte_inst_Ap_AstLint.AstLint_Fer1_OutputRampinuit_ois_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_gc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosservCcDisable_Cr	<del>_</del> -	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PowerLinittPerc_ois_i32 gt_Rte_inst_Ap_AstLint.AstLint_Per1_PreLinitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
	tgt_AstLmt_Per1_PreLimitForStail_witNi tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimit1orque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	0= = = =	-	
	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.4000001	4.4000001 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.87 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	15.9995003		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	19.9995003		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.98000002		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTGain_UIs_f32.value	0.5		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	5.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 OpTrgOvr MtrNm f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.89999976		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.38000009e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	:nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.88 (Repeat Count = 1)	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt			
k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 2.25999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006		
	-8		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	— <del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/trNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
	8	8 ± 0.0009765625	
at AstLmt Per1 PreLimitForStall MtrNm f32 value			
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value at AstLmt Per1 PreLimitTorque MtrNm f32.value		0 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.89 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
	•		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.53999996		
C_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
pt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	22	
	tgt AstLmt Per1 AssistEOTDamping I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn		
at Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_f	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ula		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_dre_oil	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrgLimitMin MtrNm 1		
		UL .	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Mirklim #22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
	0.000000040	-0.300000012 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009703023	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.90 (Repeat Count = 1)	1 (1)		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.81999993		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.200000003		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.91 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	3.0999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	3.2999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Im f32	
		MII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- •	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	rNm f32	
		1	Pac
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	
,			



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.92 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-4		
	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0.30000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	732	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt AstLmt Per1 PosServCCDisable Cnt		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	·•	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	
<del> </del>		1 11 11 1	_



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.93 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.25999999		
C_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt	rNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
at Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f3:		
pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_l	MtrNm f32	
			Pa-
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
	a		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 0 4.5999999	0 ± 0.0009765625 0 ± 0.0009765625 4.5999999 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.94 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	C	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_f	MtrNm f32	
Name	Actual Value	Expected Value	Resu
		· ·	Rest
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.20000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.300000012 4.69999981	-0.300000012 ± 0.0009765625 4.69999981 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.95 (Repeat Count = 1)	Invest Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1 1.60000002		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte Inst Ap AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.81999993		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.400001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt AstLmt Per1 CombinedDamping MtrNm f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f3	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/trNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.96 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 3.0999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCom_within_i32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.3000019		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981		
tgt_AstEmt_Fe1_AssistStallElmit_WithMi_i32.value	5.0999999		
· · ·			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	-8 1		
	-3		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value			
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Ci	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	•	
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	1trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	



Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.97 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssiststalicImit_within_i32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
	0		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LmPnCtr1Cmd_wtrNm_is2.value	- <del>4</del> -8.80000019		
tgt_AstLmt_Per1_Op1rqOvi_within1_isz.value  tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000019		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
	0.300000012		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	5.5999999		
	0.300000012		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	7.69999981		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	37.5999985		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	8		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_Lmnedrecum_wurvin_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt k		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
		32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Т				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.98 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	0		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
pt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
t_AstLmt_Per1_VehSpd_Kph_f32.value	0		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
			Da-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	
22 1 2 1 2 1 2 2 2 2 2 2 2 2 2 2 2 2 2			





Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.99 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
	tgt AstLmt Per1 LimitPercentFiltered Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_UIs_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32			
	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	20	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	11030
tgt_AstLmt_Per1_LimitPercentFillered_dis_isz.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	0 ± 0.0009765625	
	0		
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8 80000010	0 ± 0.0009765625	

8.80000019

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

8.80000019 ± 0.0009765625

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AstLmt\_Per1

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~



#### **Test Case 3: Path Coverage Test**

```
Performance Metrics:
Specification
                           (With "None" Instrumentation and WithPS
                           Environment)
                          CPU Cycles:
TS3.1 1985.00 cycles
TS3.2 1867.00 cycles
TS3.3 1821.00 cycles
TS3.4 1745.00 cycles
TS3.5 1747.00 cycles
                          TS3.5 1747.00 cycles
TS3.6 1723.00 cycles
TS3.7 1747.00 cycles
TS3.7 1747.00 cycles
TS3.8 1723.00 cycles
TS3.10 1723.00 cycles
TS3.11 1747.00 cycles
TS3.12 1723.00 cycles
TS3.13 1770.00 cycles
TS3.14 1804.00 cycles
TS3.15 1851.00 cycles
TS3.16 1790.00 cycles
TS3.17 1757.00 cycles
                          TS3.18 1702.00 cycles
TS3.19 1718.00 cycles
TS3.20 1745.00 cycles
Description
                          Vector Description:
                          TS3.1"( PosServCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          ( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          (PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = True
                          ( (ManualTrqCmdEn_Cnt_M_lgc == TRUE) =False && (VehSpd_Kph_T_f32 < FLT_EPSILON) )
                          and
                          ( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=True
                          and
                          (PreLimitTorque_MtrNm_T_f32 >= 0)=True
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=True
                          ( DefeatLimitService_Cnt_T_lgc == TRUE )=True
                          and
                          TS3.2"(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = False
                          (PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = false
                          and
                          ((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
                          and
                          (Sign_f32_m(AssistPowerLimit_MtrNm_T_f32) >= D_ZERO_CNT_S8)=False and(ThermalLimitPerc_Uls_T_f32 >= PowerLimitPerc_Uls_T_f32) = True" TS3.3"((HwTrqPolarity_Cnt_T_f32 * SignMRFMtrVel_Cnt_T_f32 * SignPreLimitForPower_Cnt_T_f32) >= D_ZERO_ULS_F32)=False
                          and
                          (AssistPowerLimit_MtrNm_T_f32 >= Abs_f32_m(k_SumLimOvrSpdMtrTrq2QLmt_MtrNm_f32))=False
                          (AbsPreLimitForPower_MtrNm_T_f32 <= ForPowerLimit_MtrNm_T_f32 )=True" TS3.4"( DefeatLimitService_Cnt_T_lgc == TRUE )= False
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = True
                          and
                          (AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = True"
                           TS3.5"( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = False
                          ànd
                          (PreLimitForStall_MtrNm_T_f32 <= 0)=False
                          and
                          (PreLimitForPower_MtrNm_T_f32 <= 0)=False"
TS3.6"( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitForPower_MtrNm_T_f32 <= AssistPowerLimit_MtrNm_T_f32 )=False" TS3.7( PosServCCDisable_Cnt_T_lgc == TRUE )=False TS3.8"(PreLimitTorque_MtrNm_T_f32 >= 8.8) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 >= 8.8)=True"
                           TS3.9"(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=False
                          TS3.10(PostLimitForAssistSumCC_MtrNm_T_f32 >= 8.8)=True
                          TS3.11"(PreLimitTorque_MtrNm_T_f32 >= 0)=False
                          and
                          and(PreLimitTorque_MtrNm_T_f32 <= -8.8) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
TS3.13(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = False
TS3.14(LimitPercentFiltered_Uls_T_f32 >= 1)=True
TS3.15(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = False
TS3.16(LimitPercentFiltered_Uls_T_f32 <= 0) = True
TS3.17"( (ManualTrqCmdEn_Cnt_M_lgc == TRUE)=True && (VehSpd_Kph_T_f32 < FLT_EPSILON) )=True
(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=False"
TS3.18(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=True
TS3.19(TrqLimitMin_MtrNm_T_f32 >= 8.8)=True
TS3.20(PreLimitForStall_MtrNm_T_f32 <= 0)=True
```





Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte Inst Ap AstLmt	tgt Rte Inst Ap AstLmt		
k SumLimPlCmpLimit MtrNm f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
	5		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
tgt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value			
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	5 7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	1		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_I	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd Mtr		
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmo	d MtrNm f32	
Name			Pocult
	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	<b>V</b>
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	<b>V</b>
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	¥
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.2 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k SumLimPlCmpLimit MtrNm f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	5		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt AstLmt Per1 AssistStallLimit MtrNm f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Towntcommand_withhin_32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TswittCommand_wi		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_Per0_0is_i32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_TriefmaiLimit_withvin_is2  tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	1

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•





Test Step 3.3 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte Inst Ap AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	8		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	4	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Al 600	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0 5		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f32.value	3		
tgt AstLmt Per1 OpTrgOvr MtrNm f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	348		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	<b>~</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2	2 ± 0.0009765625	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.5 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt Igc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	_
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	<b>~</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.75	2.75 ± 0.0009765625	<b>~</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	<b>~</b>

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.6 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	245		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mi		
	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	4.9000001	4.9000001 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	•

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Name	Test Step 3.7 (Repeat Count = 1)			<b>✓</b>
Astlin, Hawariferoria, Minhin, M. 20  Astlin, SeempAssDefect, Crit, M. 192  4 Astlin, SeempAssDefect, Crit, M. 192  4 Astlin, SeempAssDefect, Crit, M. 192  4 Strin, M. 192  4 Strin, M. 192  5 Stringer, Astlin, M. 192  6 Astlin,		Input Value		
Asstant   Semingschilder  Cort   M.   190   Rie, Irat   A.   Asstant   Semingschilder  Cort   M.   190   Rie, Irat   A.   Asstant   Semingschilder  Cort   M.   190   Rie, Irat   A.   Asstant   Semingschilder  Cort   M.   190   Asstant   Perf   I.SMiCommand, Minn, S2 value   2   I.   Asstant   Perf   I.SMiCommand, Minn, S2 value   3   I.   Asstant   Perf   I.SMiCommand, Minn, S2 value   3   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   4   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   5   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   5   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   7   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   7   I.   Asstant   Perf   AssisteD Cort   Minn, S2 value   7   I.   Asstant   Perf   LefestLandScroze, Cort   Sevalue   1   I.   Asstant   P		•		
AstLind SperingAssiDefast (Crit M.) Igo  Big Inst, A.P. AstLind  \$ Somita InffComplaint, Mirkin, 152  Inst Inst AspLant Inst Inst Inst Inst Inst Inst Inst In				
S. Sumi, Price   Assisted Part   Salicon   S				
Ingred Author, Perl   TSMICOmmand, Mithin, 192 value   2   2   2   2   2   2   2   2   2	Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
UpLAstinn Pert   AssistErOTamping   Mithins   122 value	k_SumLimPlCmpLimit_MtrNm_f32	5		
Section   Pert   AssistECTORmy  (E.Z. value   0.400000006     Section   Pert   AssistECTORmy  (E.Z. value   0.4000000006     Section   Pert   AssistECTORmy  (E.Z. value   0.40000000000000000000000000000000000	target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
Fall Assist Per   AssistSCTLINE   MRN: TSZ value	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
Fig. Astim.Perl _AssistChillm_IMFm_52 value   5     Fig. Astim.Perl _AssistChillm_IMFm_52 value   7     Fig. Astim.Perl _AssistChillm_IMFm_52 value   7     Fig. Astim.Perl _CombinedDamping_ Minnin_52 value   7     Fig. Astim.Perl _ImmedReum_Minnin_52 value   1     Fig. Astim.Perl _ImmedReum_Minnin_52 value   7     Fig. Astim.Perl _ImmedReum_Minnin_52 value   1     Fig. Astim.Perl _ImmedReum_Minnin_52 value   1     Fig. Astim.Perl _ImmedReum_Minnin_52 value   1     Fig. Astim.Perl _ImmedReum_Minnin_52 value   6     Fig. Astim.Perl _ImmedReum_Minnin_52 value   6     Fig. Astim.Perl _ImmedReum_Minnin_52 value   6     Fig. Astim.Perl _PossevCObleable_Crul_go value   0     Fig. Astim.Perl _AssistComl_Minnin_Glovalue   0     Fig. Astim.Perl _PossevCobleable_Crul_go value   0     Fig. Astim.Perl _AssistComl_Minnin_Glovalue   0     Fig. Astim.Perl _AssistComl_Minnin_Glovalue   0     Fig. Astim.Perl _AssistComl_Minnin_Glovalue   0     Fig. Rein_Ap_Astim.Astim.Perl _AssistComl_Minnin_Glovalue	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
Fig. Ast.mm.   Pert   Assistivehis   Delication   Minhm,   S2 value   5	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value			
St. Ast.mt. Pert   AssistVehSpotLinit, MrNnn, 132 value   5     Igl. Ast.mt. Pert   DefeatsLimiServece Cni Igl. value   7     Igl. Ast.mt. Pert   DefeatsLimiServece Cni Igl. value   1     Igl. Ast.mt. Pert   DefeatsLimiServece Cni Igl. value   1     Igl. Ast.mt. Pert   LimitedRetum, MiNnn, 132 value   1     Igl. Ast.mt. Pert   LimitedRetum, MiNnn, 132 value   1     Igl. Ast.mt. Pert   LimitedRetum, MiNnn, 132 value   6     Igl. Ast.mt. Pert   DefeatsCook   1     Igl. Ast.mt. Pert   DesearCook   1     Igl. Ast.mt. Pert   PossearCook   1     Igl. Ast.mt. Pert   Development   1     Igl. Ast.mt. Pert   Meelimbalance Cond. Minnn, 132 value   1     Igl. Ast.mt. Pert   Meelimbalance Cond. Minnn, 132 value   1     Igl. Ast.mt. Pert   Meelimbalance Cond. Minnn, 132 value   1     Igl. Ast.mt. Pert   Meelimbalance Cond. Minnn, 132 value   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   Minnn, 132     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   Minnn, 132     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   AssistCook   1     Igl. Rel. Inst. Ap. Ast.mt. Ast.mt. Pert   Development   1     Igl. Rel				
Total				
International Content				
Fig.   AstLmf.   Perf   LmienGeReium Mirkm   732 value   7				
Igt_AstLmt_Pert_LmPnCtrCmd_MtNmtm_f32 value   1     Igt_AstLmt_Pert_OpTnCvm_MtNmtm_f32 value   6     Igt_AstLmt_Pert_OpTnCvm_MtNmtm_f32 value   6     Igt_AstLmt_Pert_OpTnCvm_Mtnmtm_f32 value   0     Igt_AstLmt_Pert_PosservCDbasble_Cnt_Igx_value   0     Igt_AstLmt_Pert_PosservCmbasble_Cnt_Igx_value   0     Igt_AstLmt_Pert_PosservCmbasble_Cnt_Igx_value   0     Igt_AstLmt_Pert_PosservCmbasble_Cnt_Igx_value   0     Igt_AstLmt_Pert_PosservCmbasble_Cnt_Igx_value   0     Igt_AstLmt_Pert_PosservCmbasble_Cnd_Mtnmtm_f32 value   0     Igt_AstLmt_Pert_AssistComd_Mtnmtm_f32 value   0     Igt_AstLmt_				
Sgt_Asit_mt_Pert_OpTnQrv_MrNm_f32 value   6				
Igt_AstLmt_Peri_OptToptor_Mnthm_132 value   0.5     Igt_AstLmt_Peri_DoutputRampMult_Uls_152 value   0.5     Igt_AstLmt_Peri_Prosesen/CDisable_Cnt_Igo_value   0.200000003     Igt_AstLmt_Peri_ProkassictCom_Mnthm_132 value   7     Igt_AstLmt_Peri_ProkassictCom_Mnthm_132 value   7     Igt_AstLmt_Peri_ProkassictCom_Mnthm_132 value   6     Igt_AstLmt_Peri_ProkassictCom_Mnthm_132 value   6     Igt_AstLmt_Peri_ThermalLimitPerc_Uls_132 value   0.400000006     Igt_AstLmt_Peri_ThermalLimitPerc_Uls_132 value   0.400000006     Igt_AstLmt_Peri_ThermalLimitPerc_Uls_132 value   0.400000006     Igt_AstLmt_Peri_ThermalLimitPerc_Uls_132 value   166   0.400000006     Igt_AstLmt_Peri_ThermalLimitPerc_Uls_132 value   4     Igt_AstLmt_Peri_ThermalLimitMnthm_132 value   4     Igt_AstLmt_Peri_TheoleticCom_Ord_Mnthm_132   1gt_AstLmt_Peri_AssistCom_Mnthm_132   1gt_AstLmt_Peri_Com_Mnthm_132   1gt_AstLmt_Peri_Com_Mnthm_132   1gt_AstLmt_Peri_Com_Mnthm_132   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_Com_Mnthm_132   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintService_Cnt_Igo   1gt_AstLmt_Peri_LobertaintSe				
Spt_AstLmt_Pert_PosSenvCCDsable_Cnt_giz_value				
Total   Committee   Committe				
tgt_AstLmt_Perf_PirkAssistCmd_MtrNm_f32 value         6           tgt_AstLmt_Perf_PirkGmpCmd_MtrNm_f32 value         6           tgt_AstLmt_Perf_ThermalLimiRepr_Uls_f32 value         0.400000006           tgt_AstLmt_Perf_ThermalLimiRepr_Uls_f32 value         166           tgt_AstLmt_Perf_MreelimbalanecCmd_MtrNm_f32 value         4           tgt_AstLmt_Perf_WreelimbalanecCmd_MtrNm_f32 value         4           tgt_Net_Inst_Ap_AstLmtAstLmt_Perf_AssistCortDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCortDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmtAstLmt_Perf_AssistCOrtDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCOTDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmtAstLmt_Perf_AssistCOTDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCOTDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmtAstLmt_Perf_AssistCOTDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCOTDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmt.AstLmt_Perf_AssistCOTDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCOTDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmt.AstLmt_Perf_CombinedDamping_MtrNm_f32         tgt_AstLmt_Perf_AssistCOTDamping_MtrNm_f32           tgt_Ret_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercombilitered_Uls_f32         tgt_AstLmt_Perf_LimitPercombilitered_Uls_f32           tgt_Ret_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercombilitered_Uls_f32         tgt_AstLmt_Perf_LimitPercombilitered_Uls_f32           tgt_Ret_Inst_Ap_AstLmt.AstLmt_Perf_LimPrOCtCObable_Cnt_tgc         tgt_AstLmt_Perf_LimPrOCtCDable_Cnt_tgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgl_AstLmt_Per1_PullCompCmd_MtrNm_i32_value tgl_AstLmt_Per1_ThermatLimitPerc_Uis_i32_value gt_AstLmt_Per1_ThermatLimitPerc_Uis_i32_value gt_AstLmt_Per1_VerSpd_Kph_i32_value tgl_AstLmt_Per1_VerSpd_Kph_i32_value tgl_AstLmt_Per1_VerSpd_Kph_i32_value tgl_AstLmt_Per1_VerSpd_Kph_i32_value tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistComd_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistComd_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistComd_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistComd_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistSomillLimit_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistSomillLimit_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistSomillLimit_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistSomillLimit_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_AssistSomillLimit_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Ugc tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_DirtCirtCoisable_Cnt_Uigc tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_DirtCirtCoisable_Cnt_Uig_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_DirtCirtCoisable_Cnt_Uig_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_PrelimitForsall_MintNm_i32 tgl_Rsl_inst_Ap_AstLmt_AstLmt_Per1_PrelimitForsall_MintNm_i32 tgl_Rsl_inst_Ap_Ast	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgl_AstLmt_Per1_ThermalLimit_MrNm_132 value 1gl_AstLmt_Per1_ThermalLimit_MrNm_132 value 1gl_AstLmt_Per1_VenSpd_Kph_152 value 1gl_AstLmt_Per1_VenSpd_Kph_152 value 1gl_AstLmt_Per1_VenSpd_Kph_152 value 1gl_AstLmt_Per1_VenSpd_Kph_152 value 1gl_AstLmt_Per1_VenSpd_Kph_152 value 1gl_AstLmt_Per1_AssistCord_MtrNm_132 value 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistCord_Dmping_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTDamping_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTDamping_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTDamping_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistStailLimit_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_AssistStailLimit_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefastLminSevice_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefastLminSevice_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefastLminSevice_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefactCoilsable_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefactCoilsable_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefactCoilsable_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DefactCoilsable_Cnt_lge 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DevertImitPerc_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DevertImitPerc_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_DevertImitPerc_Uis_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_PerlimitForStail_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_PerlimitForStail_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1_PerlimitForStail_MtrNm_132 1gl_Rie_Inst_Ap_AstLmtAstLmt_Per1	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgl. AstLm!_Perl_ThermalLimit_MtrNm_i32-value tgl. AstLm_Perl_WheelimbalanceCord_MtrNm_i32-value tgl. AstLm_Perl_WheelimbalanceCord_MtrNm_i32-value tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_AssistCord_MtrNm_i32 tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_CombinedDamping_MtrNm_i32 tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_DefeatLimitService_Cnt_lgc tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_LimitedReturn_MtrNm_i32 tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_Dirtignory_MtrNm_i32 tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_Dirtignory_MtrNm_i32 tgl. Rel_inst_Ap_AstLmt.AstLm_Perl_PowerLimitPerc_Uls_isi22 tgl. Rel_inst_Ap_AstLmt.AstLm_Per	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgl. AstLmt. Perf _ WheelimbalanceCmd_MtrNm_f32.value  tgl. AstLmt. Perf _ WheelimbalanceCmd_MtrNm_f32.value  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistCoTDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistCoTDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistEOTDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistEOTDaming_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistEOTLimit_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistSialiLimit_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_AssistSialiLimit_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_CombinedDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_CombinedDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_CombinedDamping_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_DefeatLimilService_Cnt_Igc  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercentFiltered_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercentFiltered_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercentFiltered_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimitPercentFiltered_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimPrCttrCoDisable_Cnt_Igc  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimPrCttrCoDisable_Cnt_Igc  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimPrCttrCoMsumfilterat_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_LimPrCttrCoMsumfilterat_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_OutputRampMult_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_OutputRampMult_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_OutputRampMult_Uis_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_PerlimitForque_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Perf_TerlimitImit_MtrNm_f32  tgl. Rie_Inst_Ap_AstLmt.AstLmt_Per	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value			
tgt AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rel_inst_Ap_AstLmt_AstLmt_Per1_AssistCoTomping_MtrNm_f32 tgt_Rel_inst_Ap_AstLmt_AstLmt_Per1_AssistCoTomping_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTomping_MtrNm_f32 tgt_AstLmt_Per1_InititeRetur_MtrNm_f32 tgt_AstLmt_Per1_Ini	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value			
tgl. Rie Inst. Ap. AstLmt. Perl _ AssistCod _ MirNm_f32				
tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LomedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DortCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DortCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOv_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOv_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DosSenvCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DosSenvCodDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TerLimitPorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TerLimitPord_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt				
tgl. Rte. Inst. Ap. AstLmt. Peri_AssistEOTGain_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_AssistEOTLimit_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_AssistEoTLimit_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_AssistEoTLimit_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_AssistStalliLimit_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_CombinedDamping_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_DefatLimitService_Cnt. Igc tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_LimitBercentFiltered_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_LimitBercEnd_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_LimitBercEnd_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_LimitBercEnd_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_OrtCronad_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_PowerLimitPerc_Uls_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_PrelimitForStall_MtrNm_f32 tgl. Rte. Inst. Ap. AstLmt. AstLmt. Peri_PrelimitForStall_Mtr			roo	
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tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32  tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmnPctrCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPctrCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPctrCtDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPnCtrCTDmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPnCtrCTDmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DrTqCvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OutputRampMult_Uls_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OutputRampMult_Uls_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PerlimitForsqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForsqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForsqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForqual_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SumLimiTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TsMimCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremalLimitDerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremalLimitD				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PoservCDisable_Cnt_Igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PoservCDisable_Cnt_Igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uis_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMirNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMirNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Pe				
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tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			00	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UllCompCmd_MtrNm_f32  tagt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_TranlimImTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_Per1_TranlimImTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
	tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
Name Actual Value Expected Value R	tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	Nm_f32	
	Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 0.400000006 ± 0.000030517578125	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 2 2 ± 0.0009765625	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	~
$tgt\_AstLmt\_Per1\_PreLimitTorque\_MtrNm\_f32.value$ 0 0 0 ± 0.0009765625				~
$\label{tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value} \tag{0} \qquad \qquad 0 \pm 0.0009765625$				~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 3 ± 0.0009765625	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.8 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	437		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	<u>1_</u> f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	дс	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	7.04000044	7.03999996 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	

T				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.9 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm f32.value	1 6		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	367		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_fi tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target AstLmt Per1 TSMitCommand MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TswittCommand_intrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.10 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	<u>1_</u> f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	дс	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•





k_SumLimPlCmpLimit_MtrNm_f32         7           target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value         0           tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEoTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEoTGain_Uls_f32.value         0.60000000           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrCtDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7	t_Ap_AstLmt  24  9		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc       0         AstLmt_SteeringAsstDefeat_Cnt_M_lgc       0         Rte_Inst_Ap_AstLmt       tgt_Rte_Inst_K_SumLimPlCmpLimit_MtrNm_f32         k_SumLimPlCmpLimit_MtrNm_f32       7         target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value       0         tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value       4         tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value       6         tgt_AstLmt_Per1_AssistEOTCain_Uls_f32.value       6         tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value       7         tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value       3         tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value       3         tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value       5         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value       1         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value       7         tgt_AstLmt_Per1_LimPnCtrCtDisable_Cnt_lgc.value       1         tgt_AstLmt_Per1_LmPnCtrCtoBable_Cnt_lgc.value       1         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value       7         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value       1         tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value       1         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value       7         tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.value       7         t	t_Ap_AstLmt  24  9		
AstLmt_ManualTrqCmd_MtrNm_M_f32         6           AstLmt_SteeringAsstDefeat_Cnt_M_lgc         0           Rte_Inst_Ap_AstLmt         tgt_Rte_Inst_Ap_AstLmt           k_SumLimPlCmpLimit_MtrNm_f32         7           target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value         0           tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LimPnCtrCtDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrCtDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.val	9		
Rte_Inst_Ap_AstLmt         tgt_Rte_Inst           k_SumLimPlCmpLimit_MtrNm_f32         7           target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value         0           tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_Optrqvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PorkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PollCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PollCom	9		
k_SumLimPlCmpLimit_MtrNm_f32         7           target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value         0           tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value         0.60000000           tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LmrPnCtrCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmrPnCtrCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LmrPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7	9		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value         0           tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value         0.60000002           tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Ont_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_OpTrqOv_MtrNm_f32.value         7           tgt_AstLmt_Per1_OpTrqOv_MtrNm_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         7           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	9		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value         4           tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value         0.600000000000000000000000000000000000	9		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value         6           tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value         0.600000000000000000000000000000000000	9		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value       0.600000000000000000000000000000000000	9		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value       6         tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value       7         tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value       3         tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value       5         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value       1         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value       7         tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value       0         tgt_AstLmt_Per1_LmPnCtrTcdd_MtrNm_f32.value       1         tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value       7         tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value       1         tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value       1         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value       0.40000000         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value       0.69999998	9		
tgl_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value         7           tgl_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value         1           tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value         1           tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value         3           tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LrmPnCtrCcDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value         5           tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgl_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value         1           tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LrnPnCtrTcmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value         7           tgt_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value         0           tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value         1           tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value         -8.8000001           tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998	06		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value       -8.8000001         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value       7         tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value       1         tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value       1         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value       0.40000000         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value       0.69999998	06		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value         7           tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998			
tgl_AstLmt_Per1_OutputRampMult_Uls_f32.value         1           tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value         1           tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value         0.40000000           tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998			
tgt_AstLmt_Per1_PowerLimitPerc_UIs_f32.value       0.40000000         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value       7         tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32.value       0.69999998			
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value         7           tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value         0.69999998			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	18		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.699999998	88		
v	18		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 7			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value 24			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 7	D 4 A 1 10 1 ANN N 700		
	Per1_AssistCmd_MtrNm_f32		
	Per1_AssistEOTDamping_MtrNm_f32		
	Per1_AssistEOTGain_Uls_f32 Per1_AssistEOTLimit_MtrNm_f32		
	Per1_AssistStallLimit_MtrNm_f32		
	Per1_AssistVehSpdLimit_MtrNm_f32		
	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
	Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_	Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_	Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc tgt_AstLmt_	_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc tgt_AstLmt_	Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_AstLmt_	Per1_LrnPnCtrTCmd_MtrNm_f32		
	Per1_OpTrqOvr_MtrNm_f32		
	Per1_OutputRampMult_Uls_f32		
	Per1_PosServCCDisable_Cnt_lgc		
	Per1_PowerLimitPerc_Uls_f32		
	Per1_PreLimitForStall_MtrNm_f32 Per1_PreLimitTorque_MtrNm_f32		
	Per1_PrkAssistCmd_MtrNm_f32		
	Per1_PullCompCmd_MtrNm_f32		
	Per1_SumLimTrqCmd_MtrNm_f32		
	mt Per1 TSMitCommand MtrNm f32		
	Per1_ThermalLimitPerc_Uls_f32		
	Per1_ThermalLimit_MtrNm_f32		
	Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_	Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_	Per1_WheelImbalanceCmd_MtrNm_f32		
Name Actual Va	lue Expected Value Result		
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.69999998	8 0.699999988 ± 0.000030517578125		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019	8.80000019 ± 0.0009765625		
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -8.8000001			
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -8.8000001			
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 3	3 ± 0.0009765625		

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 CheckpointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	_





Test Step 3.12 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt AstLmt Per1 LimitedReturn MtrNm f32.value	-2.0999999		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.7999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	622	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m_t32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	00	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimit10rque_MitNim_is		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_i		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	Nm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625	<b>V</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.13 (Repeat Count = 1)	In the Walter		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.200000003		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping MtrNn	n f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lo		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	y¢	
	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	7	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg	JC	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	•	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNn	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN	\m_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	

Т						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>		





Test Step 3.14 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.89999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.10000001		
tgt_AssLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	0		
	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	7		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	-2 0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Peri TSMitCommand MtrNm f32	target AstLmt Per1 TSMitCommand MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mtm tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
	tgt_AstLmt_Per1_ThermalLimit_Per2_0is_i3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32			
	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	rhim f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	1	1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	•

Т						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>		





Test Step 3.15 (Repeat Count = 1)			· ·
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8	8 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	

Т						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>		





Test Step 3.16 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8.80000019		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	-8.80000019		
tgt AstLmt Per1 ThermalLimitPerc Uls f32.value	0		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	0		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrgOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_132	
	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2	-2 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.17 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0 5.5999999		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tqt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	<u>lgc</u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosServCCDisable_Cnt_ tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitFerC_0is_i32	82	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MitNM1_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	_
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	<b>*</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.4000001	4.4000001 ± 0.0009765625	~

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.18 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.400001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1 0.800000013		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000012		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTDamping MtrNm f32	tgt AstLmt Per1 AssistEOTDamping Mt	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<del>-</del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32	tgt AstLmt Per1 PreLimitTorque MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt AstLmt Per1 PullCompCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/trNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
<u> </u>	4.400001		

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 CheckpointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	_





Test Step 3.19 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	N. 700	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LimitedReturn_MtrNm_tgt AstLmt Per1 LrnPnCtrCCDisable Cr		
	tgt_AstLmt_Per1_tmPnCtrCcbisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_tmPnCtrPnable_Cnt_ig tgt_AstLmt_Per1_tmPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
		32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCcDisable_Cnt_igc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosServCCDisable_Cn tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerC_Ois_I32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
	tgt_AstLmt_Per1_PreLimitForStall_MtrNm tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn target AstLmt Per1 TSMitCommand Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Peri_ISMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_Thermail.imit_MtrNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_TrqLimitMin_MtrNm_132	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_FrqLimitMin_MthMin_is2	tgt_AstLmt_Per1_frqLimitwiin_withm_isz		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VeriSpd_Rpn_is2  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_venspd_kpn_i32 tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
			D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	

Т				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.20 (Repeat Count = 1)			*
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	<u>lg</u> c	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	:	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt AstLmt Per1 PrkAssistCmd MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_rrqLmitwiii1_witrviii1_i32		
tgt Rte Inst Ap AstLmt.AstLmt Perl WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
			la.
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	•

Τ				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	



Project AstLmt\_CM

Module AstLmt

Test Object AstLmt\_Scom\_SetSteeringAssistDefeat

### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg



Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

241.00 Cycles 241.00 Cycles TS1.1 TS1.2

Vector Description: Description

TS1.1 SteeringAsstDefeat1\_Cnt\_lgc = True TS1.2 SteeringAsstDefeat1\_Cnt\_lgc = False

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	1		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
target_Pim_SteerAsstDefeat	1	1	<b>✓</b>

T				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1	Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	0		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
target Pim SteerAsstDefeat	0	0	<b>✓</b>

T				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	~