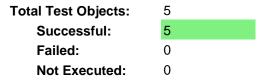
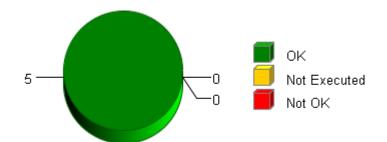


Summary

Overall Test Object Results (including Coverage)



Date: 2014-09-05 **Time:** 17:35:09+0530



Selected Project Items

Test Object "CBD_UnitTest/AstLmt/AstLmt_Init"

Test Object "CBD UnitTest/AstLmt/AstLmt Per1"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_GetSteeringAssistDefeat"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_ManualTrqCmd"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_SetSteeringAssistDefeat"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

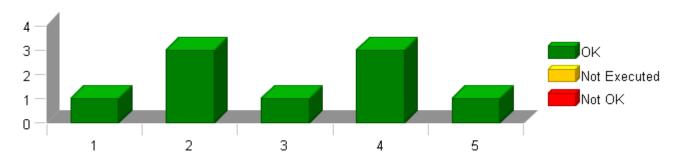
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

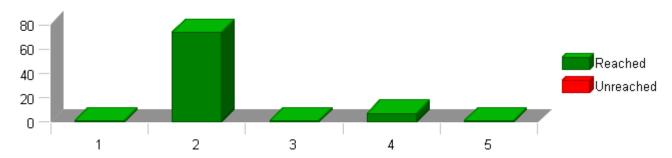
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

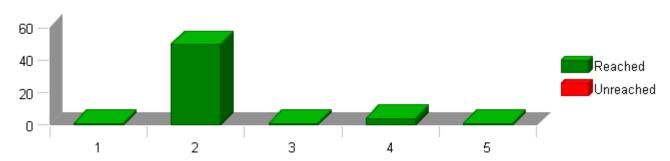


Statement (C0) Coverage: Total Statements for Each Test Object



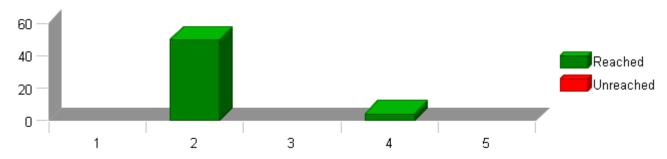
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

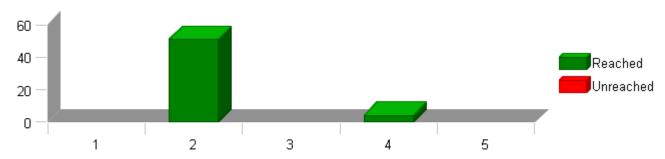


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



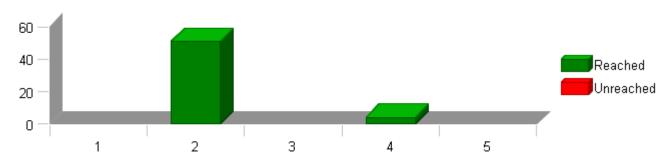
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

| No. | Name | C0 | C1 | DC | MC/DC | MCC | Test Cases Re | esult |
|-----|-------------------------------------|-------|-------|-------|-------|-------|---------------|-------|
| | AstLmt_CM | 100 % | 100 % | 100 % | 100 % | 100 % | 9 of 9 passed | • |
| | CBD_UnitTest | 100 % | 100 % | 100 % | 100 % | 100 % | 9 of 9 passed | • |
| | AstLmt | 100 % | 100 % | 100 % | 100 % | 100 % | 9 of 9 passed | • |
| 1 | AstLmt_Init | 100 % | 100 % | - | - | - | 1 of 1 passed | • |
| 2 | AstLmt_Per1 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ~ |
| 3 | AstLmt Scom GetSteeringAssistDefeat | 100 % | 100 % | - | - | - | 1 of 1 passed | • |
| 4 | AstLmt Scom ManualTrqCmd | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | • |
| 5 | AstLmt Scom SetSteeringAssistDefeat | 100 % | 100 % | - | - | - | 1 of 1 passed | • |

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2014-09-05, 17:33:39+0530



AstLmt_Scom_SetSteeringAssistDefeat

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_SetSteeringAssistDefeat

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

| Total Testcases | 1 | |
|-----------------|---|---|
| Successful | 1 | ✓ |
| Failed | 0 | |
| Not Executed | 0 | |

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\AstLmt_CM |
|------------------------|--|
| Configuration File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include |

| Comments/Descrip | otion/Specification |
|------------------|---|
| Name | Text |
| Module 'AstLmt' | ************************************** |
| | Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." |
| | *************************************** |

| Attributes | | | | |
|-----------------------|--|--|--|--|
| Name | Value | | | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | | | |
| Float Precision | 9 | | | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | | | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | | | |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd | | | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | | | |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 | | | |
| Time Unit | Cycles | | | |
| Timer Enabled | false | | | |
| Timer Prescale | 0 | | | |
| Timer Resolution | 1 | | | |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg | | | |

2014-09-05, 17:33:39+0530



AstLmt_Scom_SetSteeringAssistDefeat

| Attributes | | | |
|---|-------|--|--|
| Name | Value | | |
| Workspace File D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP | | | |



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

241.00 Cycles 241.00 Cycles TS1.1 TS1.2

Description Vector Description:

TS1.1 SteeringAsstDefeat1_Cnt_lgc = True TS1.2 SteeringAsstDefeat1_Cnt_lgc = False

| Test Step 1.1 (Repeat Count = 1) | | | ✓ |
|---|----------------------------|----------------|----------|
| Name | Input Value | | |
| Rte_Inst_Ap_AstLmt | target_Rte_Inst_Ap_AstLmt | | |
| SteeringAsstDefeat_Cnt_lgc | 1 | | |
| target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | target_Pim_SteerAsstDefeat | | |
| Name | Actual Value | Expected Value | Result |
| target_Pim_SteerAsstDefeat | 1 | 1 | ✓ |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock | 1 | Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock | 1 | ~ |

| Test Step 1.2 (Repeat Count = 1) | | | |
|---|----------------------------|----------------|----------|
| Name | Input Value | | |
| Rte_Inst_Ap_AstLmt | target_Rte_Inst_Ap_AstLmt | | |
| SteeringAsstDefeat_Cnt_lgc | 0 | | |
| target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | target_Pim_SteerAsstDefeat | | |
| Name | Actual Value | Expected Value | Result |
| target Pim SteerAsstDefeat | 0 | 0 | ✓ |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock | 1 | Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock | 1 | ~ |

2014-09-05, 17:30:46+0530



AstLmt_Per1

| Project | AstLmt_CM |
|-------------|-------------|
| Module | AstLmt |
| Test Object | AstLmt_Per1 |

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|--------------------------|-------|
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| Total Testcases | 3 | |
|-----------------|---|---|
| Successful | 3 | ~ |
| Failed | 0 | |
| Not Executed | 0 | |

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\AstLmt_CM | |
|------------------------|--|--|
| Configuration File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml | |
| Target Environment | TI TMS 570 PLS UDE (Default) | |
| Kind of Test | Unit Test | |
| Linker Options | | |
| Source File(s) | | |
| File | \$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c | |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include | |

| Comments/Descrip | otion/Specification |
|------------------|---|
| Name | Text |
| Module 'AstLmt' | ************************************** |
| | Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." |
| | *************************************** |

| Attributes | |
|-----------------------|--|
| Name | Value |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 |
| Time Unit | Cycles |
| Timer Enabled | false |

2014-09-05, 17:30:46+0530



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Prescale | 0 |
| Timer Resolution | |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Matrix Test

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

TS1.1 1848.00 cycles Shortest Execution Path TS1.2 1948.00 cycles Longest Execution Path

Description

Vector Description:

TS1.1:"Shortest execution Path:

(PosServCCDisable_Cnt_T_lgc = True) = False

(LmPnCtrCCDisable_Cnt_T_lgc = True = False

((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON)) = True

(DefeatLimitService_Cnt_T_lgc = True) = True

(TS1.2"Longest Execution Path:

(PosServCCDisable_Cnt_T_lgc = True) = True

(LmPnCtrCCDisable_Cnt_T_lgc = True) = True

((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON)) = False

(LmPnCtrEnable_Cnt_T_lgc = True) = False

(AstLmt_SteeringAsstDefeat_Cnt_M_lgc = True) = False

(DefeatLimitService_Cnt_T_lgc = True) = False"

| Test Step 1.1 (Repeat Count = 1) | 🗸 |
|---|--|
| Name | Input Value |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.80000019 |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0 |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8.80000019 |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 1 |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 1 |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 0 |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8.80000019 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrNm_f32 |

2014-09-05, 17:30:46+0530



| Name | Input Value | | |
|--|--|---------------------------|----------|
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | Nm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | ~ |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 1.2 (Repeat Count = 1) | ✓ |
|---|--|
| Name | Input Value |
| | 1 |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.80000019 |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | O |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1 |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 3 |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.5999999 |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -7 |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1 |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1.20000005 |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0 |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -7 |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 0.5 |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8 |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 512 |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -6 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt AstLmt Per1 PullCompCmd MtrNm f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrNm_f32 |
| 13-1 | |

2014-09-05, 17:30:46+0530



| Name | Input Value | | | |
|--|---|--|----------|--|
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: | 32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3. | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | ~ | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5.73999977 | 5.73999977 ± 0.0009765625 | ✓ | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -5.73999977 | -5.73999977 ± 0.0009765625 | • | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -5.73999977 | -5.73999977 ± 0.0009765625 | ✓ | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | ~ | |

| Т | | | | V |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

AstLmt_Per1

2014-09-05, 17:30:46+0530



Test Case 2: Boundary Test

2014-09-05, 17:30:46+0530

AstLmt_Per1



Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

CPU Cycles:

TS2.1 1985.00 cycles TS2.2 1879.00 cycles TS2.3 1821.00 cycles TS2.2 1879.00 cycles
TS2.3 1821.00 cycles
TS2.3 1816.00 cycles
TS2.4 1816.00 cycles
TS2.5 1747.00 cycles
TS2.6 1777.00 cycles
TS2.6 1777.00 cycles
TS2.7 1747.00 cycles
TS2.8 1723.00 cycles
TS2.9 1747.00 cycles
TS2.10 1723.00 cycles
TS2.11 1747.00 cycles
TS2.12 1723.00 cycles
TS2.13 1747.00 cycles
TS2.14 1723.00 cycles
TS2.15 1747.00 cycles
TS2.16 1723.00 cycles
TS2.16 1723.00 cycles
TS2.17 1723.00 cycles
TS2.18 1747.00 cycles
TS2.19 1723.00 cycles
TS2.19 1723.00 cycles
TS2.21 1723.00 cycles
TS2.22 1747.00 cycles
TS2.22 1747.00 cycles
TS2.23 1723.00 cycles
TS2.24 1747.00 cycles
TS2.25 1723.00 cycles
TS2.26 1747.00 cycles
TS2.27 1723.00 cycles
TS2.28 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1740.00 cycles
TS2.29 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.33 1740.00 cycles
TS2.35 1740.00 cycles
TS2.35 1740.00 cycles
TS2.36 1740.00 cycles
TS2.36 1740.00 cycles
TS2.37 1740.00 cycles TS2.35 1742.00 cycles TS2.36 1742.00 cycles TS2.37 1742.00 cycles TS2.38 1742.00 cycles TS2.39 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.40 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.43 1758.00 cycles TS2.44 1758.00 cycles TS2.45 1760.00 cycles TS2.46 1767.00 cycles TS2.47 1742.00 cycles TS2.48 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.51 1742.00 cycles TS2.51 1742.00 cycles TS2.53 1742.00 cycles TS2.55 1742.00 cycles TS2.55 1758.00 cycles TS2.55 1758.00 cycles TS2.56 1758.00 cycles TS2.60 1758.00 cycles TS2.61 1723.00 cycles TS2.63 1723.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.67 1723.00 cycles TS2.68 1767.00 cycles TS2.69 1723.00 cycles TS2.71 1723.00 cycles TS2.71 1723.00 cycles TS2.73 1723.00 cycles TS2.74 1729.00 cycles TS2.75 1729.00 cycles TS2.75 1723.00 cycles TS2.76 1729.00 cycles TS2.77 1723.00 cycles TS2.78 1723.00 cycles TS2.78 1723.00 cycles TS2.79 1723.00 cycles TS2.81 1729.00 cycles TS2.81 1758.00 cycles TS2.82 1723.00 cycles TS2.83 1729.00 cycles TS2.84 1758.00 cycles TS2.85 1767.00 cycles TS2.86 1757.00 cycles TS2.87 1829.00 cycles TS2.88 1719.00 cycles TS2.89 1712.00 cycles TS2.90 1728.00 cycles TS2.91 1714.00 cycles TS2.92 1729.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.94 1712.00 cycles TS2.94 1712.00 cycles TS2.95 1731.00 cycles TS2.96 1731.00 cycles TS2.97 1729.00 cycles TS2.97 1729.00 cycles TS2.98 1790.00 cycles TS2.99 1758.00 cycles





Description Vector Description :

TS2.1AssistCmd MtrNm f32 = Min TS2.2AssistCmd_MtrNm_f32 = Max TS2.3AssistCmd_MtrNm_f32 = Mid TS2.4AssistCmd_MtrNm_f32 = Pos TS2.5AssistCmd_MtrNm_f32 = Neg TS2.6AssistEOTDamping MtrNm f32 = Min TS2.7AssistEOTDamping MtrNm f32 = Max TS2.8AssistEOTDamping MtrNm f32 = Mid TS2.9AssistEOTDamping_MtrNm_f32 = Mid
TS2.9AssistEOTDamping_MtrNm_f32 = Pos
TS2.10AssistEOTDamping_MtrNm_f32 = Neg
TS2.11AssistEOTGain_Uls_f32 = Min
TS2.12AssistEOTGain_Uls_f32 = Mid
TS2.14AssistEOTLimit_MtrNm_f32 = Min
TS2.14AssistEOTLimit_MtrNm_f32 = Min IS2.14AssistEOTLimit_MtrNm_f32 = Min TS2.15AssistEOTLimit_MtrNm_f32 = Max TS2.16AssistEOTLimit_MtrNm_f32 = Mid TS2.17AssistStallLimit_MtrNm_f32 = Min TS2.18AssistStallLimit_MtrNm_f32 = Max TS2.19AssistStallLimit_MtrNm_f32 = Mid TS2.20AssistVehSpdLimit_MtrNm_f32 = Min TS2.21AssistVehSpdLimit_MtrNm_f32 = Max TS2.22AssistVehSpdLimit_MtrNm_f32 = Mid TS2.23CombinedDamping_MtrNm_f32 = Min TS2.24CombinedDamping_MtrNm_f32 = Max TS2.25CombinedDamping_MtrNm_f32 = Mid TS2.26CombinedDamping_MtrNm_f32 = Pos TS2.27CombinedDamping_MtrNm_f32 = Neg TS2.28DefeatLimitService_Cnt_lgc = Min TS2.29DefeatLimitService_Cnt_lgc = Max TS2.30LimitedReturn_MtrNm_f32 = Min TS2.31LimitedReturn_MtrNm_f32 = Max TS2.32LimitedReturn_MtrNm_f32 = Mid TS2.33LimitedReturn_MtrNm_f32 = Pos TS2.34LimitedReturn_MtrNm_f32 = Neg TS2.35LmPnCtrCCDisable Cnt_lgc = Min
TS2.36LmPnCtrCCDisable_Cnt_lgc = Min
TS2.36LmPnCtrCDisable_Cnt_lgc = Max
TS2.38LmPnCtrEnable_Cnt_lgc = Min
TS2.38LmPnCtrEnable_Cnt_lgc = Max
TS2.39LmPnCtrTCmd_MtrNm_f32 = Min
TS2.40LmPnCtrTCmd_MtrNm_f32 = Max TS2.41LrnPnCtrTCmd_MtrNm_f32 = Mid TS2.42LrnPnCtrTCmd_MtrNm_f32 = Pos TS2.43LrnPnCtrTCmd_MtrNm_f32 = Neg $TS2.44OpTrqOvr_MtrNm_f32 = Min$ TS2.44OpTrqOvr_MtrNm_f32 = Min
TS2.45OpTrqOvr_MtrNm_f32 = Max
TS2.46OpTrqOvr_MtrNm_f32 = Mid
TS2.47OpTrqOvr_MtrNm_f32 = Pos
TS2.48OpTrqOvr_MtrNm_f32 = Neg
TS2.49OutputRampMult_Uls_f32 = Min
TS2.50OutputRampMult_Uls_f32 = Mia
TS2.51OutputRampMult_Uls_f32 = Mid
TS2.52PosServCCDisable_Cnt_lgc = Min
TS2.53PosServCCDisable_Cnt_lgc = Max
TS2.54Powerl_imitPerr_Uls_f32 = Min TS2.54PowerLimitPerc_UIs_f32 = Min TS2.55PowerLimitPerc_UIs_f32 = Max TS2.56PowerLimitPerc_UIs_f32 = Mid TS2.57PrkAssistCmd_MtrNm_f32 = Min TS2.58PrkAssistCmd_MtrNm_f32 = Max TS2.59PrkAssistCmd_MtrNm_f32 = Mid TS2.60PrkAssistCmd_MtrNm_f32 = Pos TS2.61PrkAssistCmd_MtrNm_f32 = Neg TS2.62PullCompCmd_MtrNm_f32 = Min TS2.63PullCompCmd_MtrNm_f32 = Max TS2.64PullCompCmd_MtrNm_f32 = Mid TS2.65PullCompCmd_MtrNm_f32 = Pos TS2.66PullCompCmd_MtrNm_f32 = Neg TS2.67ThermalLimitPerc_Uls_f32 = Min TS2.68ThermalLimitPerc_Uls_f32 = Max TS2.69ThermalLimitPerc_Uls_f32 = Mid TS2.70ThermalLimit_MtrNm_f32 = Min TS2.71ThermalLimit_MtrNm_f32 = Max TS2.72ThermalLimit_MtrNm_f32 = Mid TS2.73WheellmbalanceCmd_MtrNm_f32 = Min TS2.74WheellmbalanceCmd_MtrNm_f32 = Max TS2.75WheellmbalanceCmd_MtrNm_f32 = Mid TS2.76WheelImbalanceCmd_MtrNm_f32 = Pos TS2.77WheelImbalanceCmd_MtrNm_f32 = Neg TS2.778k_SumLimPlCmpLimit_MtrNm_f32 = Min TS2.78k_SumLimPlCmpLimit_MtrNm_f32 = Min
TS2.79k_SumLimPlCmpLimit_MtrNm_f32 = Max
TS2.80k_SumLimPlCmpLimit_MtrNm_f32 = Min
TS2.81VehSpd_Kph_f32 = Min
TS2.82VehSpd_Kph_f32 = Max
TS2.83VehSpd_Kph_f32 = Pos
TS2.84ManualTrqCmdEn_Cnt_M_lgc = Min
TS2.85ManualTrqCmdEn_Cnt_M_lgc = Max
TS2.85ManualTrqCmd_MtrNm_M_f32 = Max
TS2.86ManualTrqCmd_MtrNm_M_f32 = Max
TS2.88ManualTrqCmd_MtrNm_M_f32 = Neg
TS2.89ManualTrqCmd_MtrNm_M_f32 = Neg
TS2.99ManualTrqCmd_MtrNm_M_f32 = Pos
TS2.91SteeringAsstDefeat_Cnt_M_lgc = Min
TS2.92SteeringAsstDefeat_Cnt_M_lgc = Max
TS2.93TSMitCommand_MtrNm_f32=Min
TS2.94TSMitCommand_MtrNm_f32=Min
TS2.94TSMitCommand_MtrNm_f32=Max





TS2.95TSMitCommand_MtrNm_f32=Zero TS2.96TSMitCommand_MtrNm_f32=Neg TS2.97TSMitCommand_MtrNm_f32=Pos TS2.98All Min TS2.99All max

| Fest Step 2.1 (Repeat Count = 1) | Input Value | | |
|--|---------------------------------------|---|-----|
| stLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7.36000013 | | |
| AstLmt SteeringAsstDefeat Cnt M Igc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt Rte Inst Ap AstLmt | | |
| _SumLimPlCmpLimit_MtrNm_f32 | 8.10000038 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.100000001 | | |
| pt AstLmt Per1 AssistStallLimit MtrNm f32.value | 1.79999995 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1.5 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3 | | |
| pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| pt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LmPnCtrEnable_Cnt_tgc.value | 4 | | |
| gt_AstLmt_Per1_ChrPriCthTofnid_withNin_i32.value | 5 | | |
| pt_AstLmt_Per1_Op1rqOvt_MtNnin_i32.value | 0.100000001 | | |
| pt_AstLmt_Per1_OutputRampMult_Ois_is2.value | 1 | | |
| gt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value | 0.100000001 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -3 | | |
| | 2 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 0.100000001 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.0999999 | | |
| | 42.0999985 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | -4.5 | | |
| gt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value | | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_f | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_C | _ - - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3: | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_0 | · | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNn | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtr1 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _ | |
| t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | | |
| lame | Actual Value | Expected Value | Res |
| | 0.10000001 | 0.100000001 ± 0.000030517578125 | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | | | |
| | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | | 8.80000019 ± 0.0009765625 4.5 ± 0.0009765625 | |





| Т | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.2 (Repeat Count = 1) | Inmut Value | | |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5.36999989 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.19999981 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.9000001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.89999998 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1.60000002 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 38.2000008 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | N. 700 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_i | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 00 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.20000003 | 0.200000003 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | 1 |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 1 | 1 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.200000003 | 0.200000003 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.3 (Repeat Count = 1) Name | Input Value | | |
|---|---|---------------------------------|-----|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4.3499999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 6.30000019 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.79999995 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.20000003 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1.70000005 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -7 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -5.5999999 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4 | | |
| t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.2999995 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 26.7999992 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -6 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping M | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mti | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_le | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | 1 132 | |
| | | f2.2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| gt_AstEmt_reri_Emit erecrit intered_dis_los_value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimit or3tan_withtin_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.0900000036 | 0.090000036 ± 0.0009765625 | |
| | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Add m. Manual Forgine Methy M. 192 Add m. Steering-Assel Defeat, Ch. M. Uge Ref. first, A. P. Add m. 192 Add m. Steering-Assel Defeat, Ch. M. Uge Ref. first, A. P. Add m. 192 S. Add m. Steering-Assel Defeat, Ch. M. Uge Ref. first, A. P. Add m. 192 S. Add m. Steering-Assel Defeat, Ch. M. Uge Ref. first, A. P. Add m. 192 S. Add m. Steering-Assel Defeat, Ch. M. Uge Ref. first, A. P. Add m. 192 S. Add m. Perl Assel Cort Manual Methy m. 192 value | 2.4 (Repeat Count = 1) | Input Value | |
|--|--|---------------------------|------|
| Asterni, Manual Trigothol (Millinn, M.) (2) ARIEM (1997) (2) ARI | Transcription Cost M. Inc. | • | |
| Ast.ml SelempAssinDetest On M 50 | | | |
| Sp. Asstume Sp. | | | |
| S. SumLamPri-CropLints, Minhin, 192 sq. Astum, Perl. Assiscification, Minhin, 192 value sq. Astum, Perl. Assistification, Minhin, 192 value sq. Astum, Perl. CombinedDampin, Minhin, 192 value sq. Astum, Perl. Liminoricition, Minhin, 192 value sq. Astum, Perl. Assistion, Minhin, 192 value sq. Astum, Perl. Postericition sq. Astum, Perl. Assistion, Minhin, 192 value sq. Astum, Perl. Postericition sq. Astum, Perl. Assistion, Minhin, 192 value sq. Astum, Perl. Postericition sq. Astum, Perl. Assisticom, Minhin, 192 value sq. Astum, Perl. Minhin, 192 value sq. Rein, Ind. Astum, Astum, Perl. Assistcition, Minhin, 192 sq. Rein, Ind. Astum, Astum, Perl. Assistcition, Minhin, 192 sq. Rein, Ind. Astum, Astum, Perl. Assistcition, Minhin, 192 sq. Rein, Ind. Astum, Astum, Perl. Liminoricities, Ori. 193 sq. Rein, Ind. Astum, As | | | |
| 27000005 | | | |
| 2, Asturn. Perl. AssistChot Markinn, 123 value 0, Asturn. Perl. AssistChot Markinn, 125 value 0, Asturn. Perl. ChristonEchaping, Mirkinn, 125 value 0, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value 0, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value 1, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value 1, Asturn. Perl. ChristonEchaping, Mirkinn, 125 value 0, Asturn. Perl. DispersonChristonEchaping, Mirkinn, 125 value 0, Asturn. Perl. Purchasticton, Mirkinn, 125 value 0, Asturn. Perl. Purchasticton, Mirkinn, 125 value 1, Asturn. Perl. Purc | | | |
| 3, Astum Perl AssistEOT Clam 19, 25 value 0, 200000012 0, 200000012 0, 200000012 0, 200000012 0, 200000012 0, 2000000000 0, 20000000000000000 | | | |
| Q. Astum. Perl. AssistSOTLam, Mirkm, T.S.2 value 0,00000000 0, detam. Perl. AssistSollam, Mirkm, T.S.2 value 1,000000000 0, detam. Perl. AssistSollam, Mirkm, T.S.2 value 1,000000000 0,00000000 0,00000000 0,00000000 | | | |
| Astum. Perl AssistSoll-Lim, Mirkim, 132-value 0.40000000000000000000000000000000000 | | | |
| Q.Asturn.Pert_AssistShallmin, MrNm, 152 value 2,0990909 3,48 turn.Pert_Combined-bamping, MrNm, 152 value 4,899995 3,48 turn.Pert_Combined-bamping, MrNm, 152 value 5,9 Asturn.Pert_LimitedResturn.JMrNm, 152 value 6 1,79999995 3,48 turn.Pert_LimitedResturn.JMrNm, 152 value 7,9 Asturn.Pert_LimitedResturn.JMrNm, 152 value 7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value 7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value 7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value 7,9 Asturn.Pert_Disturn.JMrNm, 152 value 7,9 Asturn.JMrNm, | | | |
| AstLim_Pert CombinesDumping Minhm, 192 value 1, 299999999999999999999999999999999999 | | | |
| Statum Pert Combined Damping Minhm 132 value | | | |
| gl. Astl.m.Pert DefeatLimisService, Crt. gc. value | | | |
| Q. AstLim. Pert Iminedretum, MnNm, _152 value 0 0 0 0 0 0 0 0 0 | | | |
| SASTEM_Pert_LimPhoCitiCoDisable_Cnt_lgcvalue | | | |
| Saltum Pert LimPhOtErable Cnt, Igc-value 1 1 1 1 1 1 1 1 1 | | | |
| Salt.ml. Perl _ UniPrictif Crid _ Mirkm _ 132 value | | | |
| AstLIM_Perl_OpTiQOr_MRh7m_132 value 1, 1 | | | |
| gl. AstLmL Pert DoutputRampMult_Uis_132-value 0.40000006 0.1 0.0000006 0.1 0.0000006 0.000000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.00000006 0.0000006 0.00000006 0.00000006 0.0000006 0.0000006 0.0000006 0.0000006 0.00000006 0.0000006 0.0000006 0.00000006 0.00000006 0.0000006 0.0000006 0.0000006 0.00000006 0.0000006 0.0000006 | | | |
| AstLim_Pert PosSerVCCDisable_Cnt_lgc_value 0 0 0 0 0 0 0 0 0 | - : : | | |
| J. AstLIM_Pert_PowerLimiPerc_UIs_122_value 0.400000006 J. AstLIM_Pert_PullCompCmd_MtrNm_152_value 1.6 J. AstLIM_Pert_PullCompCmd_MtrNm_152_value 5 J. AstLIM_Pert_PullCompCmd_MtrNm_152_value 1.4 J. AstLIM_Pert_PullCompCmd_MtrNm_152_value 2.4000001 J. AstLIM_Pert_PullCompCmd_MtrNm_152_value 1.4 J. AstLIM_Pert_Primabilimi_MtrNm_152_value 1.4 J. AstLIM_Pert_Primabilimi_MtrNm_152_value 1.4 J. AstLIM_Pert_Primabilimi_MtrNm_152_value 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_MtrNm_152_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_MtrNm_152_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_INST_10_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_CombinedDamping_MtrNm_152_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_LimiPercontFiltered_UIs_132_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_DispCripCond_UINNm_132_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_PowerLimiPerc_UIs_132_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_Post_Minn_132_ 1.4 J. Re_Inst_Post_AssLIM_AstLIM_Pert_Pos | | | |
| AstLIM_Pert_PriAssistCord_MtrNm_f32_value 5 AstLIM_Pert_ThemalLimitPerc_Uls_f32_value 5 AstLIM_Pert_ThemalLimitPert_Uls_f32_value 0.40000006 AstLIM_Pert_ThemalLimitPerc_Uls_f32_value 14.5 AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value 14.5 AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value 14.5 AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value 14.5 AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value 7.1999981 AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value 7.1999981 AstLIM_Pert_AssistCord_MtrNm_f32 AstLIM_Pert_Ass | | | |
| SalLmt_Pert_PullCompCmd_MtrNm_r32 value 5 | | | |
| Q. AstLmt. Pert ThermalLimit Perc. Uls_132 value 2,4000001 3, AstLmt. Pert ThermalLimit, Within, 132 value 14.5 3, AstLmt. Pert WheelimbalanceCmd, Mirkin, 132 value 14.5 3, AstLmt. Pert AssistCord_Mirkin, 132 value 14.5 3, AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistEOTGamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistVehSpdLimit, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert AssistVehSpdLimit, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert CombinedDamping, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert LimitPercentFiltered Uls_132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert District Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert District Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert District Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. Pert PrelLimitForGoval, Mirkin, 132 3, Rite_Inst, 3p, AstLmt. AstLmt. | | | |
| 2.4 AstLmt. Pert ThermalLimit, MirNm 32.2 value 1.4 1.4 1.5 1. | | | |
| 3.4.StLmt.Pert_VehSpd_Kph_f32 value 14.5 | | | |
| 3. AstLmt Perf WheelimbalanceCmd MirNm f32 value 7.19999981 1. Rte Inst. Ap AstLmt AstLmt Perf AssistCond MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistCond MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistCond MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistEOTDamping MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistEOTDamping MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistEOTDaming MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistEOTDaming MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistSteOTDaming MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf AssistVeNSpdLimin MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf CombinedDamping MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf CombinedDamping MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf CombinedDamping MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimitePercentFiltered Uls f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimitePercentFiltered Uls f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimitePercentFiltered Uls f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf LimiteReturm MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf DipTqOv_MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf ProvertimitPerc Uls f32 1. Rte Inst. Ap AstLmt AstLmt Perf ProvertimitPerc Uls f32 1. Rte Inst. Ap AstLmt AstLmt Perf PretimitForStall MirNm f32 1. Rte Inst. Ap AstLmt AstLmt Perf PretimitForStall MirNm f32 | | | |
| gt Rte Inst Ap AstLmt AstLmt Per1 AssistCond_MtrNm_f32 tgt_AstLmt_Per1 ImpRCond_mtrNm_f32 tgt_AstLmt_Per1 ImpRC | | | |
| gt Rte Inst Ap | | | |
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| gt Rte Inst Ap, AstLmt AstLmt, Perl AssistEOTLimit, MtrNm; f32 tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32 tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32 gt Rte Inst Ap, AstLmt AstLmt, Perl AssistVehSpdLimit, MtrNm; f32 tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32 tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32 gt Rte Inst Ap, AstLmt AstLmt, Perl _CombinedDamping, MtrNm; f32 tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc gt Rte Inst Ap, AstLmt AstLmt, Perl _LimitPercentFiltered_UIs; f32 tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc gt Rte Inst Ap, AstLmt AstLmt, Perl _LimitPercentFiltered_UIs; f32 tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32 gt Rte Inst Ap, AstLmt AstLmt_Perl _LimitPercentFiltered_UIs; f32 tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32 gt Rte Inst Ap, AstLmt AstLmt_Perl _LimitPercentFiltered_UIS; f32 tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32 gt Rte Inst Ap, AstLmt AstLmt_Perl_OpTrQVr_MtrNm; f32 tgt_AstLmt_Perl_LimitPercentFiltered_UIS; f32 gt Rte Inst Ap, AstLmt AstLmt_Perl_PoseervCDisable_Cnt_Igc tgt_AstLmt_Perl_PoseervCDisable_Cnt_Igc gt Rte_Inst Ap, AstLmt AstLmt_Perl_PowerLimitPerc_UIS; f32 tgt_AstLmt_Perl_PoseervCDisable_Cnt_Igc gt Rte_Inst Ap, AstLmt AstLmt_Perl_Prel-InitTorque_MtrNm; f32 tgt_AstLmt_Perl_Prel-InitTorque_MtrNm; f32 gt Rte_Inst Ap, | | | |
| gr. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32 tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32 tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32 tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32 tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32 tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32 tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32 tgt_AstLmt_Perl_LimitBercentFiltered_Us_f32 tgt_AstLmt_Perl_PorctrCCDisable_Cnt_Igc tgt_AstLmt_Perl_PorctrCTCmd_MtrNm_f32 tgt_AstLmt_Perl_DoutpUrkBampMult_Us_f32 tgt_AstLmt_Perl_DoutpUrkBampMult_Us_f32 tgt_AstLmt_Perl_PowerLimitBerc_Us_f32 tgt_AstLmt_Perl_PowerLimitBerc_Us_f32 tgt_AstLmt_Perl_PowerLimitBerc_Us_f32 tgt_AstLmt_Perl_PowerLimitBerc_Us_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Perl_TowerlimitMerc_Us_f32 tgt_AstLmt_Perl_TowerlimitMerc_Us_f32 tgt_AstLmt_Perl_TowerlimitMerc_Us_f32 tgt_AstLmt_Perl_TowerlimitMerc_Us_f32 tgt_AstLmt_Perl_TowerlimitMerc_Us_f32 t | | | |
| gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitGreentFiltered_Uls_f32 tgt_AstLmt_Per1_DefeatLimitGreentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitGreentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitGreentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc tgt_AstLmt_Per1_LimPnCtrCTCblable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OptrQvr_MtrNm_f32 tgt_AstLmt_Per1_DeftrGroble_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OptrQvr_MtrNm_f32 tgt_AstLmt_Per1_OptrQvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc tgt_AstLmt_Per1_OptrQvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitForCDisall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForGall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForGall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SumLimTrqComd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForGall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SumLimiTrqComd_MtrNm_f32 tgt_Ast | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_ImitPercEntFiltered_Uls_f32 tgt_AstLmt_Per1_DercEntFiltered_Uls_f32 tgt_AstLmt_Per1_DercEntFiltered_Uls_ | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFillered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFillered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PricLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PricLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TimitTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TimitImitTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt_AptLmt_Perrl_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForStall_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForStall_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_SumLimitTrqCmd_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_SumLimitTrqCmd_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_VehSpd_Kph_f32 gt_AstLmt_Perrl_TimitPercentFiltered_Uls_f32.value A | | | |
| gt_Rte_inst_Ap_AstLmt_AptLattLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Ap_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_igc tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_TremalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TremalLimitPerc_Uls_f32 gt_Rt | | | |
| tgt_AstLmt_Ap_AstLmt_AstLmt_Perl_LmPnCtrCCDisable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc tgt_AstLmt_Perl_DpTrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32 tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32 tgt_AstLmt_Perl_PosservCCDisable_Cnt_lgc tgt_AstLmt_Perl_TosmitCommand_MtrNm_f32 tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32 tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32 tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32 tgt_AstLmt_Perl_Vospd_Kph_f32 tgt_AstLmt_Perl | | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LmPnCtrEnable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Powert.imitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Powert.imitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PillCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PillCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_P | | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPorcy_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SullcompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SullcompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Uls_f32.value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_P | | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_UIs_f32 tgt_AstLmt_Per1_OutputRampMult_UIs_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_UIs_f32 tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_Per1_PretLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitPerc_UIs_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PretLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PretLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PretLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PretLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_TretlimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tg | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservLimitForc_Uls_f32 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservLimitForculs_f32 tgt_AstLmt_Por1_PoservCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservLimitForStall_MtrNm_f32 tgt_AstLmt_Por1_ToutintNotin_MtrNm_f32 tgt_AstLmt_Por1_ToutintNotin_MtrNm_f32 tgt_AstLmt_Por1_UnitNotin_MtrNm_f32 tgt_AstLmt_Por1_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value Expected Value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstL | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretmalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretmintMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 stg_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 stg_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 stg_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 | | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrmalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitForStall_MtrNm_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForque_MtrNm_f32.value 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitFercentFiltered_UIs_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 ± 0.0009765625 | | 1 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Vame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 ± 0.0009765625 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | | |
| Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | | 1 | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 7 ± 0.0009765625 | _ASILMI.ASILMI_Peri_wineeiimbalanceCmd_MtrNm_f32 | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 \$\pmu\$ 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 \$\pmu\$ 0.0009765625 | | · | Resi |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 | | | |
| v | | | |
| | | | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.79999995 2.79999995 ± 0.0009765625 | _SumLimTrqCmd_MtrNm_f32.value | 2.79999995 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.5 (Repeat Count = 1) | | | * |
|--|---------------------------------------|----------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2.25999999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 4.5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.5999999 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.5999999 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1.89999998 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 38.2999992 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_t | f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_ | MtrNm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_I | MtrNm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt AstLmt Per1 DefeatLimitService | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Ul | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt AstLmt Per1 PosServCCDisable | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtr | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_withVin_i32 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_Mtrl | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Peri PreLimitrorque_witnin_i32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrN | | |
| tgt Rte Inst Ap AstLmt.AstLmt Peri PullCompCmd MtrNm f32 | tgt AstLmt Per1 PullCompCmd MtrN | _ | |
| | | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_U | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_ | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCm | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.25 | -0.25 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.5 | 0.5 ± 0.0009765625 | |
| | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| est Step 2.6 (Repeat Count = 1) | Input Value | | |
|--|---------------------------------------|--------------|------|
| | 1 | | |
| tLmt_ManualTrqCmdEn_Cnt_M_lgc tLmt_ManualTrqCmd_MtrNm_M_f32 | -1.76999998 | | |
| tLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| | | | |
| e_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 3.5999999 | | |
| SumLimPlCmpLimit_MtrNm_f32 | | | |
| get_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.5 | | |
| _AstLmt_Per1_AssistCmd_MtrNm_f32.value | -3 | | |
| _AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8.80000019 | | |
| _AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| _AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.600000024 | | |
| _AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.2999995 | | |
| _AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2 | | |
| _AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 2 | | |
| _AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| _AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8 | | |
| _AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| _AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value | 1 | | |
| _AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1.20000005 | | |
| _AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2.0999999 | | |
| _AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| _AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| _AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8 | | |
| _AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| _AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.5999999 | | |
| | 36.4000015 | | |
| AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 1 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_ | f32 | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_Mtr | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_Mtrl | _ | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_ | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_ | _ | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_ | | |
| | | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrN | - | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cn | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrI | _ | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_U | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_ | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mt | - | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_Mtr | Nm_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrN | lm_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrN | Nm_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mt | trNm_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_ | _MtrNm_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_U | Jls_f32 | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNn | m_f32 | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | | nd MtrNm f32 | |
| | | | Res |
| | | · · | 1163 |
| | | | |
| | | | |
| | | | |
| _AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | | | |
| _Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 _Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 time _AstLmt_Per1_LimitPercentFiltered_Uls_f32.value _AstLmt_Per1_PreLimitForStall_MtrNm_f32.value _AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.7 (Repeat Count = 1) Name | Input Value | | |
|--|--|---------------------------------|-----|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.75999999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| c_SumLimPlCmpLimit_MtrNm_f32 | 2.7000005 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.4000001 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 8.80000019 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.4000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.0999999 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| gt AstLmt Per1 PrkAssistCmd MtrNm f32.value | 1.20000005 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.70000005 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 26.6000004 | | |
| st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 2 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm f32 | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | С | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | _lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt AstLmt Per1 WheelImbalanceCmd N | AtrNm f32 | |
| | | | Pac |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.48999998 | -0.49000001 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.69999988 | 0.699999988 ± 0.0009765625 | |
| | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.8 (Repeat Count = 1) Name | Input Value | | |
|---|--|--|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32 | -8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.79999995 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.2999995 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 0 | | |
| · · · · · | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.800000012 | | |
| | 2.5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | | | |
| | 2.20000005 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4.19999981 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.099999 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.79999995 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 34.7000008 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtrl | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | _lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | _lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_t | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt AstLmt Per1 TrqLimitMin MtrNm f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt AstLmt Per1 WheellmbalanceCmd M | trNm f32 | |
| Name | Actual Value | Expected Value | Resi |
| | 0.800000012 | · · | Kest |
| tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | | 0.800000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -4.5 | -4.5 ± 0.0009765625 -3.5999999 ± 0.0009765625 | |
| | | | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | -3.60000014 0.800000012 | 0.800000012 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.9 (Repeat Count = 1) | | | * |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.89999976 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.20000005 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6.19999981 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.29999995 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.9000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 65.9000015 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| | | WIII_132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | • | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | 732 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd Mt | trNm f32 | |
| Name | Actual Value | | Resu |
| | | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | |
| LIAU ID AD II VE OLUANA: CCC. | | 6.19999981 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 6.19999981 | | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -2.66000032 | -2.66000009 ± 0.0009765625 | • |
| | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.10 (Repeat Count = 1) | Innut Value | | |
|---|--|--|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -7.5 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc | 1 | | |
| | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 5.63000011 | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | -2.0999999 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_AssistCmd_withtin_i32.value | - <i>1</i> -2.5999999 | | |
| gt_AstEmt_Per1_AssistEOTGain_Uls_f32.value | 0.89999976 | | |
| gt_AstLmt_Peri_AssistEOTGain_Ois_ioz.value | 1 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.69999988 | | |
| gt_AstLmt_Peri_AssistStaliLmm_mtmm_roz.value | 2.400001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrCobisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_tmPriOttErlable_Ont_igc.value gt_AstLmt_Per1_tmPriOttTCmd_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_tmPnOtr1Cmd_withm_isz.value gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_Op1rqOvt_intrivin_is2.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_PowerLmitrerc_ois_i32.value gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 4.5 | | |
| gt AstLmt Per1 PullCompCmd MtrNm f32.value | -4.5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_ThermalLimit_erc_ors_132.value gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_ThermalLmit_MthMil_132.value | 25.5 | | |
| | 5.19999981 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrCCDisable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt AstLmt Per1 LrnPnCtrEnable Cnt I | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt AstLmt Per1 OutputRampMult Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Ci | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f: | _ · | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Fei1_FowerLinitFei0_0is_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_FowerLimitFer5_ois_i. | | |
| gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_FreLimitForstail_initiNin_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_FreLimitForStall_witNi | _ | |
| gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Per1_PriAssistCintg_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _ | |
| gt_Nte_inst_Ap_AstLmt.AstLmt_Per1_Fullcompoind_withMin_is2 | tgt_AstLmt_Per1_Fullcompcmd_withvinit tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Per1_rswiitConfinatio_wtiNffi_i52 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt AstLmt Per1 ThermalLimitPerc Uls | | |
| gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_memalLimitFerc_ois_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_t | | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Per1_TretlialLintt_WitNin_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretLinitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| gt_Rte_inst_Ap_AstLint.AstLint_Per1_InqLinitt\\ini_int\\ini_in2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_rrqLimitwiii_witwiii_rs | _ | |
| gt_Rte_inst_Ap_AstLint.AstLint_Per1_WenSpd_Rpii_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm f32 | |
| | | | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.29999995 | 3.29999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.330000013 0.69999988 | 0.330000013 ± 0.0009765625 0.699999988 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.11 (Repeat Count = 1) Name | Input Value | | |
|--|---|---|------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32 | 7.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6.23999977 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2 | | |
| | -2 -8 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.10000002 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.5 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7.5999999 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.20000003 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.20000003 | | |
| pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3.0999999 | | |
| yt_AstLmt_Per1_VehSpd_Kph_f32.value | 75.8000031 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtrl | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | - - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_t | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt AstLmt Per1 WheellmbalanceCmd M | ltrNm f32 | |
| Name | Actual Value | Expected Value | Res |
| | | | Kest |
| gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | -0.160000013 -0.160000011 0.800000012 | -0.159999996 ± 0.0009765625 0.800000012 ± 0.0009765625 | |



| Τ | | | | |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.12 (Repeat Count = 1) | Immed Value | | |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.44999981 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.89999998 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1.60000002 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.20000005 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 5 500000 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3 20000005 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3.20000005 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 28.2000008 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | N. 700 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mti | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_i | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCcDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | # N | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1.60000002 | 1.60000002 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1 | -1 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.300000012 | -0.300000012 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.899999976 | 0.899999976 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.13 (Repeat Count = 1) | | | ~ |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 6.55999994 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.24000001 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.79999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.29999995 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -3 | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 0.40000006 | | |
| tgt AstLmt Per1 PosServCCDisable Cnt Igc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.599999 | | |
| | 0.40000006 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 85.4000015 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | | irNm f22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mi | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | , |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3.64000058 | 3.6400001 ± 0.0009765625 | 1 |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.64000058 | 3.6400001 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.160000011 | 0.159999996 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.40000006 | 0.400000006 ± 0.0009765625 | |
| | | | |

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| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.15 (Repeat Count = 1) | | | |
|---|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.239999995 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.60000002 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 425.299988 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr1 | Jm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 111_102 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | trNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | 1.000 |
| tgt AstLmt Per1 PreLimitFerCentFiltereu_0is_i3z.value | 6.63999939 | 6.63999987 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitForStall_withMn_f32.value | -6.63999939 | -6.63999987 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitrorque_inin/im_isz.value | -0.360000014 | -0.360000014 ± 0.0009765625 | |
| | | 0.600000014 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.600000024 | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.16 (Repeat Count = 1) | | | • |
|--|--|---|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.36000001 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1.79999995 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3.5999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 153.100006 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt AstLmt Per1 DefeatLimitService Cnt I | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered Uls | • | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | - | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | .90 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f3 | 32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | 5 <u>2</u> | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt AstLmt Per1 PosServCCDisable Cnt | | |
| | | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | 700 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_rttc_mat_xp_xatemt_ren_venopu_rtpn_102 | | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | | |
| | tgt_AstLmt_Per1_WheelImbalanceCmd_Mti Actual Value | Expected Value | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | | Expected Value 0.699999988 ± 0.000030517578125 | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | Actual Value | | Resul |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | Actual Value 0.699999988 | 0.699999988 ± 0.000030517578125 | Resul |
| tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | Actual Value 0.699999988 5 | 0.699999988 ± 0.000030517578125 5 ± 0.0009765625 | • |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.17 (Repeat Count = 1) Name | Input Value | | |
|---|--|--|------|
| | · · · · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -1.5 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| <_SumLimPICmpLimit_MtrNm_f32 specific-street Add mt. Part. TSMitCommand. MtrNm. f32 value. | 2.48000002 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.39999998 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.70000005 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.20000005 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -7 | | |
| t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.20000003 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 12.5 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered | | |
| t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable | - | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cr | | |
| | tgt AstLmt Per1 LrnPnCtrTCmd Mtr | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_U | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable | - | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mt | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_Mtr1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_M | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_L | Jls_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNr | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCm | nd_MtrNm_f32 | |
| lame | Actual Value | Expected Value | Res |
| | 0.200000003 | 0.200000003 ± 0.000030517578125 | 1.00 |
| at Astlimt Per1 LimitPercentFiltered Uls f32 value | 3.2000000 | 0.20000000 ± 0.000000011010120 | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 20000005 | 1 20000005 + 0 0009765625 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1.20000005 | 1.20000005 ± 0.0009765625 | |
| | 1.20000005 -1.20000005 -0.24000001 | 1.20000005 ± 0.0009765625 -1.20000005 ± 0.0009765625 -0.239999995 ± 0.0009765625 | |





| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.18 (Repeat Count = 1) | | | |
|--|---|---------------------------------|-----|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 3.5999999 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.29999995 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.80000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.79999995 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.2999995 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.0999999 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 510.899994 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | _ - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3; | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | WALL FOO | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2.0999999 | 2.0999999 ± 0.0009765625 | |
| | 1.92000008 | 1.91999996 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.540000021 | 0.540000021 ± 0.0009765625 | |

1.79999995

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

1.79999995 ± 0.0009765625



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.19 (Repeat Count = 1) Name | Input Value | | |
|--|---------------------------------------|---|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd MtrNm M f32 | -7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| \te_msi_Ap_Asicint <_SumLimPlCmpLimit_MtrNm_f32 | 4.71999979 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.89999976 | | |
| gt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 1.8999998 | | |
| gt_AstLmt_Per1_AssistE01Eimit_MtrNm_f32.value | 5.5 | | |
| gt_AstLmt_Per1_Assiststalitimit_MtrNm_f32.value | 2.4000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_CombinedDamping_MithIm_i32.value gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.0999999 | | |
| | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4.5 -6.5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | | | |
| | 0.400000006 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.400000006 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 1.20000005 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.19999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 425.700012 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_I | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_0 | — - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_I | MtrNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Ul | s_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | _MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| | 4.5 | 4.5 ± 0.0009765625 | |
| gt Astemt Peri PrelimithorStall Mitrium 132.Value | | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -4.5 | -4.5 ± 0.0009765625 | |
| | | -4.5 ± 0.0009765625 -1.79999995 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.20 (Repeat Count = 1) Name | Input Value | | |
|--|--|---------------------------|-----|
| | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| _SumLimPlCmpLimit_MtrNm_f32 | 5.84000015 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.10000002 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5.5999999 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.3999998 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| pt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -3 | | |
| at AstLmt Per1 PullCompCmd MtrNm f32.value | 2.2999995 | | |
| at AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.5 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.30000019 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 373.299988 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | gc | |
| t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_C | nt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | m_f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32 | tgt AstLmt Per1 ThermalLimitPerc Uls | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt AstLmt Per1 ThermalLimit MtrNm | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt AstLmt Per1 WheelImbalanceCmd | MtrNm f32 | |
| | | | De- |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 6.8499999 | 6.8499999 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -6.8499999 | -6.8499999 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| | | | |



| T | | | | | |
|--|-------|--|-------|----------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ | |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ | |

| Fest Step 2.21 (Repeat Count = 1) | Input Value | | | |
|---|---|---|-----|--|
| | | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6 | | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | | |
| _SumLimPlCmpLimit_MtrNm_f32 | 6.96000004 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1 | | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3.2999995 | | | |
| pt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1 | | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.200000003 | | | |
| pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2.0999999 | | | |
| pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.5 | | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 | | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1 | | | |
| pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6.5 | | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3.29999995 | | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | 0.600000024 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.4000001 | | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.60000024 | | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.4000001 | | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 324.600006 | | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 2 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_UIs_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | | tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | | | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | | tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc | | |
| | | tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNn | _ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl | _ | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | | target_AstLmt_Per1_TSMitCommand_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | <u>f</u> 32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f | 32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | _MtrNm_f32 | | |
| lame | Actual Value | Expected Value | Res | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | | |
| | | | | |
| at AstLmt Per1 PreLimitForStall MtrNm f32.value | 3.29999995 | 3.29999995 ± 0.0009765625 | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorgue_MtrNm_f32.value | 3.29999995 3.29999995 | 3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625 | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 3.29999995 3.29999995 1.98000002 | 3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625 1.98000002 ± 0.0009765625 | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Name | |
|--|-----|
| Section Sect | |
| Astlum! Scientinp-Assillation Display Disp | |
| Ris_Inst_Ap_AstLmt | |
| SumLimPCropLimi Mirkin, 132 gat. Ast.mir, Pert, AssistErOT Mirkin, 132 value gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value gat. Ast.mir, Pert, Defeat. Irmited Review (and in the second seco | |
| got Ast.ml Pert _ TSMICOmmand_MitNim_152 value | |
| g. AstLm. Pert _AssistCOT (MYNn_ 152 value 5.599999 g. AstLm. Pert _AssistCOT (Comp. MYNn_ 152 value 2 2 2 2 2 2 2 2 2 | |
| gl. Aslt.mt Pert AssistEOTDamping MRNm, 152 value 2 2 2 2 2 2 2 2 2 | |
| Q. AstLim, Perl. AssistEOTGain, Us. 132 value 2 20000005 | |
| Ast.ml. Perl | |
| Jastimil Pert AssistiveNpdit imil, Mrkhm, 132 value 5.5 | |
| Ast.mt Peri AssistVehSpdLimit MtrNm 52 value 2 | |
| AstLmt_Per1_CombinedDamping_MirNm_132 value 2 2 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,299999999 3 3,2999999999 3 3,299999999999 3,2999999999999999999999999999999999999 | |
| gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 0 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 3 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 1 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 0 gl AstLIM_Pert_ImitedToChosable_Cnt_ligo_value 5 gl AstLIM_Pert_OptTroOv_MitNim_f32_value 5 gl AstLIM_Pert_OptTroOv_MitNim_f32_value 1 gl AstLIM_Pert_PosSexCCDSable_Cnt_ligo_value 1 gl AstLIM_Pert_PowerLiminPerc_Uis_f32_value 0 gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value 5 gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value 45 gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value 0 gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value 45 gl AstLIM_Pert_VerbSpx_fxb, f32_value 45 gl AstLIM_Pert_NetibilitanceCom_MitNim_f32_value 45 gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32 tgt_AstLIM_Pert_AssistCom_MitNim_f32_value gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tgt_AstLIM_Pert_AssistCom_MitNim_f32_value gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tgt_AstLIM_Pert_AssistCom_MitNim_f32_ gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tg | |
| J. AstLmt. Perl _ LimitedRetum_MtrNm_f32.value 3.29999995 J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt. gevalue 0 J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value 5.599999 J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value 1 J. AstLmt. Perl _ DoutputRampMutt_Uis_f32.value 0.69999988 J. AstLmt. Perl _ PosServ. CCDisable_Cnt. gevalue 1 J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value 5 J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ PuliCompCmd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ ThermalLimifPerc_Uis_f32.value 0.69999988 J. AstLmt. Perl _ ThermalLimifPerc_Uis_f32.value 4.5 J. AstLmt. Perl _ PuliCompCmd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ NeisylinablanceCornd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ NeisylinablanceCornd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ NeisylinablanceCornd_MtrNm_f32.value 4.5 J. Rie_Inst_Ap_AstLmt. AstLmt. Perl _ AssistEOTGam_Uis_f32 J. Rie_Inst_Ap_AstLmt. AstLmt. Perl _ DesireLimitService_Cnt. go J. Rie_Inst_Ap_AstLmt. AstLmt. Perl _ DesireLimitService_Cnt. go J. Rie_Inst_Ap_AstLmt. AstLmt. Perl _ LimiPercentFiltered_Uis_f32 J. Rie_Inst_Ap_AstLmt. AstLmt. Perl _ DerroPortCroDisable_Cnt. go J. Rie_Inst_Ap_AstLmt. AstLmt. | |
| 1, AstLmt_Perl_LmPnCtrCoDisable_Cnt_lgcvalue 1 0 0 0 0 0 0 0 0 0 | |
| J. AstLmT. Pert LmPhCritTcnde Cnt Igc. value 5.599999 J. AstLmT. Pert LmPhCritTcnde Mixhm 132 value 5.5999999 J. AstLmt. Pert OutpurRampMilt, Uls 132 value 1 | |
| AstLmt Per1 LmPnCtrTCmd MtrNm_f32 value | |
| J. AstLmt. Pert OptrqOv_MtrNm_f32 value 0.699999988 1. J. AstLmt. Pert OrtputRampMult_Uis_182 value 0.699999988 1. J. AstLmt. Pert PosservcCDisable_Cnt_Ug. value 1. J. AstLmt. Pert PosservcCDisable_Cnt_Ug. value 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert ThermalLimitPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert VenSpd_Kpn_F32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert VenSpd_Kpn_F32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value 4.5 0.6999999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_Combined Demping, MrNm_f32 value 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_LimiteDercombinitiered_Uis_f32 value 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_Derc | |
| 31, AstLmt, Pert PosSenvCCDisable, Cnt. Jgc. value 1 | |
| 1, AstLmt_Per1 PosservCCDisable_Cnt_lgc_value 1 | |
| AstLmt Perf PowerLimitPerc Uis | |
| AstLmt_Pert_PowerLimitPerc_Uis_r32.value 0.699999988 AstLmt_Pert_PrkAssistComd_MirNm_r32.value 5 | |
| 31, AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.689999988 31, AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.689999988 31, AstLmt_Per1_ThermalLimitMrMm_f32.value 4.5 31, AstLmt_Per1_WheellimbalanceCmd_MtrNm_f32.value 456.399994 31, AstLmt_Per1_WheellimbalanceCmd_MtrNm_f32.value 456.399994 32, AstLmt_Per1_WheellimbalanceCmd_MtrNm_f32.value 456.399994 33, AstLmt_Per1_WheellimbalanceCmd_MtrNm_f32.value 456.399994 34, AstLmt_Per1_AssistEOTDamping_MtrNm_f32 15, AstLmt_Per1_Powert.imitPer2_Uls_f32 15, AstLmt_Per1_Powert.imitPer2_Uls_f32 15, AstLmt_Per1_Powe | |
| AstLmt Per1_ThermalLimitPerc_UIs_f32.value | |
| AstLmt_Per1_ThermalLimitPerc_Uis_f32.value | |
| 11. AstLmt_Perl_VehSpd_Kph_f32.value 12. AstLmt_Perl_VehSpd_Kph_f32.value 13. AstLmt_Perl_WheelImbalanceCmd_MtrNm_f32.value 13. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 14. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStOTGain_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStoTlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_CombinedDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimProCirCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimProCirCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimProCirCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimProCirCoM_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimProCirCoM_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DoTrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_OpTrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_OpTrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_OpTrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitPerc_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitPerc_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_Mt | |
| ti, AstLmt, Perl_VhRopd, Kph_f32.value ti, AstLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Ap. AstLmt, Perl_AssistEOTDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_CombinedDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimitedRetur_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrTCmd_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PoseervCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PoseervCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PoseervCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PoseervCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 | |
| It AstLmt Perl WheelinbalanceCmd MtrNm f32 value It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistComd_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDaminum, MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDaminum, MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistSidall_init_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _DefeatLimitService_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DefeatLimitService_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _LimitePrecentFiltered_UIs_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _LimiteRecture_UIs_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftCrCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftCrCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftCrCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftCrCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _PoservCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _PoservCDisable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForsial_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForsial_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForsial_MtrNm_f32 It Rie Inst. Ap. AstL | |
| tgt. Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_DeftrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DoftrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DoftrCrody_MtrNm_f32 tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_DostrCCDisable_Cnt_lgc tgt_AstLmt_Per1_PostervCCDisable_Cnt_lgc tgt_AstLmt_Per1_ | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTCain_Uts_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_LimitPerceutFiltered_Uts_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCD | |
| gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTGain_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DeftrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstL | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPercEort_Igc tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_PoservLimitPerc_Uls_f32 tgt_AstLmt_Per1_PoservLimitForsLall_MtrNm_f32 tgt_AstLmt_Per1_PoservLimitForsLall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsdue_MtrNm_f32 tgt_AstLmt_Per1_SumLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsmallLimitPerc_Uls_f32 tgt_AstLmt_Per1_TsmallLimitPero_Uls_f | |
| tgt_AstLmt_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DirtrCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PreLimitForCall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForCall_MtrNm_f | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_Derfection_MtrNm_f32 tgt_AstLmt_Per1_Derfectio | |
| tgt_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvy_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvy_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvy_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvy_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Optqovr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PoservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstL | |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_M | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqOvr_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_T | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TosMitCommand_MtrNm_f32 target_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_DosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TosMi | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimi | |
| target_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 tar | |
| gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ | |
| gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | |
| | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 | |
| gt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | |
| | Pos |
| Name Actual Value Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5.5999999 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -1.01999986 -1.01999998 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -0.713999867 -0.713999987 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 1.60000002 ± 0.0009765625 | |



| Т | | | | | |
|--|-------|--|-------|----------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ | |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.23 (Repeat Count = 1) | Innut Value | | |
|---|--|---------------------------------|-----|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 0.059999987 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.800000012 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2.2999995 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.70000005 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.5999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 345.100006 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm_f32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32 | tgt AstLmt Per1 AssistEOTGain Uls f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered UI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt AstLmt Per1 LrnPnCtrEnable Cnt lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | _102 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosSetvCCDIsable_Cnt_igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PosservCCbisable_Cn tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Ois_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| | tgt_AstLmt_Per1_PreLimitForStall_MtrNm tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tqt Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Res |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1 | 1 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1 | -1 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.800000012 | -0.800000012 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 1.70000005 | 1.70000005 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.24 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|-------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 5 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt Rte Inst Ap AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.899999976 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.699999988 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2 | | |
| tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 2.4000001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.79999995 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| tgt AstLmt Per1 OpTrqOvr MtrNm f32.value | -3 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.899999976 | | |
| tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value | -7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.899999976 | | |
| tgt AstLmt Per1 ThermalLimit MtrNm f32.value | 4.69999981 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 234.199997 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_I | Jls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | Cnt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_t | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_N | ftrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | <u>_</u> f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | _MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.89999976 | 0.899999976 ± 0.000030517578125 | • |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -1.61999989 | -1.62 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 1.79999995 | 1.79999995 ± 0.0009765625 | ٠, |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.25 (Repeat Count = 1) | | | × |
|--|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.74000001 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.600000024 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -5 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 1.89999998 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.2999995 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.80000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 123.800003 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 1.20000005 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Ci | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Lmnleuretum_intrivim_is2 | tgt_AstLmt_Per1_LmnPnCtrCCDisable_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt AstLmt Per1 PosServCCDisable C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32 | tgt AstLmt Per1 PreLimitForStall MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt AstLmt Per1 PrkAssistCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _ i_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | ltrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | , |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -3 | -3 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.30000012 | -0.300000012 ± 0.0009765625 | , |
| G. Stanic, or Louisian Indone_Markin_loz.value | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.26 (Repeat Count = 1) | | | ~ |
|---|--|---------------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 4 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 2.57999992 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.4000001 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f32.value | -4 | | |
| tgt AstLmt Per1 OpTrgOvr MtrNm f32.value | -5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 1 | | |
| | 8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 4.9000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 500.700012 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 2 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr1 | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | <u>lgc</u> | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | T32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_MtrI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3; | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt AstLmt Per1 TrqLimitMin MtrNm f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | | trNm f22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | , |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 4 | 4 ± 0.0009765625 | 1 |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.77999973 | 3.77999997 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.40000006 | 0.400000006 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | |
| | | | |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.27 (Repeat Count = 1) | | | • |
|---|--|--|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -3 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.42000008 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.400000006 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.80000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2.70000005 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2.5999999 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 400.899994 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mi | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | — · | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | ls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | дс | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 5 | 5 ± 0.0009765625 | |
| | | | |
| | -5 | -5 ± 0.0009765625 | - ' |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -5 -1.5 | -5 ± 0.0009765625 -1.5 ± 0.0009765625 | • |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.28 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 3 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 4.26000023 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.300000012 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTGain_UIs_f32.value | 0.899999976 | | |
| tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 2.79999995 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.40000006 | | |
| tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value | 3 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt AstLmt Per1 ThermalLimit MtrNm f32.value | 5.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 300.5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | ls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | ıt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | ı_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3. | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7.2000029 | 7.19999981 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 7.20000029 | 7.19999981 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.880000055 | 0.879999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.20000005 | 2.20000005 ± 0.0009765625 | |

TEST DETAILS REPORT

2014-09-05, 17:30:46+0530



7

AstLmt_Per1



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.30 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 2.25999999 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 5.94000006 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.100000001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 3.5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.9000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_CombinedDaniping_withthi_ioz.value gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEcobisable_Cnt_lgc.value | 1 | | |
| | -1.20000005 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value gt_AstLmt_Per1_OpTrgOvr_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_Op1rqOvt_MitNin_isz.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PosServCcDisable_Crit_igt.value | 0.200000003 | | |
| gt_AstLmt_Per1_FowerLimitPer0_0is_i32.value gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_PriAssistCmd_wtrNm_i32.value | -3 | | |
| | 0.200000003 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 5.80000019 | | |
| yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 58.2000008 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.5 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3. | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_M | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Ci | _ · | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_C tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 52 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | Au Al Go | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1.20000005 | -1.20000005 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.24000001 | -0.239999995 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.9000001 | 2.9000001 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.31 (Repeat Count = 1) Name | Input Value | | |
|--|---------------------------------------|---|-----|
| | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 76000009 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -1.76999998 1 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 6.78000021 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.80000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 3.5999999 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.4000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8.80000019 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -4 | | |
| t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 5.9000001 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 42.5999985 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -6.5 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt | | |
| | tgt AstLmt Per1 LrnPnCtrTCmd MtrN | - · | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_0 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtr1 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_I | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Ul | s_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | f_MtrNm_f32 | |
| gt_rtte_inst_Ap_AstEint.AstEint_Feir_whieelinbalanceCind_withtin_isz | Actual Value | Expected Value | Res |
| | | | |
| Name | | 0.300000012 + 0.000030517578125 | |
| Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 6 + 0.0009765625 | |
| Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 0.300000012 6 | 6 ± 0.0009765625 | |
| lame gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.32 (Repeat Count = 1) | Innut Value | | |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.75999999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.61999989 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 3.70000005 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.400000006 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 38.0999985 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.29999995 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -4.5 | -4.5 ± 0.0009765625 | ٠, |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -1.80000007 | -1.79999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3.0999999 | 3.0999999 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.33 (Repeat Count = 1) Name | Input Value | | |
|--|---|---------------------------|-----|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| _SumLimPlCmpLimit_MtrNm_f32 | 8.46000004 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 3.79999995 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.20000005 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.5999999 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 1.5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| yt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3.29999995 | | |
| yt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6 | | |
| t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.0999999 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 26.7000008 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 5.5999999 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| | | III_132 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32 | tgt AstLmt Per1 ThermalLimitPerc Uls f3 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt AstLmt Per1 ThermalLimit MtrNm f32 | | |
| t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt AstLmt Per1 WheelImbalanceCmd Mt | rNm f32 | |
| | | | De- |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3.20000005 | 3.20000005 ± 0.0009765625 | |
| | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.34 (Repeat Count = 1) Name | Input Value | | |
|--|---|---------------------------------|-----|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| S_SumLimPlCmpLimit_MtrNm_f32 | 0.600000024 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4.5 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.20000003 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 3.9000001 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.70000005 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.5999999 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5.5999999 | | |
| gt AstLmt Per1 PullCompCmd MtrNm f32.value | -7 | | |
| at AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.600000024 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.19999981 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 3.5 | | |
| st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| | | 1111_132 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | _lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | rNm f32 | |
| | | 1 | l n |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3.29999995 | 3.29999995 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.2999995 | 3.29999995 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 1.98000002 | 1.98000002 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3.2999995 | 3.29999995 ± 0.0009765625 | |
| | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.35 (Repeat Count = 1) | Inner Makes | | |
|--|---|---------------------------------|-----|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.959999979 | | |
| rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.4000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.7999995 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -8.10000038 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.30000019 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 253.600006 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | rNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt AstLmt Per1 LrnPnCtrEnable Cnt lo | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | _102 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | 23 | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Fer1_OutputRampMulit_0is_is2 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PosservCcDisable_Cr | | |
| | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStail_MtrNn tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PreLimit1orque_MtrNm tgt_AstLmt_Per1_PreLimit1orque_MtrNm | _ | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2.0999999 | 2.0999999 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3.4000001 | 3.4000001 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Saturn Monastron Mehm M 32 7.5 | Test Step 2.36 (Repeat Count = 1) Name | Input Value | | |
|--|--|--|------------------|------|
| State Manual Trophony Mehrin March Manual March Manual March Manual March | | | | |
| Nation_Participation_Color_U_Digs | | | | |
| Start LimPC-Drymit, Mirbins, 122 Start LimPC-Drymith, Mirbins, 122 Sta | | | | |
| SumulmPrintplint Minhm, 152 whate 0.5 pLattint PET LandsCroman Minhm, 152 whate 0.5 pLattint PET LandsCroman Minhm, 152 whate 0.5 pLattint PET LandsCroman Minhm, 152 whate 0.40000000 pLattint PET LandsCroman Minhm, 152 whate 0.4000000 pLattint PET Description Minhm, 152 whate 0.4000000 pLattint PET PostScromo Minhm, 152 whate 0.40000000 pLattint Pet PostScromo Minh | | | | |
| Sept. Asturt Pert SNACcommand, Minkin, 52 value | · _ | | | |
| Ast Int. Perl. AssistCrottaphina, Different Colorable | | | | |
| Asturn. Perl. AssistCOTampung, Minhm, 52 value Asturn. Perl. AssistCOTam, Usin 22 value Asturn. Perl. Independent Asturn. 22 value | | | | |
| Ast.mt. Perl AssistCTGam Mirch Exclusion | | | | |
| Asturn Perl AssistDallimi, Mehm 152 value 4,099999 | | | | |
| Ast.mt Perl Assestitation Minhm, 192 value | | | | |
| J. Ast.m. Perl Desatt WebSpectime, Miching 12 value 20099099 | gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | | | |
| Ast m. Perl CombinedDamping Merkm 122 value | | 3.5 | | |
| Astum Pert LimiteReturn, Mirkm, 12 vaulue 1 | gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3.9000001 | | |
| AstLm. Perl LimberOctico Astlem Astlem Perl Combord Astlem Perl P | gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2.0999999 | | |
| Jastim. Pert LimPricht Collabele_Crt govalue 1 1 2 3 3 3 3 3 3 3 3 3 | gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| | gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| | gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| | gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| Jastim Pert OsporRamMut Us 52 value | yt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1 | | |
| Jastimil Pert PosSeno CODisable, Cni govalue 1 0.80000012 Jastimil Pert PirkassistCmd, Minhm, 732 value 1 0.800000012 Jastimil Pert PirkassistCmd, Minhm, 732 value 1 0.800000012 Jastimil Pert PirkassistCmd, Minhm, 732 value 0.800000012 Jastimil Pert PirkassistCmd, Minhm, 732 value 0.800000012 Jastimil Pert Nehepin Khris, 732 value 0.800000012 Jastimil Pert AssistCond, Minhm, 732 value 0.800000012 Jastimil Pert AssistCond, Minhm, 732 0.80000000000000000000000000000000000 | gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| J. AstLm. Perl PowerLimitPerc_Uis_ 52 value | gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| J. AstLmf. Perf. ProvertimitPerc. Ulis. 52 value 1, J. AstLmf. Perf. PrikAssistCmd_MthVm, 132 value 1, J. AstLmf. Perf. PrikAssistCmd_MthVm, 132 value 1, J. AstLmf. Perf. PricemationitPerc. Ulis. 132 value 1, J. AstLmf. Perf. PricemationitPerc. Ulis. 132 value 1, J. AstLmf. Perf. VehSpd. Kph. 132 value 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 100000001 342 100000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 100000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 10000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 100000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 1000000001 342 10000000001 342 10000000001 342 10000000001 342 1000 | gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| | | 0.800000012 | | |
| 1, AstLmL Perl ThermalLimit MrNm 132 value 0.80000012 1, AstLmL Perl ThermalLimit MrNm 132 value 34.000001 1, AstLmL Perl WheelimbalanceCmd MrNm 132 value 34.000001 1, AstLmL Perl WheelimbalanceCmd MrNm 132 value 32 1, Rel Inst Ap | gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2 | | |
| AstLmt_Pert_IntermatiLmit_Mtnhm_f32 value | pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 1 | | |
| J. AstLmt. Perf Thermatilumit, Mirhm. 132.value | at AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.800000012 | | |
| AstLmt.Pert VehSpd (Xph _ 132 value 342 100006 | | | | |
| AstLmt Pert WheelmbalanceCmd MtrNm; | | 342.100006 | | |
| | | | | |
| 13, Rte, Inst, Ap, AstLmtAstLmt, Perl, AssistEOTDamping, MirNim, [32] tgt, AstLmt, Perl, AssistEOTGain, Uis, [32] tgt, AstLmt, Perl, AssistStallLimit, MirNim, [32] tgt, AstLmt, Perl, CombinedDamping, MirNim, [32] tgt, AstLmt, Perl, CombinedDamping, MirNim, [32] tgt, AstLmt, Perl, DefeatLimitService, Cnt, Igc tgt, AstLmt, Perl, LimiteOffectur, MirNim, [32] tgt, AstLmt, Perl, Defoxer(CDisable, Cnt, Igc tgt, AstLmt, Perl, Defoxer(CDisable, Cnt, Igc tgt, AstLmt, Perl, Defoxer(CDisable, Cnt, Igc tgt, AstLmt, Perl, Perl, MirNim, MirNim, [32] tgt, AstLmt, Perl, Perl, Perl, MirNim, MirNim, 132 tgt, AstLmt, Perl, Perl, MirNim, MirNim, 132 tgt, AstLmt, Perl, P | | | | |
| gt Rie Jinst Ap, AstLimit AstLimt, Pert J. AssistEOTGain, Uls. [32] tgt AstLimt AstLimt, Pert J. AssistEOTLimit, Mixhm, [32] tgt AstLimt, Pert J. CombinedDamping, Mixhm, [32] tgt AstLimt, Pert J. Destect Mixhm, [32 | | | Nm f32 | |
| 12 12 13 15 15 15 15 15 15 15 | | | NIII_132 | |
| gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStalliLimit_MtrNm_f32 tgt_AstLmt. Per1_AssistStalliLimit_MtrNm_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStAllimit_MtrNm_f32 tgt_AstLmt. Per1_AssistStAllimit_MtrNm_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_DefeatLimitService_Cnt.lgc tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPerconfFiltered_Uis_f32 tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPerconfFiltered_Uis_f32 tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPerconfFiltered_Uis_f32 tgt_AstLmt. Per1_LimitPerconfFiltered_Uis_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimPnCtrCObjable_Cnt.lgc tgt_AstLmt. Per1_LimPnCtrCobjable_Cnt.lgc gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt. Per1_DefectCnable_Cnt.lgc gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt. Per1_OpTrqOvr_MtrNm_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_PoseervCobjable_Cnt.lgc tgt_AstLmt. Per1_DoptrqOvr_MtrNm_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Port-ImitTorque_MtrNm_f32 tgt_AstLmt. Per1_PowerLimitPerc_Uis_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Pre1_ImitTorque_MtrNm_f32 tgt_AstLmt. Per1_Pre1_ImitTorque_MtrNm_f32 gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Pre1_SintCommand_MtrNm_f32 tgt_AstLmt. Pe | | | f22 | |
| gt Rte inst Ap AstLmt AstLmt Pert AssistVehSpdLimit MtrNm f32 tg Rte inst Ap AstLmt AstLmt Pert CombinedDamping MtrNm f32 tg AstLmt Pert DefeatLimitService Cnt tg tg AstLmt Pert LimitedReturn MtrNm f32 tg AstLmt Pert Defeat MtrNm f32 tg AstLmt Pert Defeat MtrNm f32 tg AstLmt Pert DeventimitPerc Uls f32 tg AstLmt Pert Pret MtrNm f32 tg AstLmt Pert Defeat MtrNm f32 tg AstLmt Pert TermalLimit MtrNm f32 tg AstLmt Pert TermalLimit MtrNm f32 tg AstLmt Pert T | | | | |
| gt Rte Inst Ap AstLmt AstLmt Pert DefeatLimitService Cnt Igo Igt Rte Inst Ap AstLmt AstLmt Pert DefeatLimitService Cnt Igo Igt Rte Inst Ap AstLmt AstLmt Pert DefeatLimitService Cnt Igo Igt AstLmt Pert LimitPercentFiltered Uls 132 Igt AstLmt Pert DefeatLimitService Cnt Igo Igt AstLmt Pert LimitPerce Uls Igt Igt AstLmt Pert DefeatLimitService Cnt Igo Igt AstLmt Pert DefeatLimitPerc Uls Igt Igt AstLmt Pert DefeatLimitService Uls Igt Igt AstLmt Pert DefeatLimitPerc Uls Igt Igt AstLmt Pert DefeatLimitPerc Uls Igt Igt AstLmt Pert DefeatLimitPerc Uls Igt Igt Igt AstLmt Pert DefeatLimitPerc Uls Igt | | | | |
| | | | _ | |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimiteReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimiteReturn_MtrNm_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_DrCtrCCDisable_Cnt_Igc tgt_AstLmt_Per1_DrCtrCdd_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PowerLimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PrelimitForstall_MtrNm_f32 tgt_AstLmt_Per1_SumLimitrqomd_MtrNm_f32 tgt_AstLmt_Per1_SumLimitrqomd_MtrNm_f32 tgt_AstLmt_Per1_SumLimitrqomd_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_Per1_MtrNm_f32 tgt_AstLm | | | | |
| Steel Inst. Ap. AstLmt. AstLmt. Pert Limited Return MtrNm f32 tgt. AstLmt. Pert December Dece | | | • | |
| tgt | | | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cn_Igc tgt_AstLmt_Per1_LmPnCtrEnable_Cn_Igc tgt_AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOv_MtrNm_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TimermalLimitImt_Perc_Uls_f32 tgt_AstLmt_Per1_Uls_Perc_Uls_f32 tgt_AstLmt_Per1_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_Uls_Perc_ | | | | |
| | | | | |
| tgt_AstLmt_Ap_AstLmt_AptLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_DosServCCDisable_Cnt_lgc tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Ap_AstLmt_AptLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Apt_mt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPorc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForstall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_Tret_imitMin_MtrNm_f32 tgt_AstLmt_Per1_Tret_imitMin_MtrNm_f32 tgt_AstLmt_Per1_Tret_imitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32. | | | | |
| | | | f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TakitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TakitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tretmillimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UnitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | | | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_UIs_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitPerc_UIs_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_UIs_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_UIs_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_Us_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_MtrNm_f32 tgt_Ast_M | | | | |
| trace Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_Sumt_imtTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_Sumt_imtTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_UheslimbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_Uheslimb | | | | |
| | | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.00009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.00009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretimitMin_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.00009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 **Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 **Idea | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value target_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32.value tgt_AstLmt_Per1_Torque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value target_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_Torque_MtrNm_f32 tgt_AstLmt_Per1_ | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Iame Actual Value Expected Value Reserved Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | | target_AstLmt_Per1_TSMitCommand_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Iame Actual Value Expected Value Reserved Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 0.800000012 0.800000012 0.800000012 0.800000012 0.00009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0.00009765625 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0.00009765625 | | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.000030517578125 dct_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 dct_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 0 ± 0.0009765625 dct_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 0 ± 0.0009765625 | | | | |
| the content of the | | | | |
| Name Actual Value Expected Value Reserved gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.800000012 0.800000012 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | | | trNm f32 | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.800000012 0.8000000012 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | | | | Res |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | | | · · | Resi |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0 0 ± 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 0 ± 0.0009765625 | | | | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | · | | | |
| | | | | |
| | | - 1 | U ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.37 (Repeat Count = 1) | Input Value | | |
|---|---|---|------|
| | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| x_SumLimPlCmpLimit_MtrNm_f32 | 1.67999995 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.19999981 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.5999999 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.899999976 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.89999976 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.899999976 | | |
| yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.5 | | |
| pt_AstLmt_Per1_VehSpd_Kph_f32.value | 484.799988 | | |
| gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value | -4.5 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | ···_·- | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | | |
| | | - | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | igc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | 20 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_l | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | tgt_AstLmt_Per1_WheelImbalanceCmd_Mti | rNm_f32 | |
| | | T | 12 |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | Actual Value | Expected value | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | Actual Value | 0.89999976 + 0.00030517578125 | Res |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 0.899999976 6.5 | 0.899999976 ± 0.000030517578125 6.5 ± 0.0009765625 | Res |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | Res |



| Т | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.38 (Repeat Count = 1) | | | |
|---|---|--|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 6.55999994 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.03999996 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.70000005 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -5 | | |
| tgt AstLmt Per1 OpTrgOvr MtrNm f32.value | 3.2999995 | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value | 0 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.20000005 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| | 6.5999999 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 155.899994 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | 20 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_l | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | _Ulsf32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | Cnt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_0 | Cnt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | tgt AstLmt Per1 SumLimTrqCmd MtrNm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_UI | - | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt AstLmt Per1 VehSpd Kph f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmo | MtrNm f32 | |
| Name | Actual Value | | Page |
| | | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -5 | -5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.5 | -0.5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3.70000005 | 3.70000005 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.39 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | -5.55999994 | | |
| AstLmt SteeringAsstDefeat Cnt M Igc | 1 | | |
| | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.400001 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.4000001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.79999995 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.19999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.6999981 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 278.600006 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mi | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt AstLmt Per1 DefeatLimitService Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | _, | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | 1_132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | fo o | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ - | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 5.5999999 | 5.5999999 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ٠, |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| | 3.79999995 | 3.79999995 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.40 (Repeat Count = 1) | Input Value | | |
|--|--|---------------------------------------|-----|
| | · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 2.75999999 | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0.89999976 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.9000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.30000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| pt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.30000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.30000012 | | |
| pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.80000019 | | |
| pt AstLmt Per1 VehSpd Kph f32.value | 312.399994 | | |
| pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.29999995 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_I | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_0 | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ tgt_AstLmt_Per1_LimitedReturn_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | 0 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_0 | _ _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrN | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_ | MtrNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Ul | ls_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | tgt_AstLmt_Per1_WheelImbalanceCmo | d_MtrNm_f32 | |
| gt_Rte_inst_Ap_astLmt.AstLmt_Peri_vvneeiimbalanceCmd_ivitrivm_t32 | | Expected Value | Res |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name ot AstI mt_Per1_LimitPercentFiltered_Llls_f32_value | Actual Value | 0.300000012 + 0.000030517578125 | |
| Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 0.300000012 8.80000019 | 8.80000019 ± 0.0009765625 | |
| Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.41 (Repeat Count = 1) Name | Input Value | | |
|---|---------------------------------------|---------------------------------|-------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -4.5 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| | tgt Rte Inst Ap AstLmt | | |
| Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32 | 3.11999989 | | |
| | 1 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.5999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.4000001 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.9000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 328.299988 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_ | MtrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | Im f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_I | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt AstLmt Per1 LrnPnCtrEnable Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| | tgt_AstLmt_Per1_PosServCCDisable_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtrl | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_UI | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_ | †32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmo | d_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4 | 4 ± 0.0009765625 | • |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.42 (Repeat Count = 1) | | | 1 |
|---|--|-------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 4.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_Igc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.48000002 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.10000002 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.69999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 429.399994 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 1 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | С | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t | - [32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | n f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt AstLmt Per1 VehSpd Kph f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | AtrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| | 0.5 | · | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | | 0.5 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 1.5 | 1.5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.75 | 0.75 ± 0.0009765625 | |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.43 (Repeat Count = 1) | | | |
|--|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -3.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.83999991 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.20000005 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.19999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.5999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.5999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.0999999 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 52.7999992 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 2 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mti | Nm f32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32 | tgt AstLmt Per1 AssistEOTGain Uls f32 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | | 132 | |
| gt_Rte_inst_Ap_AstLint.AstLint_Per1_Op1rqOvi_MtiNin_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3: | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | /trNm_f32 | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 2.51999998 | 2.51999998 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.19999981 | 4.19999981 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.44 (Repeat Count = 1) Name | Input Value | | |
|---|---|---|------|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| x_SumLimPlCmpLimit_MtrNm_f32 | 4.1999981 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.29999995 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.4000001 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.80000019 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.19999981 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.30000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.69999981 | | |
| pt_AstLmt_Per1_VehSpd_Kph_f32.value | 37.5 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | ··· <u>-</u> | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNi | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | igc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | • | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_I | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 2 | |
| 1 D. 1 1 A A 1 A 1 D . 1 O . 1 T O 1 M. M. 100 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | m_f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | m_f32 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | m_f32 ? Nm_f32 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value | m_f32 2 Nm_f32 Expected Value | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.300000012 | m_f32 2 Nm_f32 Expected Value 0.300000012 ± 0.000030517578125 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.300000012 5.29999924 | m_f32 2 Nm_f32 Expected Value 0.300000012 ± 0.000030517578125 5.30000019 ± 0.0009765625 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.300000012 | m_f32 2 Nm_f32 Expected Value 0.300000012 ± 0.000030517578125 | Resi |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.45 (Repeat Count = 1) | | | |
|--|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 4.55999994 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.3999998 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.899999976 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.9000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.30000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | -5 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Peri_LmPnCtrEnable_Cnt_igc.value gt_AstLmt_Peri_LmPnCtrTCmd_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_cmPnOtr1Cmd_withtin_isz.value gt_AstLmt_Per1_0pTrq0vr_MtrNm_f32.value | 8.80000019 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_Posservoodsable_Grit_igt.value | 0.40000006 | | |
| gt_AstLift_reri_rowerLiftitrerc_ois_i32.value | 8 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.80000019 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 83.9000015 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | - ' | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | <u>lgc</u> | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | ! | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_l | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t | 732 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | rNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.400000006 | 0.400000006 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -5 | -5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -2 | -2 ± 0.0009765625 | |
| | I | | |

4.9000001

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

4.9000001 ± 0.0009765625



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.46 (Repeat Count = 1) Name | Input Value | | |
|--|--|--------------------------------------|-------|
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 4.92000008 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.4000001 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 0 0.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value | 0.5 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.9000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 73.6999969 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_irqLimitiviin_wtrvm_r32 tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_VehSpd_Kph_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 tgt_AstLmt_Per1_VehSpd_Kph_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VenSpd_Rpn_r32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_VenSpd_Kpn_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_l | MtrNm f32 | |
| | | | Danie |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | 0.5 | 0.5 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 0 | 4 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 0 ± 0.0009765625 | |
| IGE_ASIGNE_FET_SUITEITTTYONIQ_WITHIN_ISZ.VAIUE | 5 | 5 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.47 (Repeat Count = 1) | | | |
|---|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 6 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5.28000021 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.60000002 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.6999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 500.200012 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi | m f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrgOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | > | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | : | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_withNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_SumLimirqCmd_mtrNm_r32 tgt_Rte_inst_Ap_AstLmt.AstLmt Peri_TSMitCommand MtrNm_r32 | | | |
| | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | N | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -7 | -7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -4.20000029 | -4.19999981 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.0999999 | 5.0999999 ± 0.0009765625 | • |
| | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Lmt_ManualTrqCmdEn_Cnt_M_lgc Lmt_ManualTrqCmd_MtrNm_M_f32 Lmt_SteeringAsstDefeat_Cnt_M_lgc Lnt_SteeringAsstDefeat_Cnt_M_lgc Lnt_SteeringAsstDefeat_Cnt_M_lgc Lnt_SteeringAsstDefeat_Cnt_M_lgc Lnt_StLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistEOTGamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTCain_Uls_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimPnCtrCcDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrComplex_Uls_f32.value AstLmt_Per1_OpTrqovr_MtrNm_f32.value AstLmt_Per1_OpTrqovr_MtrNm_f32.value AstLmt_Per1_OpTrqovr_MtrNm_f32.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrhAssistCmd_MtrNm_f32.value AstLmt_Per1_PrhAssistCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_Limi | Input Value 0 -5 -5 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 -1 | 2 2 _f32 1_f32 c | |
|--|--|--------------------------------------|------|
| _mt_ManualTrqCmd_MtrNm_M_f32 _mt_SteeringAsstDefeat_Cnt_M_lgc _Inst_Ap_AstLmt _umLimPlCmpLimit_MtrNm_f32 _et_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCTOmd_MtrNm_f32.value AstLmt_Per1_OptrqOv_MtrNm_f32.value AstLmt_Per1_OptrqOv_MtrNm_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerl_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.As | 1 tgt_Rte_Inst_Ap_AstLmt 5.63999987 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0 -8 1 0 0 -8 -2.5999999 0.699999988 1 0.699999988 4.5 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistSoTLimit_MtrNm_f3; tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3; tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f4; tgt_AstLmt_Per1 | 2 2 _f32 1_f32 c | |
| Lmt_SteeringAsstDefeat_Cnt_M_lgc Inst_Ap_AstLmt umLimPlCmpLimit_MtrNm_f32 et_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistComd_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimpnCtrCDisable_Cnt_lgc.value AstLmt_Per1_LimpnCtrCodisable_Cnt_lgc.value AstLmt_Per1_ImpnCtrCodisable_Cnt_lgc.value AstLmt_Per1_OpTrQov_MtrNm_f32.value AstLmt_Per1_OpTeqOv_MtrNm_f32.value AstLmt_Per1_OpServCODisable_Cnt_lgc.value AstLmt_Per1_PosServCDisable_Cnt_lgc.value AstLmt_Per1_PosServCodisable_Cnt_lgc.value AstLmt_Per1_PorPrAssistCmd_MtrNm_f32.value AstLmt_Per1_PriAssistCmd_MtrNm_f32.value AstLmt_Per1_PriPriAssistCmd_MtrNm_f32.value AstLmt_Per1_PriPriPriPriPriPriPriPriPriPriPriPriPriP | 1 tgt_Rte_Inst_Ap_AstLmt 5.63999987 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0.69999998 1 0.699999988 1 0.699999988 4.5 4.6 4 0.699999988 8.1000038 58.200008 6-6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTTamping_MtrNm tgt_AstLmt_Per1_AssistCoTTamit_MtrNm_f32 tgt_AstLmt_Per1_LombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| Inst_Ap_AstLmt tumLimPlCmpLimit_MtrNm_f32 tet_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistCmd_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistSeotLimit_MtrNm_f32.value AstLmt_Per1_AssistSull.imit_MtrNm_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCTomd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OptrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Ap_AstLmt.AstLmt_Per1_AssistCOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_A | tgt_Rte_Inst_Ap_AstLmt 5.63999987 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0.699999998 1 0.699999988 1 0.699999988 4.5 4.5 4 0.699999988 8.1000038 58.200008 6-6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_AssistCoTLaimit_MtrNm_f3; tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LaimitPercentFiltered_Uis_f3; tgt_AstLmt_Per1_LaimitPercentFiltered_Uis_f3; tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3; tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f4; tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f4; tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f4; | 2 2 _f32 1_f32 c | |
| tet_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistCmd_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistSOTLimit_MtrNm_f32.value AstLmt_Per1_AssistSallLimit_MtrNm_f32.value AstLmt_Per1_AssistSallLimit_MtrNm_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LmPnCtrCTOmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OptrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_AssitLmt_Per1_AssistCOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSotllLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_ | 5.63999987 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0.69999999 0.699999988 1 0.699999988 4.55 4 0.699999988 8.10000038 58.200008 6-6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCoTTLaming_MtrNm tgt_AstLmt_Per1_AssistCoTTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistCoTLimit_MtrNm_f3: tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3: tgt_AstLmt_Per1_L | 2 2 _f32 1_f32 c | |
| et_AstLmt_Per1_TSMitCommand_MtrNm_f32.value AstLmt_Per1_AssistCmd_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_DopTctrCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OptrqOvr_MtrNm_f32.value AstLmt_Per1_PosservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrikAssistCmd_MtrNm_f32.value AstLmt_Per1_PirlemalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTTimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTTimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 I | 1.70000005 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0.68 1 1 0.699999988 1 0.699999988 4.5 4.5 4 0.699999988 8.10000038 58.200008 6-6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LomitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_AssistCmd_MtrNm_f32.value AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_DopTcrTCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OptrqOvr_MtrNm_f32.value AstLmt_Per1_PosservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_Cnt_lgc.value AstLmt_Per1_PoservCCDisable_MtrNm_f32.value AstLmt_Per1_PoservCCDisable_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_AssistCmd_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_AssistCDTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFil | 6 8 0.300000012 5.80000019 5.19999981 5.5999999 5 0.69999999 8 1 0.699999988 1 0.699999988 4.5 4.5 4 0.699999988 8.1000038 58.200008 6-6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value AstLmt_Per1_AssistEOTGain_Uls_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrimalLimitPerc_Uls_f32.value AstLmt_Per1_PimermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_As | 8 0.30000012 5.80000019 5.19999981 5.5999999 5 0 -8 1 0 -8 -8 1 0 -8 -2.5999999 0.699999988 1 0.699999988 4.5 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
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| AstLmt_Per1_AssistStallLimit_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value AstLmt_Per1_AssistLmt_Per1_AssistEOTDamping_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_P | 5.19999981 5.5999999 5 0 -8 1 0 -8 1 0 -8 2.5999999 0.69999998 1 0.699999988 4.5 4.0 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value AstLmt_Per1_DefeatLimitService_Cnt_lgc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_DopTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc It Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 It Rte_Inst_Ap_AstL | 5.5999999 5 0 -8 1 0 -8 1 0 -8 2-5,999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_CombinedDamping_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrCmd_MtrNm_f32.value AstLmt_Per1_DopTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCcDisable_Cnt_lgc.value AstLmt_Per1_PosServCcDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_Thermall.imitPerc_Uls_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | 5 0 -8 1 0 -8 1 0 -8 -2.5999999 0.699999988 1 0.699999988 -4.5 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTTamping_MtrNm tgt_AstLmt_Per1_AssistEOTTamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_DefeatLimitService_Cnt_Igc.value AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc.value AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc.value AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc.value AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OptrqOvr_MtrNm_f32.value AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_Igc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_Thermall.imitPerc_Uls_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Exte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistFOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistFOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistPOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistPOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistPOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_AssistPOTLimit_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Ite_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFilte | 0 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_LimitedReturn_MtrNm_f32.value AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_Thermall.imitPerc_Uls_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCond_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tre_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tre_Inst_Ap_AstLmt.Ast | .8 1 0 -8 -2.5999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_Thermall.imitPerc_Uls_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCOTDamping_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistPotlimit_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_AssistPotlimit_MtrNm_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tte_Inst_Ap | 1 0 -8 -2.5999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 8tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistPoTLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCcDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tRte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedRetur | 0 -8 -2.5999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistSdllLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_DpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 It Rte_Inst_Ap_AstLmt.AstLmt_Per1_LoorbeatLimitService_Cnt_lgc It Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 It Rte_Inst_Ap_AstLmt.A | -8 -2.5999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistPoTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistPoTLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_OpTrqOvr_MtrNm_f32.value AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 AstLe_Inst_Ap_AstLmt.AstLmt_Per1_ | -2.5999999 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.1000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_OutputRampMult_Uls_f32.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tRte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_L | 0.699999988 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistSellLimit_MtrNm_f3: tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f3: tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_PosServCCDisable_Cnt_lgc.value AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tRte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tRte_Inst_Ap_AstLmt.AstLmt_Per1_Limite | 1 0.699999988 -4.5 -4 0.699999988 8.10000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_PowerLimitPerc_Uls_f32.value AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DevServCCDisable_Cnt_lgc | 0.699999988 -4.5 -4 0.699999988 8.10000038 58.200008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lget | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DofTrqOvr_MtrNm_f32 | -4.5 -4 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lget | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_PullCompCmd_MtrNm_f32.value AstLmt_Per1_ThermalLimitPerc_Uls_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_ThermalLimit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DotputPosServCCDisable_Cnt_lgc | _4 0.69999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_Thermall.imitPerc_UIs_f32.value AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_UIs_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 0.699999988 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgt | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPerCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_Dtramp_Mult_Uls_f32 | 8.10000038 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_Thermall.imit_MtrNm_f32.value AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistSvehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrCTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 | 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_VehSpd_Kph_f32.value AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPctrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 | 58.2000008 -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | -6.5 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNn tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptruputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_Ootspable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_AssistEOTDamping_MtrNntgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNmtgt_AstLmt_Per1_CombinedDamping_MtrNmtgt_AstLmt_Per1_CombinedDamping_MtrNmtgt_AstLmt_Per1_DefeatLimitService_Cnt_lgrtgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 2 _f32 1_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptruputRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_DosServCCDisable_Cnt_lgc | gt_AstLmt_Per1_AssistEOTGain_Uis_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f3 | 2 2 _f32 1_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrutRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3; tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm, tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgit tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 _f32 n_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrustRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | gt_AstLmt_Per1_AssistStallLimit_MtrNm_f3z tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm, tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | 2 _f32 n_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_DTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | _f32 n_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_Igc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrutRampMult_Uls_f32 Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | tgt_AstLmt_Per1_CombinedDamping_MtrNm tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | n_f32 c | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_Defeatt.imitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igo tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | С | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrustRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptruputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | igi_AsiLini_Peri_LimiledReturi_MiliNin_i32 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc t Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lc | gc | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 t Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc t | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | 2 | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg | C | |
| | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 | | |
| | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 t | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | ! | |
| Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3 | 32 | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 t | target_AstLmt_Per1_TSMitCommand_MtrNm | n_f32 | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | |
| Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 t | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 t | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 t | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| | tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN | Nm_f32 | |
| | Actual Value | Expected Value | Resu |
| | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| | 6 | 6 ± 0.0009765625 | |
| | | | |
| | n | | |
| AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 4stLmt_Per1_TrqLimitMin_MtrNm_f32.value 5stLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0 0 | 0 ± 0.0009765625 0 ± 0.0009765625 | |





| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.49 (Repeat Count = 1) Name | Input Value | | |
|---|---|---------------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.79999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | |
| tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.900001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.30000019 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 5.69999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.19999981 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 42.2999992 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCcDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt AstLmt Per1 PowerLimitPerc Uls f3 | — - | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt AstLmt Per1 PreLimitTorque MtrNm | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt AstLmt Per1 SumLimTrqCmd MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32 | tgt AstLmt Per1 ThermalLimitPerc Uls | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt AstLmt Per1 ThermalLimit MtrNm f | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 1 | 1 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.30000019 | 5.30000019 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.50 (Repeat Count = 1) | | | |
|--|--|---------------------------------|-----|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| <_SumLimPlCmpLimit_MtrNm_f32 | 6.36000013 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.8999998 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.4000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.80000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.89999976 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.89999976 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.30000019 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 38.4000015 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32 | tgt AstLmt Per1 AssistEOTGain Uls f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | tgt AstLmt Per1 LimitPercentFiltered U | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_Lmneuretum_wurvin_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _132 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 200 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3. | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.400001 | 5.4000001 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.51 (Repeat Count = 1) | Innut Value | | |
|--|---------------------------------------|---------------------------------|-----|
| | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 4 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | - | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 6.71999979 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.0999999 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.5 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.9000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| at AstLmt Per1 PowerLimitPerc Uls f32.value | 0.10000001 | | |
| gt AstLmt Per1 PrkAssistCmd MtrNm f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -7 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.39999962 | | |
| at AstLmt Per1 VehSpd Kph f32.value | 26.5 | | |
| : - : - | -1 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_ | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_f | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_0 | Cnt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | _Ulsf32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNr | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | Cnt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt | :_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | lm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Ul: | s_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | _ | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNi | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt AstLmt Per1 PullCompCmd MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtr | _ | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target AstLmt Per1 TSMitCommand | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_U | - | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | _ | |
| | tgt_AstLmt_Per1_TnermalLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | 192 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | d Militables 622 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmo | | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 1.5 | 1.5 ± 0.0009765625 | |
| gt_Asternt_Ferr_SumetimingOnd_withVin_132.value | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.52 (Repeat Count = 1) Name | Input Value | | |
|--|--|--|-------|
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt ManualTrqCmd MtrNm M f32 | -3 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.07999992 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.0999999 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value | -6.5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.19999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 14.6000004 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | * | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PtllCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TolvitCollinand_within_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_PerC_0is_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_mermalLimit_Perc_ois | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TriefmaiLimit_MirNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt AstLmt Per1 VehSpd Kph f32 | - | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| | | · · | ivean |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 2.0999999 | 0.200000003 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 2.0999999 ± 0.0009765625 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.5999999 | 5.5999999 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.53 (Repeat Count = 1) Name | Input Value | | |
|--|---|--|------|
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt ManualTrqCmd MtrNm M f32 | 3 | | |
| AstLmt SteeringAsstDefeat Cnt M Igc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.4400006 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4.5 | | |
| tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.69999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 8.60000038 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 38.700008 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cr | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | lls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | :nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | - | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_r32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_\rqLimitMin_MtrNm_f3 | <i>7</i> 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WenSpd_Rpn_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm f32 | |
| Name | | | Page |
| | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.30000012 | 0.300000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 1.5 | 5 ± 0.0009765625 1.5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.69999981 | 5.69999981 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.54 (Repeat Count = 1) | | | |
|--|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.80000019 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.29999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.899999976 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.4000001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.80000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.19999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.400000006 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 2.29999995 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.69999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 36.7999992 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.19999981 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | /trNm_f32 | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -2.32000017 | -2.31999993 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.80000019 | 5.80000019 ± 0.0009765625 | |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.55 (Repeat Count = 1) | | | • |
|---|---|--------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 8.15999985 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.400001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.900001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.30000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.4000001 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 26.8999996 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | — - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 00 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | 4 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 3.5 | 3.5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.9000001 | 5.9000001 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Fest Step 2.56 (Repeat Count = 1) | | | |
|--|--|--|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7.36000013 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| x_SumLimPlCmpLimit_MtrNm_f32 | 8.52000046 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.5 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.20000003 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.5999999 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.4000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4.5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.19999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 34.5 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl | - Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | Igc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm f | _ | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Per1_PowerLinitFert_ols_132 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit or3tali_withtin_i32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | _ | |
| gt_Rte_inst_Ap_AsiLint.AsiLint_Per1_PreLinitTorque_intrivin_i32 gt_Rte_inst_Ap_AsiLint.AsiLint_Per1_PreLinitTorque_intrivin_i32 | tgt_AstLmt_Per1_FreLimitTorque_MtrNm f | | |
| gt_Rte_inst_Ap_AsiLint.AsiLint_PerT_PirAssistCind_intinnin_i32 gt_Rte_inst_Ap_AsiLint.AsiLint_PerT_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_inst_Ap_AsiLint.AsiLint_Peri_PullCompCind_within_is2 gt_Rte_inst_Ap_AsiLint.AsiLint_Peri_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_FullCompCmd_MtrNm_ tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | # N | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| gt AstLmt Per1 PreLimitForStall MtrNm f32.value | 5.5999999 | 5.5999999 ± 0.0009765625 | |
| ge totelle of the reculting of ordinary and the reculting of the reculting | | | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 2.81999969 1.69199991 | 2.81999993 ± 0.0009765625 1.69200003 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.57 (Repeat Count = 1) | | | • |
|---|--|--------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5.36999989 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.40000006 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.30000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.69999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.30000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 65.3000031 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | ! | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt AstLmt Per1 PosServCCDisable Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t | 132 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_Towncommand_winv tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_Ois_i32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrefmaiLimit_MtrNm_f32 | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_mqLimitwin_wtrNm_is2 tgt_Rte_inst_Ap_AstLmt.AstLmt Per1_VehSpd_Kph_f32 | | | |
| | tgt_AstLmt_Per1_VehSpd_Kph_f32 | Nm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | | |
| Name | Actual Value | Expected Value | Resu |
| tet Apti mt Dord LimitDoroomtFiltored Lillo 622 value | 0.69999988 | 0.69999988 ± 0.000030517578125 | , |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | | | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1.20000005 | 1.20000005 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1.20000005 | -1.20000005 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.58 (Repeat Count = 1) Name | Input Value | | | |
|--|---|------|--|--|
| | • | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 -4.3499999 | | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4.3499999 | | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | | |
| <_SumLimPICmpLimit_MtrNm_f32 specific-street Add mt. Part. TSMitCommand. MtrNm. f32 yellus. | 0.82999983 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.70000005 | | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2 | | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4 | | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.80000019 | | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.19999981 | | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.5999999 | | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1 | | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.0999999 | | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.0999999 | | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8.80000019 | | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6.6999981 | | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.4000001 | | | |
| pt AstLmt Per1 VehSpd Kph f32.value | 25.1000004 | | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1.20000005 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping MtrNm f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc | | | |
| | | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tqt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | | |
| gt_Rte_inst_Ap_AstLint.AstLint_Fet1_FixAssistCintu_within_i32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | | | |
| | | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | Race | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value Expected Value | Resi | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.8000000012 0.8000000012 Expected Value 0.8000000012 | Resi | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.800000012 | Resi | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.8000000012 0.8000000012 Expected Value 0.8000000012 | Resi | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.59 (Repeat Count = 1) | | | ~ |
|---|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 3.47000003 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.25999999 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.79999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.9000001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.30000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.69999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value | -4.5 | | |
| | -6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.89999976 | | |
| | | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 75.6999969 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | :_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PtKAssistCmtd_MtrNm_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PtkAssistCmd_MtrNm_t | | |
| | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 4.5 | 4.5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -4.5 | -4.5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -4.04999971 | -4.05000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.30000019 | 6.30000019 ± 0.0009765625 | |
| | · · · · · · · · · · · · · · · · · · · | | _ |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.60 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|-------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2.25999999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt Rte Inst Ap AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.69000006 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2.900001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.4000001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.10000001 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 28.8999996 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_C | _ - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt AstLmt Per1 OutputRampMult Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt AstLmt Per1 PowerLimitPerc Uls | — - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt AstLmt Per1 PreLimitForStall MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGue_MtrNm_f32 | tgt AstLmt Per1 PreLimitForstall_MtN | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNn | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32 | tgt AstLmt Per1 SumLimTrqCmd MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd | _MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 6.19000006 | 6.19000006 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 6.1900006 | 6.19000006 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.619000018 | 0.619000018 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.4000001 | 6.4000001 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.61 (Repeat Count = 1) Name | Input Value | | |
|--|---|--|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32 | -1.76999998 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 2.11999989 | | |
| <_SumLimPlCmpLimit_MtrNm_f32 Instruct_Act mt_Port_TSMitCommand_MtrNlm_f32_value | 3 | | |
| rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -5 -7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.0999999 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.5 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.9000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.5999999 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.69999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 85.1999969 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mti | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | ·Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | AtrNm f32 | |
| Name | Actual Value | Expected Value | Resi |
| | | | Kest |
| gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| 1 A II 1 B 4 B 11 IIT | | 3.29999995 ± 0.0009765625 | 1 |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.29999995 | | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.660000026 6.5 | 0.660000026 ± 0.0009765625 6.5 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.62 (Repeat Count = 1) | In the Males | | |
|---|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.75999999 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.54999995 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.19999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.5999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 5 5000000 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.89999976 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.30000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.80000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 76.5999985 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | _ - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3. | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.89999976 | 0.899999976 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 6 | 6 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 2.05000019 | 2.04999995 ± 0.0009765625 | ٠, |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.615000069 | 0.61500001 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.5999999 | 6.5999999 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.63 (Repeat Count = 1) | | | × |
|---|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.98000002 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.9000001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.69999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value | -1 | | |
| | 2 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.9000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 425.399994 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 5.19999981 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr1 | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnf | :_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt AstLmt Per1 PosServCCDisable Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PtKAssistCmtd_MtrNm_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PikAssistCmd_mtrNm_f | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _ | |
| | target_AstLmt_Per1_TSMitCommand_Mtrl | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3. | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1 | -1 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.40000006 | -0.400000006 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.6999981 | 6.69999981 ± 0.0009765625 | |
| | | 11 111 1 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.64 (Repeat Count = 1) | | | |
|---|--|---------------------------|-----|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.41000009 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.79999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.10000001 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.4000001 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.80000019 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.19999981 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -7 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 363.799988 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | lm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr1 | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | Igc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Fei1_FowerLinitFei0_0is_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| gt_Rte_Inst_Ap_AstLint.AstLint_Per1_PreLimitPolStail_witNin_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_FreLimitForstall_MtrNm | | |
| gt_Rte_inist_Ap_AstLint.AstLint_Ferr_FreLinitrorque_withhin_is2 gt_Rte_inist_Ap_AstLint.AstLint_Ferr_FreLinitrorque_withhin_is2 | tgt_AstLmt_Per1_FreLimitTorque_MitNin_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmt_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PtrAssistCmd_mtrNm_t | | |
| gt_Rte_inst_Ap_AstLint.AstLint_Fei1_FullCompCinta_wtivint_is2 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| gt_Rte_inst_Ap_AstLint.AstLint_Peri_Sunit.inffqCind_intiNni_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_Sunit.inffqCind_intiNni_is2 | target AstLmt Per1 TSMitCommand Mtrl | _ | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3. | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | v. N | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | _ | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 3.4000001 | 3.4000001 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.80000019 | 6.80000019 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.65 (Repeat Count = 1) Name | Input Value | | |
|--|--|---------------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7.5 | | |
| AstLmt_ManualTrqCind_MtNni_M_i32 AstLmt_SteeringAsstDefeat Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.83999991 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.70000005 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1 | | |
| tgt_nstEm_ eri_ testerma_wirkin_joz.value | 3 | | |
| tgt AstLmt Per1 AssistEOTGain Uls f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6.9000001 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 7.30000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 1.5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.10000038 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 246.100006 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mi | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | _ - | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ tgt_AstLmt_Per1_LmPnCtrCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LmPnCtrCobisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | 1 132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt AstLmt Per1 PowerLimitPerc Uls f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt AstLmt Per1 SumLimTrqCmd MtrNi | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 3 | 3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -3 | -3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -1.8000007 | -1.79999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6.9000001 | 6.9000001 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.66 (Repeat Count = 1) | In and Walter | | |
|--|---|--------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 7.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 4.26999998 | | |
| k_SumLimPICmpLimit_MtrNm_f32 | -2.5999999 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| | 0.300000012 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 7.5999999 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.5999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.4000001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | -4 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.5999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.19999981 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 153.300003 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | jc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_1 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | 1_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 WheellmbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.69999988 ± 0.000030517578125 | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_0is_132.value | 2 | 2 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLim1rqCmd_mtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.67 (Repeat Count = 1) Name | Input Value | | |
|---|---|-------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -6.5 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 4.69999981 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.69999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.30000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 84.5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3. | - | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt AstLmt Per1 LimitPercentFiltered Ul | | |
| | tgt_AstLmt_Per1_Limitr_ercentr_intered_Or | _ | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 700 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_1 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | 1 |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -5 | -5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -4 | -4 ± 0.0009765625 | |
| GENERAL OF LOUISING THOMAS TO SERVICE | 7 | - 1 0.00001 00020 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.68 (Repeat Count = 1) | | | · |
|--|---|---|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 6.55999994 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5.13000011 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.4000001 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.19999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.5999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.39999962 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 27.7000008 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNi | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_le | дс | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_1 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | | | |
| | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | | | |
| | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | ·Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 | | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | Expected Value | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 1 | Expected Value 1 ± 0.000030517578125 | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 1 4 | Expected Value 1 ± 0.000030517578125 4 ± 0.0009765625 | Resul |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 1 | Expected Value 1 ± 0.000030517578125 | Resul |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.69 (Repeat Count = 1) | | | |
|---|---|--------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -5.55999994 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 5.55999994 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.29999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.9000001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.30000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.6999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.5 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 2.9000001 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -4.5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32 | tgt AstLmt Per1 AssistEOTGain Uls f3. | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | tgt AstLmt Per1 LimitPercentFiltered Ul | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 222 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.69999988 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -7 | -7 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.69999988 | -0.699999988 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 7.30000019 | 7.30000019 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Name AstLmt_ManualTrqCmdEn_Cnt_M_Igc AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_Igc Rte_Inst_Ap_AstLmt <_SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | Input Value 0 5.5 | | |
|---|---|---------------------------------|------|
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte_Inst_Ap_AstLmt (_SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 5.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte_Inst_Ap_AstLmt <_SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | | | |
| Rte_Inst_Ap_AstLmt <_SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1 | | |
| c_SumLimPlCmpLimit_MtrNm_f32 arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | tgt_Rte_Inst_Ap_AstLmt | | |
| | 5.98999977 | | |
| | -2.20000005 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.4000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.80000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.10000001 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 85.1999969 | | |
| gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value | -6.5 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping Mt | Nm f32 | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | <u>.</u> | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f: | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3: | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32 | tgt AstLmt Per1 PreLimitForStall MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt AstLmt Per1 PrkAssistCmd MtrNm 1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | target AstLmt Per1 TSMitCommand Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | _ | |
| | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | Mark Inc. 422 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | , |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 6 | 6 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.71 (Repeat Count = 1) | | | · | |
|---|---|--------------------------------|------|--|
| Name | Input Value | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4.5 | | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6.42000008 | | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2.0999999 | | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1.20000005 | | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.10000038 | | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.5 | | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.9000001 | | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 1 | | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 1 | | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2.0999999 | | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 0.300000012 | | | |
| tgt AstLmt Per1 PosServCCDisable Cnt Igc.value | 0 | | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.89999976 | | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6.5 | | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8 | | | |
| | 0.200000003 | | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | | | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.8000019 | | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 53.4000015 | | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.29999995 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | gc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | 2 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | _lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3: | 2 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | | |
| | tgt_AstLmt_Per1_venSpd_kpn_t32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | rNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | | | | |
| Name | Actual Value | Expected Value | Resu | |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.89999976 | 0.89999976 ± 0.000030517578125 | , | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 1 | 1 ± 0.0009765625 | • | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.300000012 | 0.300000012 ± 0.0009765625 | , | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 7.5 | 7.5 ± 0.0009765625 | | |
| | | | | |





| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.72 (Repeat Count = 1) | | | |
|--|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 4.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 6.8499999 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -2.0999999 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.899999976 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.19999981 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.5999999 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4.5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.400000006 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 5.5 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 12.6000004 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_l | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | _lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd Mt | rNm f32 | |
| Name | Actual Value | Expected Value | Res |
| | | · | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |

5.5

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

5.5 ± 0.0009765625



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.73 (Repeat Count = 1) | | | v |
|---|--|--|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -3.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.28000021 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.8999998 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.69999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.10000038 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 510.799988 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | ! | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | ls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | дс | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | • |
| | 3 | 3 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | • |
| | | 3 ± 0.0009765625 1.5 ± 0.0009765625 | • |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.74 (Repeat Count = 1) Name | Input Value | | |
|---|--|---|-------|
| | 0 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | 3.5 | | |
| AstLmt SteeringAsstDefeat Cnt M lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt Rte Inst Ap AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7.71000004 | | |
| | | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.79999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.39999962 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7.80000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.19999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 425.5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping N | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_C | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | | | |
| | tgt_AstLmt_Per1_LimitPercentFiltered_l | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | • | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | /ltrNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd | MtrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| | 0.5 | | ixeau |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | | 0.5 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2.0999999 | 2.0999999 ± 0.0009765625 | |
| | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | 0 + 0 0000765605 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0 2.20000005 | 0 ± 0.0009765625 2.20000005 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

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| Input Value | | |
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| 1 | | |
| 5 | | |
| 5.5999999 | | |
| 0.69999988 | | |
| 0 | | |
| 0.40000006 | | |
| 5 | | |
| 6 | | |
| 0.600000024 | | |
| 2.29999995 | | |
| 373.100006 | | |
| 0 | | |
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| tgt AstLmt Per1 PreLimitTorque MtrNr | n f32 | |
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| target_AstLmt_Per1_TSMitCommand_M | ltrNm_f32 | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _f32 | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | f32 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 32 | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Actual Value | Expected Value | Resu |
| 0.600000024 | 0.600000024 ± 0.000030517578125 | • |
| 5 | 5 ± 0.0009765625 | • |
| 5 | 5 ± 0.0009765625 | • |
| 3.5 | 3.5 ± 0.0009765625 | |
| 3.3 | 0.0 2 0.0000, 00020 | |
| | 1 -2.5 0 tgt_Rte_Inst_Ap_AstLmt 8.14000034 -1.70000005 -4.5 3.29999995 0.300000012 8.5 7.9000001 8.30000019 -2.0999999 1 5 0.1 5 0.1 5 5.5999999 0.699999988 0 0.400000006 5 6 0.600000024 2.29999995 373.100006 0 tgt_AstLmt_Per1_AssistCmd_MtrNm_f3: tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTLimit_MtrNr tgt_AstLmt_Per1_AssistVehSpdLimit_Mi tgt_AstLmt_Per1_CombinedDamping_M tgt_AstLmt_Per1_DefeatLimitService_Cr tgt_AstLmt_Per1_DefeatLimitService_Cr tgt_AstLmt_Per1_LimitPercentFiltered_L tgt_AstLmt_Per1_LimitGred_MtrNm tgt_AstLmt_Per1_PosServCCDisable_C tgt_AstLmt_Per1_PosServCCDisable_C tgt_AstLmt_Per1_PosServCCDisable_C tgt_AstLmt_Per1_PosServCCDisable_C tgt_AstLmt_Per1_PreLimitForGall_MtrN tgt_AstLmt_Per1_PreLimitForGall_MtrN tgt_AstLmt_Per1_PreLimitForGall_MtrN tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_PreLimitTorque_MtrNr tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLimitTerc_UIs_tgt_AstLmt_Per1_ThermalLi | 1 2.2.5 0 tgt_Rte_Inst_Ap_AstLmt 8.14000034 1.170000005 4.5 3.2999995 0.30000012 8.5 7.3000001 8.30000019 -2.0999999 1 5.5 0.0 1 1 5.5 0.0 1 1.5 5.5999999 0.69999988 0.0 0.40000006 5 6 0.600000024 2.299999988 0.0 0.40000006 5 6 0.600000024 2.29999995 373.10006 0 tgt_AstLmt_Per1_AssistCort_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistStSalit_lmt_MtrNm_f32 tgt_AstLmt_Per1_AssistStSalit_lmt_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_LmiPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_LmitPerceDisable_Cnt_lgc tgt_AstLmt_Per1_Dept-TCTCM_MtrNm_f32 tgt_AstLmt_Per1_OptrQov_MtrNm_f32 tgt_AstLmt_Per1_OptrQov_MtrNm_f32 tgt_AstLmt_Per1_Per1_Per1_Fer1_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer2_Uis_f32 tgt_AstLmt_Per1_Per1_Per1_Fer3_Uis_mtrNm_f32 tgt_AstLmt_Per1_Per1_Per1_Fer3_Uis_mtrNm_f32 tgt_AstLmt_Per1_Per1_Fer3_Uis_mtrNm_f32 tgt_AstLmt_Per1_Per1_Ter1_InitNtrNm_f32 tgt_AstLmt_Per1_Ter1_Ter1_InitNtrNm_f32 tgt_AstLmt_Per1_Ter1_Ter1_Ter1_Ter1_Ter1_InitNtrNm_f32 tgt_AstLmt_Per1_Ter1_Ter1_Ter1_Ter1_Ter1_Ter1_Ter1_ |



| Т | | | | |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.76 (Repeat Count = 1) | | | · |
|--|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 8.56999969 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.60000002 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.60000038 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.39999962 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.4000001 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 324.200012 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 1.5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | Nm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t | 732 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | m f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt AstLmt Per1 DefeatLimitService Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | • | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3: | 2 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt AstLmt Per1 PosServCCDisable Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | . | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ProLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_withNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 6.5 | 6.5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.4000001 | 2.4000001 ± 0.0009765625 | • |
| | | | |



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|--|-------|--|-------|----------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ | |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.77 (Repeat Count = 1) Name | Input Value | | |
|--|--------------------------------------|---------------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32 | -1.5 | | |
| AstLmt SteeringAsstDefeat Cnt M lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt Rte Inst Ap AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.600000024 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.69999981 | | |
| | 8.10000038 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.5 | | |
| · · | -6.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| | 7 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 7 | | |
| tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 456.299988 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.5999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrN | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mt | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_Mtr1 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_L | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCm | nd_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | |
| | 7 | 7 ± 0.0009765625 | • |
| tgt AstLmt Per1 PreLimitTorque MtrNm f32.value | | | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 6.2999971 | 6.30000019 ± 0.0009765625 | |



| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |

| Test Step 2.78 (Repeat Count = 1) | In the Malana | | |
|---|---|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | -1.39999998 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_AssistCmd_wtrNm_is2.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.1999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.60000038 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt AstLmt Per1 LrnPnCtrEnable Cnt Igc.value | 0 | | |
| tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f32.value | 8 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.5999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 345.399994 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | :_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | IC . | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | _, | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | 1_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | # N | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5.5999999 | 5.5999999 ± 0.0009765625 | , |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.5999999 | 2.5999999 ± 0.0009765625 | - - |





| Т | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.79 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -7 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 8.80000019 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.29999995 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value | 7.19999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.30000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.69999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt AstLmt Per1 OpTrqOvr MtrNm f32.value | 7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.800000012 | | |
| tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt AstLmt Per1 ThermalLimit MtrNm f32.value | 2.70000005 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 234.5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 |) | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Ci | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f: | _ · | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 4 | 4 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1.20000005 | -1.20000005 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.24000001 | -0.239999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.7000005 | 2.70000005 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.80 (Repeat Count = 1) | | | ~ |
|---|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5.5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.39999962 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.899999976 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.20000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.79999995 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 123.599998 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| | | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | • | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mi | rNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.000030517578125 | 1.000 |
| tgt AstLmt Per1 PreLimitFerCentFiltereu_0is_i3z.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitForStall_withMn_i32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgr_note.nt_1 or 1_ount.in 11qoniu_ivitriviii_loz.value | | | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2.79999995 | 2.79999995 ± 0.0009765625 | |



| Т | | | | |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.81 (Repeat Count = 1) Name | Input Value | | |
|---|---|---------------------------------|-----|
| | · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 4 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | - | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| <_SumLimPICmpLimit_MtrNm_f32 specific-street Add mt. Part. TCMitCommand. MtrNm. f32 value. | 0.300000012 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.10000002 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3.9000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.30000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.30000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -6 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.30000012 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.80000019 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 0 | | |
| pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -7 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping Mt | rNm f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3: | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mti | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | _132 | |
| pt_Rte_Inst_Ap_AsiLint_NstEnit_Feri_OptriqOvi_withVin_i32 pt_Rte_Inst_Ap_AsiLmt.AsiLmt_Peri_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | _ - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| gt AstLmt Per1 PreLimitForStall MtrNm f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 2.6400001 | 2.6400001 ± 0.0009765625 | |
| | | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.82 (Repeat Count = 1) Name | Input Value | | |
|--|--|---------------------------------|-----|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -3 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc | 0 | | |
| | - | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 0.579999983 | | |
| C_SumLimPICmpLimit_MtrNm_f32 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.89999976 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.5999999 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.4000001 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -1 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2.0999999 | | |
| pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| st_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -7 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.400000006 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6.9000001 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 512 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mi | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_le | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 1_132 | |
| pt_Rte_Inst_Ap_AsiLint_NstEnit_Feri_OptriqOvi_withVin_i32 pt_Rte_Inst_Ap_AsiLmt.AsiLmt_Peri_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f2.2 | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ · | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5.11999989 | 5.11999989 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -5.11999989 | -5.11999989 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -1.6000002 | -1.60000002 ± 0.0009765625 | |
| | | | |





| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.83 (Repeat Count = 1) Name | Input Value | | |
|--|---|--------------------------|------|
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt ManualTrqCmd MtrNm M f32 | 3 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.860000014 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.899999976 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -5 | | |
| · · · · · · · · · · · · · · · · · · · | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.6999981 | | |
| | 4.0999999 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | | | |
| | 4.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -2 1 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 125.260002 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 1 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | Igc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3. | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Ois_I32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtiNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStail_mitrNm | _ | |
| | tgt_AstLmt_Per1_PreLimitTorque_MitNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | /ltrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.5 | 0.5 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 1 | 1 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.5 | 0.5 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.0999999 | 4.0999999 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.84 (Repeat Count = 1) Name | Input Value | | |
|--|---|--|------|
| | · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 -2 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | O | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 1.13999999 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.800000012 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -6 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.20000003 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4.80000019 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.19999981 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.5999999 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| at AstLmt Per1 PowerLimitPerc Uls f32.value | 0.600000024 | | |
| gt AstLmt Per1 PrkAssistCmd MtrNm f32.value | 1 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.0999999 | | |
| at AstLmt Per1 VehSpd Kph f32.value | 75.3000031 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 2.2999995 | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | nt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | lls_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_C | nt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_is2 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | _ | |
| | tgt_AstLmt_Peri_ThermaiLimit_MtrNm_ tgt_AstLmt_Peri_TrqLimitMin_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | 04 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | Manhlon 600 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| and Andrew Dond Dond inside and the Manager 600 colors | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Peri_PreLimitForStail_witrinm_t32.value | | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| | 8.80000019 2.51999998 | 8.80000019 ± 0.0009765625 2.51999998 ± 0.0009765625 | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.85 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|------|
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt ManualTrqCmd MtrNm M f32 | 2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1.41999996 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.69999988 | | |
| tgt_AstLmt_Per1_13/inicommand_winvin_i3z.value | -5 | | |
| | -7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.300000012 4.9000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.3000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.6999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -4.19999981 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3.2999995 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 2.29999995 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.19999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 37.4000015 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3.4000001 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | ! | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt AstLmt Per1 LrnPnCtrEnable Cnt I | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrNm | | |
| | | 1_132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | f2.2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| tgt AstLmt Per1 PreLimitForStall MtrNm f32.value | 5 | 5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 3.2999995 | 3.29999995 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 2.30999994 | 2.30999994 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.30000019 | 4.30000019 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.86 (Repeat Count = 1) Name | Input Value | | |
|---|---------------------------------------|--|------|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 -16 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | • | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| <_SumLimPICmpLimit_MtrNm_f32 specific-street Act mat PortTSMitCommandMtrNm_f32 value | 1.7000005 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.600000024 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.4000001 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.80000019 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5.30000019 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| pt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 3.4000001 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -2.0999999 | | |
| at AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.800000012 | | |
| yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.30000019 | | |
| pt AstLmt Per1 VehSpd Kph f32.value | 5.37e-009 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4.5 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping I | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_ | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | <u>-</u> f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_0 | Cnt_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | <u>f</u> 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNn | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl | Nm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_I | _ | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | _ | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 TrgLimitMin MtrNm f32 | tgt AstLmt Per1 TrqLimitMin MtrNm 1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd | MtrNm f32 | |
| | | | Pa- |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| | | | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | -3.52000022 4.4000001 | -3.51999998 ± 0.0009765625 4.4000001 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Name | | |
|--|-----|-----|
| | | |
| Asturn Seleming-Asturn Feet Indian Ind | | |
| No. Inc. Ap. Asturt | | |
| SamtumPriCroptumi, Methin 12 1,98000002 1,9800002 1,9800002 1,9800002 1,9800002 1,9800002 1,9800002 1,9800002 1,98000002 1,98000002 1,98000002 1,980000000 1,98000000000000000000000000000000000000 | | |
| gastumt Perf _ ISMCCommand Mirkm _ ISZ value -7 | | |
| J. AstL.M. Perl. AssistCTOM_MINN_DIS_Value 7 1 1 1 1 1 1 1 1 1 | | |
| Jaktim_Perl_AssistEOTLome_Unithm_E22 value 1 | | |
| Jastim Perl AssistStallim, Within (32 value 1, astim Perl AssistStallim, Within (32 value 4, 5 0000001 4, astim Perl AssistStallim, Within (32 value 4, 5 0000001 4, astim Perl AssistStallim, Within (32 value 4, 5 0000001 4, astim Perl CombinedDamping, Minhin (32 value 1 | | |
| J.AstLim. Perl AssistColl.mil., Minn. 122 value 5,0099999 1, AstLim. Perl AssistColl.mil., Minn. 122 value 4,9000001 1, AstLim. Perl AssistColl.mil., Minn. 122 value 4,9000001 1, AstLim. Perl LimitedReturn. Minn. 122 value 0, 899999976 1, AstLim. Perl LimitedReturn. Minn. 122 value 0, 1, AstLim. Perl LimitedReturn. Minn. 122 value 4, 5 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, 4, | | |
| | | |
| J. AstLim. Perl CombineDamping, Minhthm, 122 value 4,0000001 J. AstLim. Perl CombineDamping, Minhthm, 122 value 1, J. AstLim. Perl LimitedReturn, Unifidm, 122 value 1, J. AstLim. Perl LimitedPerlourn, 122 value 1, J. AstLim. Perl LimitedPerlourn, 122 value 1, J. AstLim. Perl Complete Republic Limited Return, 122 value 1, J. AstLim. Perl Complete Republic Limited Return, 122 value 0,800009976 J. AstLim. Perl PosSenCCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl PosSenCDisable, Cni. Igc. value 0,800009976 1, J. AstLim. Perl Lim. Perl Lim. Igc. value 0,8000009976 1, J. AstLim. Perl Lim. Perl Lim. Igc. value 0,80000000000000000000000000000000000 | | |
| AstLmt Pert CombinedDamping MiNhm 52 value | | |
| | | |
| Jastim_Perl_LimisdReum_MinNm_52 value 1 | | |
| Ast.ml Pert LimPhotCrCDosable, Cnt. gov.value | | |
| AstLmL Perl LimPhoChTGmd MrNm, 132 value | | |
| Ast.ml_Perl_OrpTo/mMhm_52 value | | |
| AstLmt_Pert_OpTrqOv_MrNm_G32.value | | |
| J. AstLimt. Pert DosSen/CDDisable Cnt Lgc. value 0 0 0 0 0 0 0 0 0 | | |
| J. AstLIM_Pert PossSerVCCDisable Cnt. Joz value | | |
| 2, AstLmt Pert PrivAssistCord_MthVm 522 value 4.5 2, AstLmt Pert PrivAssistCord_MthVm 522 value 4.5 3, AstLmt Pert ThermalLimitPerc, Uls 522 value 4.5 3, AstLmt Pert ThermalLimitPerc, Uls 522 value 0.89999976 3, AstLmt Pert ThermalLimit MthVm 522 value 5.80000096-008 3, AstLmt Pert WheelimbalanceCmd_MthVm 522 value 5.80000096-008 3, AstLmt Pert WheelimbalanceCmd_MthVm 522 value 5.80000096-008 3, AstLmt Pert MheelimbalanceCmd_MthVm 522 value 5.80000096-008 4, AstLmt Pert MheelimbalanceCmd_MthVm 522 value 5.80000096-008 5, AstLmt Pert MheelimbalanceCmd_MthVm 522 value 5.80000096-008 5, AstLmt Pert MheelimbalanceCmd_MthVm 522 value 5.80000096-008 5, AstLmt Pert MheelimbalanceCmd_MthVm 522 value 5.80000096-008 6, AstLmt Pert AssistEOTOamping_MthVm 522 value 5.80000096-008 7, AstLmt Pert AssistEOTOamping_MthVm 522 value 5.80000096-008 7, AstLmt Pert Pert AssistEOTOamping_MthVm 522 value 5.80000096-008 7, AstLmt Pert Per | | |
| AstLmt Pert PriAssistCrd_MtrNm f32 value 4.5 AstLmt Pert PriLoropportd, MtrNm f32 value 4.5 AstLmt Pert Thermallimitiper Us f32 value 0.889999976 AstLmt Pert Thermallimitiper Us f32 value 0.889999976 AstLmt Pert Thermallimitiper Us f32 value 5.38000009e-008 AstLmt Pert WheelimbalanacCmd_MtrNm f32 value 5.5899999 AstLmt Pert WheelimbalanacCmd_MtrNm f32 value 5.5899999 AstLmt Pert AssistComd_MtrNm f32 value 5.5899999 AstLmt Pert AssistComd_MtrNm f32 AstLmt Pert Pert AssistComd_MtrNm f32 AstLmt Pert Pert AssistComd_MtrNm f32 AstLmt | | |
| AstLmt_Pert_ThermalLimitPierc_Uis_27 saviue | | |
| AstLmt_Pert_ThermalLimit, MrNm_132 value 7.4000001 7.40000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.4000001 7.40000001 7.40000001 7.40000001 7.40000001 7.40000001 7.40000001 7.400000001 7.400000001 7.4000000001 7.4000000001 7.4000000001 7.4000000001 7.4000000000000000000000000000000000000 | | |
| AstLmt Pert ThermalLimit MirNm 152 value | | |
| J. AstLmt. Per1 ThermalLimit, Mith/m_[32 value 5.380000099-008 J. AstLmt. Per1 VehSpd, Kph, F32 value 5.380000099-008 J. AstLmt. Per1 AssistCond, Mith/m_[32 Jgt, AstLmt. Per1 AssistCond, Mith/m_[32 Jgt, AstLmt. Per1 AssistCond, Mith/m_[32 Jgt, AstLmt. Per1 AssistEOTDamping_Mith/m_[32 Jgt, AstLmt. Per1 DefeatLimitService_Cot. Jgc Jgt, AstLmt. Per1 LimitPercentFiltered_Uis_132 Jgt, AstLmt. Per1 DottpCtrCmd_Mith/m_[32 Jgt, AstLmt. Per1 DottpCtrCmd_Mith/m_[32 Jgt, AstLmt. Per1 DottpCtrCmd_Mith/m_[32 Jgt, AstLmt. Per1 PowerLimitPerc_Uis_132 Jgt, AstLmt. Per1 PreLimitForGuis_Mit | | |
| AstLmt_Per1_VehSpd_Kph_r32.value | | |
| 1, Rist_Inst_Ap_AstLmtAstLmt_Peri_AssistCond_MtrNm_f32 tgt_AstLmt_Peri_AssistCond_MtrNm_f32 tgt_AstLmt_Reri_AssistCond_MtrNm_f32 tgt_AstLmt_Reri_AssistCond_mtrNm_f32 tgt_AstLmt_Reri_AssistCond_mtrNm_f32 tgt_AstLmt_Peri_AssistCond_mtrNm_f32 tgt_AstLmt_Peri_DefeatLimitService_Cont_lgc tgt_AstLmt_Peri_DefeatLimitService_Cont_lgc tgt_AstLmt_Peri_DefeatLimitService_Cont_lgc tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_LimitPercond_mtrNm_f32 tgt_AstLmt_Peri_DimitPercond_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_DopTrqor_mtrNm_f32 tgt_AstLmt_Peri_PrelimitTorque_mtrNm_f32 tgt_Ast | | |
| | | |
| 13 Rte Inst Ap AstLmt AstLmt Pert AssistEOTDamping MtrNm f32 tgt AstLmt AstLmt Pert AssistEOTDamping MtrNm f32 tgt AstLmt Pert AssistStallLimit MtrNm f32 tgt AstLmt Pert DefeatLimitSeorece Cnt tgc tgt AstLmt Pert DefeatLimitSeorece Cnt tgc tgt AstLmt Pert DefeatLimitSeorece Cnt tgc tgt AstLmt Pert LimitPercentFillered Uls f32 tgt AstLmt Pert DefeatLimitSeorece Cnt tgc tgt AstLmt | | |
| gt Rte Inst Ap, AstLmt AstLmt Per1 AssistEOTGain, Uls 32 tgt AstLmt, Per1 AssistEOTGain, Uls 32 gt Rte Inst Ap, AstLmt AstLmt Per1 AssistEOTLimit, MtrNm, f32 tgt AstLmt, Per1 AssistEOTLimit, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 AssistStallLimit, MtrNm, f32 tgt AstLmt, Per1 AssistStallLimit, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 AssistStallLimit, MtrNm, f32 tgt AstLmt, Per1 CombinedDamping, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 DefeatLimitService, Cnt. Igo tgt AstLmt, Per1 DefeatLimitService, Cnt. Igo gt Rte Inst Ap, AstLmt AstLmt, Per1 LimitedReturn, MtrNm, f32 tgt AstLmt, Per1 LimitedReturn, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 LimitedReturn, MtrNm, f32 tgt AstLmt, Per1 LimitedReturn, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 LimPnCtrCCDisable, Cnt. Igo tgt AstLmt, Per1 LimPnCtrCCDisable, Cnt. Igo gt Rte Inst Ap, AstLmt AstLmt, Per1 LimPnCtrTond, MtrNm, f32 tgt AstLmt, Per1 LimPnCtrCCDisable, Cnt. Igo gt Rte Inst Ap, AstLmt AstLmt, Per1 Diftrqovr, MtrNm, f32 tgt AstLmt, Per1 LimPnCtrCTOnd, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 Optrqovr, MtrNm, f32 tgt AstLmt, Per1 Diftrqovr, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 PowerLimitPerc, Uls f32 tgt AstLmt, Per1 Diftrqovr, MtrNm, f32 gt Rte Inst Ap, AstLmt AstLmt, Per1 PreLimitForque, MtrNm, f32 tgt AstLmt, Per1 PreLimitForStall, MtrNm, f32 | | |
| 12 Rte Inst Ap_AstLmt AstLmt Perl AssistEOTLimit MtrNm f32 tg AstLmt Perl AssistSallLimit MtrNm f32 tg AstLmt Perl AssistSallLimit MtrNm f32 tg AstLmt Perl AssistStallLimit MtrNm f32 tg AstLmt Perl CombinedDamping MtrNm f32 tg AstLmt Perl DefeatLimitService Cnt tg tg AstLmt Perl DefeatLimitService tg tg AstLmt Perl D | | |
| gt Rte Inst Ap AstLmt AstLmt Pert AssistStallLimit MtrNm f32 gt Rte Inst Ap AstLmt AstLmt Pert AssistVehSpdLimit MtrNm f32 gt Rte Inst Ap AstLmt AstLmt Pert CombinedDamping MtrNm f32 gt AstLmt Pert AssistVehSpdLimit MtrNm f32 gt Rte Inst Ap AstLmt AstLmt Pert DefeatLimitService Cnt gc gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt Rte Inst Ap AstLmt AstLmt Pert LimitPercentFiltered Uis f32 gt AstLmt Pert DefeatLimitService Uis f32 gt AstLmt Pert DefeatLimitService Uis f32 gt AstLmt Pert DefeatLimitPercentFiltered Uis f32 gt AstLmt Pert DefeatLmitService Uis f32 gt AstLmt Pert DefeatLmitService Uis f32 gt AstLmt Pert DefeatLmitSer | | |
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| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DougrampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForGome_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForGome_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SimLommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_TremmalLimitPerc_Uls_f32 tgt_AstLmt_Per1_Verspd_Kph_f32 tgt_AstLmt_Per1_Verspd_Kph_f32 tgt_AstLmt_Per1_UmpercontFiltered_Uls_f32.value Actual Value Expected Val | | |
| | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt.Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_AstLmt.Per1_LmPnCtrCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_AstLmt.Per1_LmPnCtrTcmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CpTrqovr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqovr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPorc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPorc_Uls_f32 tgt_AstLmt_Per1_PowerLimitForc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCred_MtrNm_f32 tgt_AstLmt_Per1_PullCompCred_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Tof_Tof_MtrNm_f32 tgt_AstLmt_Per1_Tof_MiltCompand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Tof_Tof_MtrNm_f32 tgt_AstLmt_Per1_Tof_MiltMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 <td< td=""><td></td><td></td></td<> | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_DimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_DimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_Powert.imitedReturn_MtrNm_f32 tgt_AstLmt_Per1_Powert.imitedReturn_MtrNm_f32 tgt_AstLmt_Per1_Powert.imitedReturn_MtrNm_f32 tgt_AstLmt_Per1_Powert.imitedReturn_MtrNm_f32 tgt_AstLmt_Per1_TimermalLimitedReturn_MtrNm_f32 tgt_AstL | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PoservCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PoservCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PoservCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PoservLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_Pret_Pret_Pret_Pret_Pret_Pret_Pret | | |
| tritle inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_igc tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_igc tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uis_f32 tgt_AstLmt_Per1_OutputRampMult_Uis_f32 tgt_AstLmt_Per1_OutputRampMult_Uis_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_igc tgt_AstLmt_Per1_P | | |
| trace Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_TotTorque_MtrNm_f32 tgt_AstLmt_Per1_TotTorque_MtrNm_f32 tgt_AstLmt_Per1_TotTorque_MtrNm_f32 tgt_AstLmt_Per1_TotTorque_MtrNm_f32 tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_TotPosServCCDisable | | |
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| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrepLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_TrepLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value <t< td=""><td></td><td></td></t<> | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrepLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrepLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_TrepLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | |
| gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt. Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 gt. AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.89999976 ± 0.00003051757812 gt. AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0000765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TremalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForQue_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name Actual Value Expected Value gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.89999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.89999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.89999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Iame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Iame Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| Name Actual Value Expected Value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.899999976 0.899999976 ± 0.00003051757812 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | Res |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | | Nes |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 8.80000019 8.80000019 ± 0.0009765625 | 120 | |
| | | |
| 3T ASTLMT Peri Sumlimitacma Mtrnm 132, value 8.80000019 8.80000019 8.80000019 + 0.0009765625 | | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 4.5 4.5 ± 0.0009765625 | | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.88 (Repeat Count = 1) Name | Input Value | | |
|---|--|---------------------------------|-------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32 | 0 | | |
| AstLmt SteeringAsstDefeat Cnt M Igc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.25999999 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.40000006 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8 | | |
| tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.1999981 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.5999999 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -7 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 3.77999996e-008 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtrl | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3. | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | Mahlim 600 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | _ · |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | | 0 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.89 (Repeat Count = 1) Name | Input Value | | |
|--|--|--|------|
| | | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| s_SumLimPlCmpLimit_MtrNm_f32 | 2.53999996 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.300000012 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.30000019 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.69999981 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.0999999 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.20000003 | | |
| pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.2999995 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.20000003 | | |
| r | 7.5999999 | | |
| yt_AstLmt_Per1_VehSpd_Kph_f32.value | 6.38000008e-009 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrVff | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mi | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | <u>_</u> f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | n_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | m_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | trNm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | _f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm f32 | |
| lame | Actual Value | Expected Value | Res |
| | 0.200000003 | 0.200000003 ± 0.000030517578125 | 1105 |
| gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value | | | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1.5 | 1.5 ± 0.0009765625 | |
| | -1.5 | -1.5 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | 0.200000040 + 0.00070707 | |
| gt_AstLmt_Peri_PreLimitForque_MtrNm_r32.value gt_AstLmt_Peri_SumLimTrqCmd_MtrNm_r32.value gt_AstLmt_Peri_TrqLimitMin_MtrNm_r32.value | -0.300000012 4.6999981 | -0.300000012 ± 0.0009765625 4.69999981 ± 0.0009765625 | |

TEST DETAILS REPORT

2014-09-05, 17:30:46+0530



AstLmt_Per1

T
Actual Function Count Expected Function Count



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.91 (Repeat Count = 1) Name | Input Value | | |
|--|--|---------------------------------|------|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| S_SumLimPlCmpLimit_MtrNm_f32 | 3.0999999 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -0.100000001 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.30000019 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.69999981 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.0999999 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| at AstLmt Per1 PullCompCmd MtrNm f32.value | 3.2999995 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5999999 | | |
| t_AstLmt_Per1_VehSpd_Kph_f32.value | 503.5 | | |
| yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | Im f32 | |
| | | MIII_132 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | - • | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | _f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | rNm f32 | |
| | | 1 | Б. |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -3 | -3 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.600000024 | -0.600000024 ± 0.0009765625 | |
| gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.69999981 | 4.69999981 ± 0.0009765625 | |
| | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.92 (Repeat Count = 1) | | | • |
|--|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.38000011 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| tgt_AstLint_Per1_AssistEOTGain_Ois_i32.value | 5.400001 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.80000019 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 5.19999981 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.300000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.6999981 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 37.5999985 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | f2.7 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLlllllt_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtrl | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | 1 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | 1 |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |





| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.93 (Repeat Count = 1) Name | Input Value | | |
|--|--|--|------|
| | · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 2.25999999 | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.60000024 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.19999981 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.5999999 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -7 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.100000001 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6.5 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.100000001 | | |
| pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5 | | |
| pt_AstLmt_Per1_VehSpd_Kph_f32.value | 3.77999996e-008 | | |
| pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping MtrNr | n f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3: | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg | | |
| pt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32 | | | |
| | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_l | gc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3: | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| | | | |
| | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg | ıc | |
| | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | 32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 | 32 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_Ig tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | 32 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | 32 2 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | 32 2 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3 | 32 2 32 1_f32 | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSmitCommand_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | 32 2 32 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | 32 2 32 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | 32 2 32 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_TsMitCommand_MtrNm_tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 | 52 2 32 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_TSMitCommand_MtrNm_tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrI | 52 2 32 n_f32 | Rec |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm tgt_AstLmt_Per1_TsMitCommand_MtrNm tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN Actual Value | 52 2 32 1_f32 Nm_f32 Expected Value | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtrl Actual Value 0.1000000001 | 22 2 32 1_f32 Nm_f32 Expected Value 0.100000001 ± 0.000030517578125 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtrl Actual Value 0.100000001 8 | 22 22 32 32 32 32 32 32 32 32 32 32 42 43 43 43 43 44 44 44 44 44 44 44 44 44 | Resi |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 lt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreVAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtrl Actual Value 0.1000000001 | 22 2 32 1_f32 Nm_f32 Expected Value 0.100000001 ± 0.000030517578125 | Res |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.94 (Repeat Count = 1) | | | |
|---|---|--|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 2.53999996 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.69999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5999999 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 6.38000008e-009 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3: | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | :_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | C | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cn | t_lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | 1_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_f | MtrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| | | · · | Rest |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.20000003 | 0.200000003 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 1.5 | 1.5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -1.5 | -1.5 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | -0.300000012 4.69999981 | -0.300000012 ± 0.0009765625 4.69999981 ± 0.0009765625 | |



| Т | | | | |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.95 (Repeat Count = 1) | Immut Value | | |
|---|--|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc | 1.60000002 | | |
| | 1 tot Dto Inst An Astlet | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt 2.81999993 | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.80000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.4000001 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.80000019 5.19999981 | | |
| | | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.30000012 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.69999981 | | |
| gt_AstLmt_Per1_VehSpd_Kph_f32.value | 7.27999989e-008 | | |
| gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNn | _ | |
| ggt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | _ · | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | - | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNn | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_t | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 22 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | | |
| Name | Actual Value | Expected Value | Resi |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.30000012 | 0.300000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 1.60000002 | 1.60000002 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0.480000019 | 0.479999989 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.80000019 | 4.80000019 ± 0.0009765625 | |





| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.96 (Repeat Count = 1) | | | ~ |
|--|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 3.0999999 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1.5 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.30000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.69999981 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.0999999 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.20000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3.29999995 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.20000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.5999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 503.5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mtr | Nm f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtrl | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cni | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_t | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd M | ltrNm f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | Resul |
| | 3 | | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | -3 | 3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | | -3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0.600000024 | -0.600000024 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.69999981 | 4.69999981 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.97 (Repeat Count = 1) | Input Value | | |
|---|---|---|------|
| Name Acti mt ManualTraCmdEn Cat M Igo | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -7 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPlCmpLimit_MtrNm_f32 | 3.38000011 | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1.5 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 2 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.800000012 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5.4000001 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.80000019 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5.19999981 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 1 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.300000012 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.300000012 | | |
| pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5.5999999 | | |
| pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.30000012 | | |
| r | 7.69999981 | | |
| yt_AstLmt_Per1_VehSpd_Kph_f32.value | 37.5999985 | | |
| t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 111_132 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| | | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | lm_f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | 2 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mi | rNm f32 | |
| lame | Actual Value | Expected Value | Res |
| | 0.300000012 | · · | IVE2 |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | | 0.300000012 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| at Apti ant Dard Cumi im Tracond Mahler 500 miles | 0 | 0 + 0 0000765605 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0 4.80000019 | 0 ± 0.0009765625 4.80000019 ± 0.0009765625 | |



| Т | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | Rte Call AstLmt Per1 CP1 CheckpointReached | 1 | ✓ |

| Test Step 2.98 (Repeat Count = 1) Name | Input Value | | |
|---|---------------------------------------|---------------------------|-----|
| | · · | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | -16 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 | | | |
| arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 0 | | |
| gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0 | | |
| gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 0 | | |
| pt_AstLmt_Per1_VehSpd_Kph_f32.value | 0 | | |
| pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8.80000019 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f | 32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping | MtrNm f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrN | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_N | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_I | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNr | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_ | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt AstLmt Per1 LrnPnCtrTCmd MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3 | _ | |
| gt_Rte_Inst_Ap_AsiLinit.AsiLinit_rei1_OptriqOvi_withthin_i32 gt_Rte_Inst_Ap_AsiLinit.AsiLinit_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls | | |
| | | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_0 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_Mtrl | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrN | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNi | | |
| gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_Mtr | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_ | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_UI | _ | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_ | f32 | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmc | d_MtrNm_f32 | |
| Name | Actual Value | Expected Value | Res |
| gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0 | 0 ± 0.000030517578125 | |
| gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -3 | -3 ± 0.0009765625 | |
| gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0 | 0 ± 0.0009765625 | |
| UL ASILIIL FELL SUITLIITTUCITU MILIMIT 132.Value | | | |



| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.99 (Repeat Count = 1) Name | Input Value | | |
|---|---|---------------------------|------|
| | 1 | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_Igc AstLmt ManualTrqCmd MtrNm M f32 | 15.9995003 | | |
| AstLmt_ManualTrqCmd_MinnT_M_132 AstLmt_SteeringAsstDefeat Cnt M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 8.80000019 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 512 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNi | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_le | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | N. 60 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |

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AstLmt_Per1

| T | | | | V |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ~ |



Test Case 3: Path Coverage Test

```
Performance Metrics:
Specification
                           (With "None" Instrumentation and WithPS
                           Environment)
                          CPU Cycles:
TS3.1 1985.00 cycles
TS3.2 1867.00 cycles
TS3.3 1821.00 cycles
TS3.4 1745.00 cycles
TS3.5 1747.00 cycles
                          TS3.5 1747.00 cycles
TS3.6 1723.00 cycles
TS3.7 1747.00 cycles
TS3.7 1747.00 cycles
TS3.8 1723.00 cycles
TS3.10 1723.00 cycles
TS3.11 1747.00 cycles
TS3.12 1723.00 cycles
TS3.13 1770.00 cycles
TS3.14 1804.00 cycles
TS3.15 1851.00 cycles
TS3.16 1790.00 cycles
TS3.17 1757.00 cycles
                          TS3.18 1702.00 cycles
TS3.19 1718.00 cycles
TS3.20 1745.00 cycles
Description
                          Vector Description:
                          TS3.1"( PosServCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          ( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          (PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = True
                          ( (ManualTrqCmdEn_Cnt_M_lgc == TRUE) =False && (VehSpd_Kph_T_f32 < FLT_EPSILON) )
                          and
                          ( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=True
                          and
                          (PreLimitTorque_MtrNm_T_f32 >= 0)=True
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=True
                          ( DefeatLimitService_Cnt_T_lgc == TRUE )=True
                          and
                          TS3.2"(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = False
                          (PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = false
                          and
                          ((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
                          and
                          (Sign_f32_m(AssistPowerLimit_MtrNm_T_f32) >= D_ZERO_CNT_S8)=False and(ThermalLimitPerc_Uls_T_f32 >= PowerLimitPerc_Uls_T_f32) = True" TS3.3"((HwTrqPolarity_Cnt_T_f32 * SignMRFMtrVel_Cnt_T_f32 * SignPreLimitForPower_Cnt_T_f32) >= D_ZERO_ULS_F32)=False
                          and
                          (AssistPowerLimit_MtrNm_T_f32 >= Abs_f32_m(k_SumLimOvrSpdMtrTrq2QLmt_MtrNm_f32))=False
                          (AbsPreLimitForPower_MtrNm_T_f32 <= ForPowerLimit_MtrNm_T_f32 )=True" TS3.4"( DefeatLimitService_Cnt_T_lgc == TRUE )= False
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = True
                          and
                          (AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = True"
                           TS3.5"( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = False
                          ànd
                          (PreLimitForStall_MtrNm_T_f32 <= 0)=False
                          and
                          (PreLimitForPower_MtrNm_T_f32 <= 0)=False"
TS3.6"( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitForPower_MtrNm_T_f32 <= AssistPowerLimit_MtrNm_T_f32 )=False" TS3.7( PosServCCDisable_Cnt_T_lgc == TRUE )=False TS3.8"(PreLimitTorque_MtrNm_T_f32 >= 8.8) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 >= 8.8)=True"
                           TS3.9"(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=False
                          TS3.10(PostLimitForAssistSumCC_MtrNm_T_f32 >= 8.8)=True
                          TS3.11"(PreLimitTorque_MtrNm_T_f32 >= 0)=False
                          and
                          and(PreLimitTorque_MtrNm_T_f32 <= -8.8) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
TS3.13(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = False
TS3.14(LimitPercentFiltered_Uls_T_f32 >= 1)=True
TS3.15(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = False
TS3.16(LimitPercentFiltered_Uls_T_f32 <= 0) = True
TS3.17"( (ManualTrqCmdEn_Cnt_M_lgc == TRUE)=True && (VehSpd_Kph_T_f32 < FLT_EPSILON) )=True
(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=False"
TS3.18(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=True
TS3.19(TrqLimitMin_MtrNm_T_f32 >= 8.8)=True
TS3.20(PreLimitForStall_MtrNm_T_f32 <= 0)=True
```

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| Test Step 3.1 (Repeat Count = 1) | | | ✓ |
|--|---|---------------------------------|----------|
| Name | Input Value | | |
| AstLmt ManualTrqCmdEn Cnt M lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5 | | |
| tgt AstLmt Per1 OpTrqOvr MtrNm f32.value | 7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 1 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_I | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | .lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3. | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_i SMitCommand_mtrNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_i SMitCommand_mtrNm_132 | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3. | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt AstLmt Per1 TrqLimitMin MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | rNm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6 | 6 ± 0.0009765625 | ~ |
| | | | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.2 (Repeat Count = 1) | | | ✓ |
|--|--|---------------------------------|--------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -1 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 6 | | |
| tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_Mtrl | Nm f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | VIII_132 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt AstLmt Per1 LimitedReturn MtrNm f3 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | _lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mtrl | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_M | | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 6 | 6 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | • |





| Test Step 3.3 (Repeat Count = 1) | | | • |
|---|---|--------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value | 1 | | |
| | 1 | | |
| tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.40000006 | | |
| | | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 8 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | Nm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | :_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | s_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_t | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | ıc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | — · | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | n f32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32 | tgt AstLmt Per1 PreLimitTorque MtrNm | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32 | target_AstLmt_Per1_TSMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt AstLmt Per1 ThermalLimitPerc Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt AstLmt Per1 VehSpd Kph f32 | | |
| | | Marklan 500 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | | - |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.69999988 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 4 | 4 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.4 (Repeat Count = 1) | | | ~ |
|---|--|---------------------------------|---------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| tgt AstLmt Per1 LmPnCtrCCDisable Cnt lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5 | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 0.30000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.30000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| tgt AstLmt Per1 PullCompCmd MtrNm f32.value | 4 | | |
| tgt AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 348 | | |
| | 5 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cr | ıt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mi | _ | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt AstLmt Per1 WheelImbalanceCmd | MtrNm f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | . tosui |
| | | | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | |

| T | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |





| Test Step 3.5 (Repeat Count = 1) | | | ✓ |
|---|--|---------------------------------|----------|
| Name | Input Value | | |
| AstLmt ManualTrqCmdEn Cnt M lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 3 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 7 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt AstLmt Per1 LrnPnCtrCCDisable Cnt Igc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| tgt AstLmt Per1 OpTrqOvr MtrNm f32.value | 4 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 34 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | lm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt AstLmt Per1 TrqLimitMin MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mi | rNm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | _ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 5.5 | 5.5 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 2.75 | 2.75 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | ~ |
| | | | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.6 (Repeat Count = 1) | | | ✓ |
|--|---|---------------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -3 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte Inst Ap AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value | 5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt AstLmt Per1 ThermalLimit MtrNm f32.value | 6 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 245 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | - | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNi | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_le | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | 222 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt AstLmt Per1 PrkAssistCmd MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt AstLmt Per1 SumLimTrqCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc UIs f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | Nm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 4.9000001 | 4.9000001 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4 | 4 ± 0.0009765625 | ~ |
| | | | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 CheckpointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | _ |





| Test Step 3.7 (Repeat Count = 1) | | | • |
|---|--|--------------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 4 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt AstLmt Per1 PowerLimitPerc Uls f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt AstLmt Per1 PullCompCmd MtrNm f32.value | 6 | | |
| tgt AstLmt Per1 ThermalLimitPerc Uls f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 166 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| | tgt AstLmt Per1 AssistEOTDamping M | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_C | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.40000006 | 0.400000006 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 2 | 2 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009763625 0 ± 0.0009765625 | |
| | 3 | 3 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | | 3 ± 0.0003103023 | |

| T | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |





| Test Step 3.8 (Repeat Count = 1) | | | • |
|---|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -4 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 437 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | <u>1_</u> f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | ls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | дс | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 7.04000044 | 7.03999996 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 6 | 6 ± 0.0009765625 | |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | • |





| Test Step 3.9 (Repeat Count = 1) | | | • |
|---|--|---------------------------------|-------------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 6 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 7 | | |
| tgt AstLmt Per1 AssistStallLimit MtrNm f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 6 | | |
| tgt AstLmt Per1 OutputRampMult Uls f32.value | 1 | | |
| tgt AstLmt Per1 PosServCCDisable Cnt Igc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.600000024 | | |
| | 6 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 367 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_N | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Ci | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_L | lls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_0 | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_ | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt AstLmt Per1 ThermalLimit MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_veriSpd_Rpin_i32 | MtrNm f32 | |
| | | | Daarr |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.600000024 | 0.600000024 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | · · · · · · |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 CheckpointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | _ |





| Test Step 3.10 (Repeat Count = 1) | | | ~ |
|---|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 5 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 75 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_M | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | <u>1_</u> f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | ls_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | <u>f</u> 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_C | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l | дс | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_M | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.699999988 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4 | 4 ± 0.0009765625 | |

| T | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | • |





| Test Step 3.11 (Repeat Count = 1) | | | ✓ |
|--|--|--------------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 6 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte Inst Ap AstLmt | tgt Rte Inst Ap AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 7 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 4 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.600000024 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.40000006 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 24 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 7 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNn | 1_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNr | n_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg | gc | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_l | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrNi | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | N | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | 1 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.69999988 | 0.69999988 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | ✓ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 3 | 3 ± 0.0009765625 | |
| | | | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.12 (Repeat Count = 1) | | | ✓ |
|---|--|---------------------------------|----------|
| Name | Input Value | | |
| AstLmt ManualTrqCmdEn Cnt M lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.800000012 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.69999988 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.20000005 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value | 1 | | |
| tgt AstLmt Per1 LimitedReturn MtrNm f32.value | -2.0999999 | | |
| tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value | 0 | | |
| tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -4.5 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -1.20000005 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 2.7999995 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 34 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | 622 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_t32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNi | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | 00 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PreLimit10rque_MitNim_is | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_i | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr | Nm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 5 | 5 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -4.5 | -4.5 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -3.60000014 | -3.5999999 ± 0.0009765625 | V |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.800000012 | 0.800000012 ± 0.0009765625 | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.13 (Repeat Count = 1) | | | • |
|---|--|---------------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 7.5 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPICmpLimit_MtrNm_f32 | 0.200000003 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 1.10000002 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 2.5 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.200000003 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 3.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 75 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 6 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mi | trNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f3 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | ı_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mt | rNm_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cn | t_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_U | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci | nt_lgc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_t | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_PosServCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3. | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_ | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0.800000012 | 0.800000012 ± 0.0009765625 | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.14 (Repeat Count = 1) | | | ✓ |
|---|--|--------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.899999976 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 3.2999995 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.9000001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 6.30000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 0.5 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0.5 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | |
| tgt AstLmt Per1 PullCompCmd MtrNm f32.value | 3 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 26 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | m_f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_I | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_ | lgc | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3 | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | gc | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStail_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PreLimit1orque_mtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_rer1_runcompenia_wtrvii_rs | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3. | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32 | tgt AstLmt Per1 ThermalLimit MtrNm f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_Mt | rNm_f32 | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 7 | 7 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 3.5 | 3.5 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.9000001 | 5.9000001 ± 0.0009765625 | ~ |
| | - | | |

| T | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astl mt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.15 (Repeat Count = 1) | | | · · |
|---|---|---------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.80000019 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 1 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -3 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 512 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm | _f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | /ltrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 8 | 8 ± 0.0009765625 | |

| T | | | | | |
|--|-------|--|-------|----------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ | |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ | |





| Test Step 3.16 (Repeat Count = 1) | | | • |
|--|---|---------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.80000019 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -8.80000019 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0 | | |
| tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value | -8.80000019 | | |
| tgt AstLmt Per1 PullCompCmd MtrNm f32.value | -8.80000019 | | |
| tgt AstLmt Per1 ThermalLimitPerc Uls f32.value | 0 | | |
| tgt AstLmt Per1 ThermalLimit MtrNm f32.value | 0 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 0 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt AstLmt Per1 AssistEOTDamping Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32 | tgt AstLmt Per1 AssistEOTLimit MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| | | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrgOvr_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | _132 | |
| | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | 22 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | — · | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | 2 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_I | MtrNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0 | 0 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -2 | -2 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -0 | 0 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | |

| Τ | | | | ✓ |
|---|-------|---|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |





| Test Step 3.17 (Repeat Count = 1) | | | V |
|--|--|---------------------------------|---------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.80000019 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.800000012 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.400001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 5 5000000 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 0.800000012 3 | | |
| | | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | -2.0999999 0.800000012 | | |
| | 7.30000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 5.37e-009 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4 | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Peri AssistEOTDamping MtrNm f32 | tgt AstLmt Per1 Assistentumining Mtr | Nm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | _ | |
| tgt Rte Inst Ap AstLmt.AstLmt Peri AssistEOTLimit MtrNm f32 | tgt AstLmt Per1 AssistEOTLimit MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtrl | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgr | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f3 | 32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32 | tgt AstLmt Per1 PreLimitTorque MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt AstLmt Per1 PullCompCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target AstLmt Per1 TSMitCommand Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt AstLmt Per1 ThermalLimit MtrNm f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | ltrNm f32 | |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | . tosui |
| tgt_AstLmt_Per1_PreLimitFerCentFileFer0_Uis_i32.value | 8.80000012 | 8.80000012 ± 0.00030317376123 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -3.52000022 | -3.51999998 ± 0.0009765625 | |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 4.400001 | 4.400001 ± 0.0009765625 | |
| SC SCORE OF LINGERHOUSENLING VIOLENCE | 1.7000001 | 1.7000001 ± 0.0000100020 | |

| T | | | | | |
|--|-------|--|-------|----------|--|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ | |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ | |





| Test Step 3.18 (Repeat Count = 1) | | | • |
|--|---|---------------------------------|------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -8.80000019 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 0.800000012 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 0 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | -6 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | -8 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.400000006 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 4.400001 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 4.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 5.5999999 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 0.800000013 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -2.0999999 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.800000012 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.30000012 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 5.37e-009 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 4 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTDamping MtrNm f32 | tgt AstLmt Per1 AssistEOTDamping Mt | rNm f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 | - | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32 | tgt AstLmt Per1 AssistEOTLimit MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_Mtr | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Ul | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_t | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32 | tgt_AstLmt_Per1_OutputRampMult_Uls_f | 32 | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc | tgt_AstLmt_Per1_PosServCCDisable_Cn | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32 | tgt AstLmt Per1 PreLimitTorque MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt AstLmt Per1 PullCompCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_Mti | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_N | /trNm_f32 | |
| Name | Actual Value | Expected Value | Resu |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.000030517578125 | |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | -8.80000019 | -8.80000019 ± 0.0009765625 | |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | -3.52000022 | -3.51999998 ± 0.0009765625 | |
| <u> </u> | 4.400001 | | |

| Τ | | | | ✓ |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | • |





| Test Step 3.19 (Repeat Count = 1) | | | ~ |
|--|--|---------------------------|-------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 15.9995003 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k_SumLimPlCmpLimit_MtrNm_f32 | 8.80000019 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 8.80000019 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 512 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | 8.80000019 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | N. 700 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_UIs_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_UI | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt_AstLmt_Per1_LimitedReturn_MtrNm_tgt AstLmt Per1 LrnPnCtrCCDisable Cr | | |
| | tgt_AstLmt_Per1_tmPnCtrCcbisable_Cr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | tgt_AstLmt_Per1_tmPnCtrPnable_Cnt_ig | | |
| tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt AstLmt Per1 OpTrqOvr MtrNm f32 | _132 | |
| | | 32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc | tgt_AstLmt_Per1_OutputRampMult_Uls_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCcDisable_Cnt_igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PosServCCDisable_Cn tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerC_Ois_I32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm | | |
| | tgt_AstLmt_Per1_PreLimitForStall_MtrNm tgt_AstLmt_Per1_PreLimitTorque_MtrNm | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn target AstLmt Per1 TSMitCommand Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | target_AstLmt_Peri_ISMitCommand_Mt | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_Thermail.imit_MtrNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_FrqLimitMin_MthMin_is2 | tgt_AstLmt_Per1_frqLimitwiin_withm_isz | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VeriSpd_Rpn_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_venspd_kpn_i32 tgt_AstLmt_Per1_WheelImbalanceCmd_N | AtrNm f32 | |
| | | | D |
| Name | Actual Value | Expected Value | Resul |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | • |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | • |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 8.80000019 | 8.80000019 ± 0.0009765625 | |

| T | | | | V |
|--|-------|--|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP1_CheckpointReached | 1 | ✓ |





| Test Step 3.20 (Repeat Count = 1) | | | ✓ |
|---|---|--------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| k SumLimPICmpLimit MtrNm f32 | 0.899999976 | | |
| target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value | 2 | | |
| tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value | 0 | | |
| tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value | -1 | | |
| tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value | 0.100000001 | | |
| tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value | 6.5 | | |
| tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value | 5.9000001 | | |
| tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value | 6.30000019 | | |
| tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value | -6.5 | | |
| tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value | 1 | | |
| tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value | 7 | | |
| tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value | -2 | | |
| tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value | 0 | | |
| tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value | 1 | | |
| tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value | -4 | | |
| tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value | 3 | | |
| tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value | 0.5 | | |
| tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value | 7.0999999 | | |
| tgt_AstLmt_Per1_VehSpd_Kph_f32.value | 26 | | |
| tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value | -5 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTDamping_MtrN | m f32 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 | tgt_AstLmt_Per1_AssistEOTGain_UIs_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 | tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 | tgt_AstLmt_Per1_CombinedDamping_MtrN | _ | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc | tgt_AstLmt_Per1_DefeatLimitService_Cnt_I | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 | tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ | - | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32 | tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3: | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc | tgt AstLmt Per1 LrnPnCtrCCDisable Cnt | | |
| tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc | tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc | . • | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 | tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 | tgt AstLmt Per1 OutputRampMult Uls f32 | 1 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc | tgt_AstLmt_Per1_PosServCCDisable_Cnt_ | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 | tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitForStall_MtrNm_ | 732 | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 | tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 | tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3. | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 | tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 | tgt AstLmt Per1 SumLimTrqCmd MtrNm | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 | target_AstLmt_Per1_TSMitCommand_MtrN | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 | tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 | tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 | tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 | tgt_AstLmt_Per1_VehSpd_Kph_f32 | | |
| tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 | | |
| Name | Actual Value | Expected Value | Result |
| tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value | 1 | 1 ± 0.000030517578125 | ~ |
| tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value | 0 | 0 ± 0.0009765625 | ~ |
| tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value | 5.9000001 | 5.9000001 ± 0.0009765625 | ~ |
| | | | |

| Τ | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | Rte_Call_AstLmt_Per1_CP0_CheckpointReached | 1 | ~ |
| Rte Call Astl mt Per1 CP1 ChecknointReached | 1 | Rte Call Astlimt Per1 CP1 CheckpointReached | 1 | |

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AstLmt_Scom_GetSteeringAssistDefeat

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_GetSteeringAssistDefeat

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Branch (C1) Coverage | 100 % |

Statistics

| Total Testcases | 1 |
|-----------------|---|
| Successful | 1 |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\AstLmt_CM |
|------------------------|--|
| Configuration File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include |

| Comments/Descript | ion/Specification |
|-------------------|---|
| Name | Text |
| Module 'AstLmt' | Name of Tester Aif Shrivestava |
| | Name of Tester-Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 |
| | Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." |

| Attributes | | |
|-----------------------|--|--|
| Name | Value | |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 | |
| Float Precision | 9 | |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj | |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src | |
| Linker File | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre> | |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl | |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 | |
| Time Unit | Cycles | |
| Timer Enabled | false | |
| Timer Prescale | 0 | |
| Timer Resolution | | |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg | |

TEST DETAILS REPORT AstLmt_Scom_GetSteeringAssistDefeat

2014-09-05, 17:31:44+0530



Attributes

Name

Value

Workspace File

D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

Elivironment

CPU Cycles:

TS1.1 20.00 Cycles TS1.2 20.00 Cycles

Description Vector Description:

TS1.1 Rte_Pim_SteerAsstDefeat = True TS1.2 Rte_Pim_SteerAsstDefeat = False

| Test Step 1.1 (Repeat Count = 1) | | |
|--|--------------------------------|-----------|
| Name | Input Value | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | |
| SteeringAsstDefeat_Cnt_lgc | tgt_SteeringAsstDefeat_Cnt_lgc | |
| tgt_Pim_SteerAsstDefeat | 1 | |
| tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | tgt_Pim_SteerAsstDefeat | |
| Name | Actual Value Expected Value | ue Result |
| tgt_SteeringAsstDefeat_Cnt_lgc | 1 1 | ~ |

| Test Step 1.2 (Repeat Count = 1) | | | ✓ |
|--|--------------------------------|----------------|--------|
| Name | Input Value | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| SteeringAsstDefeat_Cnt_lgc | tgt_SteeringAsstDefeat_Cnt_lgc | | |
| tgt_Pim_SteerAsstDefeat | 0 | | |
| tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | tgt_Pim_SteerAsstDefeat | | |
| Name | Actual Value | Expected Value | Result |
| tgt_SteeringAsstDefeat_Cnt_lgc | 0 | 0 | ~ |

2014-09-05, 17:33:02+0530



AstLmt_Scom_ManualTrqCmd

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_ManualTrqCmd

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|--------------------------|-------|
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| Total Testcases | 3 | |
|-----------------|---|----------|
| Successful | 3 | ~ |
| Failed | 0 | |
| Not Executed | 0 | |

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\AstLmt_CM |
|------------------------|--|
| Configuration File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include |

| Comments/Descrip | otion/Specification |
|------------------|--|
| Name | Text |
| Module 'AstLmt' | ************************************** |
| | Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference." |
| | |

| Access to | |
|-----------------------|---|
| Attributes | |
| Name | Value |
| Compiler Install Path | <pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre> |
| Float Precision | 9 |
| InitObjDir | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre> |
| InitSrcDir | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre> |
| Linker File | <pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre> |
| Makefile Template | <pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre> |
| Target Install Path | <pre>\$(ProgramFiles)\pls\UDE 3.2</pre> |
| Time Unit | Cycles |
| Timer Enabled | false |

2014-09-05, 17:33:02+0530



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Matrix Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS1.1 269.00 Cycles Shortest Execution Path TS1.2 274.00 Cycles Longest Execution Path

Description Vector Description:

TS1.1 "Shortest Execution Path:
(TRUE == EnableManualCtrl) =False"
TS1.2 "Longest Execution Path:
(TRUE == EnableManualCtrl) = True
(VehSpd_Kph_T_f32 < FLT_EPSILON) = True"

| Test Step 1.1 (Repeat Count = 1) | | | |
|--|---------------------------|--------------------|--------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1 | | |
| EnableManualCtrl | 0 | | |
| MtrTrqCmd_MtrNm_f32 | 3 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 54 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | 0 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | 0 ± 0.0009765625 | ~ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| T | | | | V |
|-----------------------------------|-------|-----------------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 1.2 (Repeat Count = 1) | | | |
|--|---------------------------|--------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -6 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ✓ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6 | -6 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ✓ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | |



Test Case 2: Boundary Test

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

269.00 Cycles 272.00 Cycles 275.00 Cycles 275.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 272.00 Cycles 272.00 Cycles 269.00 Cycles 272.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11

Description Vector Description:

TS2.1 EnableManualCtrl = min TS2.2 EnableManualCtrl = max

TS2.2 EnableManualCtrl = max
TS2.3 MtrTrqCmd_MtrNm_f32 = min
TS2.4 MtrTrqCmd_MtrNm_f32 = zero
TS2.5 MtrTrqCmd_MtrNm_f32 = zero
TS2.6 MtrTrqCmd_MtrNm_f32 = neg
TS2.7 MtrTrqCmd_MtrNm_f32 = pos
TS2.8 VehSpd_Kph_f32 = min
TS2.9 VehSpd_Kph_f32 = max
TS2.10 VehSpd_Kph_f32 = pos
TS2.11 All min
TS2.12 All max

| Test Step 2.1 (Repeat Count = 1) | | | ✓ |
|--|----------------------------|-------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -15.9949999 | | |
| EnableManualCtrl | 0 | | |
| MtrTrqCmd_MtrNm_f32 | 3.4000001 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Vel | hSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 54.6699982 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | 0 | ✓ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | 0 ± 0.0009765625 | • |
| AstLmt Scom ManualTrgCmd() | 1 | 1 | ✓ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.2 (Repeat Count = 1) | | | |
|--|---------------------------|----------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -12.9949999 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -6.1999981 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 29.9200001 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -12.9949999 | -12.9949999 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 34 | 34 | ✓ |

| T | | | | V |
|-----------------------------------|-------|-----------------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.3 (Repeat Count = 1) | ✓ |
|----------------------------------|-------------|
| Name | Input Value |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -9.99499989 |
| EnableManualCtrl | 1 |

2014-09-05, 17:33:02+0530



| Name | Input Value | | |
|--|--------------------------------------|--------------------|----------|
| MtrTrqCmd_MtrNm_f32 | -16 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f: | 32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 3.45999993e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -16 | -16 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.4 (Repeat Count = 1) | | | |
|--|--------------------------|---------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -6.99499989 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 15.9995003 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_V | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 6.38000017e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 15.9995003 | 15.9995003 ± 0.0009765625 | ~ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ✓ |

| T | | | | V |
|-----------------------------------|-------|-----------------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.5 (Repeat Count = 1) | | | |
|--|---------------------------|--------------------|--------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -3.99499989 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 0 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 7.27999989e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | 0 ± 0.0009765625 | ~ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ✓ |

| Τ | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.6 (Repeat Count = 1) | | | |
|--|---------------------------|---------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -0.995000005 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -2.5 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 5.28000008e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ✓ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -2.5 | -2.5 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ✓ |

| Τ | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |



| Test Step 2.7 (Repeat Count = 1) | | | |
|--|-----------------------------|---------------------------|--------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2.00500011 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 4.55999994 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Veh: | Spd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 8.38000034e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 4.55999994 | 4.55999994 ± 0.0009765625 | ~ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.8 (Repeat Count = 1) | | | ✓ |
|--|---------------------------|---------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 5.00500011 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 15.1199999 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ✓ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 15.1199999 | 15.1199999 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.9 (Repeat Count = 1) | | | |
|--|---------------------------|---------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.00500011 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -3.67000008 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | hSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 512 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | 0 | ✓ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 8.00500011 | 8.00500011 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 34 | 34 | ~ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |



| Test Step 2.10 (Repeat Count = 1) | | | ~ |
|--|----------------------------|---------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 11.0050001 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 9.82999992 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Vel | hSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 123.260002 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | - |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 11.0050001 | 11.0050001 ± 0.0009765625 | • |
| AstLmt_Scom_ManualTrqCmd() | 34 | 34 | ✓ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.11 (Repeat Count = 1) | | | ✓ |
|--|--------------------------|--------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -16 | | |
| EnableManualCtrl | 0 | | |
| MtrTrqCmd_MtrNm_f32 | -16 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_V | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | 0 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | 0 ± 0.0009765625 | ~ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| Τ | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 2.12 (Repeat Count = 1) | | | ✓ |
|--|---------------------------|---------------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 15.9995003 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | 15.9995003 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 512 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 15.9995003 | 15.9995003 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 34 | 34 | ~ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |



Test Case 3: Path Coverage Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS3.1 269.00 Cycles TS3.2 272.00 Cycles TS3.3 275.00 Cycles

Description Vector Description:

TS3.1 (TRUE == EnableManualCtrl) = False
TS3.2 "(TRUE == EnableManualCtrl) = True
and(VehSpd_Kph_T_f32 < FLT_EPSILON)=False"
TS3.3 (VehSpd_Kph_T_f32 < FLT_EPSILON)=True

| Test Step 3.1 (Repeat Count = 1) | | | ✓ |
|--|---------------------------|-------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 1 | | |
| EnableManualCtrl | 0 | | |
| MtrTrqCmd_MtrNm_f32 | 3 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | hSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 54 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | 0 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 0 | 0 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ~ |

| Τ | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 3.2 (Repeat Count = 1) | | | |
|--|---------------------------|-------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -6 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_Ve | hSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 29 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 2 | 2 ± 0.0009765625 | ✓ |
| AstLmt Scom ManualTrgCmd() | 34 | 34 | ✓ |

| T | | | | ✓ |
|-----------------------------------|-------|-----------------------------------|-------|----------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

| Test Step 3.3 (Repeat Count = 1) | | | |
|--|--------------------------|--------------------|----------|
| Name | Input Value | | |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 0 | | |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | 3 | | |
| EnableManualCtrl | 1 | | |
| MtrTrqCmd_MtrNm_f32 | -16 | | |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data) | tgt_Rte_Read_Ap_AstLmt_V | ehSpd_Kph_f32_data | |
| tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data | 3.45999993e-008 | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_ManualTrqCmdEn_Cnt_M_lgc | 1 | 1 | ~ |
| AstLmt_ManualTrqCmd_MtrNm_M_f32 | -16 | -16 ± 0.0009765625 | ✓ |
| AstLmt_Scom_ManualTrqCmd() | 1 | 1 | ✓ |

| Τ | | | | |
|-----------------------------------|-------|-----------------------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | Rte_Read_Ap_AstLmt_VehSpd_Kph_f32 | 1 | ~ |

AstLmt_Scom_ManualTrqCmd

2014-09-05, 17:33:02+0530



2014-09-05, 17:26:14+0530



AstLmt_Init

 Project
 AstLmt_CM

 Module
 AstLmt

 Test Object
 AstLmt_Init

Instrumentation: Test Object Only

| Statement (C0) Coverage | 100 % |
|-------------------------|-------|
| Branch (C1) Coverage | 100 % |

Statistics

| Total Testcases | 1 | |
|-----------------|---|----------|
| Successful | 1 | ✓ |
| Failed | 0 | |
| Not Executed | 0 | |

Module Properties

| Project Root Directory | D:\Synergy_Work_Area\AstLmt_CM |
|------------------------|---|
| Configuration File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include |

| Name | Text |
|-----------------|---|
| Module 'AstLmt' | ************************************** |
| | Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference." |

| Attributes | |
|-----------------------|--|
| Name | Value |
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 3.2 |
| Time Unit | Cycles |
| Timer Enabled | false |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |

2014-09-05, 17:26:14+0530



AstLmt_Init

| Attributes | |
|----------------|--|
| Name | Value |
| Workspace File | D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

CPU Cycles.

TS1.1 25.00 Cycles TS1.2 25.00 Cycles

Description Vector Description:

TS1.1 SteeringAsstDefeat1_Cnt_lgc = True TS1.2 SteeringAsstDefeat1_Cnt_lgc = False

| Test Step 1.1 (Repeat Count = 1) | | | ✓ |
|--|-------------------------|----------------|--------|
| Name | Input Value | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | | |
| tgt_Pim_SteerAsstDefeat | 1 | | |
| tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | tgt_Pim_SteerAsstDefeat | | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 1 | 1 | ~ |

| Test Step 1.2 (Repeat Count = 1) | | | ✓ |
|--|-------------------------|-------------------------|----------|
| Name | Input Value | | |
| Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | tgt_Rte_Inst_Ap_AstLmt | |
| tgt_Pim_SteerAsstDefeat | 0 | 0 | |
| tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat | tgt_Pim_SteerAsstDefeat | tgt_Pim_SteerAsstDefeat | |
| Name | Actual Value | Expected Value | Result |
| AstLmt_SteeringAsstDefeat_Cnt_M_lgc | 0 | 0 | ~ |