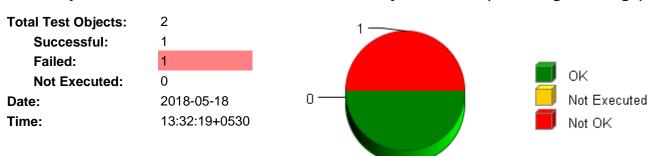


### **Summary**

### **Overall Test Object Results (including Coverage)**



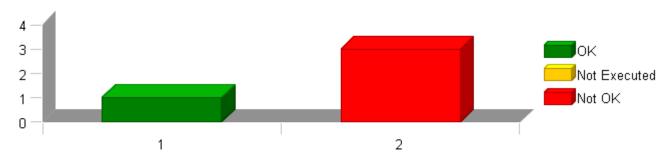
### **Selected Project Items**

Test Object "CBD\_Unittest/LoaMgr/LoaMgr\_Init1" Test Object "CBD\_Unittest/LoaMgr/LoaMgr\_Per1"

#### **Used Test Environments**

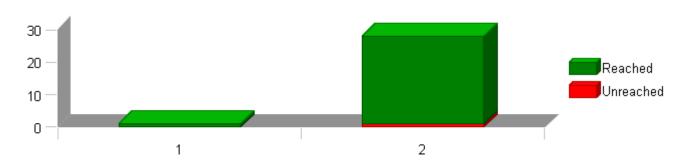
TI TMS 570 PLS UDE (Default)

### **Test Case Results for Each Test Object (without Coverage)**



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

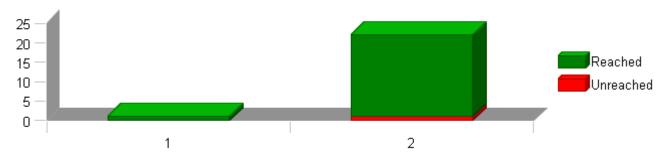
### Statement (C0) Coverage: Total Statements for Each Test Object





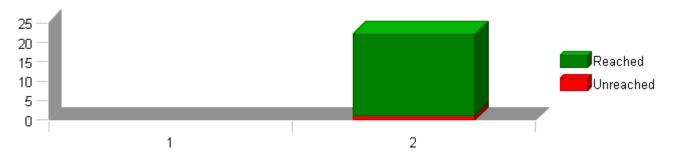
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

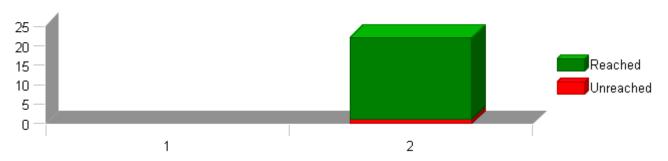
### **Decision Coverage: Total Decision Outcomes for Each Test Object**



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### **TEST OVERVIEW REPORT**

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### **Test Object List**

Project LoaMgr

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	Test Cases	Result
	LoaMgr	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
	CBD_Unittest	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
	LoaMgr	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
1	LoaMgr Init1	100 %	100 %	-	-	1 of 1 passed	•
2	<u>LoaMgr_Per1</u>	96.42 %	95.45 %	95.45 %	95.45 %	3 of 3 failed	*

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LoaMgr\_Init1

 Project
 LoaMgr

 Module
 LoaMgr

 Test Object
 LoaMgr\_Init1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'LoaMgr'

Name of Tester:Pradnya Mhetre
Code File(s) Under Test:Ap\_LoaMgr.c
Code File(s) Under Test:Ap\_LoaMgr.c
Code File(s) Under Test:Ap\_LoaMgr.c
Code File(s) Version:11
Module Design Version:9
Requirement Document Version:NA
Data Dictionary Version:8
Unit Test Plan Version:5
Optimization Level:Ogeneral
Compiler (CodeGen) Version:TMS470\_4.9.5
Model Type:None
Model Version:Nexteer EPS Unit Test Tool 1.0
Total FLASH Used (Bytes):227
Total CALS Used (Bytes):272
Total CALS Used (Bytes):273
Total CALS Used (Bytes):76
Special Test Requirements:NA
Test Date:5/18/2018
Comments:
Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
Note2:The Expected output calculated from MATLAB model.
Note3:Local functions ""Latchinputs(p", ""RqstRespConds(p", ""ArbResp()", ""AssignScale()"", ""RgstHwTqResp()", ""ChooseFinalResp()" and ""SwBasMtgtn()"" are not tested individually as they are tested in "LoaMgr Per1 Coverage" for the same.
Note4:In CSV\_9 and CSV\_10,value for boolean variable ""DiagcStsivtr1Inactv" given in "Simdata" is in float.
Hence input values for "DiagcStsIvtr1Inactv" are captured from Matlab design and given in Tessy for those test steps.
Note5: Out of range value is given to the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0.4}, to cover the path at src line no.918 in TSS.1
Note6: Out of range value is given to the variable ""MtrPosldptSig\_Cnt\_u08=3"" over the DD range {0.2}, to cover the path at src line no.918 in TSS.2
Note7: Out of range value is given to the variable ""MtrPosldptSig\_Cnt\_u08=3"" over the DD range {0.2}, to cover the path at src line no.918 in TSS.2
Note8:Output variable "LoaMgr\_AgSV\_Cnt\_M\_Igc" is failing because of Source Model Mismatch at src line no.1503

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>			
Time Unit	cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NOUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			

because of Source Model Mismatch at src line no.1503
Anomaly EA3#20083 is logged for the same.
Mote9:Parameter of "Rte\_Call\_NxtrDiagMgr\_SetNTCStatus" "Param\_Cnt\_T\_u08" is failing because of Source Model Mismatch.
Anomaly EA3#20083 is logged for the same.
Note10:Output variable "LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08" is going out of range as [0 3] over DD range [0 1] in PRM test vector TS4.103
Anomaly EA3#20083 is logged for the same.
Note11:""CBD\_Sandbox\_dbg.map"" map file is embedded for reference.



### Test Case 1: PRM Range Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS1.1 2849.00 Cycles TS1.2 2849.00 Cycles

Description Vector Description:

TS1.1k\_LoaMgr\_SnsrlsMotAgAvail\_Cnt\_lgc=>Min/Default TS1.2k\_LoaMgr\_SnsrlsMotAgAvail\_Cnt\_lgc=>Max

Test Step 1.1 (Repeat Count = 1)  ✓			
Name	Input Value		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	0		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus	6	Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus	6	_

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	1		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr SnsrlsMotAgAvail Cnt M lgc	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	~

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LoaMgr\_Per1

 Project
 LoaMgr

 Module
 LoaMgr

 Test Object
 LoaMgr\_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	96.42 %
Decision Coverage	95.45 %
Branch (C1) Coverage	95.45 %
MC/DC Coverage	95.45 %

#### **Statistics**

Total Testcases	3
Successful	0
Failed	3
Not Executed	0



### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(Compiler Install Path)\include

lame	Text
lodule 'LoaMgr'	**************************************
•	Name of Tester:Pradnya Mhetre
	Code File(s) Under Test:Ap_LoaMgr.c
	Code File(s) Version:11
	Module Design Version:9
	Requirement Document Version:NA
	Data Dictionary Version:8
	Unit Test Plan Version:5 Optimization Level:Ogeneral
	Opinitzation Level.Ogenetes Compiler (CodeGen) Version:TMS470_4.9.5
	Model Type:None
	Model Version:Nexteer EPS Unit Test Tool 1.0
	Total FLASH Used (Bytes):2244
	Total RAM Used (Bytes):27
	Total CALS Used (Bytes):76
	Special Test Requirements:NA
	Test Date:5/18/2018
	Comments:
	Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
	Note2:The Expected output calculated from MATLAB model.
	Note3:Local functions ""LatchInputs()"", ""RqstRespConds()"", ""ArbResp()"", ""AssignScale()"", ""AssignScale()", ""Ass
	""RqstHwTqResp()"", ""ChooseFinalResp()"" and ""SwBasMtgtn()"" are not tested individually as they are tested in "LoaMqr Per1" function.
	Please refer snapshot "LoaMgr Per1 Coverage" for the same.
	Note4:in CSV 9 and CSV 10,value for boolean variable "DiagcStslvtr1Inactv""
	given in "Simdata" is in float.
	Hence input values for "DiagcStsIvtr1Inactv" are captured from Matlab design
	and given in Tessy for those test steps.
	Note5: Out of range value is given to the variable ""HwTqldptSig_Cnt_u08=5"" over the DD range {0,4},
	to cover the path at src line no.908 in TS5.1
	Note6: Out of range value is given to the variable ""MtrPosIdptSig_Cnt_u08=4"" over the DD range {0,3},
	to cover the path at src line no.918 in TS5.2
	Note7: Out of range value is given to the variable ""MotCurrIdptSig_Cnt_u08=3"" over the DD range {0,2},
	to cover the path at src line no.928 in TS5.3
	Note8:Output variable "LoaMgr_AgSV_Cnt_M_lgc" is failing because of Source Model Mismatch at src line no.1503
	Decades of source whole withinstact at act line hot 1000 Anomaly EA3#20083 is logged for the same.
	Mote9:Parameter of "Rte Call NxtrDiagMgr SetNTCStatus" "Param Cnt T u08" is failing
	because of Source Model Mismatch.
	Anomaly EA3#20083 is loqued for the same.
	Note 10:Output variable "LoaMgr VltgMdSrcSV Cnt M u08" is going out of range as [0 3]
	over DD range [0 1] in PRM test vector TS4.103
	Anomaly EA3#20083 is logged for the same.
	Note11:""CBD_Sandbox_dbg.map"" map file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



```
Test Case 1: Metric Test
Specification
                                                                                                                                                   Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                                                                                                     TS1.1 2849.00 Cycles
TS1.2 5486.00 Cycles
Description
                                                                                                                                                   Vector Description:
                                                                                                                                       Vector Description:

TS1.1Shortest Execution Path

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_SPDSHTDWNREQD)=>T

(LoaMgr_LOASt_State_M_enum == LOAST_SWMTGTN_CNT_U08)=>F||(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtvTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtpTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_RORM)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_CTRLDSHTDWNREQD)=>F

if(Ivtrlnactv_Cnt_T_logl == TRUE)=>F

Max_m(MultiMtgtnResp_Cnt_T_u08_Max_m(MtvTqResp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T
                                                                                                                                                   TS1.1Shortest Execution Path
                                                                                                                                                   TS1.2 Largest Execution Path
                                                                                                                                         if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)>F
else if(LoaMgr_LOASt_State_M_enum == LOAST_CTRLDSHTDWNREQD)>F
if(MttPosldptSig_Cnt_T_u08 <= k_LoaMgr_MotAgldptSigFltThd_Cnt_u08)>>T
if(MotCurrldptSig_Cnt_T_u08 <= k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08)>>T
if(MotCurrldptSig_Cnt_T_u08 <= k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08)>>T
if(LoaMgr_LoaScaleFctr_Uls_M_f32>=1)>>F
if ((IMTGResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)>>F||(LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc == TRUE)>>F)>>F
if ((MotCurrResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)>>F||(LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc == TRUE)>>F)>>F
if ((MotCurrResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)>>F||(LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc == TRUE)>>F)>>F
if ((MotCurrResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)>>F||(LoaMgr_IotAst_State_M_enum == LOAST_NCM_D)>>F||(LoaMgr_IotAst_State_M_enum == LOAST_REDCD)>>T
else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)>>F
if (ivtrlnacty_Cnt_T_log] == TRUE)>>F
if (ivtrlnacty_Cnt_T_log] == TRUE)>>F
if (ivtrlnacty_Cnt_T_log += TruE
                                                                                                                                                   if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)>F
```

Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	

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Name	Input Value		
LoaMgr_MotAgRespSV_Cnt_M_u08	5		
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	3		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2 4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0 1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 tarnet LoaMar Per1 DiagnStelvtr1Inacty Cr	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr		
target_Rte_inst_Ap_LoaMgr.LoaMgr_Pen_blagcsisivtizinaciv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Pen1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DlagcStstvtt2tnactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0	0	<b>-</b>
LoaMgr_HwTgRespSV_Cnt_M_u08	0	0	-

0

0

LoaMgr\_HwTqRespSV\_Cnt\_M\_u08

LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08

 $LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc$ 

0 2 0

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Name	Actual Value	Expected Value	Result
LoaMgr lvtrRespSV Cnt M u08	1	1	~
LoaMgr LOASt State M enum	5	5	<b>✓</b>
LoaMgr LoaRateLimit UlspS M f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1}</b>	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace   ✓					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	-	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 1.2 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0.123400003		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0 2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum	J6	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target LoaMgr Per1 TloaAvail Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- Court
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	1	1	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0.123400003	0.123400003	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>*</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	0	1	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>*</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0.123400003	0.123400003 ± 0.001	~

LoaMgr\_Per1

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Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	•	
RqstRespConds	1	RqstRespConds	1	<b>~</b>	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	<b>✓</b>	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	-	

#### Test Case 2: Range Test

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS2.1 5287.00 Cycles
TS2.2 5486.00 Cycles
TS2.2 5486.00 Cycles
TS2.3 5054.00 Cycles
TS2.3 5054.00 Cycles
TS2.4 5021.00 Cycles
TS2.5 5005.00 Cycles
TS2.6 5005.00 Cycles
TS2.7 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.10 5005.00 Cycles
TS2.11 5005.00 Cycles
TS2.12 5005.00 Cycles
TS2.13 5005.00 Cycles
TS2.14 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.16 5005.00 Cycles
TS2.17 5005.00 Cycles
TS2.18 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.20 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.23 5005.00 Cycles TS2.23 5005.00 Cycles TS2.24 5005.00 Cycles

### Description

Vector Description:

TS2.1All Min TS2.2All Max

TS2.2All Mix
TS2.2Bl Max
TS2.3DiagcStslvtr1Inactv\_Cnt\_lgc==>Min
TS2.4DiagcStslvtr1Inactv\_Cnt\_lgc==>Mix
TS2.5DiagcStslvtr2Inactv\_Cnt\_lgc==>Mix
TS2.5DiagcStslvtr2Inactv\_Cnt\_lgc==>Mix
TS2.6DiagcStslvtr2Inactv\_Cnt\_lgc==>Max
TS2.7HwTqldptSig\_Cnt\_u08==>Max
TS2.7HwTqldptSig\_Cnt\_u08==>Pos
TS2.9HwTqldptSig\_Cnt\_u08==>Max
TS2.91MotCurrldptSig\_Cnt\_u08==>Max
TS2.11MotCurrldptSig\_Cnt\_u08==>Max
TS2.12MotCurrldptSig\_Cnt\_u08==>Max
TS2.13MtrPosldptSig\_Cnt\_u08==>Max
TS2.12MotCurrldptSig\_Cnt\_u08==>Max
TS2.14MtrPosldptSig\_Cnt\_u08==>Max
TS2.16TloaAvail\_Cnt\_lgc==>Min
TS2.16TloaAvail\_Cnt\_lgc==>Max

TS2.17TloaAvail\_Cnt\_lgc==>Max

TS2.18TloaDi\_Cnt\_lgc=>Max
TS2.18TloaDi\_Cnt\_lgc=>Min
TS2.19TloaDi\_Cnt\_lgc=>Max
TS2.20LoaScaDi\_Cnt\_lgc=>Min
TS2.21LoaScaDi\_Cnt\_lgc=>Min
TS2.21LoaScaDi\_Cnt\_lgc=>Max
TS2.22SteerMod\_Cnt\_enum=>STEERMOD\_BASEPS
TS2.23SteerMod\_Cnt\_enum=>STEERMOD\_SEMIATNMS TS2.24SteerMod\_Cnt\_enum==>STEERMOD\_FULLYATNMS

Test Step 2.1 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	





Name	Input Value		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_IvtrRespSV_Cnt_M_u08	0		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotAgRespSV_Cnt_M_u08	0		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	0		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	0.00999999978		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cni		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_rtte_inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MttPostuptoig_Crit_u00 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Steenwod_Crit_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_SteenMod_Crit_end	···	
	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target Rte Inst An LoaMor LoaMor Per1 Thophi Cot Ido			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	Actual Value	Expected Value	Resu





Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>*</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				Ť
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>

Test Step 2.2 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	500
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	5		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	4		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	500		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	2		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	500		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	3		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	5		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	5		
	5		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	· · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name		Expected Value	Result
Looklay AgCV Cat M Igo			

Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	-
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.3 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k LoaMgr lvtrMtgtnSca Uls f32	1	
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0	
k LoaMgr MotAgldptSigFltThd Cnt u08	2	
k LoaMgr MotAgMtgtnRate UlspS f32	1	
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0	
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1	

2018-05-18, 13:31:17+0530



Loamgr_Peri			MACITAL
Name	Input Value		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Nome	Actual Value	Expected Value	Result
Name			
Name LoaMgr_AgSV_Cnt_M_lgc	0	0	•
	0	0	
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 4 0	0 0 4 0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 4 0	0 0 4 0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	0 0 4 0 0 5	0 0 4 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 4 0 0 5 5	0 0 4 0 0 5 5	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 4 0 0 5	0 0 4 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Mign_sd2 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0 4 0 0 0 5 5 5 5 500 1 5	0 0 4 0 0 5 5 5 5 500 1 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 0 4 0 0 0 5 5 5 5 500 1 5 0	0 0 4 0 0 5 5 5 500 1 5 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0	0 0 4 0 0 5 5 5 5 500 1 5 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 4 0 0 0 5 5 5 5 500 1 1 5 0 0	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	0 0 4 0 0 0 5 5 5 5 500 1 1 5 0 0 0	0 0 4 0 0 0 5 5 5 5000 1 5 0 0 0 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_state_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 0 1 5	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 5	
LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08  LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08  LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08  LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc	0 0 4 0 0 0 5 5 5 5 500 1 1 5 0 0 0 0 5	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDavail_Cnt_M_lgc LoaMgr_TloaDavail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 2	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 2 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 2	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 0 5 0 1 2 1 1 2 1 1	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0}	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrleapSty_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 2 1 1 0 (170, 172) {1, 1}	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1}	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 2 1 1 0 {170, 172} {1, 1} {1, 1} 500	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0, 0} {1, 1} 5 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0 0 4 0 0 0 5 5 5 5 5 0 0 0 5 0 1 1 2 1 1 2 1 1 0 {170, 172} {1, 1} {1, 1} 5 500	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyTDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 4 0 0 0 5 5 5 5 5 0 0 0 5 0 1 1 2 1 1 1 0 {170, 172} {1, 1} {1, 1} 5 0 0	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 4 0 0 0 5 5 5 5 5 0 0 0 1 1 2 1 1 0 {170, 172} {1, 1} {1, 1} 5 0 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 2 1 1 1 0 {170, 172} {0,0,0,0,0} {1,1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 4 0 0 0 5 5 5 5 5 0 0 0 1 1 2 1 1 1 0 {1,1} {1,1} {1,1}	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 1 2 1 1 1 0 {170, 172} {0,0,0,0,0} {1,1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 4 0 0 0 5 5 5 5 5 0 0 0 1 1 2 1 1 0 {170, 172} {1, 1} {1, 1} 5 0 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 2 1 1 1 0 {170, 172} {0,0,0,0,0} {1,1} 500 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclytrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Injust Value		
Londings   Java   Londings   Lo	Test Step 2.4 (Repeat Count = 1)	
Londing   In-Virtica Mignific Cent   M. 196   Londing   Londing   Mignific Cent   M. 196   Londing   Londing   Londing   Mignific Cent   M. 196   Londing   Londing   Londing   Mignific Cent   M. 196   Londing   Londi	Name	Input Value
Londing   Invitrages   Londing   Invitrages   Content	LoaMgr_AgSV_Cnt_M_lgc	*none*
Looking_ Interpretacy V_Cmt_M_U08		*none*
Loading_Intrinsplay_Cort_M_Ups		*none*
Loading_ Introduction   Loading_ Introduction_ Introduction   Loading_ Introduction_	LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
Loading_ Inviding/insigns_V_Cnt_M_U08	LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMagr   Modulagistiss SSY Cnr   M. U8	LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
Londing	LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
Londing	LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr   MorCardipSigSPC   Cht   M   U68	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MoCurrLoaMtgnEnc_Cnt_M_ge	LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr NationApp.vall Crt_M_Up   LoaMgr_NtriApp.vall Crt_M_Up   LoaMgr_NtriApp.vall Crt_M_Up   LoaMgr_VilgMdSrcSV_Crt_M_Up8   Trone*	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_VitgMSricSV_Cnt_M_u08	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_VirtyMgMSroSV_Cnt_M_u08	LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
Ric_Inst_Ap_LoaMgr	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
L. LosMgr_CurrivrMtglnScaZeroEn_Cnt_lgc	LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
L. LosMign CurrivrMigthScaZeroEn. Cnt.lgc	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
LoaMgr_CurrivtnMgtnSca_Uls_f32	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
L. LoaMgr_Hv7lqtpKsg0TloaAvaiResp_Cnt_u08	k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_FadeOutSiRate_UlspS_132         1           k_LoaMgr_HvTqlqtpSig0TiOsaAvalResp_Cnt_u08         2           k_LoaMgr_HvTqlqtpSig1TiOsaAvalResp_Cnt_u08         4           k_LoaMgr_HvTqlqtpSig2TiosaDiResp_Cnt_u08         2           k_LoaMgr_HvTqlqtpSig2TiosaDiResp_Cnt_u08         1           k_LoaMgr_HvTqlqtpSig2TiosaTiosaPsep_Cnt_u08         0           k_LoaMgr_HvTqlqtpSig2TiosaTiosaPsep_Cnt_u08         0           k_LoaMgr_HvTqlqtpSigFitThd_Cnt_u08         1           k_LoaMgr_HvTqlqtpSigFitThd_Cnt_u08         1           k_LoaMgr_HvTqltqtsigTesp_Cnt_u08         0           k_LoaMgr_HvTqltqtsig1Tesp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSisq2Resp_Cnt_u08         1           k_LoaMgr_IvtrlqtpSisq1NoSmaResp_Cnt_u08         1           k_LoaMgr_IvtrlqtpSisq1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotQlqtpSig1SigResp_Cnt_u08         1           k_LoaMgr_MotQlqtpSig1SigResp_Cnt_u08         0           k_LoaMgr_MotQlqtpSig1SigResp_Cnt_u08         2           k_LoaMgr_MotQlqtpSigQResp_Cnt_u08         0	k LoaMgr CurrlytrMtgtnSca Uls f32	1
LoaMgr_HWTqldptSig0TloaAvailResp_Cnt_u08		1
k_LoaMgr_HwTqldplSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldplSig1TroaAvaiResp_Cnt_u08         2           k_LoaMgr_HwTqldplSig2TroaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldplSig2TroaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldplSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldplSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldglSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldplSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldplSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldplSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldplSig4Resp_Cnt_u08         0           k_LoaMgr_IvtrldplSig2Resp_Cnt_u08         1           k_LoaMgr_IvtrldplSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldplSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldplSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldplSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldplSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldplSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldplSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldplSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldplSig1Re		
k_LoaMgr_HwTqldpfsig1TioaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldpfsig2ToaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldpfsig2ToaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldpfsig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldpfsig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldpfsig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtghtRate_Ulps_132         1           k_LoaMgr_IwTqhtghsig4Resp_Cnt_u08         0           k_LoaMgr_IwTdpfsig4Resp_Cnt_u08         0           k_LoaMgr_IwTdpfsig4Resp_Cnt_u08         0           k_LoaMgr_IwTdpfsig4Resp_Cnt_u08         0           k_LoaMgr_IwTdpfsig4Resp_Cnt_u08         0           k_LoaMgr_IwTdpfsig4Resp_Cnt_u08         1           k_LoaMgr_MotAgldpfsig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldpfsig4Resp_Cnt_u08         1           k_LoaMgr_MotAgldpfsig4Resp_Cnt_u08         2           k_LoaMgr_MotAgldpfsig4Resp_Cnt_u08         2           k_LoaMgr_MotAgldpfsig7Resp_Cnt_u08         2           k_LoaMgr_MotAgldpfsig6Resp_Cnt_u08         2           k_LoaMgr_MotCurridpfsig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridpfsig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridpfsig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridpfsig1Resp_Cnt_u08 </td <td></td> <td></td>		
LoaMgr_HvTqlqtSig2TloaDiResp_Cnt_u08		
K_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           K_LoaMgr_HwTqldptSig1Thd_Cnt_u08         0           K_LoaMgr_HwTqldptSig1Thd_Cnt_u08         1           K_LoaMgr_HwTqldptSig1Thd_Cnt_u08         1           K_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           K_LoaMgr_IvtrlqptSig1Resp_Cnt_u08         0           K_LoaMgr_IvtrlqptSig2Resp_Cnt_u08         0           K_LoaMgr_IvtrlqptSig2Resp_Cnt_u08         0           K_LoaMgr_IvtrlqptSig2Resp_Cnt_u08         0           K_LoaMgr_IvtrlqptSig2Resp_Cnt_u08         1           K_LoaMgr_IvtrlqptSig2Resp_Cnt_u08         5           K_LoaMgr_MotAglqptSig1RoSmaResp_Cnt_u08         5           K_LoaMgr_MotAglqptSig2Resp_Cnt_u08         1           K_LoaMgr_MotAglqptSig1Sig1NoSmaResp_Cnt_u08         2           K_LoaMgr_MotAglqptSig1Sig1NoSmaResp_Cnt_u08         2           K_LoaMgr_MotAglqptSig1Sig1Sig2Resp_Cnt_u08         0           K_LoaMgr_MotAglqptSig1Sig1Sig2Resp_Cnt_u08         2           K_LoaMgr_MotAgltqptSig2Resp_Cnt_u08         2           K_LoaMgr_MotCurrlqptSig1Resp_Cnt_u08         2           K_LoaMgr_MotCurrlqptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurrlqptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurrlqptSig2Resp_Cnt_u08         0           K_		
LoaMgr_HwTqldptSig4Resp_Cnt_u08	1	
K_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           K_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           K_LoaMgr_HwTqMtgtnSca_Uls_f32         1           K_LoaMgr_IwtridptSigFitThd_Cnt_u08         0           K_LoaMgr_IvtridptSigFitThd_Cnt_u08         0           K_LoaMgr_IvtridptSigFitThd_Cnt_u08         0           K_LoaMgr_IvtridptRate_UlspS_f32         1           K_LoaMgr_IvtridptRate_UlspS_f32         1           K_LoaMgr_IvtridptRate_UlspS_f32         1           K_LoaMgr_IvtridptRate_UlspS_f32         1           K_LoaMgr_MotAgldptSig NoSmaResp_Cnt_u08         5           K_LoaMgr_MotAgldptSig NoSmaResp_Cnt_u08         1           K_LoaMgr_MotAgldptSigGResp_Cnt_u08         0           K_LoaMgr_MotAgldptSigGResp_Cnt_u08         2           K_LoaMgr_MotAgldgtnRate_UlspS_f32         1           K_LoaMgr_MotAgldgtnRate_UlspS_f32         1           K_LoaMgr_MotAgldgtnSigGresp_Cnt_u08         2           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         2           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         2           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGresp_Cnt_u08		
LoaMgr_HwrqMtgtnRate_UlspS_f32		
k_LoaMgr_HwTqMignSca_Uls_132		
k_LoaMgr_IvtridptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtridptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtridptSig1RtThd_Cnt_u08       0         k_LoaMgr_IvtriMgtnRate_UlspS_f32       1         k_LoaMgr_IvtriMgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtriMgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       1         k_LoaMgr_RedcdSiRate_UlspS_f32       <		
k_LoaMgr_lvtridptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtridptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnSca_UispS_f32       1         k_LoaMgr_lvtrMtgtnSca_UispS_f32       1         k_LoaMgr_lvtrMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnSca_UispS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       2         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnSca_Uis_f32       1         k_LoaMgr_RedcdStRate_UispS_f32       1         k_LoaMgr_RedcdStSca_Uis_f32       1		
k_LoaMgr_lvtrldptSigFitTnd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_lvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Lips_f32       1         k_LoaMgr_MotCurrldptSca_Uls_f32       1         k_LoaMgr_RedcdSiRate_UlspS_f32       1         k_LoaMgr_RedcdSiRate_UlspS_f32       1         k_LoaMgr_RedcdSiSca_Uls_f32       1         k_LoaMgr_RedcdSiSca_Uls_f32       1 <td></td> <td></td>		
k_LoaMgr_IvtrMtgtnRate_UispS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       2         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgltgtnRate_UispS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgMtgtnSca_Uis_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1TiTnd_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1TiTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UispS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uis_f32       1         k_LoaMgr_RedcdStSca_Uis_f32       1         k_LoaMgr_RedcdStSca_Uis_f32       1		
k_LoaMgr_IvtrMtgtnSca_eroEn_Cnt_lgc       0         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig4Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig4FitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_RotCurridptSig1Resp_Cnt_u08       1         k_LoaMgr_RotCurridptSig1Resp_Cnt_u08       1         k_LoaM		
k_LoaMgr_NvtrMtgtnSca_Uls_i32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrlMgtSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrlMgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMgtnSca_ZeroEn_Cn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtmScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtmSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1FlThd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtinSigFlThd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_RotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Vis_f32       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurridptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSig 1Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurridptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         t_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1		
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_RedcdStSca_Uls_f32 1	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1	k_LoaMgr_RedcdStSca_Uls_f32	
	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In	actv Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2In		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	•	
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	4	4	_
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	
LoaMgr_MotAglopMgrtpEp_Cot_M_lga	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
	1	1	_
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)			
	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1, 1, 1} 500	{1, 1, 1, 1, 1} 500 ± 0.01	
	1		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target LoaMgr Per1 HwTqLoaMtgtnEn Cnt lgc.value	0	1 ± 0.001	
	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value			
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.5 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*  *none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	target Rte Inst Ap LoaMgr
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr CurrivtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2 3
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_Steermod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.6 (Repeat Count = 1)		×.
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

2018-05-18, 13:31:17+0530

Input Value



Name



le Lander Combite Market Con Zone For Cost Lon	niput value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k LoaMgr MotAgldptSigFltThd Cnt u08	2		
	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
	1 0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 0 0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci	nt_lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Clarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarget_DiagcStslvtr2Inactv_Clarg	nt_lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc _lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	nt_lgc _lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_tq target_LoaMgr_Per1_LOASt_State_enum	nt_lgc _lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc lc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ig target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ig target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	nt_lgc _lgc ic t_lgc	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ci target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_i target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_i target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_i target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	nt_lgc _lgc ic t_lgc 08	
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIcoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_loamgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_udarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	nt_igc _igc t_igc 08 cnt_igc 8  Expected Value 0	<b>→</b>
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_ud target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0	nt_igc _igc t_igc 08 cnt_igc 8  Expected Value 0 0	<b>*</b>
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_tg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 0	nt_igc _igc  t_igc  t_igc  08  cnt_igc  8  Expected Value  0 0 0 4	\ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Ivtr_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 0	nt_lgc _lgc lc  t_lgc lbs  Expected Value  0 0 4 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ct target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500		***
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqltptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1	Ligc   Color   Color	*******
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlpdtSigSV_Cnt_M_u08 LoaMgr_HwTqlpdtSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrIcoaMtgtnEn_C0 target_LoaMgr_Per1_MtPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500 1 5	Ligc   Color   Color	***
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqltptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ct target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1	Ligc   Color   Color	*******

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





	I		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
Landau Andri Cat Malan	1.1.1		ittosuit
LoaMgr_AgSV_Cnt_M_lgc	0	0	√ V
LoaMgr_HwTqldptSigSV_Cnt_M_u08		0	✓ ✓
	0		~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 4	0 0 4	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 0 0 4 0	0 0 4 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 0 4 0	0 0 4 0 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 0 4 0 0 0	0 0 4 0 0 0 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 0 4 0 0 0 5	0 0 4 0 0 5 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 0 4 0 0 5 5 5	0 0 4 0 0 5 5 5 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_Mign_Sig2 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_Mign_Sig2 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_Mign_S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500	0 0 4 0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 1 5	0 0 4 0 0 5 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Mign_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 0 0 4 0 0 0 5 5 5 5 500 1 5 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 5	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrlCaAMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_lo08 LoaMgr_MotCurrRespSV_Cnt_M_lo08 LoaMgr_TloaAvail_Cnt_M_lgc	0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 0 0 0 0 1 5 5	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgltdptSigSV_Cnt_M_u08 LoaMgr_MotAgltdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 0 0 4 0 0 0 5 5 5 500 1 1 5 0 0 0 0 1 2 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 500 1 1 5 0 0 0 0 1 2 0	0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 5 0 1 2 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 2	0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 0 4 0 0 0 5 5 5 500 1 1 5 0 0 0 0 1 2 0	0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 2 1 0 1 2 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 4 0 0 5 5 5 5 5 0 0 1 5 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 0 4 0 0 5 5 5 5 0 0 1 5 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 2 0 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtlgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 4 0 0 5 5 5 5 0 0 1 5 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	0 0 0 4 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	0 0 0 0 4 0 0 5 5 5 5 5 0 0 1 2 0 1 2 0 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 5 1 0 0 5 1 0 1 0 0 5 1 0 0 5 1 0 0 1 0 0 5 1 0 0 0 5 1 0 0 0 5 5 0 0 1 0 0 0 5 5 0 0 0 1 0 0 0 5 0 0 0 5 0 0 0 5 0 0 0 0	0 0 0 4 0 0 0 0 5 5 5 5 5 5 5 5 5 5 5 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtlgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 4 0 0 5 5 5 5 0 0 1 2 0 1 2 0 1 0 (170, 172, 173, 174) (1, 1, 1, 1) 500 1 0 0	0 0 0 4 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ir	nacty Cnt loc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2Ir		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtr	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 IvtrLoaMtgtnEn Cnt Igc	target LoaMgr Per1 lvtrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgr		
Name	Actual Value	Expected Value	Result
	0	0	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	Ž
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgloptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgBoogS\/ Cot M v08	5	5	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	Ž
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	·		
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaDi_Ort_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	0		
LoaMgr_VitgMdSrcSV_Cnt_M_u08		0	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1} (4, 4)	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1} 500	{1, 1} 500 + 0.01	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	Y
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>•</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>V</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 2.9 (Repeat Count = 1)	
Name LoaMgr AgSV Cnt M Igc	Input Value *none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k LoaMgr CurrivtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0 1
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_igc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_LoaScabl_Crit_igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 1 2 3 3 3 2 3 2 3 3 3 3 3 3 3 3 3	, , , , , , , , , , , , , , , , , , , ,



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt Igc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAgloptSigSV_Cnt_M_u08	0	0	<b>Y</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	-	~
LoaMgr MotAgRespSV Cnt M u08	5	5	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





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Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	<b>S</b>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaning	ус	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr lvtrldptSigSV Cnt M u08			
	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_Loawigt_Ferr_Load_State_enum.value	J	9	
target LoaMar Port MotAglicoMtateEn Cot laguation	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0 1	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclytrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	_ <del>-</del>	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.13 (Repeat Count = 1)	18
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_UIs_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurridptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
0	, , , , , , , , , , , , , , , , , , , ,



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 2.14 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k LoaMgr_MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnRate_UispS_t32 k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value			
	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_igc } t_igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaMgr_Per1_IvtrLo	nt_igc } t_igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LOASt_State_enum	nt_igc } t_igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc 3 t_lgc gc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc 3 t_lgc gc nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0	nt_lgc 3 t_lgc gc nt_lgc 08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc	
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIoptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgcActual Value  0 0 0 4	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Cnt_lgc 08 0 0 0 0	<b>✓</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtcurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtcurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgcdarget_LoaMgr_Per1_TloaDi_Cnt_U	nt_lgc st_lgc gc st_lgc	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgcActual Value  0 0 0 4 0 0	nt_lgc st_lgc gc st_lgc gc st_lgc st_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqIdptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_Igctarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaDi_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_IgcActual Value  Actual Value  0  0  4  0  0  5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_igc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlpdtSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500	nt_lgc st_lgc st_lgc gc  nt_lgc 08 Cnt_lgc 08 Expected Value 0 0 0 4 0 0 5 5 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_ICOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1 5	nt_lgc st_lgc st_lgc gc  nt_lgc os st_lgc st	***************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500 1	nt_lgc st_lgc gc  nt_lgc gc  nt_lgc 08  Cnt_lgc 08  Expected Value 0 0 0 0 4 0 0 5 5 5 5000 1 5	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Lankage ModaphysisgRess or it u8	Name	Innut Value		
Landary Monigensignation Company	Name	Input Value		
Landary Mandagoling Treat Cort up				
Landay Morighterise Use   12				
Landing Antidophies Care List		1		
Looking MoCardisignResp. Crit Look	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
Loading / McCorrispisign Resp. Crit L08   0	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
Landary_MonCorridge(Spireton_Cort_Line)	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landing John-Carristop/Prince Cort Linds	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landary McCardingSepTime Cort Lands Landary McCardingSecal Lands Landary McCardingSecal Lands Landary McCardingSecal Lands Landary Reductification Lands Landary Reductification Lands Landary Reductification Lands Landary L		· ·		
Lacety McCurringhreSazzerien   Legis   1				
Looking / Redough Pert Disposition for Image Continue of the				
Loading-Red States   Use   D2				
Landary   Rescolations   Landary				
Image_Loady_Der_Loagestein**   Coll_govalue				
Image   Landly First   Modify (1500) (2011) (1500		1		
		1		
Images   Landarg Pert   Medicinariplisis   Cert   Landarg   Landarg   Pert   Medicinariplisis   Landarg	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
Image   Loading   Pert   Merit Production   Service   1	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
Integred_Loading_PerT_TORDI_COL_EQUE value	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_Loader_Perf_TimoDi_Cnt_go:cellue	target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
Images Rev. Inst. Ap. Loadsgr. Loadsgr. Pert. Disposition (Lipicate)				
target, Rein, Rad, Loadlyr, Loadlyr, Perf. DiagosSisht/Zinac/, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Hw7qt, Coadlyr, Perf. Lynacy, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Lynacy, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Lynacy, Loadlyr, Perf. Lynacy, Rein, Rad, Loadlyr, Loadlyr, Perf. Lynacy, Rein, Rad, Lynadlyr, Loadlyr, Perf. Lynacy, Rein, Rad, Loadlyr, Loadlyr, Perf. Lynacy, Loadlyr, Loadlyr, Perf. Lynacy, Rein, Rad, Lynadlyr, Loadlyr, Perf. Mordura, Coadlyrien, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Toadlyr, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Mordura, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Radiologi, Radiologi, Cnt. Igo target, Rein, Rad, Loadlyr, Radiologi, Radiologi, Radiologi, Radiologi, Radiolog				
target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_Nrt*Qroadkgrift. Ort.lgc   target_Rel_inst_Ap_Loadkgr_Per1_Nrt*Qroadkgrift. Ort.lgc   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_LOASS_Dtate_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_MoCurdoptsig_Ort.lgc   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_Toadkgr_Der1_MoCurdoptsig_Ort.lgc   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1_Toadkgr_Der1_MoCurdoptsig_Ort.lgc   target_Loadkgr_Per1_Toadkgr_Der1_Toadk				
larger, Rite_Inst_Ap_LoaMgr_LoaMgr_Perl_Unit_calNtginEn_Cnt_lgc			_	
target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASL_State_enum   target_LoaMgr_Pert_LoaSL_State_enum   target_LoaMgr_Pert_LoaSL_State_enum   target_LoaMgr_Pert_LoaSL_Cont_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridptSig_Cnt_U08   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_Inst_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_Inst_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_Inst_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_Inst_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_Pert_Inst_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_ToaAvail_Cnt_Igc   target_LoaMgr_ToaAvail				
larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_Igo   larger_LoaMgr_Pert_MortAgLoaMgr_Enc_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MortAgLoaMgr_Enc_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MortCurrioptSig_Cnt_U08   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MortCurrioptSig_Cnt_U08   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MortCurrioptSig_Cnt_U08   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MortCurrioptSig_Cnt_U08   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_SiteemMod_Cnt_enum   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_SiteemMod_Cnt_enum   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_LoaMgr_Pert_ToaAvail_Cnt_Igo   larger_LoaMgr_Pert_ToaAv				
target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNocurtoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNocurtoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Ticoaklyr_Dert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Ticoaklyr_Dert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Ticoaklyr_Dert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Ticoaklyr_Dert_Size_Inst_Ap_Loaklyr_Loaklyr_Pert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Ticoaklyr_Dert_Dert_Dert_Dert_Dert_Dert_Dert_Der				
Integret   Ref.   net   Ap   Loakingr   Loamingr   Pert   Microciantiquisting   Cnt   Loamingr   Pert   Toamingr   Pert   Toamingr   Pert   Toamingr   Pert   Toamingr   Pert			•	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Perf_MirPosidptSig_Cnt_u08   target_LoaMgr_Perf_NewMod_Cnt_enum   target_LoaMgr_Perf_NewMod_Cnt_enum   target_LoaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_Perf_Perf_Perf_ToaMgr_Perf_Perf_Perf_Perf_Perf_Perf_Perf_ToaMgr_Perf_Perf_Perf_Perf_Perf_Perf_Perf_Per				
target_Rie_Inst_Ap_LoakIgr_LoakIgr_Pert_TloakVail_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgr	tnEn_Cnt_lgc	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaNc_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0	Cnt_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_LvirRespSV_Cnt_M_u08         4         4         4           LoaMgr_LvirRespSV_Cnt_M_lgc         0         0         0           LoaMgr_LvirRespSV_Cnt_M_u08         5         5         5           LoaMgr_LvirRespSV_Cnt_M_u08         5         5         5           LoaMgr_LoaRscale-furr_UIS_M_132         500         500         0           LoaMgr_LoaRscale-furr_UIS_M_132         0         0         0           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         5         5         5           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         0         0         0           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         0         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         1         1         1           LoaMgr_TioaAvail_Cnt_M_lgc		target_LoaMgr_Per1_TloaAvail_Cnt_l	gc	
LoaMgr_HvTqlptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loaklyr_HwTqlcpKisjqSV_Cnt_M_u08	Name		·	Result
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc				
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_IvitridptSigSV_Cnt_M_u08				
LoaMgr_IvtrLoaMtghEn_Cnt_M_log         0           LoaMgr_LOASt_State_M_enum         5           LoaMgr_LOASt_State_M_enum         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500           LoaMgr_LoaScaleFctr_Uls_M_f32         0           LoaMgr_MaxMtghrResp_Cnt_M_u08         5           LoaMgr_MotAgltoftSigSV_Cnt_M_u08         0           LoaMgr_MotAglcoaMtghtEn_Cnt_M_lgc         0           LoaMgr_MotOcurridptSigSV_Cnt_M_u08         5           LoaMgr_MotCurridptSigSV_Cnt_M_u08         5           LoaMgr_MotCurridptSigSV_Cnt_M_u08         0           LoaMgr_MotCurridptSigSV_Cnt_M_u08         0           LoaMgr_MotCurridptSigSV_Cnt_M_u08         0           LoaMgr_MotCurridptSigSV_Cnt_M_u08         2           LoaMgr_TioaDi_Cnt_M_lgc         1           LoaMgr_TioaDi_Cnt_M_lgc         1           LoaMgr_TioaDi_Cnt_M_lgc         0           LoaMgr_TioaDi_Cnt_M_lgc         0           LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         (170, 173, 174, 175)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (170, 173, 174, 175)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1, 1)           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1, 1)				
LoaMgr_VtrRespSV_Cnt_M_u08				
LoaMgr_LoaSt_State_Menum				
LoaMgr_LoaRateLimit_UispS_M_f32         500         500           LoaMgr_LoaRateLimit_UispS_M_f32         0         0           LoaMgr_MaxMigtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgLoaMtigtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtignEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VitgMSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         (170, 173, 174, 175)         (170, 173, 174, 175)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_Set				<b>*</b>
LoaMgr_LoaScaleFct_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotQrudtptSigSV_Cnt_M_u08         5         5           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotOut_M_lgc         1         1           LoaMgr_ToaDvail_Cnt_M_lgc         0         0           LoaMgr_TloaDvail_Cnt_M_lgc         0         0           LoaMgr_VltgMSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_Ntm_loaMgr_SetNTCStatus(NTC_Cnt_Tenum)         (170, 173, 174, 175)         (170, 173, 174, 175)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Vitle_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Vitle_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1, 1)         (1, 1, 1, 1)         (1, 1, 1, 1)           Rte_Vitle_Ap_LoaMgr_LoaScaleFctr_Uls				_
LoaMgr_MotAgldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VitgMdSrcsV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Call_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value				<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VttgMotScSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         **           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_LoASt_State_enum.value         5         5         5           target	LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         4           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         4           rarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         4	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         5           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           raget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         4           raget_LoaMgr_Per1_LoASt_State_enum.value         5         5         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         4	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         4           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         4           rarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         4           rarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0         4	LoaMgr_MotCurrIdptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc         1         1         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}         X           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         X           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         X           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         X				~
LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174, 175}         {170, 173, 174, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         4           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         4           rarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0         4           rarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         4           rarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0				~
LoaMgr_VitgMdSrcSV_Cnt_M_u08       0       0         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 174, 175}       {170, 173, 174, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1, 1}       {0, 0, 0, 0, 0}       3         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1, 1}       {1, 1, 1, 1}       {1, 1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       \$0       \$0         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       \$0       \$0         Rte_UndMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       \$0       \$0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       \$0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       \$0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 174, 175}       {170, 173, 174, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1, 1}       {1, 1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0		· ·		· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)       {1, 1, 1, 1}       {1, 1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)       0       0 ± 0.001       1         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       0				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 vtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 vtarget_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt_lgc.value 0 vtarget_LoaMgtnEn_Cnt				<b>~</b>
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
		5	5	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	<b>~</b>
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cnt enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	-
LoaMgr HwTqldptSigSV Cnt M u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr lvtrldptSigSV Cnt M u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTG_CRt_T_endIII)	{1, 1, 1}	{0, 0, 0, 0, 0}	<u>₩</u>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	The second secon	<b>V</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 2.17 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target Dte Inst An LeaMar LeaMar Dord MetCurridation Cat 100	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	-

Test Step 2.18 (Repeat Count = 1)		<b>2</b>
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 2.19 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

LoaMgr Per1

2018-05-18, 13:31:17+0530



Input Value k\_LoaMgr\_MotAgldptSig2Resp\_Cnt\_u08 k\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08 0 k LoaMar MotAaldptSigFltThd Cnt u08 2 k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32 k LoaMar MotAaMtatnScaZeroEn Cnt lac 0 k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32 1 k\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08 2  $k\_LoaMgr\_MotCurrldptSig0Resp\_Cnt\_u08$ 2 k\_LoaMgr\_MotCurrldptSig1Resp\_Cnt\_u08 0 k\_LoaMgr\_MotCurrldptSig2Resp\_Cnt\_u08 n k\_LoaMgr\_MotCurrldptSigFltThd\_Cnt\_u08 0 k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc k LoaMgr MotCurrMtgtnSca Uls f32 1 k\_LoaMgr\_RedcdStRate\_UlspS\_f32 k\_LoaMgr\_RedcdStSca\_Uls\_f32 target\_LoaMgr\_Per1\_DiagcStsIvtr1Inactv\_Cnt\_lgc.value target\_LoaMgr\_Per1\_DiagcStslvtr2Inactv\_Cnt\_lgc.value 1 target\_LoaMgr\_Per1\_HwTqldptSig\_Cnt\_u08.value 2 target\_LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc.value 0  $target\_LoaMgr\_Per1\_MotCurrIdptSig\_Cnt\_u08.value$ 2 target\_LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08.value 2  $target\_LoaMgr\_Per1\_SteerMod\_Cnt\_enum.value$ 2 target\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc.value target LoaMgr Per1 TloaDi Cnt lgc.value  $target\_Rte\_Inst\_Ap\_LoaMgr\_Per1\_DiagcStsIvtr1Inactv\_Cnt\_Igc$ target\_LoaMgr\_Per1\_DiagcStslvtr1Inactv\_Cnt\_lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_DiagcStsIvtr2Inactv\_Cnt\_Igc target LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_HwTqldptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_HwTqIdptSig\_Cnt\_u08 target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc target LoaMgr Per1 HwTqLoaMtgtnEn Cnt lgc  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_IvtrLoaMtgtnEn\_Cnt\_Igc$ target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum target LoaMgr Per1 LOASt State enum target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc target\_LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MotCurrIdptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_MotCurrldptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc$  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08$ target\_LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08 target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_SteerMod\_Cnt\_enum target\_LoaMgr\_Per1\_SteerMod\_Cnt\_enum  $target\_Rte\_Inst\_Ap\_LoaMgr\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc$ target\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_TloaDi\_Cnt\_lgc target\_LoaMgr\_Per1\_TloaDi\_Cnt\_lgc Name **Actual Value Expected Value** LoaMgr AgSV Cnt M lgc 0 0 LoaMgr\_HwTqIdptSigSV\_Cnt\_M\_u08 0 0 LoaMgr HwTgLoaMtgtnEn Cnt M lgc 0 0 LoaMgr\_HwTqRespSV\_Cnt\_M\_u08 0 0 LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08 0 0 LoaMgr\_lvtrLoaMtgtnEn\_Cnt\_M\_lgc 0 n LoaMgr\_lvtrRespSV\_Cnt\_M\_u08 5 LoaMgr\_LOASt\_State\_M\_enum 5 5 LoaMgr\_LoaRateLimit\_UlspS\_M\_f32 500 500 LoaMgr\_LoaScaleFctr\_Uls\_M\_f32 0 n LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08 5 5 • LoaMar MotAaldptSiaSV Cnt M u08 0 0 LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc 0 0 **~** LoaMgr\_MotAgRespSV\_Cnt\_M\_u08 5 5 LoaMgr\_MotCurrIdptSigSV\_Cnt\_M\_u08 0 0 **~** LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc 1 1 LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08 2 2 LoaMgr\_TloaAvail\_Cnt\_M\_lgc 1 LoaMgr TloaDi Cnt M lgc LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08 Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) ~ {170, 173, 175} {170, 173, 175} **X** Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08 Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) {1, 1, 1} {1, 1, 1} Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data) 500 500 ± 0.01 Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data) 0 ± 0.001 0 target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value 0 0 target LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc.value 0 0 target\_LoaMgr\_Per1\_LOASt\_State\_enum.value 5 5 target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc.value 0 0 target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value 1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	jc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target LoaMgr Per1 TloaDi Cnt lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	result
LoaMgr HwTqldptSigSV Cnt M u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	· ·
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	·
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	~
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
	0	0	
LoaMgr_MotAglaptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	5	~
LoaMgr_MotAgRespSV_Cnt_M_u08		0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	•		Ž
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	_
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>'</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 2.21 (Repeat Count = 1)	18
Name	Input Value
LoaMgr AgSV Cnt M lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k LoaMgr lvtrldptSig2Resp Cnt u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtataRato_UlanS_f33	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_igc.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
5	0. 7





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•	
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.22 (Repeat Count = 1)		<b>23</b>
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0	0.1.1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_ target_LoaMgr_Per1_HwTqldptSig_Cnt_ul		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum	0-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>Y</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	<b>•</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	· ·
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	· · · · · ·
LoaMgr_LoaRateLimit_UlepS_M_f32	5	500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
Loamgr_Loascaler-ctr_Uis_im_tsz Loamgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr MotAgldptSigSV Cnt M u08		0	-
	10		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Landary Monigoristic Press, Ord. 108	Name	Input Value		
Leading Javingsprings Proc. pr. units		· ·		
Loading Principalities Lines   2				
Landary_Montengenises_Usep_102				
Landary Dischargemann   Land		1		
Leading Michael Principality (Principality	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
L. Lookgy / McCordingSigPResp, CPLL/08   0	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landary   Modification   Landary	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landay   MaiCraftingSignTime   Col. 108				
Labelage MoCartingResizeLeville   1				
Labely_McCurintginScalus_102   1				
Loading-Perf   Disposition/Intends Cert   Spowahe   1				
Image_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_LoaseStair_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady_Per_Loady_Per_LoageStairVineacy_Cort_govaine   Image_Loady_Per_Loady				
Samper   Mary   Perf   Mary   Mary   Mary		1		
Image   Loading   Perf   Loss-Seal   Crit   government		0		
Image: Landay	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
Integer   Losding Pert   Merchantipsis   Cert   Losding Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Losding   Pert   Descript   Pert   Per	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
Image   Loading   Pert   Total   Total   Loading   Loading   Pert   Total   Loading   Loading   Pert   Total   Loading   Loading   Pert   Loading   Loading   Pert   Loading   Loading   Pert   Loading   Pert   Loading   Pert   Loading   Pert   Pe	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_Loading_Pert_TroaDi_Cot_type-roll	target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
larger, Rie, Inst, Ap., Loadlyr, Louflyr, Perf. Diago-Sishirt Insert., Cnt. Igc				
Larger, Rel. nat. Ap. Loadlyr, Loadlyr, Perf. DiagosSist Virlance, Ont. Igo:				
larget_Rel_inst_Ap_LoaMyr_LoaMyr_Per1   HvTqlopKisig_Cnt_U80   target_Rel_inst_Ap_LoaMyr_LoaMyr_Per1   HvTqloaMignEn_Cnt_Ugc   target_Rel_inst_Ap_LoaMyr_LoaMyr_Per1   NotSt_State_enum   target_LoaMyr_Per1 LoASS_LoaMyr_LoaMyr_Per1 LoASS_LoaMyr_Per1 LoASS_LoaMyr_P				
target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1   MritoplandkgrinEn_Cnt_lgc   target_Rel_Inst_Ap_Loadkgr_Per1   MritoplandkgrinEn_Cnt_lgc   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_State_enum   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1_ModCurridgrisig_Cnt_ugs   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1_Toadvaldgrien_Cnt_ugs   target_Loadkgr_Per1_Toadvaldgrien_Cnt_ugs   target_Rel_Inst_Ap_Loadkgr_Loadkgr_Per1_Toadvaldgrien_Cnt_ugs   target_Loadkgr_Per1_Toadvaldgrien_Cnt_ugs   target_Loadkgr_Per1_T				
Images   Resize   Ang.   Loadwy   Loadwy   Pert   Loadwood   Loadwy   Loadwy   Loadwy   Pert   Loadwood   Loadwy   Loadwy   Loadwy   Pert   Loadwood   Loadwy   Loadwy   Pert   Loadwood   Loadwy   Loadwy   Pert   Loadwood   Loadwy   Pert   Loadwood   Loadwy   Pert   Loadwood   Loadwy   Pert   P				
target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_LoASs_Diste_enum   target_LoaMgr_Pert_LoaSs_Diste_enum   target_LoaMgr_Pert_LoaSs_Diste_enum   target_LoaMgr_Pert_LoaSs_Diste_enum   target_LoaMgr_Pert_LoaMgr_Dert_Mort_CoaMgr_Pert_Mort_C				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_Igc   target_LoaMgr_Pert_MolAgLoaMgr_Enc_Cnt_Igc   target_LoaMgr_Pert_MolAgLoaMgr_Enc_Cnt_Igc   target_LoaMgr_Pert_MolAgLoaMgr_Enc_Cnt_Igc   target_LoaMgr_Pert_MolCurrioaMgr_Enc_Cnt_Igc   target_LoaMgr_Pert_MolCurrioaMgr_Enc_Cnt_Igc   target_LoaMgr_Enc_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurrioaMgr_Enc_Cnt_Igc   target_LoaMgr_Enc_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurrioaMgr_Enc_Cnt_Igc   target_LoaMgr_Enc_Inst_Ap_LoaMgr_LoaMgr_Pert_SteenMod_Cnt_enum   target_LoaMgr_Enc_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc   target_Rite_Inst_Ap_LoaMgr_Enc_Inst_A			<del></del>	
target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MotGundMightEn_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MotGundMightEn_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MotGundMightEn_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MidPosidpisig_Cnt_u08   target_LoaMgr_Per1_MotGundMightEn_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MidPosidpisig_Cnt_u08   target_LoaMgr_Per1_MidPosidpisig_Cnt_u08   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_SteaMcQ_Cnt_enum   target_LoaMgr_Per1_SteaMcQ_Cnt_enum   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   target_LoaMgr_Per1_TioaAvail_Cnt_Igc   0				
largel_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MicOurtdoptSig_Cnt_U08   largel_LoaMgr_Per1_MicOurtdoptSig_Cnt_U08   largel_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirPostoptSig_Cnt_U08   largel_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirPostoptSig_Cnt_U08   largel_Rie_Inst_Ap_LoaMgr_Per1_MirPostoptSig_Cnt_U08   largel_Rie_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum   largel_Rie_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_Rie_Inst_Ap_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_LoaMgr_Per1_ToaDe_Cnt_Ing   largel_LoaMgr_Per1_Ing_CoaMgr_Per1_Ing_				
larget Rie_Inst Ap_LoaMgr_LoaMgr_Pert_MitrPosidptSig_Cnt_u08   target_LoaMgr_Pert_Mellor_SteemMod_Cnt_enum   target_LoaMgr_Pert_ToaMgr_P				
target_Rie_Inst_Ap_Loaklgr_Loaklgr_Pert_Tioakvail_Cnt_lgo         target_Loaklgr_Pert_Tioakvail_Cnt_lgo           target_Rie_Inst_Ap_Loaklgr_Loaklgr_Pert_TioabC_Cnt_lgo         target_Loaklgr_Pert_TioabC_Cnt_lgo           Name         Actual Value         Expected Value         Result           Loaklgr_HwTqLoakltgrEnc_Cnt_M_Llgo         0         0         0           Loaklgr_HwTqCapSigSV_Cnt_M_Llgo         0         0         0           Loaklgr_LoASL_State_M_enum         5         5         5           Loaklgr_Loakltant_Imt_Ulsps_M_132         500         500         5           Loaklgr_MaxMtglnResp_Cnt_M_Llgo         0         0         0           Loaklgr_MotAgldakltgrienc_Cnt_M_Llgo         0         0         0           Loaklgr_MotAgldakltgrienc_Cnt_M_Llgo         0         0         0           Loaklgr_MotCurrLoakltgrienc_Cnt_M_Llgo         0         0         0           Loaklgr_MotCurrLoakltgrienc_Cnt_M_Llgo         0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE	n_Cnt_lgc	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaN_Cnt_lgc         target_LoaMgr_Pert_TioaD_Cnt_lgc           Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt	_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_HwTqLoaMignEn_Cnt_M_u08         4         4         4           LoaMgr_HvtToaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_LvtTreapSV_Cnt_M_u08         5         5         0           LoaMgr_LvtTreapSV_Cnt_M_u08         5         5         5           LoaMgr_LvtTreapSV_Cnt_M_u08         5         5         5           LoaMgr_LoaRstal_mit_UlspS_M_f32         500         500         0           LoaMgr_LoaRsdalptState_M enum         5         5         5           LoaMgr_MotAglotSigSV_Cnt_M_u08         5         5         5           LoaMgr_MotAglotSigSV_Cnt_M_u08         0         0         0           LoaMgr_MotAglotSigSV_Cnt_M_u08         0         0         0           LoaMgr_MotCurrloaMgrRespC_vnt_M_u08         0         0         0           LoaMgr_MotCurrloaMgrespSV_cnt_M_u08         0         0         0           LoaMgr_MotCurrloaMgrespSV_Cnt_M_u08         0         0         0           LoaMgr_TloaAvall_Cnt_M_lgc		target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
LoaMgr_HvTqlpdSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loaklgr_HwTqicpKisjsSV_Cnt_M_u08	Name		· ·	Result
LoaMgr_HwTqLoaMighEn_Cnt_M_lgc         0         0           LoaMgr_HwTqRespSV_Cnt_M_u08         4         4           LoaMgr_WrtdoshignSV_Cnt_M_u08         0         0           LoaMgr_WrtdoshignEn_Cnt_M_lgc         0         0           LoaMgr_LwTRespSV_Cnt_M_u08         5         5           LoaMgr_LoASt_State_M_enum         5         5           LoaMgr_LoASt_State_M_enum         5         500           LoaMgr_LoaScaleFcrt_Uls_M_f32         500         500           LoaMgr_MaxMignResp_Cnt_M_u08         5         5           LoaMgr_MaxMignResp_Cnt_M_u08         5         5           LoaMgr_MotAgloabitginSpV_Cnt_M_u08         0         0           LoaMgr_MotAgloabitginEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrIdspiSpV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdspiSpV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         1         1           LoaMgr_TioaAvail_Cnt_M_lgc         0         0           LoaMgr_TioaAvail_Cnt_M_lgc         0         0           LoaMgr_TioaAvail_Cnt_M_lgc         0         0           LoaMgr_TioaAvail_Cnt_M_lgc         0         0           LoaMgr_NtipMdSrcSV_Cnt_M_u08         0<				<b>~</b>
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_IvtridptSigSV_Cnt_M_u08				
LoaMgr_IvtrLoaMtghEn_Cnt_M_log         0         0           LoaMgr_LOASt_State_M_enum         5         5           LoaMgr_LOASt_State_M_enum         500         500           LoaMgr_LoaScaleFctr_Ulls_M_f32         500         500           LoaMgr_MaxMtghrResp_Cnt_M_u08         5         5           LoaMgr_MotAgLoghtSigSV_Cnt_M_u08         0         0           LoaMgr_MotAgLoaMtghEn_Cnt_M_log         0         0           LoaMgr_MotOcurrloaMtghEn_Cnt_M_u08         5         5           LoaMgr_MotCurrloaMtghEn_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtghEn_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtghEn_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtghEn_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtghEn_Cnt_M_u08         2         2           LoaMgr_TloaDi_Cnt_M_u08         2         2           LoaMgr_TloaVail_Cnt_M_log         0         0           LoaMgr_TloaDi_Cnt_M_log         0         0           LoaMgr_TloaDi_Cnt_M_log         0         0           Rec_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Rrac_Cnt_Tenum)         (170, 173, 174)         (170, 173, 174)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Rrac_Cnt_Tenum)         (1, 1,				
LoaMgr_IvtrRespSV_Cnt_M_u08				-
LoaMgr_LoaSt_State_M_enum				
LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAglosNigsV_Cnt_M_u08         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaNtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaNtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaNtgtnEn_Cnt_M_lgc         0         0           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_NttpBgSrSV_Cnt_M_u08         0         0           LoaMgr_NtgMSrcSV_Cnt_M_u08         2         2           LoaMgr_NotDia_Cnt_M_lgc         0         0           LoaMgr_NtgBgSrSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         (170, 173, 174)         (170, 173, 174)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (1, 1, 1)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (1, 1, 1)				
LoaMgr_LoaScaleFctr_Uls_M_I32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         0         0           LoaMgr_MotCurldptSigSV_Cnt_M_u08         5         5           LoaMgr_MotCurldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurltoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurltoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_TloaDaVail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NtxtDiagMgr_SetNTCStatus(NTC_Cnt_Tenum)         (170, 173, 174)         (170, 173, 174)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         (1, 1, 1)         (1, 1, 1)         (1, 1, 1)           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)         (1, 1, 1)           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0         0         0           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0         0         0           Rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_C	·			_
LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigsV_Cnt_M_u08         0         0           LoaMgr_MotAglcanMtghtEn_Cnt_M_lgc         0         0           LoaMgr_MotAgrespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrloahttgheEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrLoaMtgheEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)				<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrloptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VtlgMotScSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         X           Rte_Write_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         X           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_LoASt_State_enum.value         5		5	5	•
LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         ***           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_LoASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value<	LoaMgr_MotAgldptSigSV_Cnt_M_u08	Ō	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         0           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}         470, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         0           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrRespSV_Cnt_M_u08       2       2         LoaMgr_TloaAvail_Cnt_M_lgc       0       0         LoaMgr_VltgMdSrcSV_Cnt_M_u08       0       0         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 174}       {170, 173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       3         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       0         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc         0         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}         4170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         0           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0				~
LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				~
LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 174}         {170, 173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 174}       {170, 173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0		-		-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				-
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				-
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				•
0 = 0 = 0 = 0 = 0		5	5	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1lr	nacty Cnt lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStsIvtr2Ir		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTgldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_t		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTgLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtr	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg	<del></del>	
			Result
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<u> </u>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



#### Test Case 3: PIM\_R Test

Specification

```
Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
```

TS3.1 5058.00 Cycles
TS3.2 4977.00 Cycles
TS3.3 4970.00 Cycles
TS3.3 4970.00 Cycles
TS3.4 5001.00 Cycles
TS3.5 5063.00 Cycles
TS3.6 5014.00 Cycles
TS3.7 4991.00 Cycles
TS3.8 5009.00 Cycles
TS3.10 4960.00 Cycles
TS3.11 5102.00 Cycles
TS3.12 5027.00 Cycles
TS3.13 5004.00 Cycles
TS3.14 4980.00 Cycles
TS3.15 4998.00 Cycles
TS3.15 4998.00 Cycles
TS3.17 5095.00 Cycles
TS3.18 5068.00 Cycles
TS3.19 4981.00 Cycles TS3.19 4981.00 Cycles TS3.20 5026.00 Cycles TS3.21 4954.00 Cycles TS3.22 5041.00 Cycles TS3.22 5014.00 Cycles TS3.24 4989.00 Cycles TS3.25 5024.00 Cycles TS3.26 4982.00 Cycles TS3.27 5011.00 Cycles TS3.28 4960.00 Cycles TS3.29 5024.00 Cycles TS3.30 5076.00 Cycles TS3.30 5076.00 Cycles TS3.31 5038.00 Cycles TS3.32 5056.00 Cycles TS3.31 5038.00 Cycles TS3.32 5056.00 Cycles TS3.33 4975.00 Cycles TS3.34 5023.00 Cycles TS3.35 4968.00 Cycles TS3.36 5057.00 Cycles TS3.37 5025.00 Cycles TS3.38 5037.00 Cycles

#### Description

Vector Description:

TS3.1LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Min TS3.2LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max TS3.2LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max
TS3.3LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Pos
TS3.4LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Min
TS3.5LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max
TS3.6LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Pos
TS3.7LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Min
TS3.8LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Max
TS3.9LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Mot
TS3.11LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08==>Min
TS3.11LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_StgSt\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_StgSt\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_StgSt\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_StgSt\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_StgSt\_Cnt\_M\_u08==>Min TS3.12LoaMgr\_VtrIdptSigSV\_Cnt\_M\_u08==>Pos
TS3.13LoaMgr\_SnsrlsMotAgAvail\_Cnt\_M\_lgc==>Min
TS3.14LoaMgr\_SnsrlsMotAgAvail\_Cnt\_M\_lgc==>Max
TS3.15LoaMgr\_MotAgRespSV\_Cnt\_M\_u08==>Min
TS3.16LoaMgr\_MotAgRespSV\_Cnt\_M\_u08==>Max
TS3.17LoaMgr\_MotAgRespSV\_Cnt\_M\_u08==>Max
TS3.17LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Min
TS3.18LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Min
TS3.19LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Pos
TS3.21LoaMgr\_VtrRespSV\_Cnt\_M\_u08==>Pos
TS3.21LoaMgr\_VtrRespSV\_Cnt\_M\_u08==>Min
TS3.22LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Pos
TS3.24LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Pos
TS3.24LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Pos
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Pos
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Pos
TS3.26LoaMgr\_HwTqLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.28LoaMgr\_HwTqLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.29LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc==>Min TS3.29LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc==>Min TS3.30LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc==>Max TS3.31LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc==>Min TS3.32LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_Igc==>Max TS3.33LoaMgr\_lvtrLoaMtgtnEn\_Cnt\_M\_Igc==>Min TS3.34LoaMgr\_lvtrLoaMtgtnEn\_Cnt\_M\_Igc==>Max TS3.35LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08==>Max
TS3.36LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08==>Min
TS3.37LoaMgr\_AgSV\_Cnt\_M\_lgc==>Max TS3.38LoaMgr\_AgSV\_Cnt\_M\_lgc==>Pos

Test Step 3.1 (Repeat Count = 1)		<b>23</b>
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_IvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMar MotAqLoaMtqtnEn Cnt M lac	0	

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LoaMgr\_Per1



Name	Input Value		
LoaMgr_MotAgRespSV_Cnt_M_u08	5		
LoaMgr_MotCurrloaMtgtnEn_Cnt_M_los	0		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	3		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1 0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr Per1 DiagcStslvtr2Inactv Cnt lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr_Der1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_Der1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	_	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>-</b>

CalclytrState

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LoaMgr\_Per1 **Actual Value Expected Value** LoaMgr\_lvtrRespSV\_Cnt\_M\_u08 5 LoaMgr\_LOASt\_State\_M\_enum 5 LoaMgr\_LoaRateLimit\_UlspS\_M\_f32 500 500 LoaMgr\_LoaScaleFctr\_Uls\_M\_f32 1 LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08 5 LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08 0 LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc 0 LoaMgr\_MotAgRespSV\_Cnt\_M\_u08 5 LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08 0  $LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc$ 1 LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08 3 LoaMgr\_TloaAvail\_Cnt\_M\_lgc 1 LoaMgr\_TloaDi\_Cnt\_M\_lgc 0 ✓✓✓✓ LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08 0 Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) {170, 172, 175} {170, 172, 175} Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08 {0, 0, 0, 0, 0}  $Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ {1, 1, 1} {1, 1, 1} Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data) 500 500 ± 0.01  $Rte\_Write\_Ap\_LoaMgr\_LoaScaleFctr\_Uls\_f32(data)$ 1 ± 0.001  $target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value$ 1  $target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc.value$ 0 target\_LoaMgr\_Per1\_LOASt\_State\_enum.value 5  $target\_LoaMgr\_Per1\_MotAgLoaMtgtnEn\_Cnt\_lgc.value$ 0 target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value Test Step Call Trace **Actual Function** Count **Expected Function** Count Result

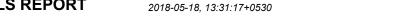
CalclytrState

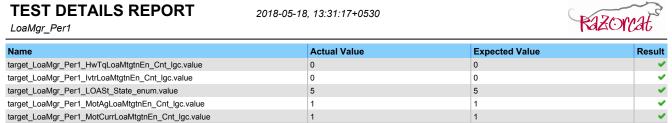
RqstRespConds 1 RqstRespConds





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	<del></del>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target LoaMgr Per1 TloaDi Cnt lgc		
	Actual Value	Funna eta d Value	Daguile
Name	0	Expected Value	Result
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	-
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrPaspSV_Cnt_M_ugs	1 2	1 2	-
LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~





Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr lvtrLoaMtgtnEn Cnt M lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr MotCurrldptSigSV Cnt M u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr MotCurrRespSV Cnt M u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte Inst Ap LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k LoaMgr lvtrldptSig1Resp Cnt u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k LoaMgr lvtrldptSigFltThd Cnt u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr lvtrMtgtnSca Uls f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k LoaMgr MotAgldptSig2Resp Cnt u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k LoaMgr MotAgSmaAvailResp Cnt u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
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Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1}       {1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001	Name	Input Value		
	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
Londing-Pert Despitable Liles   12	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Pert_Hard policytics_Cont_Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Pert_Despitation_Cont_pip value   Integrt_Loading_Cont_pip valu	k LoaMgr MotCurrMtgtnSca Uls f32	1		
		1		
Integred Loading First   DiagoStativir/Cinety, Cell Servative   Image Loading First   Loading Cell (Lode Seale   Image Loading First   Loading Cell (Lode Seale   Image Loading First   Morthologic Cell (Lode Seale   Image Loading First   Toading Cell (Lode Seale First   Image First   Image Loading First   Toading Cell (Lode Seale First   Image First   Image Loading First   Image Fir		1		
Integred Loading Fort Disagnishint/Principle (1988) and Hart Hart Spiciality (2018) and Hart Hart Hart Spiciality (2018) and Hart Hart Hart Hart Hart Hart Hart Hart		0		
Langet_Loading_FerT_McCornicidistic_Crit_UBX value   1				
		0		
Segret   Anthronogonics   Cort   Joseph   Anthronogonics   Cort   Joseph   Continger   C		1		
Image   Landay   Pert   Seemfood Ont enum value   1		3		
Integral_Loading_Per1_ToalOp_Cot_log_cvalue   1				
Image: Loading - Pert   Trios Di, Cot. Igo: value				
target_Rels_Inst_Ap_LoaMgr_LoaMgr_Pert   DespoSelsAr/Enack Cnt_lgc   target_Rels_Inst_Ap_LoaMgr_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert   Maritiquists_Cnt_uis   target_LoaMgr_Pert_Inst_C			Cnt lac	
			_ <del>_</del>	
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1   MirrOLoaMkginEn_Cnt_lgc   target_LoaMgr_Per1_MirrOLoaMkginEn_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASS_State_enum   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_LoaSS_State_Enum   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MoRourdingEn_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDu_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaDu_Cnt_lgc   target_LoaMgr_Per1_ToaDu_Cnt_lgc   target_LoaMgr_Per1_ToaDu_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_Per1_ToaDu_Cnt_lgc   target_LoaMgr_Per1_ToaDu_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_Per1_ToaDu_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_Per1_ToaDu_Cnt_lgc   target_Rel_Inst_Ap_LoaMgr_Per1_ToaDu_Cnt_lgc   target_LoaMgr_Per1_ToaDu_Cnt_lgc   ta				
larget_Ret_Inst_Ap_LoaMgr.caMgr_Pert_UnctaAftgnEn_Cnt_Upc   larget_LoaMgr_Pert_LoaMgr_Pe				
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASE_State_enum         sarget_LoaMgr_Pert_LoASE_State_enum           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_MorSpacaDi_Cnt_ligo         target_LoaMgr_Pert_MorSpacaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_MorSpacaMgr_Pert_MorSpacaMgr_Pert_MorSpacaMgr_Pert_ToaDi_Cnt_ligo           Managet_MorSpacaMgr_Pert_MorSpacaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo           Managet_MorSpacaMgr_Pert_MorSpacaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo           Managet_MorSpacaMgr_Pert_MorSpacaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo         target_LoaMgr_Pert_ToaDi_Cnt_ligo           LoaMgr_AndSyCort_M_Nigo         0         0         0           LoaMgr_MorSpackSyCort_M_Nigo         1         1         1           LoaMgr_MorSpackSyCort_M_Nigo         5         5         5           LoaMgr_MorSpackSyCort_M_Nigo         5         5         5           LoaMgr_MorSpacaMgr_Pert_MorSpacaMgr_Pert_MorSpacaMgr_P				
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolGuridptSig_Cnt_U08   target_LoaMgr_Pert_MolGuridptSig_Cnt_U08   target_Rie_Inst_Ap_LoaMgr_Pert_MolGuridptSig_Cnt_U08   target_LoaMgr_Pert_MolGuridptSig_Cnt_U08   target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolSuridptSig_Cnt_U08   target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolSuridptSig_Cnt_U08   target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_Steephood_Cnt_enum   target_LoaMgr_Pert_ToaDal_Cnt_Uge   target_LoaMgr_				
			Ent lac	
Iraget_Rie_Inst_Ap_LoakIgr_LoakIgr_Pert_Microurt.oakIgnEnc_Cnt_lgc				
larget_Rie_Inst_Ap_LoakIgr_LoakIgr_Pert_TioaAvail_Cnt_lgc         target_LoakIgr_Pert_TioaAvail_Cnt_lgc           target_Rie_Inst_Ap_LoakIgr_LoakIgr_Pert_TioaAD_Cnt_lgc         target_LoakIgr_Pert_TioaAD_Cnt_lgc           Name         Actual Value         Expected Value         Result           LoakIgr_ABV_Cnt_M_lgc         0         0         Call           LoakIgr_HwTqLoghtigisSV_Cnt_M_u08         1         1         4           LoakIgr_HwTqLoakIngtinEn_Cnt_M_lgc         1         1         4           LoakIgr_HwTqLoakIngtinEn_Cnt_M_lgc         1         1         4           LoakIgr_HwTqLoakIngtinEn_Cnt_M_lgc         1         1         4           LoakIgr_LwtLoakIngtinEn_Cnt_M_lgc         0         0         4           LoakIgr_LotAckIngtinEn_Cnt_M_lgc         1         1         4           LoakIgr_LotAckIngtinEn_Cnt_M_lgc         5         5         5           LoakIgr_LoakIngtinEn_Cnt_M_lgc         5         5         5           LoakIgr_LoakIngtinEn_Cnt_M_lgc         0         0         4           LoakIgr_MotAglackIngtinEn_Cnt_M_u08         5         5         5           LoakIgr_MotAglackIngtinEn_Cnt_M_u08         1         1         1           LoakIgr_MotAglackIngtinEn_Cnt_M_u08         1         1 <th< td=""><td></td><td></td><td></td><td></td></th<>				
larget, Rie_Inst, Ap_LoaMgr_LoaMgr_Pert_TioaNr_Cnt_Igc   target_LoaMgr_Pert_TioaNr_Cnt_Igc   target_LoaMgr_Pert_TioaDr_Cnt_Igc   target_LoaMgr_Pert_TioaDr_Cnt_Igc				
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc         target_LoaMgr_Pert_TloaDi_Cnt_lgc         Result           LoaMgr_AgsV_Cnt_M_lgc         0         0            LoaMgr_HwTqtoptSigsV_Cnt_M_u08         1         1         1           LoaMgr_HwTqtoptSigsV_Cnt_M_u08         3         3            LoaMgr_HvtTqcMgtSigsV_Cnt_M_u08         1         1         1           LoaMgr_HvtTqcMgtSigsV_Cnt_M_u08         1         1         1           LoaMgr_HvtTqcMgtSigsV_Cnt_M_u08         5         5         5           LoaMgr_HvtTqcspSV_Cnt_M_u08         5         5            LoaMgr_LoaRateLimit_UlspS_M_G2         0         0            LoaMgr_LoaRateLimit_UlspS_M_f32         500         500            LoaMgr_LoaRateLimit_UlspS_M_f32         0         0            LoaMgr_MotAjcpIsiQsV_Cnt_M_u08         5         5            LoaMgr_MotAjcpIsiQsV_Cnt_M_u08         1         1         1           LoaMgr_MotAjcpIsiQsV_Cnt_M_u08         1         1         1           LoaMgr_MotCurrloaNigtnEn_Cnt_M_igc         1         1         1           LoaMgr_MotCurrloaNigtnEn_Cnt_M_igc         1         1         1			1	
Name         Actual Value         Expected Value         Result           LoaMrg_AgsV_Cnt_M_Ugc         0         0         0           LoaMrg_HwTqlcpdSigsV_Cnt_M_Ugc         1         1         1           LoaMrg_HwTqlcaMignEn_Cnt_M_Ugc         1         1         1           LoaMrg_HvTqlcaSigsV_Cnt_M_Ug8         3         3         3           LoaMrg_HvTrlosSigsV_Cnt_M_Ug8         1         1         1           LoaMrg_HvTrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMrg_LvTrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMrg_LvTrLoaMignEn_Cnt_M_lgc         5         5         5           LoaMrg_LvTrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMrg_LvTrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMrg_LoaRignEn_Cnt_M_lgn         5         5         5           LoaMrg_LoaRignEn_Cnt_M_ug8         1         1         1           LoaMrg_LoaRignEn_Cnt_M_ug8         1         1         1           LoaMrg_McQurityResp_Cnt_M_ug8         1         1         1           LoaMrg_McQurityResp_Cnt_M_ug8         1         1         1           LoaMrg_McQurityResp_Cnt_M_ug8         2				
LoaMgr_HwTqIdpISigSV_Cnt_M_u08			le	I
LoaMgr_HwTqlcpRisgSV_Cnt_M_u08			·	
LoaMgr_HWTQLoaMtgInEn_Cnt_M_lgc		· ·		
LoaMgr_HwTqRespSV_Cnt_M_u08	· · · · · ·			
LoaMgr_IvtrldptSigSV_Cnt_M_u08				
LoaMgr_IvtrLoaMtgthEn_Cnt_M_Igc				
LoaMgr_NtrRespSV_Cnt_M_u08				-
LoaMgr_LOASL_State_M_enum         5         5           LoaMgr_LoaRateLimit_UlspS_M_f32         500         500           LoaMgr_MaxMtgtnResp_Cntl_M_u08         5         5           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotAgRespSV_Cnt_M_u08         2         2           LoaMgr_MotCurrlositigSV_Cnt_M_u08         1         1           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VitgMSrcSV_Cnt_M_ u08         0         0           Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}           Rte_Call_Ap_LoaMgr_LoaScaleFctr_Uls_S2(data)	· · · · · · · · · · · · · · · · · · ·			
LoaMgr_LoaRateLimit_UispS_M_f32				
LoaMgr_LoaScaleFctr_Uls_M_f32         0         0         ✓           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5         ✓           LoaMgr_MotAglothSigsV_Cnt_M_u08         1         1         1           LoaMgr_MotAgleapSy_Cnt_M_u08         2         2         ✓           LoaMgr_MotCurrldptSigsV_Cnt_M_u08         1         1         1           LoaMgr_MotCurrldptSigsV_Cnt_M_u08         1         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum)         (170)         (170)         √           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         (1)         (0,0,0,0,0)         X           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1)         (1)         (0,0,0,0,0)         X           Rte_Unite_Ap_LoaMg				
LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5 5 5				
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         4           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         1         1         4           LoaMgr_MotAgRespSV_Cnt_M_u08         2         2         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         4           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         4           LoaMgr_TloaDi_Cnt_M_lgc         0         0         4           LoaMgr_VitgMdSrcSV_Cnt_M_u08         0         0         4           Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {170}         4           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_U18pS_322(data)         500         500 ± 0.01         2           Rte_Write_Ap_LoaMgr_LoaRateLimit_U18pS_322(data)         0 <td></td> <td></td> <td></td> <td></td>				
LoaMgr_MotAgLoaMtgnEn_Cnt_M_lgc         1         1         ✓           LoaMgr_MotAgRespSV_Cnt_M_u08         2         2         ✓           LoaMgr_MotCurrloqtSigSV_Cnt_M_u08         1         1         1         ✓           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         1         ✓           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2         ✓           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1         ✓           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0         ✓           LoaMgr_NtdRoscSV_Cnt_M_u08         0         0         0         ✓           Rte_Call_Ap_LoaMgr_NtdDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {1770}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cotatus_Cnt_T_enum)         {1}         {1}         ✓           Rte_Wite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus_Cnt_T_enum)         {1}         {1}         ✓           Rte_Wite_Ap_LoaMgr_LoaRateLimit_UlspS_52(data)         500         500 ± 0.01         ✓           Rte_Wite_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         ✓				-
LoaMgr_MotAgRespSV_Cnt_M_u08         2         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VitgMdSrcsV_Cnt_M_u08         0         0           LoaMgr_VitgMdSrcsV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {1770}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         ★           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         ★           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         ★           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         4           target_LoaMgr_Per1_UntLoaMtgtnEn_Cnt_lgc.value         5         5	LoaMgr_MotAgldptSigSV_Cnt_M_u08			
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       1       1       4         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       1       1       2         LoaMgr_MotCurrRespSV_Cnt_M_u08       2       2       2         LoaMgr_TloaAvail_Cnt_M_lgc       1       1       1         LoaMgr_TloaDi_Cnt_M_lgc       0       0       0         LoaMgr_VttgMdSrcSV_Cnt_M_u08       0       0       0         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170}       {170}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1}       {0, 0, 0, 0, 0}       3         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1}       {1}       {1}         Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1}       {1}       **         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       **         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       **         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       **         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       **         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1       **				~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {170}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}         **           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         **           rtarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         **           rtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         1         1         **			2	~
LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {170}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           rte_Write_Ap_LoaMgr_Deral_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         **           rtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         0         0         **           rtarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         **           rtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         1         1         **	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc         1         1         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         0           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {170}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         {1}         ✓           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         ✓           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         ✓           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         1         1         ✓		1	1	~
LoaMgr_TloaDi_Cnt_M_lgc         0         0         ✓           LoaMgr_VltgMdSrcSV_Cnt_M_u08         0         0         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170}         {170}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1}         {1}         ✓           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         ✓           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         ✓           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         1         1         ✓	·			~
LoaMgr_VltgMdSrcSV_Cnt_M_u08       0       0       ✓         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170}       {170}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1}       {1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         **arget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         **target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         **target_LoaMgr_Per1_LOASt_State_enum.value       5       5         **target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1				<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170}       {170}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1}       {0,0,0,0,0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1}       {1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1	LoaMgr_TloaDi_Cnt_M_lgc			~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         {1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         {1}         {1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         1         1	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)       {1}       {1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1		Time in the second seco	Ti i	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1			{0, 0, 0, 0, 0}	×
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       ✓         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       ✓         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0       ✓         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       ✓         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1       ✓			{1}	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       1       1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 vtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 vtarget_LoaMgr_Per1_MotAgLoaMgr_Per1_			0 ± 0.001	
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 4 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
0 = 0 = = 0 0 = = = 0	target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Input Value		
LoaMgr_AgSV_Cnt_M_lge	Test Step 3.4 (Repeat Count = 1)	
Loakigr. HwTqlothSigSV_Cnt.M_u08	Name	
Loading: HwTqLoadingtinEr, Cnt, M. logs         0           Loading: HwTqRespSV, Cnt, M. u08         2           Loading: HwTqLoadingtinEr, Cnt, M. logs         0           Loading: MxthodingtinEr, Cnt, M. logs         0           Loading: MxthodingtinEr, Cnt, M. u08         1           Loading: MxthodingtinEr, Cnt, M. u08         0           Loading: MxthodingtinEr, Cnt, M. u08         0           Loading: MxthodingtinEr, Cnt, M. u08         5           Loading: MxthodingtinEr, Cnt, M. u08         1           Loading: MxthodingtinEr, Cnt, M. u08         1           Loading: MxthodingtinEr, Cnt, M. u08         1           Loading: Mxthodination MightinEr, Cnt, M. u08         1           Loading: Mxthodination MightinEr, Cnt, M. u08         1           Loading: Mxthodination MightinEr, Cnt, M. u08         1           Loading: CurrindingtinEr, Dxt, u08         1           Loading: CurrindingtinEra, U18pS, 122         1           L. Loading: CurrindingtinEra, U18pS, 122         1           L. Loading: CurrindingtinEra, U18pS, 122         1           L. Loading: HwTqldrisingtinToavaritesp. Cnt, u08         2           L. Loading: HwTqldrisingtinEra, U18pS, 122         1           L. Loading: HwTqldrisingtinSingtinEra, Cnt, u08         1           L. Loading: HwTqldrisingt		
Loading	LoaMgr_HwTqldptSigSV_Cnt_M_u08	
Loaklgr_IntricakltgniEn_Cnt_M_ige	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	
Loaking_IntricoskingtnEn_Cnt_M_lige	LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_WrRespSV_Cnt_M_u08	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loaklgr_MotAglopSigSV_Cnt_M_u08	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
Loaklgr_MotAglt-oaktignEn_Cnt_M_lgc	LoaMgr_lvtrRespSV_Cnt_M_u08	1
Loaklgr_MotCurriqptisjSV_Cnt_M_u08	LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrtoptSigSV_Cnt_M_u08	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrLeasPSV_Cnt_M_u08	LoaMgr_MotAgRespSV_Cnt_M_u08	5
Loaldgr_MotCurRespSV_Cnt_M_u08         1           Loaldgr_SnistMotAgAvail_Cnt_M_lgc         1           Loaldgr_SnistMotAgAvail_Cnt_M_lgc         0           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_Loaldgr_CurrivrMtgtRste_UlspS_f32         1           k_LoaMgr_CurrivrMtgtmSca_Uls_f32         1           k_LoaMgr_EadeOutsRate_UlspS_f32         1           k_LoaMgr_HwTqldptSigtTloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSigtTloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSigtTloaDirSep_Cnt_u08         2           k_LoaMgr_HwTqldptSigtTloaDirSep_Cnt_u08         2           k_LoaMgr_HwTqldptSigtTloaDirSep_Cnt_u08         0           k_LoaMgr_HwTqldptSigtTloaDirSep_Cnt_u08         0           k_LoaMgr_HwTqldptSigtTloaDirSep_Cnt_u08         0           k_LoaMgr_HwTqldptSigtTlotEn_Eng_Cnt_u08         0           k_LoaMgr_HwTqldptSigtTltCo_Lu08         1           k_LoaMgr_HwTqldptSigtTltCo_Lu08         0           k_LoaMgr_HwTqltgtisigtTltCo_Lu08         0           k_LoaMgr_HwTqltgtisigtTltCo_Lu08         0           k_LoaMgr_IvrtdptSigtResp_Cnt_u08         0           k_LoaMgr_IvrtdptSigtResp_Cnt_u08         0           k_LoaMgr_IvrtdptSigtResp_Cnt_u08         1           k_LoaMgr_Ivrtdp	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_VitgMdSrcSV_Cnt_M_u08	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_VitgMdSrcSV_Cnt_M_u08	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1.
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NfoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_IvtrlqtptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAg	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
k_LoaMgr_CurrivtrMigtnSca_Uis_132         1           k_LoaMgr_FadeOutStRate_UispS_132         1           k_LoaMgr_HwTqldptSigOTloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSigOTloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotA	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtMtgtnSca_Uls_f32         1           k_LoaMgr_EndeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         1           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1SigFitThd_Cnt_u08         5           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_HavTqldptSigOTloaAvaliResp_Cnt_u08         2           k_LoaMgr_HwTqldptSigTNoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvaliResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1TloaDiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqMgtnRate_UlspS_f32         1           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig5ItThd_Cnt_u08         0           k_LoaMgr_IwtrldtgtnSca_Uls_f32         1           k_LoaMgr_IwtrldtgtnSca_Uls_f32         1           k_LoaMgr_IwtrldtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSig		0
k_LoaMgr_FadeOutSiRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1TloaDxvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSa_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrltgtnSca_Uls_f32         1           k_LoaMgr_IvtrltgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFiThd_Cnt_u08         0           k_LoaMgr_MotAgldptSigFiThd_Cnt_u08         0           k_LoaMgr_MotAgldptSigFiThd_Cnt		1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqMtghrRate_UlspS_J32         1           k_LoaMgr_HwTqMtghrSca_Uls_f32         1           k_LoaMgr_IvtrlqbtSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1FitTnd_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Rate_UlspS_j32         1           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig7Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig7Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_M		1
k_LoaMgr_HwTqldptSig1NoTioaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TioaAvaiiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TioaDirResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtgtnSca_Uis_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_IvtrMtgtnSca_Uis_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotAgldptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotAgldptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Res		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Rtnate_UlspS_f32         0           k_LoaMgr_IvtrMtgtnSca_Eore_n_Cnt_los         0           k_LoaMgr_IvtrMtgtnSca_Eore_n_Cnt_los         0           k_LoaMgr_IvtrMtgtnSca_Core_n_Cnt_los         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1Sresp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Fesp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAglMtgtnScaZeroEn_Cnt_u08         2           k_LoaMgr_MotAglMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAglMtgtnScaZeroEn_Cnt_u08         2           k_LoaMgr_MotAglMtgtn		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFitTnd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSigFitTnd_Cnt_u08       0         k_LoaMgr_IvtrMgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig1Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig1Resp_Cnt_u08       5         k_LoaMgr_MotAgldptSig1Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgldtgtRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtRate_UlspS_f32       1		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08       2         k_LoaMgr_MotAgldtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgldtgtnScaZeroEn_Cnt_igc       0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_IvtrldptSigFitTnd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_IvtrMgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc       0         k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc       0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 1 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0		
k_Loanigr_motAgnitgtnsca_Dis_f32		
1 1 M M M 0 A 1 ID 0 1 00		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1		
k_LoaMgr_RedcdStRate_UlspS_f32		
k_LoaMgr_RedcdStSca_Uls_f32		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value 1	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr MotCurrRespSV Cnt M u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•
	·		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



LoaMgr\_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.6 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



le Lander Combathatacon Tour Co. Out les	niput value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k LoaMgr lvtrldptSigFltThd Cnt u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k LoaMgr MotAgldptSigFltThd Cnt u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
	4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	ent lac	
		_ ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_t		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	0	0	
LoaMgr_AgSV_Cnt_M_lgc			_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08			_
0	2	12	_
LoaMar LOASt State M enum	2	2	
LoaMgr_LoaN_state_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	5 500	5 500	<b>v</b>
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 500 1	5 500 1	*
LoaMgr_LoaRateLimit_UlspS_M_f32	5 500	5 500	<b>v</b>
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 500 1	5 500 1	*
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 500 1 5	5 500 1 5	*

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.7 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enui	m 	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1=	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Y
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	<b>-</b>
LoaMgr_LoASt_State_M_enum	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	-
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	4	4	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	-
LoaMgr MotCurrRespSV Cnt M u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	·
LoaMgr_TloaDi_Cnt_M_lgc	•	0	
20d.ingd2oning.	0		
LoaMar VltgMdSrcSV Cnt M u08	0		<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)			~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 {170, 173, 174}	1 {170, 173, 174}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 {170, 173, 174} {1, 1, 1}	1 {170, 173, 174} {0, 0, 0, 0, 0}	× ×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 {170, 173, 174} <b>{1, 1, 1</b> } {1, 1, 1}	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1}	<u> </u>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 {170, 173, 174} <b>{1, 1, 1</b> } {1, 1, 1} 500	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	32 32 34
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1 1 1 1	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	**************************************
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1 1 1 5	1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 5	**************************************



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
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Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.9 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2 0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtetaCoaZosaEn_Cot loa	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k LoaMgr MotCurrldptSig0Resp Cnt u08	2
k LoaMgr MotCurrldptSig1Resp Cnt u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1 0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScant_Gnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



LoaMgr\_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.10 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
· •= =			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2 0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 0 2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2 0 2 1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	2 0 2 1	nt lac	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	2 0 2 1	nt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	2 0 2 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr	nt_lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc _lgc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	nt_lgc _lgc	
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	nt_lgc _lgc c t_lgc	
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name	2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc _lgc c t_lgc 08 nnt_lgc	Result
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotOurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	_lgc _lgc _c t_lgc _08 sint_lgc 8  Expected Value 0	~
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotOurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ccc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cccc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ccccc target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 1	int_lgc  _lgc c t_lgc 08 int_lgc 8  Expected Value 0	<b>V</b>
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQuraldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	int_lgc _lgc c t_lgc 08 int_lgc 8  Expected Value 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlcaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaSt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQuraltptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0	int_lgc  _lgc c  t_lgc 08 int_lgc 8  Expected Value 0 1 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_log target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 0 0 0 5	Expected Value  0 1 1 0 0 0 5	***
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_Indeltslg_Cnt_u08 target_LoaMgr_Per1_Indeltslg_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_MrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 0 0 0 5 5 5	Ligc	***
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_ADi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_log target_LoaMgr_Per1_MotAgaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 0 0 0 5 5 5 5000	Ligc	***
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_ADi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 0 0 0 5 5 5 5000 0	Ligc	******
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqlcoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 0 0 0 5 5 5 500 0 0 5	Ligc   Ligc	***
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 0 0 0 5 5 5 5000 0	Ligc	******

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.11 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Innut Value		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inact	v_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inact	v_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnB	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>Y</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	5	5	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	<b>V</b>
	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 0	1 ± 0.001 0	· ·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0	1 ± 0.001 0 0	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 0 0 5	1 ± 0.001 0 0 5	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0	1 ± 0.001 0 0	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k LoaMgr MotCurrldptSig0Resp Cnt u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
gg org	1.





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	/ (COUIT
LoaMgr HwTqldptSigSV Cnt M u08	1	1	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr MotCurrRespSV Cnt M u08	3	3	
LoaMgr TloaAvail Cnt M lgc	1	1	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 175}	{170, 175}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC_CITE_1_enum)	{1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Simple   S	Test Step 3.13 (Repeat Count = 1)	
Loading   Amin's   Loading   Loadi		
Leading Hart Goodball Co. M. Ng.  Leading Hart Co. M. Ng.  Leading Hart Co. M. Ng.  Leading Hart Leading Hart Co. M. Ng.  Leading Hart Leading Hart Co. M. Ng.  Leading Hart Leading Leading Co. M. Ng.  Leading Hart Leading Leading Leading Co. M. Ng.  Leading Hart Leading Leading Leading Co. M. Ng.  Leading Hart Leading Leading Leading Leading Co. M. Ng.  Leading Hart Leading Leadi		·
Leading   Lead		
Leading Junifrication Com Music Leading Junifrication Com Musi		
Loading		
Loading   Marked (Cop M   Load   Lo		
Cooling   Michigangle   Cert   M. 196		
Louding: Moderage Sty Crit M. Us6   3	LoaMgr_lvtrRespSV_Cnt_M_u08	5
Londing / MacCurricality (See V. Ord. M. 108   1   1   1   1   1   1   1   1   1	LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
Louding McCurrados(part). Cell M 198  Louding McCurrados(part). Cell M 198  Louding McCurrados(part). Cell M 199  Louding McCurrados(part). Cell M 199  Louding May Marked Cell M 199  Louding Marked M 199  Louding	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
Looking / MortCare analogomic Corpt M. Jos   Looking / Septembook P. Corpt M. Jos   Looking / Septembook P. Corpt M. Jos   Looking / Septembook P. Corpt M. Jos   Rite / Looking / Looki	LoaMgr_MotAgRespSV_Cnt_M_u08	3
Loading - Mayabases V - Citt M -	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	
Loadey_ NumberSey Cen_L M_ 100		
Loading _ Juny Assertion _ Loading		
Reg.   Inst. Ap.   Loadlog	· ·	
L. Looking. Currich/MighResize_Uries_Crut. pte		
		V = V
L. Loadby_L-MindpiSign_Labs_St_122		
L. Loading-   The Topics   T		
L. Looking-Introductions  Introductions  Crit. USB		
Loading-In-VirgidoSign Troboulinescy_Crd_USS		
L. Looking-In-WritightSigThook-Wallersp, Cht. U08	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	
L. Looking-In-Wrightsig2PTotal-Raiseg_Cnt_U88		
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
K_Lookig-Hw7foltginRate_Uisp5_12	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
Loading-Invirightsprace_Dru_08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_MridpiSig2Resp_Cnt_u08         0           k_LoaMgr_MridpiSig2RishEnd_Cnt_u08         0           k_LoaMgr_MridpiSig2Resp_Cnt_u08         0           k_LoaMgr_MridpiSig2Resp_Cnt_u08         1           k_LoaMgr_MolApidpiSig1Nesp_Cnt_u08         5           k_LoaMgr_MolApidpiSig2Resp_Cnt_u08         1           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         0           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         1           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         2           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         2           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         1           k_LoaMgr_MolApidpiSig3Resp_Cnt_u08         2           k_LoaMgr_MolApidpiSig2Resp_Cnt_u08         2           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         2           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         2           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurdispiSig2Resp_Cnt_u	k_LoaMgr_HwTqMtgtnSca_Uls_f32	
K, LoaMg_NrtMtgnRate_UlipS_132         1           K, LoaMg_NrtMtgnRate_UlipS_132         1           K, LoaMg_NrtMtgnRate_UlipS_132         1           K, LoaMg_NrtMtglotSigNoSmaResp_Cnt_u08         5           K, LoaMg_MotAglotSigResp_Cnt_u08         1           K, LoaMg_MotAglotSigResp_Cnt_u08         0           K, LoaMg_MotAglotSigResp_Cnt_u08         2           K, LoaMg_MotCuridptSigResp_Cnt_u08         2           K, LoaMg_MotCuridptSigResp_Cnt_u08         0           K, LoaMg_MotCuridptSigResp_Cnt_u08         0 <td></td> <td></td>		
LoaMgr_IntMitghisGazPeriEn_Cnt_Ug8		
K. LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           K. LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           K. LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           K. LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           K. LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           K. LoaMgr_MotAglMtgnRate_UlspS_132         1           K. LoaMgr_MotAglMtgnSca2reofn_Cnt_u05         2           K. LoaMgr_MotAglMtgnRate_UlspS_132         1           K. LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K. LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           K. LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K. LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           K. LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           K. LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           K. LoaMgr_RodSiStar_us_is_32         1           K. LoaMgr_RodSiStar_us_is_32         1           K. LoaMgr_Pot_Disig2SiStar_us_is_32         1           LoaMgr_P		
LoaMgr_MotAgldptSig2Resp_Cnt_u08		
k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgidptSigFiThd_Cnt_u08         2           k_LoaMgr_MotAgidptSinac_Uslps_G22         1           k_LoaMgr_MotAgidptSinac_Usl_f32         1           k_LoaMgr_MotAgidptSinac_Usl_f32         1           k_LoaMgr_MotAgidptSinac_Usl_f32         1           k_LoaMgr_MotAgidptSinac_Usl_f32         1           k_LoaMgr_MotAgidptSinac_Usl_f32         2           k_LoaMgr_MotOurridptSinac_Sp_Cnt_u08         2           k_LoaMgr_MotCurridptSinac_Sp_Cnt_u08         0           k_LoaMgr_MotCurridptSinac_Sp_Cnt_u08         0           k_LoaMgr_MotCurridptSinac_Usl_f32         1           k_LoaMgr_MotCurridptSinac_Usl_f32         1           k_LoaMgr_MotCurridptSinac_Usl_f32         1           k_LoaMgr_MotCurridptSinac_Usl_f32         1           k_LoaMgr_RedodSis_Rale_Usl_f32         1           k_LoaMgr_RedodSis_Rale_Usl_f32         1           k_LoaMgr_RedodSis_Rale_Usl_f32         1           k_LoaMgr_RedodSis_Rale_Usl_f32         1           k_LoaMgr_Per1_DiagcSis_Usl_f32         1           k_LoaMgr_Per1_DiagcSis_Usl_f32         1           k_LoaMgr_Per1_DiagcSis_Usl_f32         1           k_LoaMgr_Per1_DiagcSis_Usl_f32         1 <td< td=""><td></td><td></td></td<>		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_EroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Us_152         1           k_LoaMgr_MotAgMtgtnSca_Us_152         1           k_LoaMgr_MotCurridptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSig-UspS_f32         1           k_LoaMgr_MotCurridptSig-UspS_f32         1           k_LoaMgr_RedGSISca_Uis_f32         1           k_LoaMgr_Perd_DiagcStstvtrIlnactv_Cnt_lgc.value         1           target_LoaMgr_Perd_DiagcStstvtrIlnactv_Cnt_lgc.value         0           target_LoaMgr_Perd_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Perd_MrcTlotptSig_Cnt_u08 value         0           target_LoaMgr_Perd_MrcTlotptSig_Cnt_u08 value         0           target_LoaMgr_Perd_MrcTlotptSig_Cnt_u08 value         0           target_LoaMgr_Perd_MrcTlotptSig_Cnt_u08 value         0           target_LoaMgr_Perd_TloaAgvai_Cnt_lgc.value         0           target_LoaMgr_Perd_MrcTloaAgvai_Cnt_lgc.value	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_Igc         0           k_LoaMgr_MotAgMtgtnSca_Uis_132         1           k_LoaMgr_MotCurridptSigOResp_Cnt_U08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_U08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_U08         1           k_LoaMgr_MotCurridptSig2CaZeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2CaZeroEn_Cnt_Igc         1           k_LoaMgr_RedcdSitSca_Uis_132         1           t_LoaMgr_RedcdSitSca_Uis_132         1           t_arget_LoaMgr_Per1_DiagcStshtr/Inactv_Cnt_Igc.value         0           target_LoaMgr_Per1_DiagcStshtr/Inactv_Cnt_Igc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_U6x-value         0           target_LoaMgr_Per1_MitPosidptSig_Cnt_U6x-value         0           target_LoaMgr_Per1_ToaAvail_Cnt_Igc.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_Igc.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_Igc.value	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Sig2         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_Per1_DiagcStsivtr1nactv_Cnt_lgc_value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_u08 value         0           target_LoaMgr_Per1_DiagcAgMgr_Per1_DiagcAgMarte         0           target_LoaMgr_Per1_SteerMod_Cnt_lecvalue         0           target_LoaMgr_Per1_SteerMod_Cnt_lecvalue         0 <td>k_LoaMgr_MotAgMtgtnRate_UlspS_f32</td> <td>1</td>	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotOurdptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurrdptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurrdptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurrdptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurrdptSigFIThd_Cnt_u08         0           k_LoaMgr_MotCurrdptSigFIThd_Cnt_u08         0           k_LoaMgr_MotCurrMigtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMigtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_igc_value         0           target_LoaMgr_Pert_DiagcStsIvtr2Inactv_Cnt_igc_value         1           target_LoaMgr_Pert_MotCurrdptSig_Cnt_u08_value         0           target_LoaMgr_Pert_MrPosidptSig_Cnt_u08_value         0           target_LoaMgr_Pert_MrPosidptSig_Cnt_u08_value         2           target_LoaMgr_Pert_MrPosidptSig_Cnt_u08_value         0           target_LoaMgr_Pert_TincaAvaii_Cnt_igc_value         0           target_LoaMgr_Pert_TincaAvaii_Cnt_igc_value         0           target_Re_inst_Ap_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_igc         target_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_igc           target_Re_inst_Ap_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_igc         target_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_igc           ta	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig1str2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2Sig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2Sig2Sig2Sig2Sig2Sig2Sig2Sig2	k_LoaMgr_MotAgMtgtnSca_UIs_f32	
k_LoaMgr_MotCurridptSig1Resp_Cn_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cn_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMigtnScat_Sig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurrMigtnScat_Sig2Sig2         1           k_LoaMgr_MotCurrMigtnScat_Uis_Fig2         1           k_LoaMgr_RedcdStRate_UispS_632         1           k_LoaMgr_RedcdSitSca_Uis_Fig2         1           target_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MrOtCurridptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MrPotGptSig_Cnt_u08_value         2           target_LoaMgr_Per1_ToADidSig_Cnt_u08_value         0		
k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSigFIThd_Cnt_u08         0           k_Loaklgr_MotCurridptSigFIThd_Cnt_u08         1           k_Loaklgr_MotCurridptSigSa_CreEn_Cnt_lgc         1           k_Loaklgr_MotCurridptSigSa_CreEn_Cnt_lgc         1           k_Loaklgr_MotCurridptSigSa_CreEn_Cnt_lgc         1           k_Loaklgr_RedcdSitSa_Uls_f32         1           k_Loaklgr_RedcdSitSa_Uls_f32         1           target_Loaklgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_Loaklgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         0           target_Loaklgr_Per1_HwTqlpfSig_Cnt_u08.value         0           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_Loaklgr_Per1_MrPosidptSig_Cnt_u08.value         2           target_Loaklgr_Per1_MrPosidptSig_Cnt_u08.value         0           target_Loaklgr_Per1_TioaAvail_Cnt_lgc.value         0           target_Loaklgr_Per1_TioaAvail_Cnt_lgc.value         0           target_Loaklgr_Per1_TioaAvail_Cnt_lgc.value         0           target_Re_Inst_Ap_Loaklgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_Loaklgr_Loaklgr_Per1_		
k_LoaMgr_MotCurridptSigFltTnd_Cnt_u08         0           k_LoaMgr_MotCurridptnState_UlspS_132         1           k_LoaMgr_MotCurrMtgtnScazeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_132         1           k_LoaMgr_RedcdSiRate_UlspS_132         1           k_LoaMgr_RedcdSiSca_Uls_132         1           target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPostptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MrDqLoaMgr_Dcn_uous.value         2           target_LoaMgr_Per1_TloaDqLort_lgc.value         0           target_LoaMgr_Per1_TloaDqLort_lgc.value         0           target_Re_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08         target_LoaMgr_Per1_HvTqldptSig_Cnt_u08           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_VrtLoaSttgtnE		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32 t_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 t_target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_IntPosldptSig_Cnt_u08.value target_LoaMgr_Per1_TloaDic_nt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDic_nt_lgc.value target_LoaMgr_Per1_TloaDig_Cnt_u08.value 0 target_LoaMgr_Per1_TloaDig_Cnt_u08.value target_LoaMgr_Per1_DoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_IoaNgr_LoaMgr_Deta_Uls_value target_LoaMgr_Per1_IoaNgr_LoaMgr_Deta_Uls_value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_Enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_Enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_Enum target_Re_Inst_Ap_LoaMgr_Per1_LoaSc_State_Enum target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtmSca_Uls_f32		
k_LoaMgr_RedcdStRate_UlspS_f32 1  k_LoaMgr_RedcdStSca_Uls_f32 1  target_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value 1  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_Ret_inst_Ap_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaScsDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc		0
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target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 2  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
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target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Ina		
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_ne_met_rp_teamign_teamign_rent_motedinapterig_ent_table	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.14 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 2		
k LoaMgr MotCurridptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_UIs_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Der1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum	<del>_</del> -	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enui	m	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Dogu.
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	-
Louingi_ivantespov_oni_ivi_doo		5	<b>-</b>   <b>✓</b>
LoaMgr_LOASt_State_M_enum	5		
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	500	0	
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 0 5	0 5	~
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	500	0	

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.15 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	Input Value		
Name			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr1Inac	ty Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lge		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
	3	3	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0		
LoaMgr_TloaAvail_Cnt_M_lgc		0	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	**
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value			
	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	1 5	5	· ·
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1 5 1	5 1	~
target_LoaMgr_Per1_LOASt_State_enum.value	1 5	5	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.16 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
	·		

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Indicates   Indi	Test Step 3.17 (Repeat Count = 1)	13
Landery _ Barry Cont   Jigs		
Leading In-Indipolation Co. M. 19  Leading In-Indipolation Co. M. 19  Leading Indipolation Co.		·
	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Leading   Juniferpings   Cort   M. 100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Loading - Michagos (C. M. 1986)	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Landary, Medigensigner, Cert. JU, ger Landary, Madagensigner, Cert. JU, ger Landary, Controllaries, Cert. Jugs Landary,	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading   MacApples (Part   Muse)	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading   Amalgraphing   Cort   Ungre		
Looking MacContack (SSY Co. M. 1.08  Looking MacContack (SSY Co. M. 1.08  Looking MacContack (SSY Co. M. 1.09  Looking Mac		
Londing Modurant professory Crist M. 196		1
Landagy Land-Care-Registry Cort M. 199  Looking S. Resides/Caphanal, Crist M. 199  Looking S. Resides/Caphanal, Crist M. 199  Landagy Currinor/Statistical Uses 5 2  Landagy Landagy Trans-Care-Registry Cort. 198  Landagy Landagy Trans-Care-Registry Cort. 198  Landagy Landagy Landagy Currinor Uses 5 2  Landagy Landagy Landagy Landagy Currinor Uses 5 2  Landagy L		
Loading	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading	LoaMgr_MotCurrRespSV_Cnt_M_u08	4
Has Jeach Journal Age Loading  Loading Currin Might Residue Uses 522  Loading Currin Might Residue Uses 522  Loading Furth Res	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landing Controllingtonia (Ling 1925)	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Landing	Rte_Inst_Ap_LoaMgr	V = V
Loading-FairCodingName_Ulass_S122		
Loading / Info@pdisp(Toalvailless) 072		
L. Landary_InvTopidSignTrackAvaiReap_Cnt_U68		
Lankgr   Mr Glopisg   Na Tolariseg   Cht   Juli   Lankgr   Mr Glopisg   Transvallers   Cht   Juli   Cht   Cht   Cht   Cht   Cht   Cht   Cht   Ch		
L. Loads/g. Har-Topic/Sig/Train-Enrice Cnt_U08		
Landary   Mindpoissy   Temporation   Century	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Lankgr_Hw7qkgriSg4ftep_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
L. Loading-HwTqdNgriSpFITTIC_Cnt_U08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
K_Loakigs_HwTqMtghrRate_UlspS_132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K. Loaking/ Intridisting/Resp_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loading   NrtigisSig1Resp_Cnt_U08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
LoaMgr_NtridgnSigPitThd_Crt_u08		
Loaking		
Loading   IntridignScaZeroEn   Cnt.   gc		
Loading		
L. LoaMgr_MotAgdpSig1NoSmaResp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
L. LoeMgr_MotAglighSigi3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
L. LoaMgr_MortAgidpNigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
LoaMgr_MotAgMtginSca_ZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgSmaAvaiResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Sig2         1           k_LoaMgr_MotCurridptSig2Resp_Sig2         1           k_LoaMgr_RedodSiRea_Uls_Sig2         1           k_LoaMgr_RedodSiRea_Uls_Sig2         1           k_LoaMgr_Pert_DiagcSistvirtinactv_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistvirtinactv_Cnt_lgc value         0           target_LoaMgr_Pert_AllorqidptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         0           target_LoaMgr_Pert_MotOurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_ToaOul_Cnt_lgc_value         0           target_LoaMgr_Pert_ToaOul_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaOul_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaOul_Cnt		·
k_LoaMgr_MotCurridptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdSiSrate_UlspS_32         1           k_LoaMgr_RedcdSiSrate_UlspS_32         1           k_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_L		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_EUIspS_f32         1           k_LoaMgr_MotCurridptSig2D_UIs_f32         1           k_LoaMgr_RedcdSiRate_UIspS_f32         1           k_LoaMgr_RedcdSiRate_UIspS_f32         1           k_LoaMgr_Per1_DiagcStsIvtTrlnactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtTv2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_log value         2           target_LoaMgr_Per1_LoaScaDi_Cnt_log value         1           target_LoaMgr_Per1_MctCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum_value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum_value         0           target_LoaMgr_Per1_TloaAval_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pe		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStsIvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perd_MotCurridptSig_Cnt_u08.value         2           target_LoaMgr_Perd_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perd_MotPosldptSig_Cnt_u08.value         0           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         0           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TinoaAvail_Cnt_lgc.value		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotPosidptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Ner1_ToaDi_Cnt_lgc.value         1           target_Ner1_ToaDi_Cnt_lgc.value         1           target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc         target_LoaMgr_Per1_Writ_InvaltiptSig_Cnt_u08           target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_Writ_InvaltiptSig_Cnt_lgc           target_Ret_Inst_Ap_Lo		
k_LoaMgr_MotCurrMigtnSca_Uis_f32	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMigtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perf_DiagcStsIvtr2inactv_Cnt_lgc.value 0 target_LoaMgr_Perf_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perf_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perf_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perf_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Perf_SteerMod_Cnt_enum_value 0 target_LoaMgr_Perf_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Perf_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Perf_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_DiagcStsIvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_DiagcStsIvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_HwTqloaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_VirLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Perf_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMgr_Perf_MotAgLoaMtgnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMgr_Perf_MotAgLoaMtgnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMgr_Perf_MotAgLoaMtgnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Perf_MotCurrldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_Perf_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Perf_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvaii_Cnt_Igc.value target_LoaMgr_Per1_TloaAvaii_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IwTqloaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LwTqloaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrl	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MitPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_VrtLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Ret_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_DiapCstslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvTLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaStslvtnen  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaStslvtnen  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		1 - 1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 - 1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173, 174}	{171, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.18 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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	1		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc		at lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0i		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>*</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1.5	1.	
	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e	enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	:	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
			Result
Name	Actual Value	Expected Value	Result
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	~
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value 1	Expected Value 1	~
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	Actual Value  1 1 1	Expected Value  1 1 1	•
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value  1 1 1 0	Expected Value  1  1  0	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08	Actual Value  1 1 1 1 1 1	Expected Value  1 1 1 0 1	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value  1 1 1 0 1	Expected Value  1 1 1 0 1	• • • • • • • • • • • • • • • • • • •
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08	Actual Value  1 1 1 0 1 1	Expected Value  1  1  1  0  1  1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	Actual Value  1 1 1 0 1 1 1 5	Expected Value  1 1 1 0 1 1 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32	Actual Value  1 1 1 0 1 1 5 500	Expected Value  1 1 1 0 1 1 5 500	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value  1 1 1 0 1 1 5 500	Expected Value  1 1 1 0 1 1 5 500 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	Actual Value  1  1  1  0  1  1  1  5  500  1  5  1  1	Expected Value  1 1 1 0 1 1 1 5 500 1 5 1 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value  1  1  1  0  1  1  5  500  1  5  1  1  1  3	Expected Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 3	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_logc	Actual Value  1  1  1  0  1  1  1  5  500  1  5  1  1  1  3  0	Expected Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 3 0	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value  1  1  1  0  1  1  1  5  500  1  5  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value  1  1  1  0  1  1  1  5  500  1  5  1  1  1  5  1  1  5  1  1  1  1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 1 5 1 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 5 5 5 1 1 1 1 1 5 5 5 1 1 1 1 1 5 5 5 1 1 1 1 1 5 5 5 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value  1  1  1  0  1  1  5  500  1  1  1  1  1  5  1  1  1  1  1  1  1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 1 5 1 1 1 5 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  1  1  1  0  1  1  5  500  1  1  1  1  5  1  1  1  1  1  1  1  1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 5 1 0 1 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRaleLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  1  1  1  0  1  1  5  500  1  1  1  1  5  1  1  1  1  1  1  1  1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 5 1 0 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value  1  1  1  0  1  1  5  500  1  5  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 1 5 1 1 1 1 3 0 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value  1  1  1  1  0  1  1  1  5  500  1  5  1  1  1  3  0  1  5  1  1  1  1  1  1  1  1  1  1  1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 1 5 1 1 1 1 3 0 1 5 1 1 1 5 1 0 1 1 5 1 0 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1	
Name  LoaMgr_AgsV_Cnt_M_lgc  LoaMgr_HwTqldptSigsV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigsV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1	
Name  LoaMgr_AgsV_Cnt_M_lgc  LoaMgr_HwTqldptSigsV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigsV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcsV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  1 1 1 0 1 1 1 5 5 500 1 5 1 1 1 1 1 3 0 1 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 500 1 5 1 1 1 1 1	
Name  LoaMgr_AgsV_Cnt_M_lgc  LoaMgr_HwTqldptSigsV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigsV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 500 1 5 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotOcurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 500 1 5 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIcoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VtIgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_LOASt_State_enum.value	Actual Value  1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>

Test Step 3.20 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	1	1	/ v
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr lytrldptSigSV Cnt M u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	-
LoaMgr_MotAglaptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	3	-
LoaMgr_MotAgRespSV_Cnt_M_u08	3	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	· ·		
LoaMgr_MotCurrRespSV_Cnt_M_u08	4 0	4	-
LoaMgr_TloaAvail_Cnt_M_lgc		0	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>Y</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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	1		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k LoaMgr MotAgSmaAvailResp Cnt u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3 2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	ent lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1-	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	7
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.23 (Repeat Count = 1)	18
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnRate_0ispS_i32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	- <del>-</del> ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cn target_LoaMgr_Per1_LOASt_State_enum	<del></del>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	!	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScabi_Ont_Igc	target_LoaMgr_Per1_LoaScaDi_Crit_igc	Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEi	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	2	1 2	<b>Y</b>
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc			
		1	
	1	1	
LoaMgr_TloaDi_Cnt_M_lgc		1 1 1	· ·
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 1 1	1	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 1 1 {170, 172, 173, 174}	1 1 {170, 172, 173, 174}	V V
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1}	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1, 1} {1, 1, 1, 1}	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 1 5	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1 5	**************************************
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 1 1 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	V   V   V   V   V   V   V   V   V   V



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	<b>~</b>
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	_
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr TloaAvail Cnt M lgc	0	0	<b>~</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 174}	{170, 174}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•
	- I -	- I -	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Injust Value   Injus Value	Test Step 3.25 (Repeat Count = 1)	13
Looking   American   American		
Looking   Invited achieved   Dec   Dec		
Loading   Junispies   Loading   Loading   Loading   Loading   Junispies   Loading		2
Landary - Merchanger Cort. M. 208  Landary - Landar	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landing Medigenesity Crit M. 108  Landing Medicensesity Crit Medicenses Crit Line  Landing Medicenses Lungs Crit Line  Landing Medicenses Lungs Crit Line  Landing Medicenses Crit Line  Landing Medicense	LoaMgr_HwTqRespSV_Cnt_M_u08	5
Loading   Londergo   Loading   Loa	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking   MacApigenesis   Cort   M. 196		
Loading   MacAge and light   Loading   Loading   Loading   MacAge and light   Loading   MacConteglistips   Cert   M. 198		
Loading MacQuardigist Cort M, USB Loadin		
Leading Juniforcia Septing Cort. My Juniforcian Septing Sept		
Leading Mochanis (M. Muge  Leading Mochanis (M. Muge  Leading Sentiability (M. Muge  Leading Leading Centrohympirate Libros (F.2)  Leading Leading Leading Leading Centrohympirate Libros (F.2)  Le		
Loadley_LoydesSec_Con_Lo_Uside   Residence   Loadley_Con_Lo_Uside   Loadley_Con_HorteringTrain_Usides_Con_Lon_Uside   Loadley_Con_HorteringTrain_Usides_Con_Lon_Uside   Loadley_Con_HorteringTrain_Usides_Con_Lon_Uside   Loadley_Con_HorteringTrain_Usides_Con_Lon_Uside   Loadley_Con_HorteringTrain_Usides_Con_Lon_Uside   Loadley_LorderingTrain_Usides_Con_Lon_Uside   Loadley_HorteringTrain_Usides_Con_Lon_Uside   Loadley_HorteringTrain_Usides_Con_Uside   Loadley_HorteringTrain_Usides_Con_Usid	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loadley_Carly Agriculture   Loadley_Carly Lines   Loadley_Carly	LoaMgr_MotCurrRespSV_Cnt_M_u08	5
Reg.   Text   Canadag   Curindring France   Units   Size	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Linable_Driftonthalpriss_Driftonthalpr	Rte_Inst_Ap_LoaMgr	V =
Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Crit_UBB   Loading-		
K. Loadey_HwTopicsSg2TranDirects_Crd_Lu08		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MridghtSeap_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loadig_NvtightSigResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_Intringtsig2Resp_Cnt_U08		
L. Louking / Intriducising Fittind_Cnt_U08		
k_Looklogr_IvrinfignRate_UispS_132         1           k_Looklogr_IvrinfignScac_UispS_2         0           k_Looklogr_IvrinfignScac_UispS_2         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         5           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         2           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0 <t< td=""><td></td><td></td></t<>		
K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         5           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSigNSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         0           K, LouMgr_MoNajdipSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         2           K, LouMgr_MolCurridptSignResp_Cnt_U08         2           K, LouMgr_MolCurridptSignResp_Cnt_U08         2           K, LouMgr_MolCurridptSignResp_Cnt_U08         2           K, LouMgr_MolCurridptSignResp_Cnt_U08         0           K, LouMgr_MolCurridptSignResp_Cnt_U08         1           K, LouMgr_MolCurridptSign_Uis_Uis_Uis_Uis_Uis_Uis_Uis_Uis_Uis_Uis		
LoaMgr MoRajdiptSig3Resp_Cnt_U08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         2           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         1           k_LoaMgr_MorAgMignRate_UBsp5_f22         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Sf122         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig1Resp_Usp_Sf22         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_RedodSf3Res_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Res_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Res_Ulsp_Sf22         1           k_LoaMgr_Pert_DiagcStstvr1inactv_Cnt_lgc value         0           target_LoaMgr_Pert_LoaScaD_LoaT_lgc value         1           target_LoaMgr_Pert_SteerMor	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldplSigFitTnd_Cnt_u08         2           K_LoaMgr_MotAghtghtRate_UlspS_132         1           K_LoaMgr_MotAghtghtScace_Cnt_crt_ge         0           K_LoaMgr_MotAghtghtScace_Uls_132         1           K_LoaMgr_MotAghtghtScace_Cnt_u08         2           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         2           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_lgc         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_lgc         1           K_LoaMgr_RedcdSiRat_UlspS_132         1           K_LoaMgr_RedcdSiRat_UlspS_132         1           K_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         0           Larget_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         1           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         1           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         1           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         1           Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value         1	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
K_LoaMgr_MotAyMightRate_UlspS_f32         1           K_LoaMgr_MotAyMightSacZencEn_Cnt_igc         0           K_LoaMgr_MotAyMightSacZencEn_Cnt_igc         1           K_LoaMgr_MotAyMightSac_Ulsp_f32         1           K_LoaMgr_MotCurridpsigGResp_Cnt_u08         2           K_LoaMgr_MotCurridpsigGResp_Cnt_u08         0           K_LoaMgr_MotCurridpsigGresp_Cnt_u08         0           K_LoaMgr_MotCurridpsigGresp_Cnt_u08         0           K_LoaMgr_MotCurridpsigGresp_Cnt_u08         0           K_LoaMgr_MotCurridpsigGresp_Cnt_u08         0           K_LoaMgr_MotCurridpsigGresp_Cnt_u08         1           K_LoaMgr_MotCurridpsigGresp_Cnt_u0g         1           K_LoaMgr_MotCurridpsigGresp_Stylt_inact_Cnt_igc         1           K_LoaMgr_MotCurridpsigGresp_Stylt_inact_Cnt_igc_value         1           LoaMgr_Pert_losgcSistvitInact_Cnt_igc_value         0           Larget_LoaMgr_Pert_inact_Cnt_igc_value         1           Larget_LoaMgr_Pert_inact_Cnt_igc_value         1           Larget_LoaMgr_Pert_MotCurridpsig_Cnt_u08 value         0           Larget_LoaMgr_Pert_MotCurridpsigC_ott_u08 value         0           Larget_LoaMgr_Pert_MotCurridpsig_Cnt_u08 value         1           Larget_LoaMgr_Pert_Invacava_Cnt_igc         1           Larget_LoaMgr_Pert_MotCurridpsig_Cnt_u	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtgthSca_ZeroEn_Cn_Igc         0           K_LoaMgr_MotAgMtgthSca_Uls_132         1           K_LoaMgr_MotAgMtgthSca_Uls_132         2           K_LoaMgr_MotCurridptSigGResp_Cnt_u08         2           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         1           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         1           K_LoaMgr_MotCurridptSigScreen_Cnt_u0g         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_ReddSiSca_Uls_5122         1           K_LoaMgr_Pert_DiagcStsivtrInack_Cnt_lgc_value         1           Larget_LoaMgr_Pert_DiagcStsivtrInack_Cnt_lgc_value         0           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         2           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         2           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         2           Larget_LoaMgr_Per	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMiginSca_Ulis_f32         1           k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_Pert_DiagcStsivfTlaactv_Cnt_lgc value         1           target_LoaMgr_Pert_DiagcStsivfTlaactv_Cnt_lgc value         1           target_LoaMgr_Pert_MotSig_Cnt_u08 value         2           target_LoaMgr_Pert_Tipacit_Cnt_lgc value         1           target_LoaMgr_Pert_Tipacit_Cnt_lgc value         1           target	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurdiptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrdiptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdiptSig1FIThd_Cnt_u08         0           k_LoaMgr_MotCurrMigInSca_Usp_S_132         1           k_LoaMgr_MotCurrMigInSca_Usp_S_132         1           k_LoaMgr_MotCurrMigInSca_Usp_S_132         1           k_LoaMgr_MotCurrMigInSca_Usp_S_132         1           k_LoaMgr_RedGISSca_Usp_S_132         1           k_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value         0           target_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value         1           target_LoaMgr_Pert_DiagcSisivIrtZinactv_Cnt_igc_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         2           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         2           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_TioaDi_Cnt_igc_value         0           target_LoaMgr_Pert_TioaDi_Cnt_igc_value         0           target_LoaMgr_Pert_TioaDi_Cnt_igc_value         0 <td< td=""><td></td><td>1</td></td<>		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRate_UlspS_f32         1           k_LoaMgr_MotCurridptSig_Cnt_u52         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Pert_Desc_Stslvtr_Ushactv_Cnt_lgc_value         1           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08 value         2           target_LoaMgr_Pert_MtrPostdptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MtrPostdptSig_Cnt_u08 value         2           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_TloaDi_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Pert_Mtr_DiagcStslvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HvTqLoaMtgfnEn_		
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRsez_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSea_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Perf_IndeptSig_Cnt_u08_value         1           target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value         2           target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value         1           target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value         2           target_LoaMgr_Perf_SteerMod_Cnt_enum_value         0           target_LoaMgr_Perf_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Perf_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Perf_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Perf_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Perf_DoaMgr_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc           target_LoaMgr_Perf_LoaMgr_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc </td <td></td> <td></td>		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSlSca_Uls_f32         1           k_LoaMgr_Perd_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         0           target_Rel_inst_Ap_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc           target_LoaMgr_Per1_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc         target_Lo		
k_Loaklgr_MotCurridptSigFitThd_Cn_u08         0           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRisca_Uls_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         1           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         2           target_Loaklgr_Per1_SteerMod_Cnt_enum.value         0           target_Loaklgr_Per1_ToaAvail_Cnt_lgc.value         1           target_Loaklgr_Per1_Toabl_Cnt_lgc.value         0           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08         target_Loaklgr_Per1_HvTqldptSig_Cnt_u08           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_lgc         t		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_ToaDvall_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDvall_Cnt_lgc.value         1           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc         ta		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SterMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
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target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  2  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvain_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.26 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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	1		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08			
	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci	nt_lgc	
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc ic	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	nt_lgc _lgc ic t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc ic t_lgc 08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	nt_lgc _lgc c t_lgc 08 cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc _lgc c t_lgc 08 cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	nt_lgc _lgc c t_lgc 08 cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc _lgc c t_lgc 08 cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_lgc	nt_lgc _lgc c t_lgc 08 cnt_lgc	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	nt_lgc _lgc ic t_lgc 08 cnt_lgc 8	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	nt_lgc _lgc t_lgc 08 cnt_lgc 8	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	nt_lgc _lgc t_lgc 08 cnt_lgc 8  Expected Value 0	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0	nt_lgc _lgc t_lgc 08 cnt_lgc 8  Expected Value 0 1 0 0	<b>✓</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0	t_lgc lgc lgc t_lgc lgc lgc lgc lgc lgc lgc lgc lgc lgc	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrIoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 0	ent_lgc L_lgc lgc lgc lgc lgc lgc lgc lgc lgc lgc	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 0 5	Ligc   Ligc	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotGurrIdptSig_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIcaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqltpdtSigSV_Cnt_M_Io8 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_Io8 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_Io8 LoaMgr_IvtrRespSV_Cnt_M_Io8 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MotCurrloptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5	Ligc   Ligc	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotGurrIdptSig_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_Io8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_Io8 LoaMgr_IvtrRespSV_Cnt_M_Io8 LoaMgr_IOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 0 0 5 5 5 5000	Expected Value	***************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 5000 1	Ligc   Ligc	***************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 5 5 5 5000 1 5	Expected Value	***************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 5000 1	Ligc   Ligc	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.27 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

LoaMgr\_Per1





Name -	In most Walter		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_loatarget_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	<b>Y</b>
LoaMgr_LOASt_State_M_enum	5 500	5	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	1	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
	1	1	_
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value			



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	<b>V</b>
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	•
LoaMgr MotCurrRespSV Cnt M u08	4	4	•
LoaMgr TloaAvail Cnt M lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target LoaMgr Per1 HwTgLoaMtgtnEn Cnt Igc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•
and a second sec	1°	-1 <del>-</del>	

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 3.29 (Repeat Count = 1)	<b></b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 torrest Dto Jost An Lookfer
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.30 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1 0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>Y</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	_
Loaivigi_iviotAgiuptoigo.v_Ont_ivi_uoo	U	U .	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.31 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTgldptSigSV Cnt M u08	1	1	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_INITRESp3v_Cnt_M_u06 LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
		0 ± 0.001	_
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
	0	0	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>*</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 5	0 0 5	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	· -	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	· 100aii
LoaMgr HwTqldptSigSV Cnt M u08	1	1	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>V</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr TloaAvail Cnt M lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC_CITE_1_enum)	{1, 1}	{0, 0, 0, 0, 0}	<b>B</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1} {1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	11, 13 500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMigr_Per1_Intr_LoaMigtriEn_Cnt_lgc.value target_LoaMigr_Per1_lvtrLoaMigtriEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	
target_LoaMgr_Peri_LOASt_State_enum.value target LoaMgr_Peri MotAgLoaMtgtnEn Cnt lgc.value	1	1	
		1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.33 (Repeat Count = 1)	E Company of the Comp
	put Value
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 3	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 LoaMgr_MotAgRespSV_Cnt_M_u08 0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Rte_Inst_Ap_LoaMgr tar	rget_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1	
k_LoaMgr_RedcdStRate_UlspS_f32	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1	
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 3	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_Rte_Inst_Ap_LoaMgr_Per1_DiageStelutrI leachy_Cnt_lgc	rnet LeaMar Part DigasStelutrilipachi Cet las
	rget_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc rget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
	rget_LoaMgr_Per1_DiagcStstvti2finactv_Crit_igc rget_LoaMgr_Per1_HwTqldptSig_Crit_u08
	rget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
	rget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
	rget_LoaMgr_Per1_LOASt_State_enum
	· - ·
	rget_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.LoaMgr.Per1_LoaScaDi_Cnt_Igc	rget_LoaMgr_Per1_LoaScaDi_Cnt_lgc rget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	4	4	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171}	{171}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.34 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrIvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2Inactv C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Nesuit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.35 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	·		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target LoaMgr Per1 SteerMod Cnt enum.value	1		
	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target LoaMgr Per1 TloaDi Cnt lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1Ina	cty Cpt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_loaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lg		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	/ / / / / / / / / / / / / / / / / / /
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr HwTgRespSV Cnt M u08	3	3	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr LOASt State M enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr MotAgldptSigSV Cnt M u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08		0	
Loavigi vilgividorco v oni ivi doo	0	0	<b>▼</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 {170, 173, 175}	{170, 173, 175}	
		1	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 173, 175} {1, 1, 1}	{170, 173, 175} {0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{170, 173, 175} {1, 1, 1} {1, 1, 1}	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{170, 173, 175} {1, 1, 1} {1, 1, 1} 500	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	{170, 173, 175} <b>{1, 1, 1}</b> <b>{1, 1, 1}</b> 500	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	{170, 173, 175} <b>{1, 1, 1}</b> <b>{1, 1, 1}</b> 500 1	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	{170, 173, 175} <b>{1, 1, 1}</b> {1, 1, 1}  500  1  1	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	{170, 173, 175} <b>{1, 1, 1}</b> {1, 1, 1}  500  1  1  1  5	{170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 1	



est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 3.36 (Repeat Count = 1)	<b>1</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	0
LoaMgr MotAgRespSV Cnt M u08	5
LoaMgr MotCurrIdptSigSV Cnt M u08	2
	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
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Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	<del></del>	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr HwTgLoaMtgtnEn Cnt M Igc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	•
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
		·	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.37 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotApDoorSV Cot M v08	0 3
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrIvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1 5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Peri_TloaAvaii_Cnt_lgc.value target_LoaMgr_Peri_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	-

Test Step 3.38 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k LoaMgr HwTqMtgtnRate UlspS f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgNdthRate_Ulsps_f32	1			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca2eroEn_Cnt_igc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target LoaMgr Per1 MotCurrIdptSig Cnt u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target LoaMgr Per1 TloaAvail Cnt Igc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	:nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l	gc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at Inc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MttPostaptsig_Crit_ubo target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Crit_enum	target_LoaMgr_Per1_MtrPostaptSig_Crit_ut			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace								
Actual Function	Count	Expected Function	Count	Result				
CalclvtrState	2	CalclvtrState	2	~				
LatchInputs	1	LatchInputs	1	<b>✓</b>				
RqstRespConds	1	RqstRespConds	1	~				
RqstHwTqResp	1	RqstHwTqResp	1	~				
ArbResp	1	ArbResp	1	~				
ChooseFinalResp	1	ChooseFinalResp	1	~				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~				
AssignScale	1	AssignScale	1	<b>✓</b>				
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~				
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~				