Integration Manual –HOWDetect

Table of Contents

[1 Dependencies 2](#_Toc378860928)

[1.1 SWCs 2](#_Toc378860929)

[1.2 Global Functions(Non RTE) to be provided to Integration Project 2](#_Toc378860930)

[2 Configuration 3](#_Toc378860931)

[2.1 Build Time Config 3](#_Toc378860932)

[2.2 Configuration Files to be provided by Integration Project 3](#_Toc378860933)

[2.2.1 Da Vinci Parameter Configuration Changes 3](#_Toc378860934)

[2.2.2 DaVinci Interrupt Configuration Changes 3](#_Toc378860935)

[2.2.3 Manual Configuration Changes 3](#_Toc378860936)

[3 Integration 4](#_Toc378860937)

[3.1 Required Global Data Inputs 4](#_Toc378860938)

[3.2 Required Global Data Outputs 4](#_Toc378860939)

[3.3 Specific Include Path present 4](#_Toc378860940)

[4 Runnable Scheduling 5](#_Toc378860941)

[5 Memory Mapping 6](#_Toc378860942)

[5.1 Mapping 6](#_Toc378860943)

[5.2 Usage 6](#_Toc378860944)

[5.3 Non RTE NvM Blocks 6](#_Toc378860945)

[5.4 RTE NvM Blocks 6](#_Toc378860946)

[6 Compiler Settings 6](#_Toc378860947)

[6.1 Preprocessor MACRO 6](#_Toc378860948)

[6.2 Optimization Settings 6](#_Toc378860949)

[7 Revision Control Log 7](#_Toc378860950)

# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be refered. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

Ap\_HOWDetect\_Cfg.h generated by Ap\_HOWDetect\_Cfg.h.tt

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| HOWDetectGeneral/HOWDetectCPEnable | To enable the check point |  |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| None |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Required Global Data Inputs

None

## Required Global Data Outputs

None

## Specific Include Path present

None

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| HOWDetect\_Init1 | None | RTE(Init) |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| HOWDetect\_Per1 | None | RTE (2ms) |

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| HOWDETECT\_START\_SEC\_VAR\_CLEARED\_32 | float32 |  |
| HOWDETECT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED | LPF32KSV\_Str, HPF32KSV\_Str |  |
| HOWDETECT\_START\_SEC\_CONST\_16 |  |  |
| RTE\_START\_SEC\_AP\_HOWDETECT\_APPL\_CODE |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| None |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 16-Apr-14 | Jzk9cc |