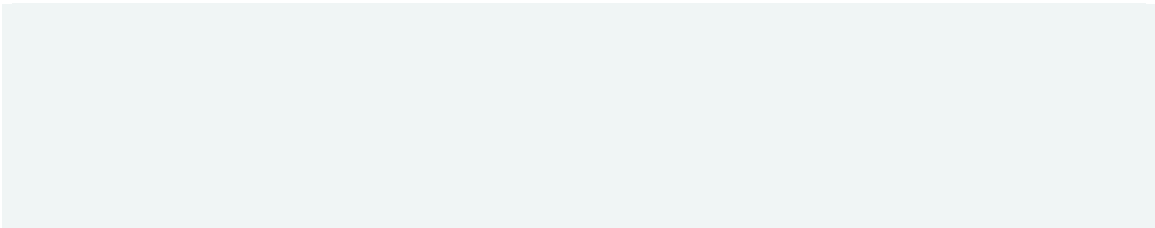


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4.1 Scope of Delivery

4.1.1 Static Files

[illegible]

[illegible][illegible]

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4.2 Restrictions

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4.3 RTE Analyzer Command Line Options

-c <config>	
-I <dir>	
-D <name>[=<value>]	
-o <path>	
-e	
-d	
-j	
-v	

-h
-w

Example:
MicrosarRteAnalyzer.exe -c RteAnalyzerConfiguration.json -o Reports

4.4 Analysis Report Contents

4.4.1 Analyzed Files

4.4.2 Configuration Parameters

	Rte COMCbK, Rte LdCom,

	Rte_GetMirror, Rte_SetMirrors,

-

4.4.3 Findings

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		<i>uint8* ptr = 0xdeadbeef;</i> <i>*ptr = 5;</i>

		<i>asm("add %al, (%rax)");</i>
		<i>typedef struct { uint8* a; uint8* b; } struct_t; struct_t s; uint8** ptr = &s.a; ptr[1][0] = 7;</i>
		<i>uint8 a[5]; a[5] = 1;</i>

		<pre>SuspendAllInterrupts(); a = 5; ResumeOSInterrupts();</pre>
		<pre>for (i = 0; i < 20; i++) { if (i == 5) { DisableAllInterupts(); } if (i == 6) { EnableAllInterrupts(); } }</pre>
		<pre>if (a == 0) { DisableAllInterrupts(); } Function();</pre>

		<pre>void func() { DisableAllInterrupts(); func(); EnableAllInterrupts(); }</pre>
		<pre>DisableAllInterrupts(); if (a) { return; } EnableAllInterrupts(); return;</pre>
		WaitEvent
		SuspendOSInterrupts



		-
		<pre>#define RTE_START_SEC_VAR #include "MemMap.h" uint8 var; #define RTE_STOP_SEC_CONST #include "MemMap.h"</pre>
		<pre>uint8 var; #define RTE_START_SEC_VAR #include "MemMap.h"</pre>
		<pre>#define RTE_START_SEC_VAR #include "MemMap.h" uint8 var; #define RTE_STOP_SEC_VAR #include "MemMap.h"</pre>

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4.4.4 Configuration Feedback

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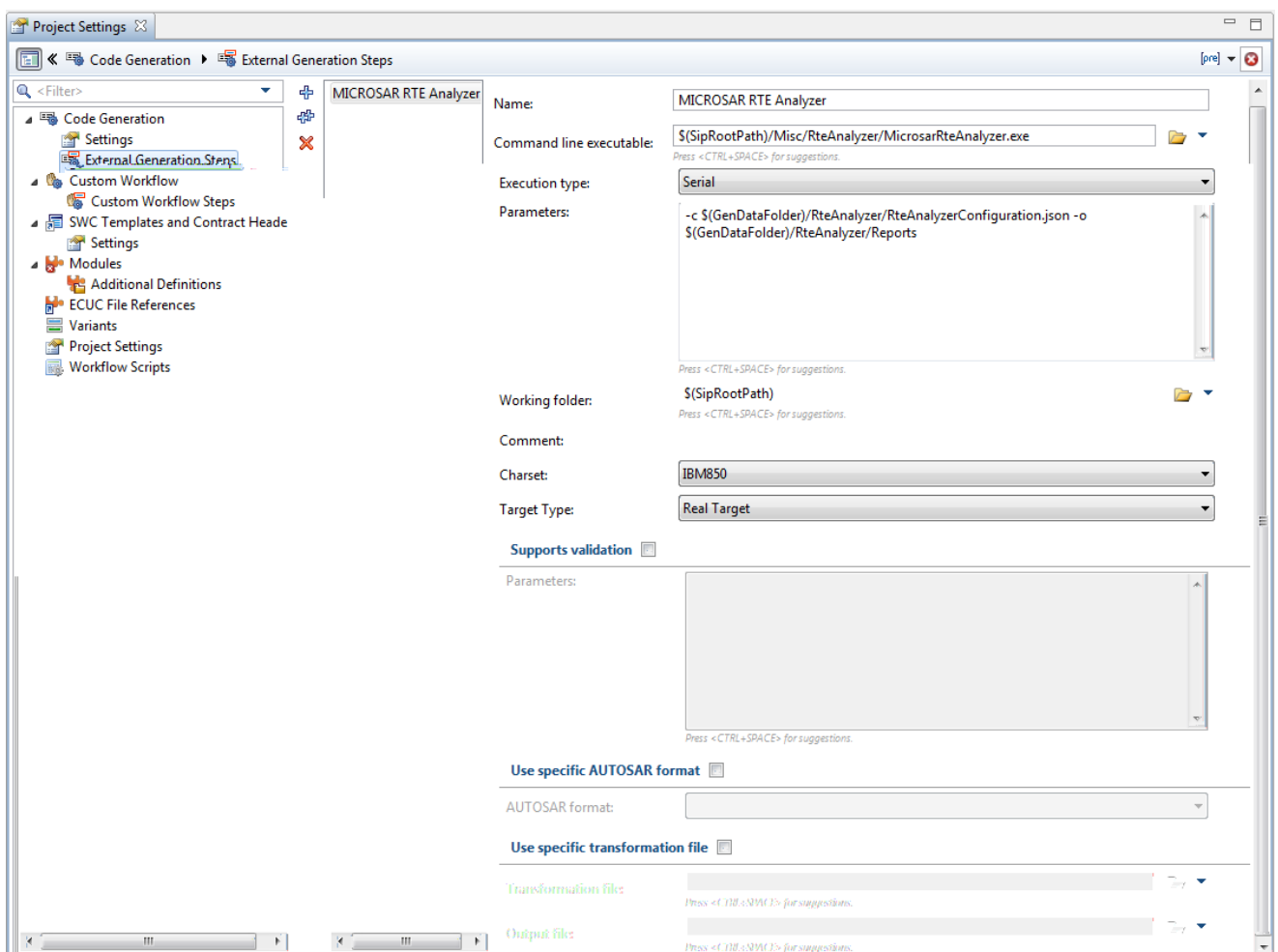
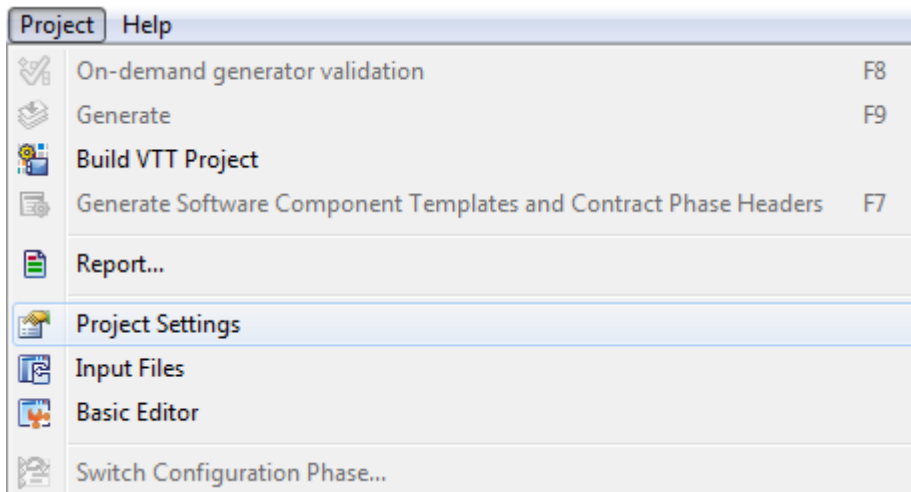
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Rte_Write, Rte_IWrite, Rte_Read,
Rte_DRead, Rte_IRead.
- Rte_Send Rte_Receive
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Rte_Switch Rte_Mode
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Rte_Read

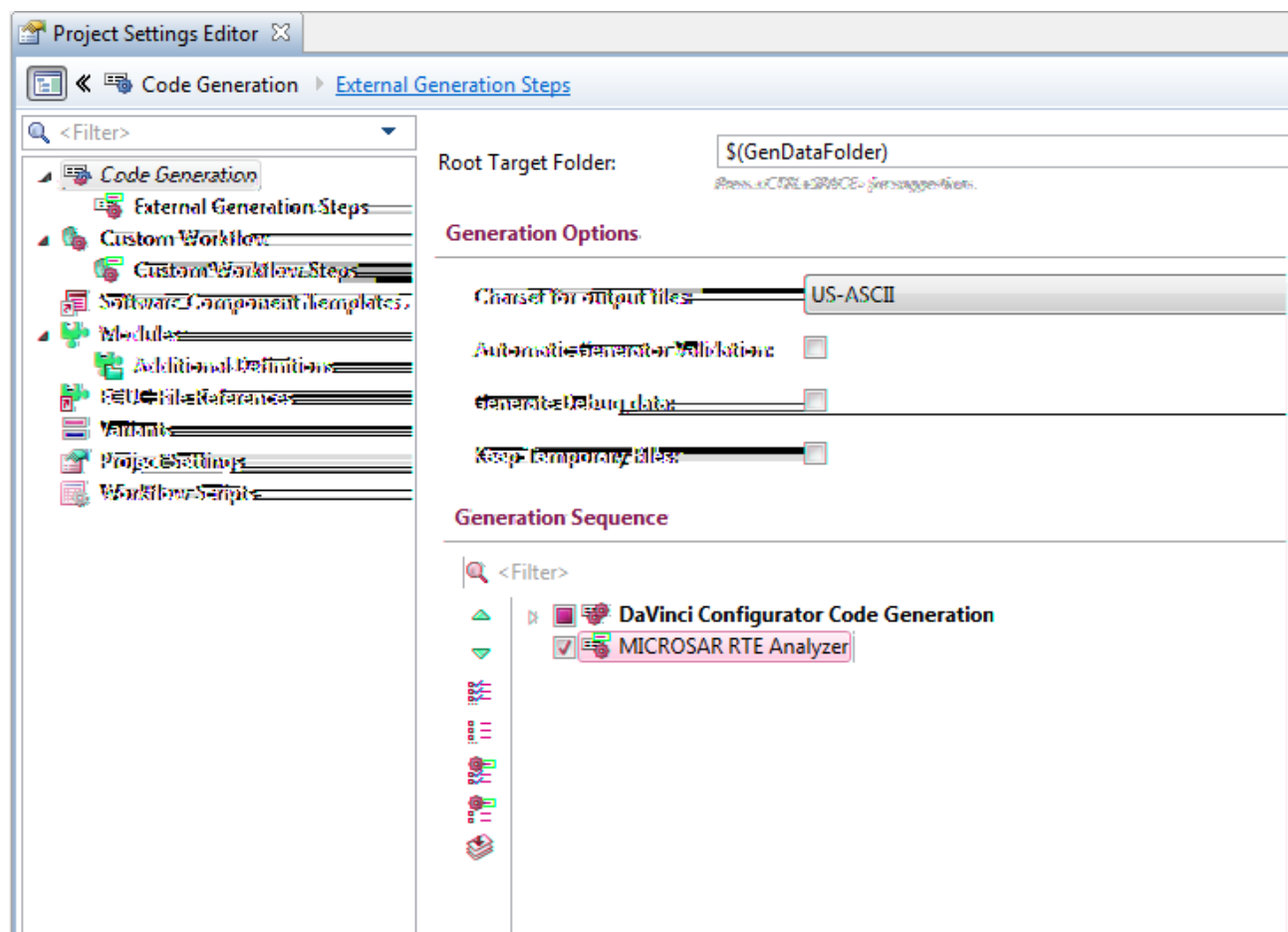
4.4.5 Template Variant Check

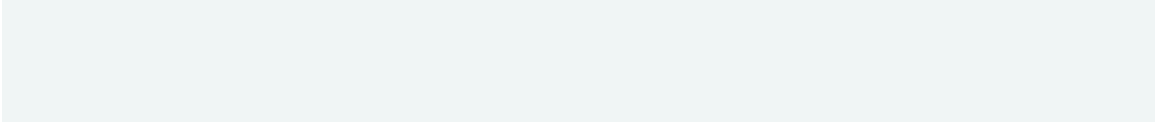
4.5 Integration into DaVinci CFG



```
$(SipRootPath)/Misc/RteAnalyzer/MicrosarRteAnalyzer.exe
```

```
-c $(GenDataFolder)/RteAnalyzer/RteAnalyzerConfiguration.json -o  
$(GenDataFolder)/RteAnalyzer/Reports
```





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5.1 Glossary

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5.2 Abbreviations

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