

ID	Category	Summary	Description	ASR_TicketType	Status
22712	General	Usage of alue ! " not accor#ing ASR re\$uirements	%ro&lem #escription' Accor#ing AUT(SAR_T%S_)CUConfiguration t*e alue #inf+#eri e# from stan#ar# mo#ule #efinition ST, D must &e use# as follo-s' . /ecuc_s-s_01234 If t*e min alue e\$uals 5inf or t*e ma6 alue e\$uals inf in t*e St, D t*e min7ma6 alues in t*e 8S, D s*all &e replace# -it* t*e actually supporte# min7ma6 alues for t*is implementation9)6pecte# &e*a ior' !! " s*all not &e use#: &ut instea# t*e actual , !! 7, A; alues s*all &e a aila&le in %D"s Current &e*a ior' See pro&lem #escription fiel#9	<UG	(%)! ISSU)
20=27	General	/%ort4 AUT(SAR_%(RT_Component_User, anual9 p#f is lacking information a&out)6clusi es areas for CRITICA>S)CTI(! %R(T)CTI(!	%ro&lem #escription' >ack of information a&out)6clusi es areas in AUT(SAR_%(RT_Component_User, anual9p#f9 As a result user is facing #ifficulty #uring integration9 Source %ackage' AUT(SAR_R?@31_%16_, CA>_)291A)6pecte# <e*a ior' T*e user manual s*oul# contain information a&out !! IT_C(! "IG_%R(T)CTI(!:R)"R)S?_%(RT_!! T)R! A>_%R(T)CTI(! an# S)T_T(_DI(_A>T_%R(T)CTI(! 9 Actual &e*a iour' U, only #escr!&es S)T_%!! _, (D)_%R(T)CTI(! in c*apter 292: &ut S)T_%!! _DIR_%R(T)CTI(!:!! IT_C(! "IG_%R(T)CTI(!:R)"R)S?_%(RT_!! T)R! A>_%R(T)CTI(! an# S)T_T(_DI(_A>T_%R(T)CTI(!:S)T_%!! _D)"AU>T_, (D)_%R(T)CTI(!:S)T_%!! _D)"AU>T_DIR_%R(T)CTI(! are not mentione#9	<UG	(%)! ISSU)
20=@@	General	CA! an# >! mo#ules not follo-ing Autosar re\$uirement <SB11A27	%ro&lem #escription' As per AUT(SAR re\$uirement <SB11A27: t*e #ri er mo#ules s*all &e name# as per C, S! D_C8en#orl#D_C8en#orSpecific! ameD_CSer ice! ameD9 "or)6ample ' <Can_InitF+ - ill &ecome <Can_3=_Renesas_InitF# It s*all &e follo-e# for "ile ! ames: %u&lic A%ls: %u&lis*e# %arameters: , emory allocation Gey-or#s an# %u&lic #ata types9 <ut t*is is not follo-e# in CA! an# >! mo#ules - *ic* support multiple instance as per autosar &ase #efinition file9)6pecte# &e*a iour' T*e #ri er mo#ules s*all &e name# as per C, S! D_C8en#orl#D_C8en#orSpecific! ameD_CSer ice! ameD9 Actual &e*a iour'♂ T*e #ri er mo#ules[CA! an# >! F are not name# as per C, S! D_C8en#orl#D_C8en#orSpecific! ameD_CSer ice! ameD9 T*e follo-ing , CA> mo#ules *a e t*e tag <U%%>R5, U>Tl%>ICITH5l! "!! IT)+is set to #true+in Autosar <ase Definition file AUT(SAR_, (D_)CUConfiguration%arameters%ar6ml an# *ence support multiple instance9 19 CA! 29)t*ernet A9 ">S 29 "le6ray 39 ICU 09 >! 79 %B, @9 BDG <ut for)t*ernet: ">S: ICU: an# %B, mo#ules: t*e re\$uirement <SB11A27 is mo e# to ! A re\$uirements in t*e Tracea&ility section9	<UG	(%)! ISSU)

270A=	General	%RAG, A #efine inconsistent to #e ice *ea#er file package	CuD%ro&lem Description'C7uD %RAG, A #efine #iffers &et- een io_macros_ 29* from #e ice *ea#er file packages an# compiler9* in , CA> package9 In Compiler9*' I#efine %RAG, AE6F_%ragmaE6F In io_macros_ 29*' I#efine %RAG, AE6F_%ragmaE16F CuDCurrent <e*a iour'C7uD In customer application t*is mig*t cause a compilation - arning #ue to a macro re#efinition if &ot* *ea#er files are use#9 CuD)6pecte# <e*a iour'C7uD Consistent #efine use# in &ot* *ea#er files9	<UG	(%)! ISSU)
27721	General	Comman# line option 5" not - orking	%ro&lem #escription' T*e 5"7"l>)8)RSI(! option of generation tool is not - orking9 Instea# of listing t*e ersion of tool co#e files: t*e tool is t*ro- ing error ->RR111111')CU Configuration Description "ile is not pro i#e# as input to t*e Generation Tool#)6pecte# &e*a ior' (n t*e usage of 5"7"l>)8)RSI(! option: generation tool must list t*e ersion of tool co#e files9 Actual &e*a iour' See %ro&lem #escription9	<UG	(%)! ISSU)
27727	General	, akefiles use in ali# inclu#e pat*s for G?S &uil#er	%ro&lem Description' T*e G?S makefiles for sample applications use in ali# inclu#e pat*s parameters9 T*is &e*a iour *as currently no effect to G?S &uil#er &ut t*is mig*t c*ange9 It lengt*ens t*e comman# lines - it*out any use9 Actual &e*a iour' G?S &uil#er is calle# - it* in ali# options like 5IJ2919A 5IJcommon)6pecte# &e*a iour' (nly ali# inclu#e pat* parameters s*all &e use#9	<UG	(%)! ISSU)
27700	General	"unctional co#es are e6ecuting) en after D), is reporte#9	%ro&lem Description') en after D), error is reporte#: functional co#es are getting e6ecute#: - *ic* may result in une6pecte# &e*a ior of #ri er9 Similar issue is foun# in S%l - *ile #oing functional testing for)16 82911912 release: An# an issue is reporte# in mantis I &ug'207A19 Deci#e# to create ne- ticket to start in estigation for similar issues in all ot*er mo#ule fsee note'1@1@2=F9)6pecte# &e*a ior' "unctional co#es s*all not e6ecute after reporting D),)rror9 Actual &e*a ior' "unctional co#es are e6ecute# e en after reporting D),)rror9	<UG	(%)! ISSU)

27=72	General	, akefiles specify irrele ant fol#ers for *ea#er searc*	<p>%ro&lem #escription'</p> <p>T*e 5l parameter is use# to specify fol#ers – *ere t*e G?S &uil#er s*s all searc* for *ea#er files9 <ut also source fol#ers are gi en9</p> <p>Actual &e*a iour'</p> <p>, any irrele ant fol#ers are gi en as parameter to G?S &uil#er9 B*en prolect &ecom es large t*e ma6imum comman# line lengt* f@kf is e6cee#e#9</p> <p>)6pecte# &e*a iour'</p> <p>(nly rele ant fol#ers s*s all &e gi en – it* 5l parameter9</p>	<UG	(%)! ISSU)
2@27@	General	CA! 5)! T)R5); C>USI8)5AR)A5R)" tag is missing in t*e <SB, DT	<p>can)nter)6clusi eArea is re\$uire# insi#e t*e entity in <SB, DT: if t*e reference# e6clusi e area is use# in t*e entitys co#e9</p> <p>T*e entity can &e <s– Calle#)ntity: <s– Sc*e#ula&le)ntity or <s– Interrupt)ntity9</p> <p>Actual &e*a iour'</p> <p>C<SB5! T)R! A>5<)?A8I(R UUIDLIM)CUS'=31@2Aa=50@2@52a@#5@0=a57&2=a@713@faMD</p> <p>CS?(RT5! A,)D<s– Internal<e*a ior_1C7S?(RT5! A,)D</p> <p>C); C>USI8)5AR)A UUIDLIM)CUS'1&=03#e253e2a52=#@5a2c&5e77@2A@OfA27MD</p> <p>CS?(RT5! A,)D8ARIA<>)_%R(T)CTI(! C7S?(RT5! A,)D</p> <p>C7); C>USI8)5AR)AD</p> <p>C7); C>USI8)5AR)ASD</p> <p>C)! TITHSD</p> <p>C<SB5! T)RRU%T5)! TITH UUIDLIM)CUS'@#=#11c#53=a252f=#5a1c35a20=00e1&2a#MD</p> <p>CS?(RT5! A,)D<s– Interrupt)ntity_1C7S?(RT5! A,)D</p> <p>CI, %>),)! T)D5)! TRH5R)" D)STLIM<SB5, (DU>)5)! TRHMD7Ar%ackage_17, CU_")! T_ISRC7I, %>),)! T)D5)! TRH5R)"D</p> <p>CI! T)RRU%T5CAT)G(RHDCAT51C7I! T)RRU%T5CAT)G(RHD</p> <p>CI! T)RRU%T5S(URC)DI! T>8IC7I! T)RRU%T5S(URC)D</p> <p>C7<SB5! T)RRU%T5)! TITHD</p> <p>)6pecte# &e*a iour'</p> <p>C<SB5! T)R! A>5<)?A8I(R UUIDLIM)CUS'=31@2Aa=50@2@52a@#5@0=a57&2=a@713@faMD</p> <p>CS?(RT5! A,)D<s– Internal<e*a ior_1C7S?(RT5! A,)D</p> <p>C); C>USI8)5AR)A UUIDLIM)CUS'1&=03#e253e2a52=#@5a2c&5e77@2A@OfA27MD</p> <p>CS?(RT5! A,)D8ARIA<>)_%R(T)CTI(! C7S?(RT5! A,)D</p> <p>C7); C>USI8)5AR)AD</p>	<UG	(%)! ISSU)
2@3A2	General	Brong upper multiplicity #efinition for Configuration container9	<p>%ro&lem #escription'</p> <p>In %D" of some , CA> mo#ules t*e upper multiplicity is #efine# as</p> <p>CU%%)R5, U>Tl%>ICITH5I! "!! IT)D; ; C7U%%)R5, U>Tl%>ICITH5I! "!! IT)D</p> <p>T*e a&o e #efinitions are not correct accor#ing to ASR ecuc_s– s_21119</p> <p>Actual &e*a ior'</p> <p>, ultiple configuration is not possi&le #ue to t*e a&o e pro&lem9</p> <p>)6pecte# &e*a ior'</p> <p>T*e correct #efinitions must &e as follo– s'</p> <p>CU%%)R5, U>Tl%>ICITHD; ; C7U%%)R5, U>Tl%>ICITHD</p>	<UG	(%)! ISSU)

20@12	ADC	Une6pecte# D)T ADC_)_ID>) is &een raise# from A#c_Disale?ar#- are Trigger	<p>%ro&lem Description'</p> <p>Une6pecte# D)T ADC_)_ID>) is &eing raise# - *en A#c_Disale?ar#- areTrigger is in oke# for an alrea#y ena&le# group Eusing t*e api A#c_)na&le?ar#- areTriggerf- *ose status is ADC_STR)A, _C(, %>)T)D9</p> <p>)6pecte# <e*a iour'</p> <p>As per re\$uirement ADCA12: t*e D)T ADC_)_ID>) s*oul# not &e reporte# - *en A#c_Disale?ar#- areTrigger is calle# for a group t*at *as alrea#y &een ena&le# using A#c_)na&le?ar#- areTrigger9</p> <p>Actual <e*a iour'</p> <p>T*e D)T ADC_)_ID>) is reporte# - *en A#c_Disale?ar#- areTrigger is calle# for a group t*at *as alrea#y &een ena&le# using A#c_)na&le?ar#- areTrigger9</p>	<UG	(%)! ISSU)
272=2	ADC	?B triggere# (ne5s*ot con ersion in Circular Streaming is not - orking as e6pecte#	<p>%ro&lem Description'</p> <p>As per AUT (SAR specification: one ?B trigger s*oul# trigger only one ADC c*annel group con ersion stream9 T*e con ersion must finis* once it recei es t*e ?B trigger t*at is e\$ual to num&er of streams configure# for t*e group9</p> <p><ut in t*e current #esign single trigger e6ecutes t*e - *ole stream of con ersions9</p> <p>)6pecte# <e*a ior'</p> <p>(nly one ADC c*annel Group con ersion stream s*oul# *appen per ?7B trigger9</p> <p>Actual <e*a ior'</p> <p>Streaming con ersion is getting complete# - it* single ?7B trigger</p>	<UG	(%)! ISSU)
27313	ADC	+ucGroupSettings+element of A#c_GstGroupConfig/4 is not generate# properly	<p>%ro&lem Description'</p> <p>>S< of +ucGroupSettings+element #eci#es - *et*er a group is one5s*ot or continuous9</p> <p>+1+ means continuous group an#</p> <p>+1+ means one5s*ot group9</p> <p><ut co#e generator is not generating t*is properly9</p> <p>"or one5s*ot mo#e an# circular streaming group it generates +1+an#</p> <p>for continuous mo#e an# linear streaming group it generates +1+</p> <p>)6pecte# <e*a ior'</p> <p>>S< of +ucGroupSettings+ must &e +1+for continuous group an# +1+for one5s*ot group9</p> <p>Actual <e*a ior'</p> <p>Co#e generator is generating t*e &elo- alues:</p> <p>"or one5s*ot mo#e an# circular streaming group it generates +1+an#</p> <p>for continuous mo#e an# linear streaming group it generates +1+</p>	<UG	(%)! ISSU)
273=2	ADC	D, A ena&le# ADC con ersion gi es - rong con ersion result if DT"RRNn register signals a pen#ing transfer re\$uest	<p>%ro&lem #escription'</p> <p>Con ersion of an ADC group - it* +A#cResultAccess, o#e+ LD +ADC_ISR_ACC)SS+EGroup - it*out D, AF lea#s to t*e flagging of DT"RRNn9DRN &it9</p> <p>Con ersion of an ADC group - it* +A#cResultAccess, o#e+ LD +ADC_D, A_ACC)SS+ED, A ena&le# groupF - *en DT"RRNn9DRN &it alrea#y set lea#s to t*e return of con erte# alue from pre ious cycle9</p> <p>T*erefore DT"RRNn9DRN &it must &e cleare# eac* time - *en D, A ena&le# ADC groups con ersion is starte#9</p> <p>)6pecte# <e*a ior'</p> <p>D, A ena&le# ADC Groups s*oul# return t*e con erte# result of t*e current input oltage9</p> <p>Actual <e*a ior'</p> <p>D, A ena&le# ADC Groups are not returning t*e con erte# result of t*e current input oltage &ecause of alrea#y set DT"RRNn9DRN &it9</p>	<UG	(%)! ISSU)

2701A	ADC	Conversion of a D, A enable# one5s*ot ADC group is *appening only for t*e first ?B trigger an# not for t*e ne6t triggers	<p>%ro&lem #escription'</p> <p>(ne5s*ot ADC groups – it* D, A as result access mo#e is getting con erte# only for t*e first trigger an# not for t*e consecuti e triggers9</p> <p>! ote t*at if t*e result access mo#e is selecte# as interrupt t*en it is – orking as e6pecte#9</p> <p>)6pecte# <e*a ior'</p> <p>(ne5s*ot ADC groups – it* D, A as result access mo#e s*oul# con ert t*e group c*annels for e ery ?B trigger until it is stoppe# e6plicitly &y A#c_Disable?ar# – areTriggerFF</p> <p>Actual <e*a ior'</p> <p>(ne5s*ot ADC groups – it* D, A as result access mo#e is getting con erte# only for t*e first trigger an# not for t*e consecuti e triggers after ena&ling t*e group ia A#c_)na&le?ar# – areTriggerFF</p>	<UG	(%)! ISSU)
2701A	ADC	?B triggere# ADC group – it* D, A circular streaming access s*oul# con ert one sample per one trigger	<p>%ro&lem #escription'</p> <p>Current &e*a iour of t*e #ri er is: ?B triggere# ADC group – it* D, A an# circular streaming access mo#e is con erts more t*an one sample per ?7B trigger9</p> <p>Sample con ersion continues until t*e group is stoppe# &y calling A#c_Disable?ar# – areTriggerFF9</p> <p>As per t*e AUT (SAR specification only one sample con ersion must &e initiate# for one ?B trigger9</p> <p>)6pecte# <e*a ior'</p> <p>As per t*e AUT (SAR specification only one sample con ersion must &e initiate# for one ?B trigger9</p> <p>Actual <e*a ior'</p> <p>Stream con ersion continues – it* a single ?B trigger9</p>	<UG	(%)! ISSU)
27022	ADC	A#c_GetStream>ast%ointerFF A%l #oes not return t*e ali# sample count – *en t*e status of t*e group is ADC_STR)A, _C(, %>)T)D	<p>%ro&lem #escription'</p> <p>A#c_GetStream>ast%ointerFF A%l #oes not return correct num&er of ali# samples – *en t*e status of t*e circular streaming group is ADC_STR)A, _C(, %>)T)D9 T*is A%l must return t*e alue e\$ual to t*e configure# +A#cStreaming! umSamples+parameter of t*at group9</p> <p>)6pecte# <e*a ior'</p> <p>A#c_GetStream>ast%ointerFF A%l must return t*e ma6imum sample alue – *en t*e status of t*e circular streaming group is ADC_STR)A, _C(, %>)T)D9</p> <p>Actual <e*a ior'</p> <p>A#c_GetStream>ast%ointerFF A%l is returning ran#om alue – *en t*e status of t*e group is ADC_STR)A, _C(, %>)T)D9</p>	<UG	(%)! ISSU)

2@1=2	ADC	8alue assigne# to register EADCDnT?GSRF is not correct in A%l A#c_? - InitE9	<p>%ro&lem Description'5</p> <p>In %ri ate A%l A#c_? - InitE9 alue assigne# to register EADCDnT?GSRF usADC; nT?GSR is not correct: assignment to t*is register is as mentione# &elo-9</p> <p>C&D >pA#cRegisters5DusADC; nT?GSR L Euint10fE>p? - UnitConfig5DucGroupSelect, askf0 C7&D</p> <p>Generate# alue in structure element MucGroupSelect, askM is not correct: T*ere&y selection of TP? group < - ill not - ork properly9</p> <p>In tool co#e alue for structure element C&DMucGroupSelect, askMC7&D is as mentione# &elo-9?ere final alue in MucGroupSelect, askM is t*e alue in local aria&le C&DQgrp_maskC7&D: Un#erstan#ing is t*at for register ADCDnT?GSR &it positions are in e en num&er: as mentione# in section A19A92A of De ice manual R11U?12A0)R1171 Re 91971: <ut in Generation Tool co#e its consi#ere# t*at ADCDnT?GSR register &it positions are continues: As mentione# in Actual <e*a ior9</p> <p>%lease c*eck an# #o nee#ful9</p> <p>Actual <e*a ior'5</p> <p>S>ine ! o 72= of <s- %&lm9pm file: S8! Re ision Q1@3A22</p> <p>>oothing &elo- mentione# co#e for eac* c*anneI9</p> <p>I "ill ucGroupSelect, ask</p> <p>Qgrp_mask L 10</p> <p>Q<s- %&lm"D&rom_%&ImageTA#c_Gst?BUnitConfigUTQin#e6U</p> <p>TucGroupSelect, askU L Qgrp_mask0</p> <p>if EQc*n_trck e\$ MADC_T?_GR(U%<MF</p> <p>T</p> <p>C&DQgrp_mask L Qgrp_mask V E1 CC Qc*_i#f0 C7&D</p> <p>Q<s- %&lm"D&rom_%&ImageTA#c_Gst?BUnitConfigUTQin#e6U</p> <p>TucGroupSelect, askU L Qgrp_mask0</p> <p>U</p>	<UG	(%)! ISSU)
2@11=	ADC	>imit c*eck implementation is not proper for %olling mo#e	<p>%ro&lem #escription'</p> <p>In t*e case of %olling: A#c_Rea#Group A%l is calle# multiple times in a >oop9 "rom A#c_%ollingRea#Group functionEcalles# form A#c_Rea#Group A%lF: A#c_)rrlsr is calle#9 In A#c_)rrlsr t*e follo- ing errors are &eing cleare# &y - riting 161) to ADCDn)CR register9</p> <p>19 Upper >imit7>o- er >imit)rror9</p> <p>29 (er- rite)rror9</p> <p>A9 %arity)rror Clear9</p> <p>In ne6t pollingEcalling A#c_Rea#GroupF: e en if t*e alue is &eyon# >imit7>o- er limit: ADC group notification is calle# an# t*e con ersion status is c*ange# to ADC_STR)A, _C(, %>)T)D9</p> <p>)6pecte# <e*a ior'</p> <p>ADC #ri er s*outl# trigger t*e ne6t con ersion e en if it is configure# for one5s*ot con ersion9</p> <p>Actual <e*a iour'</p> <p>"urt*er con ersion is not triggere# for one5s*ot groups9</p> <p>ADC group notification is calle# an# t*e con ersion status is c*ange# to ADC_STR)A, _C(, %>)T)D9</p>	<UG	(%)! ISSU)

2@111	ADC	Conversion is not *appending for T? groups - *en parameter A#cSuspend#, o#e+ is +ADC_ASH! C?R(! (US_SUS%))! D+an# t*e Trigger is SB	<p>%ro&lem Description'</p> <p>Conversion is *appending for ADC groups - *ic* contains Track an# ?ol# ena&le# c*annels - *en t*e configuration parameter A#cSuspend#, o#e+ in container A#c?- Unit+is configure# as +ADC_ASH! C?R(! (US_SUS%))! D+an# t*e parameter A#cGroupTriggSrc+is +ADC_TRGG_SRC_SB-#</p> <p>T*e track an# *ol# functionality is - orking fine for ?B triggere# groups e en if t*e parameter A#cSuspend#, o#e+is +ADC_ASH! C?R(! (US_SUS%))! D-#</p> <p>Also: it is - orking properly for &ot* SB an# ?B triggere# groups if t*e parameter A#cSuspend#, o#e+is +ADC_SH! C?R(! (US_SUS%))! D+</p> <p><ut if t*e parameter A#cSuspend#, o#e+is configure# as +ADC_SH! C?R(! (US_SUS%))! D+t*e generator tool pro#uces t*e follo- ing - arning9</p> <p>MT*e parameter A#cSuspend#, o#e+s*oul# &e configure# as CADC_ASH! C?R(! (US_SUS%))! DD - *en t*e c*annels are ena&le# for Track an# *ol# feature9M</p> <p>)6pecte# <e*a ior'</p> <p>conversion s*oul# &e *appene# for &ot* SB an# ?B triggere# groups if parameter A#cSuspend#, o#e+is +ADC_ASH! C?R(! (US_SUS%))! D+</p> <p>Actual <e*a iour'</p> <p>Conversion is not *appending if parameter A#cSuspend#, o#e+is +ADC_ASH! C?R(! (US_SUS%))! D+for SB triggere# groups9</p>	<UG	(%)! ISSU)
2@11@	ADC	Critical section protection for glo&al structure array MA#c_GpGroupRamDataM is not implemente#9	<p>%ro&lem Description'</p> <p>Critical section protection for glo&al structure array MA#c_GpGroupRamDataM is not implemente#: e en t*oug* t*e alues of t*e structure MA#c_GpGroupRamDataM is mo#ifie# in most of t*e A%ls9</p> <p>In most of t*e pri ate A%l: a##ress of glo&al structure array MA#c_GpGroupRamDataM is assigne# to a local pointer an# t*en t*e elements alues are c*ange# 7 mo#ifie# 7 rea#9</p> <p>) en t*oug* t*e alues are c*ange# using local pointer: t*e alue in glo&al structure array MA#c_GpGroupRamDataM also gets c*ange#9</p> <p>)6ample'</p> <p>T*e alue of structure element M##GroupStatusM of MA#c_GpGroupRamDataM glo&al structure is &eing mo#ifie# in A#c_GroupComplete, o#eEF</p> <p>pri ate A%l calle# from A#c_IsrEF9</p> <p>7W Set group status as conversion complete# W7</p> <p>>pGroupData5D##GroupStatus L ADC_STR)A, _C(, %>)T)D0</p> <p>Consi#er a situation in - *ic* A%l A#c_StopGroupConversionEF is calle# from a *ig* priority task E say Timer Isrf t*an t*at of A#c_IsrEF: An# if A#c_StopGroupConversionEF is calle# E for same groupF kust after start e6citing A#c_IsrEF: &ut not reac*e# a&o e mentione# co#e9</p> <p>In t*is case t*e status Group Status remain ADC_STR)A, _C(, %>)T)D: e en after calling A#c_StopGroupConversionEF9</p> <p>To a oi# suc* issues: critical sections nee# to &e implemente# properly9</p> <p>Re\$uire# , (suggestion on same9</p> <p>Actual <e*a ior'</p> <p>Critical section protection not implemente#9</p>	<UG	(%)! ISSU)

2@121	ADC	Autosar re\$uirement ADC21A is not taken care9	<p>Problem Description '</p> <p>As per AUT (SAR re\$uirement ADC21A all A%l functions: e6cept A#c_Init: A#c_Delnit an# A#c_Get8ersionInfo are re5entrant9 <ut in current implementation it is not taken care9 If t*e functions are calle#</p> <p>for #ifferent c*annel groups: in current implementation t*ese re5entrant A%l – ill not – ork properly9</p> <p>Example'5</p> <p>Consider t*at SB trigger# ADC c*annel group 1 is alrea#y in \$ueue9</p> <p>Consider# t*at – e call A#c_Rea#GroupEF for ADC group 1 an# A%l A#c_Disale?ar# – areTriggerEF is in oke# for ADC C*annel group 1 form a interrupt E>ike Timer ISRF – *en only D)T c*eck in A#c_Rea#GroupEF A%l is complete#9 T*en e6ecution of A#c_Rea#GroupEF is pus*e# to stack an# start e6ecution of A#c_Disale?ar# – areTriggerEF A%l9</p> <p>B*en A#c_Disale?ar# – areTriggerEF A%l complete e6ecution: it pops t*e ADC c*annel group 1 from \$ueue: trigger its con ersion: an# – *en A#c_Rea#GroupEF start e6ecution: it – ill gi e une6pecte# &e*a ior9</p> <p>similar issues e6ist in most of t*e A%ls:</p> <p>Re\$uesting , (suggestions on same9</p> <p>6pecte# <e*a ior '</p> <p>Re\$uirement s*oul# &e taken care properly an# A%ls e6cept a&o e mentione# s*oul# &e re5entrant9</p> <p>Actual <e*a ior '</p> <p>Re\$uirement is not taken care9</p>	<UG	(%)! ISSU)
2@121	ADC	, emory section mapping use# for glo&al aria&le MA#c_Gaa? – UnitIn#e6/4M an# MA#c_GaaResultGroupRamData/4M is not correct9	<p>Problem Description'5</p> <p>19T*e glo&al aria&le MA#c_GaaResultGroupRamData/4M is mappe# to memory section MC (! "IG_DATA_U! S%)CI")D_S)C_START)DMEADC_START_S)C_C (! "IG_DATA_U! S%)CI")I)DF: <ut t*is glo&al aria&le is not initialixe# in A#c_%<cfg% EGenerate# file9</p> <p>Declaration of t*is aria&le is as mentione# &elo – 9</p> <p>e6tern 8AREA#c_8alueGroupType: ADC_! (! IT_DATAF A#c_GaaResultGroupRamData/40 of A#c_Ram% file9</p> <p>29T*e glo&al aria&le MA#c_Gaa? – UnitIn#e6/4M is mappe# to memory section MBAR_! (! IT_U! S%)CI")D_S)C_START)DMEADC_START_S)C_C (! "IG_8AR_! (! IT_U! S%)CI")I)DF: <ut t*is glo&al aria&le is of type const an# initialixe# in A#c_%<cfg% EGenerate# file9</p> <p>Declaration of t*is aria&le is as mentione# &elo – 9</p> <p>e6tern C (! STuint@: ADC_C (! STF A#c_Gaa? – UnitIn#e6/40 of A#c_Ram% file9</p> <p>Suggeste# Solution'</p> <p>19A#c_GaaResultGroupRamData/4 glo&al aria&le nee#s to mappe# to Uninitialixe# aria&le section9</p> <p>29A#c_Gaa? – UnitIn#e6/4 glo&al aria&le nee#s to mappe# to initialixe# constant aria&le section9</p> <p>6pecte# <e*a ior'5 ! A</p> <p>Actual <e*a ior'5 ! A</p>	<UG	(%)! ISSU)

2@123	ADC	Register ADCDnT?ST%CR EucADC; nT?ST%CRF nee#s to use instea# of ADCDnT?S, %STCR EucADC; nT?S, %STCRF to stop TP?9	<p>ro&lem Description '</p> <p>Register ADCDnT?ST%CR EucADC; nT?ST%CRF nee#s to use instea# of ADCDnT?S, %STCR EucADC; nT?S, %STCRF to stop TRACG P ? (>D in follo- ing mentione# >ine of co#e9</p> <p>As per #e ice manual R11U?12A0)R1171 Re 91971 section A19A913 ADCDnT?S, %STCR register is use# for starting TP?9</p> <p>As per #e ice manual R11U?12A0)R1171 Re 91971 section A19A910 ADCDnT?ST%CR register is use# for stop TP?9</p> <p>19>pA#cRegisters5DucADC; nT?S, %STCR L ADC_Y)R(0 of A#c_%ri ate_AD CD_ADC<% file in %ri ate A%l A#c_? - DelnitEF9</p> <p>29>pA#cRegisters5DucADC; nT?S, %STCR L ADC_<HT)_Y)R(0 of A#c_%ri ate_AD CD_ADC<% file in %ri ate A%l A#c_? - StopGroupCon ersionEF9</p> <p>A9 Also A#c_InitEF nee#s to up#ate to stop TP? &y setting t*is register9</p> <p>Actual <e*a ior '</p> <p>Register use ADCDnT?S, %STCR EucADC; nT?S, %STCRF to stop TRACG P ? (>D9</p> <p>)6pecte# <e*a ior '</p> <p>Register ADCDnT?ST%CR EucADC; nT?ST%CRF nee#s to &e use to stop TRACG P ? (>D9</p>	<UG	(%)! ISSU)
2@1A2	ADC	Implementation of , RS re\$uirement MAR_%! 1170_"R_1211M is not proper9	<p>ro&lem Description'</p> <p>If - e call A#c_)na&leC*anneIEF A%l &efore calling A#c_Dis a&leC*anneIEF A%l: illegal memory access - ill occur9</p> <p><ecause - *en - e call A#c_)na&leC*anneIEF A%l internally pri ate A%l A#c_IntDis a&le)na&leC*anneIEF - ill call as mentione# &elo - 9</p> <p>A#c_IntDis a&le)na&leC*anneIEGroup: C*anneII#: ADC_TRU)f</p> <p>an# in pri ate A%l A#c_IntDis a&le)na&leC*anneIEF:</p> <p>if >&lApiTypeE Ar# argumentf LL ADC_TRU):</p> <p>t*en: #ecrement t*e num&er of c*annels to #isa&le#:as mentione# in &elo - co#e</p> <p>>pGroupData5Duc! oofC*Dis a&le#550</p> <p>Initial alue of Muc! oofC*Dis a&le#M is Xero: so after #ecrement it &ecome 233: - *ic* is not correct result in</p> <p>illegal memory access or une6pecte# &e*a ior in A%l A#c_Rea#GroupEF: A#c_GetStream>ast%ointerEF: A#c_ConfigureGroup"orCon ersionEF an# A#c_IsrConfigureGroup"orCon ersionEF9</p> <p>)6pecte# <e*a ior '</p> <p>! 7A</p> <p>Actual <e*a ior '</p> <p>! 7A</p> <p>Suggeste# solution'</p> <p>A## a D)T c*eck if particular c*annel is trying to ena&le &efore it is #isa&le#9</p>	<UG	(%)! ISSU)
2@1A3	ADC	8ersion c*eck for Dem9* file is not present9	<p>ro&lem #escription'</p> <p>As per autosar re\$uirement ADC122 T*e ADC mo#ule s*all perform Inter , o#ule C*ecks to a oi# integration of incompati&le files9</p> <p><ut ersion c*eck for Dem9* file is not present in t*e a&o e mentione# file9</p> <p>"l>) ' A#c_%ri ate_AD CD_ADC<%</p> <p>)6pecte# <e*a ior '</p> <p>8ersion c*eck s*oul# &e #one9</p> <p>Actual <e*a ior '</p> <p>! o ersion c*eck is performe#9</p>	<UG	(%)! ISSU)

2@12A	ADC	General re\$uirement MAR_%! 11A2_"R_1123M is not consi#ere#9	<p>%ro&lem #escription '</p> <p>T*e D, A relate# registers are not initialiXe# in A#c_initEF9</p> <p>T*e same issue is t*ere – it* follo–ing registers also'</p> <p>19 ADCDnT?<CR EucADC; nT?<CRF: 29 ADCDnSGCR6 EucADC; nSGCR6F: A9 ADCDnSG8CS%6 EucADC; nSG8CS%6F 29 ADCDnSG8C)%6 EucADC; nSG8C)%6F 39 ADCDnSG, CHCR6 EucADC; nSG, CHCR6F 09 ADCDnU>>, SR6 EucADC; nU>>, SR6F 79 ADCDnADTI%Ry EulADC; nADTI%RyF @9 AD(%DIGn EulAD(%DIGnF =9 ADCDnT?ACR 119 ADCDnT?CR 119 ADC; nU>>, T<R 1 to 2</p> <p>are not initialiXe# in A#c_InitEF9</p> <p><ut as per General , RS MAR_%! 11A2_"R_1123M '</p> <p>T*e C, S! D_Init A%l s*all ensure t*at t*e relate# perip*eral is running correctly: e en if t*e perip*eral – as pre iously configure# &y anot*er Application t*at c*ange# t*e registers+ #efault alues9</p> <p>T*ere&y t*is General re\$uirement MAR_%! 11A2_"R_1123M is not consi#ere# for a&o e mentione# registers9</p> <p>)6pecte# <e*a ior'</p> <p>T*e general , RS re\$uirement MAR_%! 11A2_"R_1123M s*oul# &e taken care for all a&o e mentione# registers an# D, A relate# registers an# all s*oul# &e initialiXe# in A#c_initEF9</p>	<UG	(%)! ISSU)
2@131	ADC	AUT(SAR re\$uirement ADC177 is not taken care – *ile implementing A#c_InitEF9	<p>%ro&lem Description'</p> <p>As per t*is re\$uirement:</p> <p>/ADC1774 T*e function A#c_Init s*all #isa&le t*e notifications an# *ar# – are trigger capa&ility Eif statically configure# as acti ef9</p> <p>Configure# ?B triggers are not #isa&le# in A#c_InitEF9 If – e consi#er General , RS re\$uirement MAR_%! 11A2_"R_1123M: it nee#s to &e correcte# accor#ingly9</p> <p>As per t*is re\$uirement'5 MT*e C, S! D_Init A%l s*all ensure t*at t*e relate# perip*eral is running correctly: e en if t*e perip*eral – as pre iously configure# &y anot*er Application t*at c*ange# t*e registers+ #efault alues9M</p> <p>)6pecte# <e*a ior'</p> <p>Configure# ?B triggers to &e #isa&le# in A#c_InitEF</p> <p>Actual <e*a ior'</p> <p>Configure# ?B triggers are not #isa&le# in A#c_InitEF9</p>	<UG	(%)! ISSU)

2@13@	ADC	T*e Cautions mentione# in De ice manual are not implemente#9	<p>%ro&lem Description'</p> <p>"or e6ample</p> <p>As per Caution/14' section A19A9=</p> <p>To pre ent malfunctions: make ADCDnADCR1 settings after making or confirming t*e follo ing settings9</p> <p>E1F ?>DT) of TP? group A an# < are 19</p> <p>E2F ADSTART) of all scan groups are 1 an# TRG, D of all scan groups are 1?9</p> <p>EAF SGACT of all scan groups are 1 E&efore scan groups are starte#F9</p> <p><efore setting alue to ADCDnADCR1 EucADC; nADCR1F Register in A#c_%ri ate_AD CD_ADC<% file in A#c_? InitEF A%: e are not setting t*e &its or alues of t*ese register &its mentione# in Caution are unkno n to t*e #ri er9 So &ase# on General , RS re\$uirement MAR_%! 11A2_"R_1123M t*is is to &e implemente#9</p> <p>Same type of issues are applica&le for follo ing registers also9 %lease c*eck all applica&le Cautions9</p> <p>19 ADCDnADCR2 EucADC; nADCR2F</p> <p>29 ADCDnS"TCR EucADC; nS"TCRF</p> <p>A9 ADCDnTDCR EucADC; nTDCRF</p> <p>29 ADCDn (DCR EucADC; n (DCRF</p> <p>39 ADCDnT?ACR EucADC; nT?ACRF</p> <p>09 ADCDnT?)R EucADC; nT?)RF</p> <p>79 ADCDnT?GSR EucADC; nT?GSRF</p> <p>@9 ADCDnSG8CS%6 EucADC; nSG8CS%6F</p> <p>=9 AD (%DIGn EulAD (%DIGnF</p> <p>)6pecte# <e*a ior'</p> <p>none</p>	<UG	(%)! ISSU)
2@103	ADC	D), error is not reporte# for (er rite)rror in error ISR A#c_)rrlsrEF	<p>%ro&lem #escription'</p> <p>In t*e A%l A#c_)rrlsrEF: ucADC; n (B)R register is only use# to calculate %*ysical c*annel ID9 Un#erstan#ing is t*at D), error of (er rite)rror also nee#s to &e reporte#9 Also a## similar D), error report to ot*er errors like %arity)rror: Upper 7 lo er limit error an# ID)rror9</p> <p>)6pecte# <e*a ior'</p> <p>D), error nee#s to &e reporte#9</p> <p>Actual <e*a ior'</p> <p>D)m error is not reporte#9</p>	")ATUR)	(%)! ISSU)

2@17=	ADC	De ice manual CAUTI(! is not consi#ere# for implementation in A#c_? – InitEF an# A#c_? – DelnitEF	<p>%ro&lem Description'</p> <p>CAUTI(! mentione# in section 79119291A of R11U?12A0)R1171 Re 91971: is not consi#ere# for implementation 9</p> <p>As per #e ice manual Caution</p> <p>MDT"R9R)NS)> can &e c*ange# – *ile DT"R9R)N)! is 19M</p> <p><ut in current implementation &ot* &its R)NS)> an# R)N)! of DT"Rn Register are up#ating simultaneously as mentione# in &elo – co#e snippet9</p> <p>In A#c_? – InitEF'</p> <p>>pDmaRegisters5DulDT"Rn L >pSGmDmaConfig5DulDmaDtfrReg8alue0</p> <p>ulDmaDtfrReg8alue is generate# alue contain alue of &ot* &its R)NS)> an# R)N)! of DT"Rn Register9</p> <p>In A#c_? – DelnitEF'</p> <p>>pDmaRegisters5DulDT"Rn L ADC_D(U<>)_B(RD_Y)R(0</p> <p>Un#erstan#ing is t*at &efore c*anging t*e alue of DT"R9R)NS)>: nee#s to clear &it DT"R9R)N)! 9</p> <p>)6pecte# <e*a ior'</p> <p>%lease up#ate t*e #esign as &elo – 9</p> <p>In A#c_? – InitEF</p> <p>19 Reset' DT"RRNn register E1611F</p> <p>29 Clear &its >pDmaRegisters5DulDC)! n L ADC_D, A_DT)_DISA<>)0</p> <p>A9 >pDmaRegisters5DulDT"Rn L >pSGmDmaConfig5DulDmaDtfrReg8alue0</p> <p>"or A#c_? – DelnitEF</p> <p>, ake similiar c*anges</p> <p>Actual <e*a ior'</p> <p>In A#c_? – InitEF'</p>	<UG	(%)! ISSU)
2@1@2	ADC	Clearing of ID)rror is not correct9	<p>%ro&lem Description '</p> <p>Clearing of)rror flags are not correct: In current implementation ADCDnI)R9ID) error flag for ID)rror is not clearing9</p> <p>>pA#cRegisters5DucADC; n)CR L ADC_C?GC>R_)RR(R_ ">AG0</p> <p>of A#c_%ri ate_ADCD_ADC<%c file in A#c_)rrlsrEF %ri ate A%l is use# to clear all t*e error flag9</p> <p>As per #e ice manual R11U?12A0)R1171 Re 91971 Section A19A92=: register ADCDn)CR E)rror Clear Registerf last 2 #igits s*outl# &e set to clear all error flag9</p> <p>In A#c_%<Types_ADCD_ADC<9* file ADC_C?GC>R_)RR(R_ ">AG alue of t*is macro is 161)9 So last ADCDnI)R9ID) &it is not clear9</p> <p>Suggeste# Solution '</p> <p>A#c_%<Types_ADCD_ADC<9* file ADC_C?GC>R_)RR(R_ ">AG alue of t*is macro s*outl# &e 161" to clear all t*e flag9</p> <p>Actual <e*a ior ' ID error is not getting clear – *en all t*e error flags are ma#e clear9</p> <p>)6pecte# <e*a ior ' ID error s*outl# also getting clear – *en all t*e error flags ma#e clear9</p>	<UG	(%)! ISSU)

2@1@7	ADC	Implementation of , RS re\$uirement AR_%! 1170_"R_11@7 an# AR_%! 1170_"R_11@= is not proper9	<p>problem Description '</p> <p>19, RS re\$uirement MAR_%! 1170_"R_11@7M is nee#s to &e take care in Tool co#e: <ut in current implementation its not taken care9 As per De ice manual R11U?12A0)R1171 Re 91971 section A19A92@: num&er of ADCDnU>>, T<R6 register is A E6 L 1: 1: 2F9 In current implementation tool co#e – ill not gi e any error e en if – e configure c*annel *a ing limit c*eck more t*an 2: – *en alue of parameter MA#c%riorityImplementationML ADC_%RI(RITH_! (!)9</p> <p>29, RS re\$uirement MAR_%! 1170_"R_11@=M is nee#s to &e take care in Tool co#e: <ut in current implementation its not taken care9 As per De ice manual R11U?12A0)R1171 Re 91971 section A19A92@: num&er of ADCDnU>>, T<R6 register is A E6 L 1: 1: 2F9 In current implementation tool co#e – ill not gi e any error e en if – e configure c*annel *a ing limit c*eck more t*an 2: – *en alue of parameter MA#c%riorityImplementationML ADC_%RI(RITH_?B_SB or ADC_%RI(RITH_?B9</p> <p>Actual <e*a ior '</p> <p>"or &ot* case generation tool co#e is not generating ali#ation error: if limit c*eck ena&le# for more t*an A c*annel Group – it* #ifferen >imit c*eck setting9 Un#erstating is t*at: it – ill not – ork properly – *en t*ese groups are \$ueue#9</p> <p>)6pecte# <e*a ior '</p> <p>"or &ot* case proper ali#ation nee#s to a## in Tool co#e9</p> <p>Re\$uire# , (suggestion on same9</p>	<UG	(%)! ISSU)
2@1@@	ADC	Register MADCDn)CRM EucADC; n)CRF is not *an#le# properly in A#c_DeInit an# A#c_InitE A%ls9	<p>problem #escription'</p> <p>In pri ate A%l A#c_InitE register MADCDn)CRM EucADC; n)CRF is not implemente#9</p> <p>an# also in pri ate A%l A#c_DeInitE register MADCDn)CRM EucADC; n)CRF is upate# – it* Xero as mentione# &elo- '</p> <p>>pA#cRegisters5DucADC; n)CR L ADC_Y)R (0</p> <p>Un#erstan#ing is t*at to clear error flags ADCDn(B)R9(B): ADCDn%)R9%) an# ADCDnI)R9ID): – e nee# to set E – rite 1F to respecti e &its of t*e MADCDn)CRM EucADC; n)CRF register in A#c_DeInitE an# A#c_InitE A%ls9</p> <p>Be can also consi#er general , RS re\$uirement MAR_%! 11A2_"R_1123M: As per t*is re\$uirement:</p> <p>T*e A#c_Init A%l s*all ensure t*at t*e relate# perip*eral is running correctly: e en if t*e perip*eral – as pre iously configure# &y anot*er Application t*at c*ange# t*e registers+ #efault alues9</p> <p>an# autosar re\$uirement ADC111 '</p> <p>T*e function A#c_DeInit s*all return all ADC ?B Units to a state compara&le to t*eir po– er on reset state9 8alues of registers – *ic* are not – ritea&le are e6clu#e#9 Itz t*e responsi&ility of t*e *ar# – are #esign t*at t*is state #oes not lea# to un#efine# acti ities in t*e [C9</p> <p>)6pecte# <e*a ior '</p> <p>Register ADCDn)CRM EucADC; n)CRF s*oul# &e up#ate# correctly in A#c_Init an# A#c_DeInit A%ls as per re\$uirements9</p> <p>Actual <e*a ior '</p> <p>Register ADCDn)CRM EucADC; n)CRF is not *an#le# as per t*e re\$uirements9</p>	<UG	(%)! ISSU)

2@212	ADC	AUT(SAR Re\$uirement ADC1=1 an# ADC277 is not taken care9	<p>19 as per ADC1=1 re\$uirement ' MT*e ADC mo#ulels configuration s*all &e suc* t*at an ADC C*annel group contains at least one ADC C*annelM</p> <p>In current implementation AUT(SAR Re\$uirement ADC1=1 is not taken care: in generation tool9 As per current implementation: tool co#e is not gi ing any proper error e en if no c*annel is configure# un#er a ADC Group an# tool co#e – ill cras* &y gi ing error M)RR12A111M: – *ic* is not correct9</p> <p>29 As per ADC277 Re\$uirement: MT*e ADC mo#ulels configuration s*all &e suc* t*at all c*annels containe# in one ADC C*annel group s*all &elong to t*e same ADC ?B Unit9M</p> <p>Its foun# t*at t*is SBS re\$uirement is not mappe# properly in TSDD: Tracea&ility an# TST%: t*at:s nee#s to fi6 accor#ingly9</p> <p>, ost of t*e re\$uirement are not tracke# properly in Tracea&ility s*eet9</p> <p>)6pecte# <e*a ior ' point 19 Tool co#e s*oul# gi e a error message stating no c*annel is configure# for particular ADC group9</p> <p>point 29 Up#ate TSDD: TST%: Tracea&ility</p> <p>Actual <e*a ior ' point 19 Tool co#e cras* if no c*annel is configure# for a ADC group9 point 29 Re\$uirements are not tracke# properly in Tracea&ility s*eet9</p>	<UG	(%)! ISSU)
20222	Can	Transmission ?istory >ist issues	<p>1F Canlf ECanlf_T6ConfirmationF is &eing calle# – *ile looping &y t*e Can_T6Confirmation%rocessing function9 Any processing isn't &eing #one &y *ar# – are: so l#m t*inking t*e time5out isn't &eing confirme#9 So #on#t – e *a e to c*eck t*e time out *an#ling *ere\</p> <p>2F It#s – ritten on MC>)ARI! G (" A>> TRA! S, IT ,)SSAG) <U""')RSM an# a comment in t*e Can_Start, o#e function: &ut *o– is a transmission *istory &uffer initialiXe#\</p> <p>RSCA! 1T?>ACCM an# RSCA! 1T; N%CTRM resister – eren't &eing rea#: so l #i#n#t un#erstan# *o– to &e initialiXe#9</p>	<UG	(%)! ISSU)
20=1A	Can	%<cfg file Generation operation terminate# #ue to Illegal #i ision &y Xero	<p>%ro&lem #escription' Generation terminate# #ue to Illegal #i ision &y Xero at 7%erlApp7<s– Config8ali#ate9pm line 3019</p> <p>can_ ; 1; %e6e Can%ar6ml Sample_Application_"16%tr6ml R21A_can_"16_<SB, DT%ar6ml)cu, %ar6ml , cu%ar6ml (s%ar6ml Dem%ar6ml</p> <p>!! "111111' Tool 8ersion' 192%A</p> <p>!! "111112' Comman# line arguments' Can_ ; 16%e6e Can%ar6ml</p> <p>Sample_Application_"16%tr6ml R21A_can_"16_<SB, DT%ar6ml)cu, %ar6ml</p> <p>, cu%ar6ml (s%ar6ml Dem%ar6ml</p> <p>Illegal #i ision &y Xero at 7%erlApp7<s– Config8ali#ate9pm line 3019</p> <p>)6pecte# &e*a ior' %<cfg file Generation operation s*oul# not &e terminate#9 If any error occurre# Generator tool s*oul# t*ro– out error – it* am&iguous error message9</p> <p>Actual &e*a ior' %<cfg file Generation operation is terminate# #ue to Illegal #i ision9 If any error occurre# Generator tool is t*ro– ing out error – it* unam&iguous error message9</p>	<UG	(%)! ISSU)

27121	Can	Balking 1 pattern is not implemented in Can_RamTst_Balk%at*_Algorit*mFF A%l	<p>Problem description'</p> <p>As per Renesas requirement AR_110="R_112A:the RA, is checked & using data patterns checker pattern: - alking51 and - alking51 pattern</p> <p>but in the current implementation - alking51 pattern is not implemented? The is s'all & implemented as an enhancement: & it is not a & since Balking M1Ms pattern and Balking M1Ms pattern is similar</p> <p>Expected &e*a iour'</p> <p>As per requirement - alking51 pattern s'all & implemented</p> <p>Actual &e*a iour'</p> <p>In the current code: - alking51 pattern is not implemented</p>	<UG	(%)! ISSU)
271@A	Can	Can_SelfTestC*annel A%l executes in modes other than ST(%)D	<p>Problem description'</p> <p>If the controller is not in ST(%)D state: the function <Can_SelfTestC*annel+s'all & a&orte# and returns)_! (T_(G) but as per the current implementation: the is checked is not provided and the code try to set the operation mode as ?alt mode</p> <p>Expected &e*a ior'</p> <p>The function s'all & a&orte# and returns)_! (T_(G: if the controller is not in the ST(%)D state</p> <p>Actual &e*a ior'</p> <p>Please see the problem description</p>	<UG	(%)! ISSU)
27027	Can	Transmission occurs even if the return of Can_riteFF A%l is CA! _<USH	<p>Problem description'</p> <p>If cancellation is enabled#: cancellation *as to & initiate# for the lower priority ID!#entical ID If identical!# cancellation is also enabled# re\$uest - *en the - rite re\$uest came - it* the *ig*er priority ID 7 identical ID for the same ?T?The T; re\$uest for the ne- >5%DU s'all & repeat# & the CanIf module: inside the notification function CanIf_CancelT6Confirmation 5 requirement /CA! 2@@4</p> <p>If cancellation is #isa&le#: the ne- Can_BriteFF re\$uest for the same ?T? s'all not & accepted# and returns# - it* CA! _<USHThe first - rite re\$uest s'all & transmitte# - *ic* - as in pending state</p> <p>The same information is code# - it* the requirements /CA! 21A4: /CA! 2124: /CA! 2134 and /CA! 2A24</p> <p>Expected <e*a iour'</p> <p>1The transmission s'all not & there for the Can_BriteFF re\$uest - *en its return value is CA! _<USH</p> <p>2The cancellation of the pending transmission *as to *appen properly: - *en transmission cancellation is enabled#</p> <p>Actual <e*a iour'</p> <p>In different scenarios the transmission of the frame *appens even after returning the reply as CA! _<USH for the Can_BriteFF A%l call</p> <p>);'</p> <p>1B*en the cancellation is ("" Eln polling mode: *o-e er the return value of the Can_BriteFF re\$uest for the same ?T? is CA! _<USH: the transmission of the frame is o&ser e# on the CA! AlyXer and also T6 confirmation is received#</p> <p>2B*en identical ID cancellation is (! : *o-e er the return for the Can_BriteFF re\$uest for the same ?T? - it* identical ID is CA! _<USH as expected# and cancel confirmation is received#: the transmission is *appening - it*out re\$ueste# as state# in requirement /CA! 2@@4 Also complete frame data is +1+and T6 confirmation is received# as - ell</p> <p>3B*en cancellation is (! : *o-e er the return for the Can_BriteFF re\$uest for the same ?T? - it* *ig*er priority ID is CA! _<USH as expected# and cancel confirmation is received#: the transmission is *appening - it*out re\$ueste# as state# in requirement /CA! 2@@4 and also T6 confirmation is received#</p>	<UG	(%)! ISSU)

2702=	Can	?t* Cancellation is not notife# correctly to t*e upper layer ECanlff in case of multiple ?t* to cancel	<p>%ro&lem #escription'</p> <p>T*ere is no – *ile loop: &ut unnecessarily >ucArr%osition incremente# an# – rong comment pro i#e#9</p> <p>uint@_least >ucArr%osition0</p> <p>55co#e5555</p> <p>if E>&IT6Cancel"lag LL CA! _TRU)F</p> <p>T</p> <p>7W Set t*e <asicCA! ?T? count to ma6imum to e6it t*e – *ile loop W7</p> <p>>ucArr%osition L >p%<Controller5Duc! o(f<asicCan?t*0</p> <p>7W Set t*e T; Cancellation Status flag of t*e ?T? W7</p> <p>Can_RSCA! _GaaT6CancelSts"Igs/E>ucArr%osition DD CA! _T?R))F4 L</p> <p>ECan_RSCA! _GaaT6CancelSts"Igs/E>ucArr%osition DD CA! _T?R))F4F V</p> <p>EEuint@FECA! _(!) CC E>ucCount] CA! _)IG?TFFF0</p> <p>7W Increment t*e array position to point to ne6t</p> <p>W <asicCA! ?T? of t*e controller W7</p> <p>>ucArr%osition^^0</p> <p>U</p> <p>else</p> <p>T</p> <p>7W ! o action re\$uire# W7</p> <p>U</p> <p>)6pecte# &e*a ior'</p> <p>! one</p> <p>Actual &e*a ior'</p> <p>! one</p>	<UG	(%)! ISSU)
27031	Can	, ultiple D)T error reporting from one A%! not compliant – it* AUT(SAR re\$uirement	<p>Description'</p> <p>Accor#ing to CA! 1=1 from AUT(SAR 2919A CA! SBS 5 a function t*at reports a #e elopment error s*all return imme#iately after9</p> <p>In many CA! #ri er A%ls multiple errors are c*ecke# an# reporte#: &efore t*e function returns9)6amples'</p> <p>W Can_InitEF may report CA! _)_TRA! SITI(! : CA! _)_%ARA, _%(!! T)R &efore it returns</p> <p>W Can_Can_InitControllerEF may report e99 CA! _)_U! !! IT an# CA! _)_%ARA, _%(!! T)R &efore it returnsF</p> <p>)6pecte# &e*a iour'</p> <p>) ery CA! #ri er A%! s*oul# return imme#iately – it* not action after a #e elopment error is #etecte# is reporte#</p> <p>Actual &e*a iour'</p> <p>In some case in CA! A%ls multiple #e elopment errors can &e reporte#: like for Can_InitEF: Can_InitControllerEF</p>	<UG	(%)! ISSU)
27032	Can	D), ersion C*eck is missing	<p>%ro&lem Description'</p> <p>As per t*e re\$uirement /CA! 1114E<SB112F: t*e AUT(SAR ma6or an# minor release ersion nee#s to &e c*ecke# for all t*e e6ternal mo#ules9 <ut t*e ersion c*eck for t*e D), mo#ule is missing9</p> <p>)6pecte# <e*a iour'</p> <p>T*e compilation error s*all &e reporte# – *en t*e D), mo#ule – it* #ifferen# AUT(SAR release ersion is integrate#9</p> <p>Actual &e*a iour'</p> <p>T*e compilation is *appening successfully e en – *en t*e D), mo#ule – it* #ifferen# AUT(SAR release ersion is integrate#9</p>	<UG	(%)! ISSU)
27722	Can	T*e , CA> CA! #ri er s*all clear t*e correspon#ing BU" flag in t*e ISR EAR_%! 110=_R_1123F	<p>%ro&lem Description' T*e , CA> CA! #ri er – ill not clear t*e correspon#ing BU" flag in t*e ISR if t*e precompile configuration parameter CanBakeUp"actorClearlsr is set to TRU)9</p> <p>Default alue s*oul# &e "A>S)9%D" is re ie– e# an# foun# t*at t*e parameter CanBakeUp"actorClearlsr is not implemente#9</p> <p>)6pecte# <e*a ior' AR_%! 110=_R_1123 s*all &e implemente#9</p> <p>Actual <e*a ior' AR_%! 110=_R_1123 is not implemente# properly</p>	<UG	(%)! ISSU)

27@72	Can	Autosare re\$uirment CA! 103_Conf is not taken care	<p>%ro&lem Description' As per Autosare SBS CA! 103_Conf: Can!#Type s*all support ID's of type STA! DARD: , l;)D an#); T)! D)! D)! In t*e current implementation only STA! DARD an#); T)! D)! D types are taken care!</p> <p>)6pecte# <e*a ior' , l;)D ID type s*all also &e supporte# &y t*e %D''</p> <p>Actual <e*a ior' ! A</p>	<UG	(%)! ISSU)
27@@1	Can	A##itional A%! to cancel T6 is not a aila&le for Can!f 7 Upper layer!	<p>%ro&lem Description' T*e internal function _Can_T6Cancel` is a aila&le as pri ate A%! As per t*e re\$uirement #escription: t*e A%! s*all &e a aila&le as pu&lic A%! suc* t*at Can!f AUT (SAR mo#ule can *a e correspon#ing call to t*is A%! in or#er to use it!</p> <p>)6pecte# <e*a ior' T*e internal function _Can_T6Cancel` s*all &e a aila&le as a##itional A%! Epu&licf</p> <p>Actual <e*a ior' ! A</p>	<UG	(%)! ISSU)
27@@2	Can	CA! _)_DATA>(ST #e elopment error is not reporte#	<p>%ro&lem #escription' Accor#ing to AUT (SAR 2919A CA! SBS CA! A=3' M!f t*e #e elopment error #etection for t*e Can mo#ule is ena&le#: t*e Can mo#ule s*all raise t*e error CA! _)_DATA>(ST in case of _o er-rite` or _o errun` e ent #etectionM</p> <p>T*is D)T error CA! _)_DATA>(ST is #eclare# in Can! * &ut it is not use# in t*e co#e!</p> <p>)6pecte# &e*a iour' De elopment error CA! _)_DATA>(ST s*oul# &e reporte# if o er-rite or o errun e ents are #etecte# in t*e reception &uffers!</p> <p>Actual &e*a iour' CA! _)_DATA>(ST #e elopment error reporting is no- *ere encountere# in CA! #ri er co#e</p>	<UG	(%)! ISSU)
27=A2	Can	Une6pecte# <e*a ior of in Can -ake up	<p>%ro&lem #escription' CA! -ake up s*o-s une6pecte# &e*a ior! Bake up ISR is getting trigger#: e en if -ake up is not initiate#!</p> <p>)6pecte# <a*a ior' Bake up s*all &e trigger# only on occurance of a -ake up e ent</p> <p>Actual <e*a ior' Un#efine# &e*a ior of CA! -ake up</p>	<UG	(%)! ISSU)
27=23	Can	Can_Brite re\$uest returns CA! _(G - *en ?T? is &usy	<p>%ro&lem Description' Can_Brite re\$uest returns CA! _(G - *en ?T? is &usy to process anot*er transmit re\$uest! Can transmission s*all only &e initiate# after getting t6 confirmation on t*e last transmitt# message!</p> <p>)6pecte# <e*a ior' Can_Brite re\$uest s*all return CA! _<USH - *en ?T? is &usy to process anot*er transmit re\$uest</p> <p>Actual <e*a ior' ! A</p>	<UG	(%)! ISSU)
27=03	Can	Can mo#ule is malfunctioning on ?B T6 Cancellation!	<p>%ro&lem #escription' Can mo#ule is malfunctioning - *en ?B T6 Cancellation support is ena&le# an# a Transmit a&ort re\$uest - as not successful Et*e CA! frame - as transmitt#f!</p> <p>It t*is particular case: once t*e T6 Cancellation is initiate# insi#e t*e Can mo#ule source co#e t*e glo&al transmit cancel flag _Can_G&IT6CancelInt"lg` is set to CA! _TRU)! >ater t*is flag _Can_G&IT6CancelInt"lg` is cleare# insi#e t*e source co#e in oke# - *en t*e M! TCnBU%M interrupt is ser ice#! <ut in t*e current use case: since t*e Transmit a&ort re\$uest - as not successful: t*e M! TCnBU%M interrupt is not acti ate#! Instea#: t*e M! TCnTR;M interrupt is acti ate# Eframe successfully transmitt# from message &uffer mf: &ut t*e source co#e in oke# - *en M! TCnTR;M interrupt is ser ice# Ee6! CA! _C(! TR(>>)R1_T; _ISRf #oes not clear t*e _Can_G&IT6CancelInt"lg` flag!</p> <p>T*e flag remains set an# t*is causes su&se\$uent calls to MCan_BriteFM to fail an# return MCA! _<USHM result! T*is ren#ers t*e Can mo#ule incapa&le to transmit CA! frames any more Euntil &eing re5initialiXe#f!</p> <p>Actual &e*a ior' ! 7A</p> <p>)6pecte# &e*a ior' ! 7A</p>	<UG	(%)! ISSU)
2@171	Can	/; 164/CA! 4 Can_RamTst_Balk%at*_Algorit*m is not calle# from Can_RA, Test A%!	<p>%ro&lem Description' B*ile up#ating)C(D) for merging t*e c*anges #one as part of "1?)2911911 release to trunk: in Can_RA, Test A%! call of <Can_RamTst_Balk%at*_Algorit*m+ - as replace# &y <Can_RamTest_C*ecker_Algorit*m# So no- instea# of calling Can_RamTst_Balk%at*_Algorit*m: Can_RamTest_C*ecker_Algorit*m is calle# secon# time!</p> <p>Bork Aroun#' Repeate# call of <Can_RamTest_C*ecker_Algorit*m+ *as to &e replace# - it* <Can_RamTst_Balk%at*_Algorit*m#</p> <p>)6pecte# &e*a ior' Can_RamTst_Balk%at*_Algorit*m s*oul# &e calle# from Can_RA, Test A%!!</p> <p>Actual &e*a ior' Can_RamTst_Balk%at*_Algorit*m is not calle# from Can_RA, Test A%!!</p>	<UG	(%)! ISSU)

2@31A	Can	CA! _) _DATA>(ST #e elopment error is not reporte#	<p>%ro&lem #escription'</p> <p>Accor#ing to AUT(SAR 2919A CA! SBS CA! A=3' Mlf t*e #e elopment error #etecion for t*e Can mo#ule is ena&le#: t*e Can mo#ule s*all raise t*e error CA! _) _DATA>(ST in case of _o er-rite` or _o errun` e ent #etecion9M</p> <p>T*is D)T error CA! _) _DATA>(ST is #eclare# in Can9* &ut it is not use# in t*e co#e9</p> <p>)6pecte# &e*a iour'</p> <p>De elopment error CA! _) _DATA>(ST s*oul# &e reporte# if o er-rite or o errun e ents are #etecte# in t*e reception &uffers9</p> <p>Actual &e*a iour'</p> <p>CA! _) _DATA>(ST #e elopment error reporting is no- *ere encountere# in CA! #ri er co#e</p>	<UG	(%)! ISSU)
2@313	Can	, ultiple polling perio# for T6 an# R6 E ali# for RA92an# R2%F is not supporte#	<p>%ro&lem Description'</p> <p>T*is is a task to erify t*e possi&ility to configure an# *an#le multiple transmission or reception in polling mo#e &ase# on SBS RA92 ID' CA! A30:CA! 2A0 ER6F an# CA! A3@: CA! A23 ET6F9</p> <p>T*e solution to t*is issue is to up#ate t*e CA! co#e generator to count *o- many instances of t*e parameter Can, ain"unctionBrite%erio# an# Can, ain"unctionRea#%erio# are configure#</p> <p>an# &ase# on t*is to generate t*e opportune num&er of macro</p> <p>I#efines Can_, ain"unction_Brite_1</p> <p>I#efine Can_, ain"unction_Rea#_1</p> <p>for t*e 1st instance:</p> <p>I#efine Can_, ain"unction_Brite_1</p> <p>I#efine Can_, ain"unction_Rea#_1 for t*e 2n# instance</p> <p>T*e generate# co#e s*oul# look like t*is'</p> <p>7W%olling %erio# 1 for BriteW7</p> <p>I#efine Can_, ain"unction_Brite_1F Can_, ain"unction_BriteF</p> <p>7W%olling %erio# 1 for BriteW7</p> <p>I#efine Can_, ain"unction_Brite_1F Can_, ain"unction_BriteF</p> <p>999etc9</p> <p>)6pecte# &e*a iour'</p> <p>! 7A</p> <p>Actual <e*a iour'</p>	<UG	(%)! ISSU)
2@321	Can	Restrict t*e &au#rate to only a limite# range is - rong9	<p>Restrict t*e &au#rate to only a limite# range is - rong9T*ere are ot*er &ua#rate like 31G or 23G t*at can &e realiXe#9</p> <p>Actually t*e generator impelementation is to occur error: if t*e alue of t*e parameter CanController<au#Rate is ot*er t*an AA: @A:111:123:231:311 or 1111G&ps for t*e particular controller9</p> <p>Actual <e*a iour')rror occurs: if t*e alue of t*e parameter CanController<au#Rate is ot*er t*an AA: @A:111:123:231:311 or 1111G&ps for t*e particular controller9</p> <p>)6pecte# <e*a iour')rror s*oul# not occur: if t*e alue of t*e parameter CanController<au#Rate is ot*er &ua#rate like 31G or 23G9</p>	<UG	(%)! ISSU)

20231	DI(Glo&al aria&le EDio_Gus! o(fC*annelGroupsF an# config structure mem&er Eus! oofC*annelGroupsF names are mislea#ing	%ro&lem Description' T*e glo&al aria&le +Dio_Gus! o(fC*annelGroups+ - *ic* is initialise# - it* t*e alue of t*e config structure mem&er +us! oofC*annelGroups+ is use# as offset for accessing t*e c*annel group structure - *en multi config set comes into picture9 <ut t*e name suggests t*at it *ol#s t*e alue of Mnum&er of c*annel groups configure#M - *ic* mislea#ing9)9g9' If @ c*annel groups are configure# - it* t-o config sets Eeac*F: t*e *an#les - ill &e generate# as: I#efine DioConf_DioC*annelGroup_DioC*annelGroup1 EPDio_GstC*annelGroupData/14F I#efine DioConf_DioC*annelGroup_DioC*annelGroup2 EPDio_GstC*annelGroupData/14F I#efine DioConf_DioC*annelGroup_DioC*annelGroupA EPDio_GstC*annelGroupData/24F I#efine DioConf_DioC*annelGroup_DioC*annelGroup2 EPDio_GstC*annelGroupData/A4F I#efine DioConf_DioC*annelGroup_DioC*annelGroup3 EPDio_GstC*annelGroupData/24F I#efine DioConf_DioC*annelGroup_DioC*annelGroup0 EPDio_GstC*annelGroupData/34F I#efine DioConf_DioC*annelGroup_DioC*annelGroup7 EPDio_GstC*annelGroupData/04F I#efine DioConf_DioC*annelGroup_DioC*annelGroup@ EPDio_GstC*annelGroupData/74F It s*all generate @ *an#les pointing @ c*annel group structures an# t*e c*annel structures =510 - *ic* is applica&le for secon# config set s*all &e accesse# - it* t*e *elp of same *an#les - it* offset alue in +Dio_Gus! o(fC*annelGroups9 i9e9 B*en config set 1 is initialise# its alue - ill &e 19 B*en config set 1 is initialise# its alue - ill &e @ for t*e a&o e case9)6pecte# <e*a ior' T*e name of t*is glo&al aria&le an# respecti e config structure mem&er *as to &e correcte# - it* meaningful name - *ic* is near to it usage9)6' +Dio_GusC*annelGroups(ffset+an# +usC*annelGroups(ffset+respecti ely9 Actual <e*a ior' 8aria&le names are mislea#ing	<UG	(%)! ISSU)
20372	DI((ptimiXe t*e e6ecution time of RSR register setting se\$uence	%ro&lem #escription' C*eck t*e pin #irection an# prepare t*e alue an# - rite to psr register - ill take less time to e6ecute - *en t*e pin #irection c*eck is faile#E&beginning c*eck itself it - ill come outf9 As per current met*o#) en t*e pin #irection c*eck is faile#: %repare t*e alue to set to t*e register operation is performe#9)6pecte# &e*a ior' 19C*eck t*e pins #irection%EC*eck t*e %, SR register% 29%repare t*e alue to set to t*e register9 A9Brite t*e %SR register9 Source ' %lease refer to t*e attac*e# file%ropose# amen#mentsf Dio_BriteC*annel 10@51=1 Dio_BriteC*annelGroup 2175213 Dio_, aske#Brite%ort 3@253=1 Actual &e*a ior' 19%repare t*e alue to set to t*e register9 29C*eck t*e pins #irection%EC*eck t*e %, SR register% A9Brite t*e %SR register9 "unction ' Dio_BriteC*annel: Dio_BriteC*annelGroup: Dio_, aske#Brite%ort >ine ' Dio_BriteC*annel =A15=30 Dio_BriteC*annelGroup 137A513@A Dio_, aske#Brite%ort 171251721	<UG	(%)! ISSU)

2772=	DI(Unreac*a&le co#e present in Dio%c	<div>In A%! Dio_Rea#C*annelGroup an# A%! Dio_BriteC*annelGroup: In t*e &elo- co#e if t*e first con#ition got true: co#e - ill not go insi#e else part9 If t*e first con#ition fails t*e secon# con#ition - oul# also &e false9 So t*e co#e insi#e t*e secon# if &lock is unreac*a&le t*at is #ea# co#e9 T*e issue is foun# #uring UT9</div> <div>if EC*annelGroupl#%tr LL ! U>>_%TRF</div> <div>T</div> <div>7W TRAC) /RA: DI(1214/R2: DI(1214 W7</div> <div>7W TRAC) /R2: DI(17@4 W7</div> <div>7W Report)rror to D)T W7</div> <div>E oi#fDet_Report)rrorEDI(_, (DU>)_ID: DI(_! STA! C)_ID:</div> <div>DI(_BRIT)_C?A! !)>_GR(U%_SID: DI(_)_%ARA, _%(! T)RF0</div> <div>>enDet)rr"lag L)_(G0</div> <div>U</div> <div>else</div> <div>T</div> <div>7W Get t*e pointer to correspon#ing in#e6 in t*e</div> <div>array Dio_GstC*annelGroupData W7</div> <div>7W , ISRA 8iolation' START , sgE2'12=2F50 W7</div> <div>>pC*annelGroup%tr L PC*annelGroupl#%tr/Dio_Gus! o(fC*annelGroups40</div> <div>7W)! D , sgE2'12=2F50 W7</div> <div>if E! U>>_%TR LL >pC*annelGroup%trf</div> <div>T</div> <div>7W Report)rror to D)T W7</div> <div>E oi#fDet_Report)rrorEDI(_, (DU>)_ID: DI(_! STA! C)_ID:</div> <div>DI(_BRIT)_C?A! !)>_GR(U%_SID: DI(_)_%ARA, _! 8A>ID_GR(U%F0</div> <div>>enDet)rr"lag L)_(G0</div> <div>U</div> <div>else</div>	<UG	(%)! ISSU)
203=2	">S	/"1>4/">S4 , ismatc* in memory mapping of "Is_, ain"unction	<div>%ro&lem #escrpaon'δ</div> <div>T*ere is a mismatc* in t*e memory mapping of "Is_, ain"unction9</div> <div>In "Is9*: "Is_, ain"unction is mappe# to ">S_START_S)C_%U<>IC_C(D)'ESee t*e co#e &elo-F</div> <div>232 ' I#efine ">S_START_S)C_%U<>IC_C(D)</div> <div>233 ' Iinclu#e M, em, ap9*M</div> <div>2@1 ' e6tern "U! CE oi#: ">S_%U<>IC_C(D)F "Is_, ain"unctionE oi#F0</div> <div>In "Is9c: "Is_, ain"unction is mappe# to ">S_START_S)C_SC?)DU>)R_C(D)ESee t*e co#e &elo-F</div> <div>10@= ' #efine ">S_START_S)C_SC?)DU>)R_C(D)</div> <div>10=1 ' Iinclu#e M, em, ap9*M</div> <div>10=1 '</div> <div>10=2 ' "U! CE oi#: ">S_%U<>IC_C(D)F "Is_, ain"unctionE oi#F</div> <div>An# in , em, ap9*: &ot* ">S_START_S)C_%U<>IC_C(D) an# ">S_START_S)C_SC?)DU>)R_C(D) are #efine# as M9">S_%U<>IC_C(D)_RA, M9</div> <div>)6pecte# &e*a iour'δ</div> <div>, emory mapping in &ot* #eclaration an# #efinition of function s*all &e uni\$ue9</div> <div>Actual &e*a iour'</div> <div>T*ere is #ifference in t*e memory mapping of #eclaration an# #efinition of function "Is_, ain"unction9</div>	<UG	(%)! ISSU)

20=23	">S	>engt* calculation for misalign# access fails	<p>Description'</p> <p>>engt* calculation in "Is_Internal lea#s to in ali# alues9</p> <p>Actual &e*a iour'</p> <p>Resulting lengt* is aroun# 2 &illion – *ic* causes ot*er function to &e in en#less loop9</p> <p>)6pecte# &e*a iour'</p> <p>Correct lengt* calculation</p>	<UG	(%)! ISSU)
273@1	">S	"Is_G8ar structure is not initialise# properly accor#ing to C@=7C=1 EIS(7I)C =@== '1==1F	<p>%ro&lem #escription'</p> <p>T*e initialiXation of "Is_G8ar in "Is_Ram9c is not accor#ing to C=1 stan#ar#9A structure t*at contains pointers: aria&les an# furt*er structures is simply initialiXe# – it* M1M9</p> <p>T*is is possi&le in C== EIS(7I)C =@== '1===: c*apter 0979@921F: &ut , CA> s*all &e implemente# accor#ing to C@=7C=1 EIS(7I)C =@== '1==1F9</p> <p>In Co#ing Gui#elines)AAR5G>511@29p#f:)AAR_%! 11@2_! R_1103 an e6ample is gi en'</p> <p>7W Usage an# initialiXation in a C file' W7</p> <p>, y, o#ule_8ector_tst 8ector1_st L T 1: 1 U0</p> <p>)6pecte# &e*a iour'</p> <p>)ac* element in t*e structure s*all &e initialise# properly9</p> <p>Actual &e*a iour'</p> <p>Structure is initialise# as 8ARE"Is_G8ar%roperties: ">S_! IT_DATAF "Is_G8ar L T">S_Y)R(U0</p>	<UG	(%)! ISSU)
27@1=	">S	"Is_Resume cannot interrupt t*e ">S ISRER(<)! DF9AR_%! 1172_"R_1120F	<p>%ro&lem Description'</p> <p>As per current implementation in "Is_ResumeF A%!: If ">S_D)8_)RR(R_D)T)CT L STD_(! an# ">S_TI,)(UT_, (! IT(RI! G L STD_(! t*en after –aiting for ">S_ISR_TI,)(UT_8A>U): It starts ">S Resume process – it*out c*ecking – *et*er ">S ISR is ser ice# EC*eck – *et*er "Is_G8ar9"Is_, ute6"lag LL ">S_Y)R(F9 In current implementation: It – ill also not report any D)T if timeout occurre# after –aiting for ">S ISR is ser ice#9</p> <p>T*is issue e6ist in file "Is9c 819A909</p> <p>As per re\$uirement AR_%! 1172_"R_1120'5</p> <p>C"Is_Suspen# cannot interrupt t*e ">S ISRER(<)! DF9 It means – *en ">S ISRER(<)! DF *as alrea#y entere# critical section Eprotecte# &y semap*ore7mute6f t*e upcoming "Is_Suspen# must –ait until ISRER(<)! DF e6its critical section9 until ISRER(<)! DF e6its critical section9D</p> <p>Un#erstan#ing is t*at a&o e mentione# point in , RS is not correct9 As per our un#erstan#ing correct one is as mentione# &elo–9</p> <p>C"Is_Resume cannot interrupt t*e ">S ISRER(<)! DF9 It means – *en ">S ISRER(<)! DF *as alrea#y entere# critical section Eprotecte# &y semap*ore7mute6f t*e upcoming "Is_Resume must –ait until ISRER(<)! DF e6its critical section9 until ISRER(<)! DF e6its critical section9D</p> <p>)6pecte# <e*a ior' , RS re\$uirement AR_%! 1172_"R_1120 nee#s to implement properly9</p> <p>Actual <e*a ior' , RS re\$uirement AR_%! 1172_"R_1120 is not implemente# properly9</p>	<UG	(%)! ISSU)

27@2=	">S	"Is_Suspen# cannot interrupt t*e ">S ISRER(<)! DF%EAR_%! 1172_"R_1123F	<p>%ro&lem Description'</p> <p>As per current implementation in "Is_Suspen#EF A%I: If ">S_D)8_)RR(R_D)T)CT L STD_(! an# ">S_TI,)(UT_, (! IT(RI! G L STD_(! t*en after – aiting for ">S_ISR_TI,)(UT_8A>U): It starts ">S Suspen# process – it*out c*ecking – *et*er ">S ISR is ser ice# EC*eck – *et*er "Is_G8ar9"Is_, ute6"lag LL ">S_Y)R(F9 In current implementation: It – ill also not report any D)T if timeout occurre# after – aiting for ">S ISR is ser ice#9</p> <p>T*is issue e6ist in file "Is% 819A909</p> <p>As per re\$uirement AR_%! 1172_"R_1123'5 "Is_Suspen# cannot interrupt t*e ">S ISRER(<)! DF9 It means – *en ">S ISRER(<)! DF *as alrea#y entere# critical section Eprotecte# &y semap*ore7mute6f t*e upcoming "Is_Suspen# must – ait until ISRER(<)! DF e6its critical section9</p> <p>)6pecte# <e*a ior' , RS re\$uirement AR_%! 1172_"R_1123 nee#s to implement properly9</p> <p>Actual <e*a ior' , RS re\$uirement AR_%! 1172_"R_1123 is not implemente# properly9</p>	<UG	(%)! ISSU)
27@A=	">S	"Is_Cancel cannot interrupt t*e ">S ISRER(<)! DF%EAR_%! 1172_"R_1111F	<p>%ro&lem Description'</p> <p>As per current implementation in "Is_CancelEF A%I if ">S_D)8_)RR(R_D)T)CT L STD_(! an# ">S_TI,)(UT_, (! IT(RI! G L STD_(! t*en after – aiting for ">S_ISR_TI,)(UT_8A>U): It starts ">S Cancel process – it*out c*ecking – *et*er ">S ISR is ser ice# EC*eck – *et*er "Is_G8ar9"Is_, ute6"lag LL ">S_Y)R(F9 In current implementation: It – ill also not report any D)T if timeout occurre# after – aiting for ">S ISR is ser ice#9</p> <p>T*is issue e6ist in file "Is% 819A909</p> <p>As per re\$uirement AR_%! 1172_"R_1111'5 "Is_Cancel cannot interrupt t*e ">S ISRER(<)! DF9 It means – *en ">S ISRER(<)! DF *as alrea#y entere# critical section Eprotecte# &y semap*ore7mute6f t*e upcoming "Is_Cancel must – ait until ISRER(<)! DF e6its critical section9</p> <p>)6pecte# <e*a ior' , RS re\$uirement AR_%! 1172_"R_1111 nee#s to implement properly9</p> <p>Actual <e*a ior' ! A</p>	<UG	(%)! ISSU)
27@07	">S	%re compile s– itc* re\$uire# to partition "C> an# "D> in source co#e EAR_%! 1172_"R_1127F	<p>%ro&lem #escription'</p> <p>As per AR_%! 1172_"R_1127: T*e source co#e of ">S mo#ule must &e partione# in "C> part an# "D> part &y using pre5compile s– itc*es9</p> <p>T*e "las* li&rary files: in t*is case "C>7"D> files: s*all &e inclu#e# in &uil# process as per ">S mo#ule configuration for respecti e use5cases9 Re#un#ant li&rary files s*all &e e6clu#e# from &uil# process</p> <p>)6pecte# &e*a ior' T*e re\$uirement AR_%! 1172_"R_1127 s*all &e implemente#</p> <p>Actual <e*a ior' ! A</p>	<UG	(%)! ISSU)
27@=1	">S	"las* li&rary status mapping	<p>%ro&lem #escription'</p> <p>Internal status of un#erlying li&raries: ie9"C> an# "D> s*all &e mappe# to ">S #ri er status accor#ingly an# s*all lea# to proper lo& processing result of ">S #ri er9</p> <p>In case of critical internal errors: notification must &e gi en an# user can #eci#e to take necessary reme#y9</p> <p>)6pecte# &e*a ior' , RS re\$uirement AR_%! 1172_"R_1122 nee#s to implement9</p> <p>Actual &e*a ior' , RS re\$uirement AR_%! 1172_"R_1122 is not implemente# properly9</p>	<UG	(%)! ISSU)

2@23=	">S	Incomplete linker #irecti e file for ">S sample application	<p>C<D%ro&lem Description'C7<D</p> <p>T*e linker #irecti e file of ">S sample application #oes not contain t*e entries for copying t*e initial aria&le alues from R(, to RA, #uring startup9</p> <p>T*is is typically #one &y e99CpreD9rom#ata R(, F9#atafC7preD</p> <p>C<D)6pecte# <e*a iour'C7<D</p> <p>8aria&les are initialixe# &y G?S startup as re\$uire# &y C stan#ar#9</p> <p>C<DAActual <e*a iour'C7<D</p> <p>8aria&les are uninitialixe#: typically at 1 after po-er on: at any un#efine# alue after reset9</p> <p>Application mig*t s*o- strange &e*a iour9</p>	<UG	(%)! ISSU)
2@312	">S	! egati e test cases re\$uire# to erify t*e &oun#ary c*eck of ">S_C"_(""S)T_8A>U)	<p>In "Is9c:in section</p> <p>I if E">S_ ">AS?_ACC)SS LL ">S_C(D)">AS?_ACC)SSF</p> <p>7W 8irtual a##ress is mappe# to p*ysical a##ress W7</p> <p>TargetA##ress L TargetA##ress 5 ">S_C"_(""S)T_8A>U)0</p> <p>T*ere s*all &e a c*eck for TargetA##ress against ">S_C"_(""S)T_8A>U) &efore #oing su&traction9</p> <p>"urt*er: it re\$uires negati e test cases to erify t*e &oun#ary c*eck9</p>	<UG	(%)! ISSU)
2@313	">S	"Is_GulTimeout to &e remo e# from functions "Is_C"%rocessRea#Comman# an# "Is_C"%rocessCompareComman#	<p>In "Is_Internal9c: "Is_GulTimeout is actually not use# in functions "Is_C"%rocessRea#Comman# an# "Is_C"%rocessCompareComman#9</p> <p>So it s*outl# &e remo e# from a&o e mentione# t-o su&functions9</p>	<UG	(%)! ISSU)
2@310	">S	Setting of aria&le llo& notification to True is actually not #epen#ing on #ata flas* status or co#e flas* status	<p>In "Is_Internal9c: in A%l "Is_)n#Ro&%rocessEF: >&lRo&! otification+ aria&le is set to true irrespecti e of t*e con#ition c*eck Mif ER_"D>_(G LL "Is_Gst8ar9GucD"StatusFMD</p> <p>Similar is t*e case - it* t*e c*eck Mif ER_"C>_(G LL "Is_Gst8ar9GucC"StatusFMD</p> <p>So t*e re#un#ant co#e can &e merge# in t*ese cases9</p>	<UG	(%)! ISSU)
2@317	">S	D), error report s*outl# &e a##e# #epen#ing on t*e)rase7Brite operations9	<p>In "Is_Ir\$9c: Dem_Report)rrorStatusE">S_)RAS)_ "Al>)D: D), _)8)! T_STATUS_ "Al>)Df an# Dem_Report)rrorStatusE">S_)_BRIT)_ "Al>)D: D), _)8)! T_STATUS_ "Al>)Df s*outl# &e a##e# #epen#ing on t*e)rase7Brite operations9</p>	<UG	(%)! ISSU)
2@31@	">S	Tool s*all t*ro- error message - *en + "IsUseInterrupt L (! +an# t*e call &ack functions are not mappe#	<p>If interrupt is supporte# E "IsUseInterrupt L (! F an# t*e call &ack functions are not mappe# E! U>>F: t*e program - ill *ang9</p> <p>T*e tool co#e s*all &e up#ate# to t*ro- error message for a&o e mentione# user configuration9</p>	<UG	(%)! ISSU)
2@321	">S	Default alue of >enReturn8alue s*all &e)_! (T_(G	<p>In "Is9c:</p> <p>Default alue of >enReturn8alue s*all &e)_! (T_(G9 In t*is - ay: ">S llo& re\$uest - ill &e reflecte# in t*e first place - *en #ri er state is &usy9 T*is logic is not #epen#ent on D)T settings9</p>	<UG	(%)! ISSU)
2@321	">S	T*e logic for up#ating "Is_G8ar9"Is_GenState an# "Is_G8ar9"Is_GenRo&Result s*all &e in line - it* t*e logic in "Is_)n#Ro&%rocess	<p>In "Is_Ir\$9c: at en# of llo& processing: t*e logic for up#ating "Is_G8ar9"Is_GenState: "Is_G8ar9"Is_GenRo&Result an# triggering Ro&)n# or Ro&)rror notifications s*all &e in line - it* t*e logic in "Is_)n#Ro&%rocess function in "Is_Internal9c9</p> <p>In t*e current implementation: "Is_)rase an# "Is_Brite support interrupt &ase# llo& processing9 An# "Is_)n#Ro&%rocess is not re\$uire# at en# of interrupt &ase# llo& processing9</p>	<UG	(%)! ISSU)

2@322	">S	Ro&)n# an# Ro&)rror! otification s*oul# #epen# on &ot* E">S_R(<_! (TI"_C(! "IG LL STD_(! F PP E">S_! T)RRU%T_, (D) LL STD_(! F	In "Is_Ir\$%c: "Is_GpConfig%tr5DpRo&)n#! otification%ointerEf an# "Is_GpConfig%tr5DpRo&)rror! otification%ointerEf s*oul# &e #epen#ing on &ot* E">S_R(<_! (TI"_C(! "IG LL STD_(! F PP E">S_! T)RRU%T_, (D) LL STD_(! F9 Actual <e*a iour ' if ER_"D>_(G LL "Is_Gst8ar9GucD"StatusF T 7W Set t*e Ko& Result to (G W7 "Is_G8ar9"Is_GenRo&Result L ,), I"_R(<_(G0 7W If Ko& en#e# – it* success an# call t*e Ko& en# call &ack W function9 W7 "Is_GpConfig%tr5DpRo&)n#! otification%ointerEf0 U else T 7W Set t*e Ko& Result to "aile# W7 "Is_G8ar9"Is_GenRo&Result L ,), I"_R(<_"Al>)D0 7W If Ko& en#e# – it* error an# call t*e Ko& error call &ack W function9 W7 "Is_GpConfig%tr5DpRo&)rror! otification%ointerEf0 U)6pecte# <e*a iour ' if ER_"D>_(G LL "Is_Gst8ar9GucD"StatusF	<UG	(%)! ISSU)
2@32A	">S	Impro ement in %D" of %16	T*e follo–ing points s*oul# &e taken care in t*e %arameter Definition "ile of %169 a9T*e upper &oun#7ma6imum alue of "Is"clRamA##ress s*all &e 22701=2=27 E16")D" _ """"F for %169 &9T*e parameters use# in eac* of t*e follo–ing containers s*oul# &e re5or#ere# to gi e a &etter o er ie– of parameters9 19 "IsData"las* 29 "IsCo#e"las* A9 "Is%u&lis*e#Information c9%parameter M"IsD"TotalSiXeM in +!IsData"las*+container s*oul# &e rename# as M"IsData"las*SiXeM9 #9T*e statement in t*e #escription of follo–ing parameters s*all &e up#ate# as mentione#9 19 "Is, a6Brite! ormal, o#e ' a## into #escription 5 T*is parameter is not use# for implementation9 29 "Is, a6)rase! ormal, o#e ' a## into #escription 5 T*is parameter is not use# for implementation9 A9 "IsTotalSiXe ' impro e #escription 5 T*is parameter specifies t*e total amount of flas* memory in &ytes t*at is accessi&le &y ">S #ri er9 29 "Is! um&er (fSectors ' a## into #escription 5 T*is parameter setting s*all &e in line – it* "IsSectorStarta##ress9 39 "Is"clRamA##ress ' a## into #escription 5 T*is parameter is not use# for implementation9 09 "IsD"TotalSiXe ' impro e #escription 5 T*is parameter in#icates t*e p*ysical total siXe of Data "las* memory9	<UG	(%)! ISSU)
2@323	">S	Safe e6it from – *ile5loop for R_"D>_?an#ler in "Is_Init s*all &e realiXe# as per general re\$uirement	In "Is%c: Safe e6it from – *ile5loop for R_"D>_?an#ler in "Is_Init s*all &e realiXe# as per general re\$uirement9 Timeout monitoring can &e consi#ere# *ere9	<UG	(%)! ISSU)
2@320	">S	D), error s*oul# &e reporte# for transient failures	">S InitialiXation E"Is_Initf can fail #uring #ri ing cycle of)CU: #ue to eg9 operation oltage c*ange: clock fre\$uency c*ange: etc9Etransient failuresf9 Suc* kin# of fault must &e notifie# an# after– ar#s t*e upper layer can: for e6ample: retry – it* init proce#ure until succee#s: or t*e system can &e s– itc*e# to a safety state if re\$uire#9 D), error s*oul# &e reporte# *ere9	<UG	(%)! ISSU)
2@327	">S	R_"D>_?an#lerEf call in "Is_, ain"unction an# "Is_Init s*oul# &e protecte# – it* critical section	In "Is%c: R_"D>_?an#lerEf call in "Is_, ain"unction an# "Is_Init s*oul# &e protecte# – it* critical section9	<UG	(%)! ISSU)

2@3@7	">S	Une6pecte# Interrupt pen#ing &it is set in "Is_Brite an# "Is_)rase A%ls	<p>%ro&lem #escription'</p> <p>In "Is_Brite an# "Is_)rase A%ls: interrupt processing is ena&le# as follo-s'</p> <p>I if E">S_! T)RRU%T_, (D) LL STD_(! F</p> <p>7W)na&le interrupt processing W7</p> <p>R?@31_S8_, (D)_I, R_A! DE10: E"Is_GpConfig%tr5Dp")n#ImrA##ressF:</p> <p>E"Is_GpConfig%tr5Dus")n#Imr, askFF0</p> <p>I en#if</p> <p>At t*at point SB nearly al-ays *a# t*e interrupt pen#ing &it set: so a first un- ante# interrupt occurs \$uite fast t*ere9</p> <p>%en#ing interrupts are not properly *an#le# in t*e co#e so t*at t*is - ill cause some confusion &ecause of a pen#ing interrupt from t*e pre ious operation9</p> <p>T*ere is a c*ance of setting interrupt pen#ing &it from t*e pre ious Rea# operation: - *ic* *a e in&uilt &lank c*eck9</p> <p>)6pecte# &e*a iour'</p> <p><efore ena&ling interrupts: interrupt pen#ing &it s*all &e cleare#9</p> <p>Actual &e*a iour'</p> <p>Interrupt pen#ing &it is set - *en interrupt processing is ena&le# in "Is_Brite an# "Is_)rase A%ls</p>	">ATUR)	(%)! ISSU)
2@317	"IsTst	As per Autosar re\$uirement: "IsTst9* s*oul# inclu#e St#_Types9* #irectly9	<p>Accor#ing to Autosar re\$uirement specification: t*e inclu#e of St#_Types9* s*oul# &e #one in "IsTst9*</p> <p><ut in "IsTst: St#_Types9* is inclu#e# t*roug* "IsTst_%<Types9* an# "IsTst_Types9* in t*e current implementation9 T*e files can &e foun# in t*e follo-ing s n pat* 5</p> <p>7trunk7e6ternal7; 1; 7common_platform7mo#ules7flstst7inclu#e</p> <p>)6pecte# &e*a iour '</p> <p>"IsTst9* s*oul# inclu#e St#_Types9* #irectly9</p> <p>Actual &e*a iour '</p> <p>St#_Types9* is inclu#e# t*roug* "IsTst_%<Types9* an# "IsTst_Types9*</p>	<UG	(%)! ISSU)
2@311	"IsTst	"IsTst_Gen>ast"gn#Result an# "IsTst_Gen(erall<gn#Result s*all &e #eclare# as ">STST_! IT_DATA	<p>"IsTst_Gen>ast"gn#Result an# "IsTst_Gen(erall<gn#Result s*all &e #eclare# as ">STST_! IT_DATA: so as to matc* - it* memory section ">STST_START_S)C_8AR_U! S%)CI")D9</p> <p><ut in t*e current implementation "IsTst_Gen>ast"gn#Result an# "IsTst_Gen(erall<gn#Result are #eclare# as ">STST_! (IT_DATA9 T*is can &e foun# in t*e pat* 5</p> <p>JtrunkJe6ternalJ; 1; Jcommon_platformJmo#ulesJflststJsrcJ"IsTst_Ram%&c</p> <p>)6pecte# <e*a iour '</p> <p>7W 8aria&le to store t*e fgn# test result W7</p> <p>8ARE"IsTst_TestResult"gn#Type: ">STST_! IT_DATAF"IsTst_Gen>ast"gn#Result</p> <p>L ">STST_! (T_T)ST)D0</p> <p>7W TRAC) /R2: "IsTst1324 W7</p> <p>7W 8aria&le to store t*e o erall <gn# test result W7</p> <p>8ARE"IsTst_TestResultType: ">STST_! IT_DATAF"IsTst_Gen(erall<gn#Result</p> <p>L ">STST_R)SU>T_! (T_T)ST)D0</p> <p>Actual <e*a iour '</p> <p>7W 8aria&le to store t*e fgn# test result W7</p> <p>8ARE"IsTst_TestResult"gn#Type: ">STST_! (IT_DATAF"IsTst_Gen>ast"gn#Result</p> <p>L ">STST_! (T_T)ST)D0</p> <p>7W TRAC) /R2: "IsTst1324 W7</p> <p>7W 8aria&le to store t*e o erall <gn# test result W7</p> <p>8ARE"IsTst_TestResultType: ">STST_! (IT_DATAF"IsTst_Gen(erall<gn#Result</p> <p>L ">STST_R)SU>T_! (T_T)ST)D0</p>	<UG	(%)! ISSU)

2732@	"r	<p>/%164/"r4 B*ile #oing NAC Static Analysis: Some of t*e , isra iolations are not kustifie#9</p>	<p>%ro&lem Description' B*ile running NAC Static Analysis for t*e "r_3=% file an# "r_3=_Internal%: NAC , isra rules iolations are occuring%Some of t*e iolations are kustifie# an# some iolations are not kustifie#% "or e6ample' T*e messages suc* as 2'1@2A: 2'1@0A: 2'2=@3 etc% are not kustifie#% Common co#e c*ange is not in t*e scope of 82911912 %16 release%</p> <p>)6pecte# <e*a iour' ! A</p> <p>Actual <e*a iour' ! A</p>	<UG	(%)! ISSU)
27727	"r	<p>/%164/829119124/"R4 T*e test cases relate# to Dem_Report)rrorStatusE"rDemCtrlTestResultRef: D), _>8)! T_STATUS_"AI>)DF 5 "R_)TC_103: Dem_Report)rrorStatus E"rlfDem"TSlotStatusRef: D), _>8)! T_STATUS_"AI>)DF 5 "R_)TC_100 an# "R_)TC_107 are failing%</p> <p>"R_)TC_103' In t*e)STS: in t*e Teste# functionality: Dem_Report)rrorStatusE"R_)_ACC)SS: D), _>8)! T_STATUS_"AI>)DF is teste#%<ut in t*e e6pecte# test result: Dem_Report)rrorStatusE"rDemCtrlTestResultRef: D), _>8)! T_STATUS_"AI>)DF is c*ecke# for t*e A%ls "r_Recei eR6>%#u: "r_C*eckT6>%#uStatus: "r_TransmitT6>%#u: "r_CancelT6>%#u% T*e test cases "R_)TC_103 is not teste# &ecause t*e Dem_Report)rrorStatus E"rDemCtrlTestResultRef: D), _>8)! T_STATUS_"AI>)DF is not implemente# in t*e follo-ing A%ls "r_Recei eR6>%#u: "r_C*eckT6>%#uStatus: "r_TransmitT6>%#u: "r_CancelT6>%#u% "R_)TC_100 an# "R_)TC_107' In t*e)STS: in t*e Teste# functionality: Dem_Report)rrorStatusE"R_)_ACC)SS: D), _>8)! T_STATUS_"AI>)DF is teste#%<ut in t*e e6pecte# test result:Dem_Report)rrorStatus E"rlfDem"TSlotStatusRef: D), _>8)! T_STATUS_"AI>)DF is c*ecke# for t*e A%ls "r_TransmitT6>%#u: "r_Recei eR6>%#u% T*e test cases "R_)TC_100 an# "R_)TC_107 are not teste# &ecause t*e Dem_Report)rrorStatus E"rlfDem"TSlotStatusRef: D), _>8)! T_STATUS_"AI>)DF is not implemente# in t*e follo-ing A%ls "r_TransmitT6>%#u: "r_Recei eR6>%#u% "R_)TC_103' As per t*e Autosar R291A "le6Ray SBS re\$uirement: if any *ar#- are error occurs - *ile running t*e A%ls "r_Recei eR6>%#u/"R2A24: "r_C*eckT6>%#uStatus/"R22A4: "r_TransmitT6>%#u/"R22A4: "r_CancelT6>%#u/"R01A4: t*en it s*oul# call Dem_Report)rrorStatus E"rDemCtrlTestResultRef: D), _>8)! T_STATUS_"AI>)DF an# return)_! (T_(G% "R_)TC_100 an# "R_)TC_107' As per t*e Autosar R291A "le6Ray SBS re\$uirement: In t*e A%l "r_Recei eR6>%#u: /"R0134If t*e optional configuration parameter "rlfDem"TSlotStatusRef e6ists an# a single slot status error &it E SSbSynta6)rror: SSbContent)rror: SSb< iolationf is set: t*en t*e slot status information s*all &e reporte# to D), as Dem_Report)rrorStatus E"rlfDem"TSlotStatusRef: D), _>8)! T_STATUS_"AI>)DF%</p> <p>)6pecte# <e*a iour' "R_)TC_103'</p>	<UG	(%)! ISSU)	
2772@		<p>/%164/829119124/"R4 t*e functional testcases relate# to Transmit Nueue an# Recei e Nueue functionality are failing%</p>	<p>%ro&lem Description' In)STS t*e functional testcases relate# to Transmit Nueue an# Recei e Nueue functionality E"R_)TC_1=@: "R_)TC_1==: "R_)TC_111: "R_)TC_111: "R_)TC_112: "R_)TC_111Af are failing%</p> <p>)6pecte# <e*a iour' After transmitting t*e Data &y in oking "r_TransmitNueue_Ta&leF it is returning)_(G as per t*e)6pecte# Test result an# - *en "r_Recei eNueue_Ta&leF is in oke#: it s*oul#)_(G an# also t*e transmitt# #ata s*oul# &e recei e# &y t*e controller%</p> <p>Actual <e*a iour' After transmitting t*e Data &y in oking "r_TransmitNueue_Ta&leF it is returning)_(G as per t*e)6pecte# Test result an# - *en "r_Recei eNueue_Ta&leF is in oke#: it is returning)_(G as per t*e e6pecte# Test Result &ut t*e #ata is not recei e#%</p>	<UG	(%)! ISSU)
2@127	"r	<p>/%164/"r4/R2914 "r_User_Re\$uest_(utput_Transfer an# "r_User_Re\$uest_Input_Transfer A%ls are not - orking%</p>	<p>%ro&lem Description' "r_User_Re\$uest_(utput_Transfer an# "r_User_Re\$uest_Input_Transfer A%ls are returning)_(G &ut no transmit7recei e functionality is *appening%</p> <p>)6pecte# <e*a iour' Transmit7recei e functionality s*oul# *appen in t*is "r_User_Re\$uest_(utput_Transfer an# "r_User_Re\$uest_Input_Transfer A%ls % Actual <e*a iour' Transmit7recei e functionality is not *appen in t*is "r_User_Re\$uest_(utput_Transfer an# "r_User_Re\$uest_Input_Transfer A%ls %</p>	<UG	(%)! ISSU)

2@A20		<p>/%164/"R4Some)co#e lines are more t*an @1 c*aracters an# trailing spaces are present9</p>	<p>%ro&lem Description' 19, ore t*an @1 c*aracters in follo- ing lines ' "r_3=_Internal9c ' S>1AA2 : >1A32: 1213:1222:131=:13=1:1021:10A2:1772:1=13:2111 "r_3=9c ' S>1=1=:2301 "r_3=_De&ug9* ' S>@A "r_3=_GeneralTypes9*' S>277:2@0 "r_3=_Internal9* ' S>71: S>=7 "r_3=_8ersion9*' S>@0 "r_3=9* ' S>2A0: >71</p> <p>29 Trim trailing space not #one in follo- ing files' "r_3=9*: "r_3=_Ram9*: "r_3=_%<Types9*: "r_3=_Internal9*: "r_3=9c: "r_3=_Ram9c: "r_3=_Internal9c</p> <p>Actual <e*a ior' ! A</p> <p>)6pecte# <e*a ior' ! A</p>		(%)! ISSU)
2@272	"r	<p>/%164/"r4/R2914 ! ull pointer c*ecking is not performing9</p>	<p>%ro&lem Description' ! ull pointer c*ecking is not performing in t*e follo- ing A%ls</p> <p>19"r_3=_Recei eR6>%#u line' A13A EW"r_>%#uStatus%tr L "R_3=_! (T_R)C)I8)D0f line' A13@ EW"r_>S#u>engt**%tr L "R_3=_Y)R(0f 29"r_3=_C*eckT6>%#uStatus line' @2@3 EW"r_T6>%#uStatus%tr L "R_3=_! (T_TRA! S, ITT)D0f</p> <p>Actual &e*a iour' B*en #eference a ! U>> pointer t*ere&y raising a ! ull%ointer)6ception9 It - ill cause t*e controller to reset9</p> <p>)6pecte# &e*a iour' ! ull pointer c*ecking s*oul# &e performe# in t*e A%l "r_3=_Recei eR6>%#u: "r_3=_C*eckT6>%#uStatus</p>	<UG <UG	(%)! ISSU)

2@A=7	G%T	Bake up #isa&le# c*annels are gi ing D)T after G%T_, (D)_S>))% to G%T_, (D)_! (R, A> mo#e transition9	<p>Description</p> <p>55555555555</p> <p>B*ile Running AT" test case MG%T_"TC_122M its o&ser e# t*at une6pecte# D)T - it* Apil# L 1; 2 LD Ser ice l# of Gpt_Delnit A%l9)rrorl# L 1; < LD D)T co#e to report Timer is alrea#y running9</p> <p>is occurring - *en calling Gpt_DelnitEF: *ere all c*annels are e6pecte# &e in Mstoppe#M state9</p> <p>Be teste# as mentione# &elo- : in AT" configuration MAT"_cfg11M9</p> <p>Test Scenario</p> <p>55555555555</p> <p>19 Call Gpt_InitEPG%T_C(! "IG_11F to initialixe t*e #ri er - it* /G%T_C(! "IG_1149</p> <p>29 Call Gpt_)na&le! otificationEF for c*annel 19</p> <p>A9 Call Gpt_)na&le! otificationEF for c*annel 19</p> <p>29 Call Gpt_)na&le! otificationEF for c*annel A9</p> <p>39 Call Gpt_)na&leBakeupEF for c*annel 19</p> <p>09 Call Gpt_Dis&leBakeupEF for c*annel 19</p> <p>39 Call Gpt_Dis&leBakeupEF for c*annel A9</p> <p>09 Call Gpt_StartTimerEF for c*annel 19</p> <p>79 Call Gpt_StartTimerEF for c*annel 19</p> <p>@9 Call Gpt_StartTimerEF for c*annel A9</p>	<UG	(%)! ISSU)
2@213	G%T	Timer is starting automatically - *en call Gpt_)na&leBakeupEF in G%T_, (D)_S>))% mo#e9	<p>B*ile testing AT" test case MApp_Gpt_SampleM its o&ser e# t*at notification is occurring - *en call Gpt_)na&leBakeupEF in G%T_, (D)_S>))% mo#e &efore starting t*e timer9</p> <p>Be teste# as mentione# &elo- : in AT" configuration MAT"_cfg13M9</p> <p>19 Call Gpt_InitEPG%T_C(! "IG_11F to initialixe t*e #ri er - it* /G%T_C(! "IG_1149</p> <p>29 Call Gpt_)na&le! otificationEF for c*annel 19</p> <p>A9 Start timer for c*annel 19</p> <p>29 Bait for 211 Ems9</p> <p>39 call Gpt_GetTime)lapse#EF for c*annel 19</p> <p>09 C*eck - *et*er Time)lapse# D 1 : an# its o&taine# as e6pecte#9</p> <p>79 Bait for 11 Ems9</p> <p>@9 call Gpt_GetTimeRemainingEF for c*annel 19</p> <p>=9 C*eck - *et*er Time Remaining D 1 : an# its o&taine# as e6pecte#9</p> <p>119 Bait for 3 Secon#s9</p> <p>119 C*eck - *et*er notification o&taine# is D 1 : an# its o&taine# as e6pecte#9</p> <p>129 call Gpt_Dis&leBakeupEF for c*annel 19</p> <p>1A9 Set G%T mo#e to &G%T_, (D)_S>))%+&y calling MGpt_Set, o#eEG%T_, (D)_S>))%FM9</p> <p>129 Bait for 1 Secon#s9</p> <p>139 Clear T*e ! otification Count9</p> <p>109 Bait for 2 Secon#s9</p> <p>179 C*eck - *et*er notification o&taine# is L 1 : an# its o&taine# as e6pecte#9</p> <p>1@9 Set G%T mo#e to &G%T_, (D)_! (R, A>+&y calling MGpt_Set, o#eEG%T_, (D)_! (R, A>FM9</p> <p>1=9 Bait for 2 Secon#s9</p> <p>219 C*eck - *et*er notification o&taine# is L 1 : an# its o&taine# as e6pecte#9</p> <p>219 Set G%T mo#e to &G%T_, (D)_S>))%+&y calling MGpt_Set, o#eEG%T_, (D)_S>))%FM9</p> <p>229 call Gpt_)na&leBakeupEF for c*annel 19</p> <p>2A9 Set G%T mo#e to &G%T_, (D)_! (R, A>+&y calling MGpt_Set, o#eEG%T_, (D)_! (R, A>FM9</p>	<UG	(%)! ISSU)

23@30	ICU	(&solete co#e in lcu_?B_InitEf function9	<p>%ro&lem #escription'</p> <p>At line 732 in lcu_>>Dri er%c t*e follo-ing loop is present'</p> <pre>for E>ucCnt L ICU_, A;_TI,)R_C?A! !)>S_C(! "IGUR)D0>ucCnt C ICU_, A;_C?A! !)>0 >ucCnt^^F</pre> <p>T</p> <p>T*is MforM loop is use# for e6ternal interrupts only an# not for timer c*annels initialiXation9</p> <p>Insi#e t*e loop you *a e c*ecks for timer c*annels: - *ic* - ill al-ays fail: since t*e loop is only for c*annels configure# - it* e6ternal interrupts9</p> <p>It seems t*at e eryt*ing a&o e t*e Ms- itc*M at line @2A in t*is loop is o&solete co#e9</p> <p>)6pecte# &e*a ior'</p> <p>! 7A</p> <p>Actual &e*a ior'</p> <p>! 7A</p>	<UG	(%)! ISSU)
27022	ICU	<p>InterruptsEl, Rf are ena&le# All C*annel</p> <p>After calling lcu_Set, o#eEf Api form</p> <p>ICU_, (D)_S>))% to ICU_, (D)_! (R, A>9</p>	<p>%ro&lem Description'</p> <p>As per Autosar2919A ICU re\$uirement says t*at ICU1=2 ICU_, (D)_! (R, A>' ! ormal operation: all use# interrupts are ena&le# accor#ing to t*e notification re\$uests9 ICU_, (D)_S>))%'</p> <p>Re#uce#po-er mo#e9 In sleep mo#e only t*ose notifications are a aila&le - *ic* are configure# as - akeup capa&le9</p> <p>Current implementation is</p> <p>In lcu_Set, o#eElCU_, (D)_! (R, A>F Api calle# after lcu_Set, o#eElCU_, (D)_S>))%F Api9</p> <p>All interrupts are ena&le# - it* out consi#ering Current notification status9</p> <p>8er2911910 55 release lcu_>>Dri er%c</p> <p>1703' 7W)na&le Interrupt W7</p> <p>1700' R?@31_S8_, (D)_I, R_A! DE10: E>plmrIntpCntrlRegf:</p> <p>E>pC*annelConfig5Duslmr, ask8alueff0</p> <p>)6pecte# <e*a iour'</p> <p>All use# interrupts are ena&le# accor#ing to t*e notification re\$uests</p> <p>Actual <e*a iour'</p> <p>All interrupts are ena&le# - it* out consi#ering notification status9</p>	<UG	(%)! ISSU)
2@3@3	ICU	<p>Component User , anual 5 Unimplemente#</p> <p>A%ls an# ! ot supporte# features</p>	<p>%ro&lem Description'</p> <p>19T*e functionalities - *ic* are not supporte# &y t*e *ar# - are are present in t*e component user manual9lcu_C*eckBakeup: lcu_Disa&leBakeup an# lcu_)na&leBakeup s*outl# &e</p> <p>remo e# from user manual9</p> <p>29 If a feature is not supporte# please mention M! ot supporte#M in Ta&le 351</p> <p>)6pecte# <e*a iour'</p> <p>! A</p> <p>Actual <e*a iour'</p> <p>Bakeup functionalitieslcu_C*eckBakeup: lcu_Disa&leBakeup an# lcu_)na&leBakeupf - *ic* are not implemente# are mentione# in t*e user manual: - *ic* mislea#s t*e user9</p>	<UG	(%)! ISSU)

272=1	, CU	, cuResetReason coul# not &e accesse# <y)cu, ResetReason from , cu%u&lis*e#Information17, cuResetReas onConf1 container	%ro&lem Description' T*e contents of , cu%u&lis*e#Information17, cuResetReasonConf17, cuResetReason coul# not &e accesse# from)cu, ResetReason since t*e implementation met*o#ology in , cu%u&lis*e#Information #ont seem to &e accor#ing to - *at AUT (SAR *as prescri&e# ! ote' B*y is t*e %16 , cuResetReason implementation #ifferent from "16 ET*e , cuResetReason implemente# in #ifferen - ay for "16 an# %16 #e ices f)6pecte# <e*a ior' ! one Actual <e*a ior' ! one	<UG	(%)! ISSU)
2@101	, CU	Get8ersionInfoF A%l of eac* mo#ule s*all also return _instanceID` as one of t*e parameter in St#_8ersionInfoType	%ro&lem Description' T*is ticket is create# to track t*e pre5release re ie- #one on %16 , CU - ork pro#ucts as part of 82911912 release9 Re\$uirement' AR_%! 11A2_"R_1117 "in#ing' As per t*e re\$uirement Get8ersionInfoF A%l of eac* mo#ule s*all also return _instanceID` as one of t*e parameter in St#_8ersionInfoType pointe# &y t*e output parameter ersioninfo9 In t*e current implementation only mo#ulei#: an# en#ori# are returne#9 Instance i# is not returne#)6pecte# <e*a ior' Get8ersionInfoF A%l of eac* mo#ule s*all return mo#ulei#: en#ori# an# instanceID Actual <e*a ior' Get8ersionInfoF A%l of eac* mo#ule s*all return mo#ulei# an# en#ori#	<UG	(%)! ISSU)
2@171	, CU	Dummy rea# to register must &e performe# after - riting to register9	%ro&lem Description' T*is ticket is create# to track t*e pre5release re ie- #one on %16 , CU - ork pro#ucts as part of 82911912 release9 Re\$uirement' AR_%! 11A2_"R_110@ "in#ing' Sync*roniXing perip*erals register - rite operation &y #ummy rea#9 As per t*is re\$uirement: Dummy rea# to register must &e performe# after - riting to register9 T*e re\$uirement is not taken care in , cu source co#e9)6pecte# <e*a ior' ! A Actual <e*a ior' ! A	<UG	(%)! ISSU)

2@212	, CU	A%ls , cu_GetResetReasonEf an# , cu_GetResetRa-8alueEf s*all return t*e same result in case t*ey are calle# multiple times	<p>%ro&lem Description'</p> <p>T*is ticket is create# to track t*e pre5release re ie- #one on %16 , CU -ork pro#ucts as part of 82911912 release9</p> <p>Re\$uirement')AAR_%! 117=_ "R_11@0</p> <p>T*e A%ls , cu_GetResetReasonEf an# , cu_GetResetRa-8alueEf s*all return t*e same result in case t*ey are calle# multiple times after a reset or a po- er on e ent9</p> <p>"in#ing' A## test case to test t*e re\$uirement9</p> <p>)6pecte# &e*a iour'</p> <p>! A</p> <p>Actual &e*a iour'</p> <p>! A</p>	<UG	(%)! ISSU)
2@A@2	, CU	, cuResetReason a##e# in , cu sc*ema cannot &e reference# &y)cu,	<p>In , cu Sc*ema se eral reset reason -as a##e# in , cu%u&lis*e#Information container9</p> <p>Accor#ing -it*)CU, 12@_Conf , cu Reset Reason s*oul# &e in , cuResetReasonConf container so t*is can &e reference# &y)cu, mo#ule 9</p>	<UG	(%)! ISSU)
2@221	, CU	In #efinition file , an#atory parameters >o-er an# Upper multiplicity alues are not taken care properly	<p>%ro&lem Description'</p> <p>In #efinition %ar6ml file , an#atory parameters >o-er an# Upper multiplicity alue s*oul# &e one9</p> <p>parameters are , cu>oopCount an# , cu%&usBaitCount9</p> <p>e6ample'</p> <p>In , cu_%<Types9* , CU_%<USBAITC(U! T is #efine -it* , CU_%<USBAITC(U! T_8A>U) an# is use# in , cu%c file</p> <p>If , cu%&usBaitCount alue is not set: In , cu_Cfg9* for , CU_%<USBAITC(U! T_8A>U) - ill not &e generate# any alue'</p> <p>In %<cfg9* file</p> <p>7W %&us Count 8alue for t*e , CU_%<USBAITC(U! T W7</p> <p>I#efine , CU_%<USBAITC(U! T_8A>U)</p> <p>)6pecte# <e*a ior'</p> <p>Cb55 %ARA,)T)R D)"! ITI(! ' , cu%&usBaitCount 55D</p> <p>C)CUC5! T)G)R5%ARA, 5D)" UUIDLM)CUS'#&eaf&a15&faf52ca15@37252A3fAc=&=&A3MD</p> <p>CS?(RT5! A,)D, cu%&usBaitCountC7S?(RT5! A,)D</p> <p>CD)SCD</p> <p>C>52 >LM)! MDT*e parameter represents t*e %<us access -ait time9T*e loop can &e minimum 1 to ma6imum 033A3C7>52D</p> <p>C7D)SCD</p> <p>C>(B)R5, U>T1%>ICITHD1C7>(B)R5, U>T1%>ICITHD</p> <p>CU%%)R5, U>T1%>ICITHD1C7U%%)R5, U>T1%>ICITHD</p> <p>Actual <e*a ior'</p> <p>Cb55 %ARA,)T)R D)"! ITI(! ' , cu%&usBaitCount 55D</p> <p>C)CUC5! T)G)R5%ARA, 5D)" UUIDLM)CUS'#&eaf&a15&faf52ca15@37252A3fAc=&=&A3MD</p> <p>CS?(RT5! A,)D, cu%&usBaitCountC7S?(RT5! A,)D</p> <p>CD)SCD</p>	<UG	(%)! ISSU)
2@222	, CU	Reset reson *an#ling #epen#ing on %("9%(" &it	<p>B*at is t*e #ifference &et- een M, cuResetReasonConf%o-er(nResetM an# M, cuResetReasonConf%o-er(n"lagResetM\</p> <p>T*e implementation seems to &e -rong as %("9%(" &it seems to &e set in &ot* cases mentione#: - *ic* means only , cuResetReasonConf%o-er(n"lagReset is reporte# an# , cuResetReasonConf%o-er(n"lagReset is ne er reporte#9</p>	<UG	(%)! ISSU)

2027A	, CU	App_, CU_De ice_Sample9* B*ic* is not as per AR_%! 11A2_"R_11A=9	<p>%ro&lem Description'</p> <p>App_, CU_De ice_Sample9* B*ic* is not as per AR_%! 11A2_"R_11A=9</p> <p>)6pecte# <e*a ior'</p> <p>! A</p> <p>Actual <e*a ior'</p> <p>! A</p>	<UG	(%)! ISSU)
231A2	%ort	B*en c*anging port pin to a DI(mo#e: *an#ling of %SRnE%nF is #ifferen&y A%!9	<p>%ro&lem Description'</p> <p>T*e port pin can &e c*ange# to a DI(mo#e at A%! %ort_Set%in, o#e: %ort_SetToDio, o#e an# %ort_Set%inDefault, o#e%Among t*ese t*e %ort_SetToDio, o#e #oesn# set to %SR register%Is t*is - *at De elopment Team inten#e#\</p> <p>DI(output le el c*ange s*oul# &e performe# in DI(Dri er an# t*e user can also #eci#e at t*e timing of c*ange in t*e DI(output le el%Im t*inking t*is specification is simplest an# is - it*out mistakes9</p> <p>Coul# you tell me - *y it# suc* specification\</p> <p>)6pecte# <e*a ior '</p> <p>It is necessary to unify t*ese specifications</p> <p>Actual <e*a ior '</p> <p>t*e %ort_SetToDio, o#e #oesn# set to %SR register9</p>	<UG	(%)! ISSU)
20207	%ort	B*en %ort_Set%inDirectionE c*ange #irection from o7p to o7p Erefes*ingF for RTAG pins: t*e o7p le el - ill set to #efault state9	<p>%ort%inDirectionC*angea&le L True</p> <p>%ort%in, o#eC*angea&le L True</p> <p>%ort%in>e el8alue L %(RT_%! _>)8)>_>(B</p> <p>%ort%inInitial, o#e L DI(_SU%_%"C_, CSR</p> <p>%ort%inDirection L C&D%(RT_%! _ (UTC7&D</p> <p>Test case'</p> <p>%ort_InitE%ortConfigSet1F0</p> <p>- *ileE1F</p> <p>T</p> <p>7W T*is A%! - ill initilixe all t*e registers to t*e initial alues W7</p> <p>%ort_InitE%ortConfigSet1F0</p> <p>7W Set %ort %in le el of for R%1_0 to ?ig*9 W7</p> <p>R%SR1 L 16""""""11210</p> <p>7W Refres* t*e pin9 W7</p> <p>%ort_Set%inDirection E%ort_%ortGroupRtag11_%ort%in01: C&D%(RT_%! _ (UTC7&Df0</p> <p>U</p> <p>)6pecte# result'</p> <p>R%1_0 remains ?ig*9</p> <p>Actual result'</p> <p>R%1_0 sets to >o-9</p>	<UG	(%)! ISSU)

20@32	%ort	>ocal aria&les may remain not initialixe# in %ort_SetToDio(rAlt, o#e A%l9	<p>%ro&lem #escription'</p> <p>If - e *a e a configuration - it* t*e follo - ing generate# co#e</p> <p>7W A aila&ility of numeric port groups W7</p> <p>I#efine %(RT_! U, _%(RT_GR(U%S_A8Al>A<>) STD_ (""</p> <p>7W A aila&ility of alp*a&etic port groups W7</p> <p>I#efine %(RT_A>%?A_%(RT_GR(U%S_A8Al>A<>) STD_ (""</p> <p>7W A aila&ility of ktag port groups W7</p> <p>I#efine %(RT_RTAG_%(RT_GR(U%S_A8Al>A<>) STD_ (""</p> <p>t*en in %ort_SetToDio(rAlt, o#eEF A%l t*e local aria&les >p"uncCtrlReg an# >ul<aseA##ress - ill &e use# - it*out &eing initialixe#9</p> <p>)6pecte# result'</p> <p>! 7A</p> <p>Actual result'</p> <p>! 7A</p>	<UG	(%)! ISSU)
271A=	%ort	%ort Group 2 %in 0 , U; appears to &e mis5 la&ele# in t*e %arameter #efitnition file9	<p>%ro&lem #escription'</p> <p>%ort Group 2 %in 0 , U; appears to &e mis5la&ele# in t*e %arameter #efinition file9 It is la&ele# as TAUD212_A>T0_ (UT: - *ile accor#ing to t*e ?B user manual M2929197 %ort 2 E%2FM it is relate# to TAUD2(A9</p> <p>)6pecte# &e*a ior'</p> <p>%ort Group 2 %in 0 , U; must &e la&ele# as TAUD2(A_A>T0_ (UT9</p> <p>Actual &e*a ior'</p> <p>%ort Group 2 %in 0 , U; is la&ele# as TAUD2(2_A>T0_ (UT9</p>	<UG	(%)! ISSU)
2717=	%ort	%ort Generator t*ro - ing un - ante# error	<p>%ro&lem #escription'</p> <p>If %ortIpControl is ena&le# for e9g9 CSI pins as recommen#e# in #escription of %ortIpControl: t*en error 12211@ is raise#9</p> <p>%in names in #escription of %ortIpControl #o not matc* to a aila&le options in %ort%inInitial, o#e9</p> <p>T*is issue is ali# for 711111 #e ice: &ut not for 7111A39</p> <p>)6pecte# &e*a iour'</p> <p>! o error s*oul# occur9</p> <p>Actual &e*a iour'</p> <p>)RR12211@)rror occurs t*at is in contra#iction to #escription9</p>	<UG	(%)! ISSU)
27070	%ort	"ail to initialixe t*e %"CA) registers correctly	<p>%ro&lem Description'</p> <p>T*e register initialiXation se\$uence in %ort_InitConfigEF api is not as mentione# in r11u*12A0ek1171_r*@31p169p#fE 1971F at page 122 Section 29A92909</p> <p>)6pecte# <e*a ior '</p> <p>%"CA) register along - it* %"C an# %"C) s*oul# &e initialixe# after initialiXing</p> <p>%!! 8 register9</p> <p>Actual <e*a ior '</p> <p>T*e function control registersE%"C:%"C):%"CA)F are initialixe# &efore %!! 8 register initialiXation</p>	<UG	(%)! ISSU)

2@A=1	%ort	%!! 8n register is not setting properly in A%! %ort_Set%inDirectionEF	%ro&lem #escription' 8alue up#ating to %!! 8n C %ort (utput >e el In ersion Register – rite protection is not implemente# as in #e ice User , anual9)6pecte# &e*a ior' ! ee#s to follo– t*e – rite protection se\$uence mentione# in #e ice User , anual9 Actual &e*a ior' Register – rite protection is not implemente# properly9	<UG	(%)! ISSU)
2@227	%ort	%, SR register access is not correct	%ro&lem #escription' %, SR register is accessing – it*out c*ecking – *et*er %, SR register is present for t*at particular %ort group9)6pecte# &e*a ior' <efore accessing %, SR register c*eck – *et*er %, SR register is e6ist is re\$uire#9 7WC*eck for %, SR register a aila&ility W7 if E%(RT_R)G_! (TABAl>A<>) bL >pSet%in, o#eGroupStruct5Duc%, SRRegln#e6f T 999 U Actual &e*a ior' T*is c*eck is not present result in illegal memory access9	<UG	(%)! ISSU)
2@3A=	%ort	%re compiler , acro is surroun#e# co#e at – rong place in %ort_"ilterConfigEF	%ro&lem #escription' %re compiler , acro M%(RT_D! "A_R)G_C(! "IGM is surroun#e# co#e at – rong place in %ort_"ilterConfigEF9 I if EE%(RT_D! "A_R)G_C(! "IG LL STD_(! F W E%(RT_"C>A_R)G_C(! "IG LL STD_(! F F I #efine %(RT_START_S)C_%Ri8AT)_C(D) I inclu#e M, em, ap9*M STATIC "U! CE oi#: %(RT_%Ri8AT)_C(D)F %ort_"ilterConfigE oi#F T 7W %ointer to #igital filter D! "A register #ata structure W7 I if E%(RT_D! "A_R)G_C(! "IG LL STD_(! F %2C(! STE olatile %ort_D! "ARegs: AUT(, ATIC: %(RT_C(! "IG_DATAF >pD! "AReg0 7W %ointer to)#ge control)DC register #ata structure W7 I if E%(RT_)DG)_D)T)CT_C(! TR(> LL STD_(! F %2C(! STE olatile %ort_)DCRegs: AUT(, ATIC: %(RT_C(! "IG_DATAF >p)DCReg0 I en#if 7W)n# of %(RT_)DG)_D)T)CT_C(! TR(> LL STD_(! W7 55555co#e55555 I en#if 7W)n# of %(RT_D! "A_R)G_C(! "IG LL STD_(! W7 55555co#e55555 %ort_"ilterConfigEF A%l is ena&le# &y %(RT_D! "A_R)G_C(! "IG is STD_(! or %(RT_"C>A_R)G_C(! "IG is STD_(! : In si#e aria&le #eclaration is #one for %(RT_D! "A_R)G_C(! "IG is STD_(! : B*en %(RT_D! "A_R)G_C(! "IG is STD_(! "" an# %(RT_"C>A_R)G_C(! "IG is STD_(! it – ill corrupte#9)6pecte# &e*a ior'	<UG	(%)! ISSU)

23722	%B,	Det %B, _)%_ARA, _C?A! !)> is not reporting for %-m_SetTriggerDelayEf9	<p>ro&lem #escription'</p> <p>"or t*e current %B, #ri er implementation -e face t*e pro&lem t*at it is not reporting Det %B, _)%_ARA, _C?A! !)> for %-m_SetTriggerDelayEf - *en configure# for %B, TAU c*annel9</p> <p>6pecte# &e*a ior'</p> <p>Det %B, _)%_ARA, _C?A! !)> s*oul# report for %-m_SetTriggerDelayEf - *en configure# for %B, TAU c*annel9</p> <p>Actual &e*a ior'</p> <p>D)T is not occurring9</p>	<UG	(%)! ISSU)
20@72	%B,	%-m_SelectC*annelClk is starting t*e %B, #iag c*annels configure# for sync start	<p>ro&lem Description'</p> <p>If you configure 2 c*annels 1 on TAU an# one %B, #iag in sync start mo#e9 If after %-m_Sync*ronousInitEf A%l: %-m_SelectC*annelClkEf is calle#: t*e p-m #iag c*annel - ill start e en &efore calling %-m_Sync*ronousStartEf9</p> <p>6pecte# &e*a ior'</p> <p>C*annels marke# as sync*ronous s*all start only after %-m_Sync*ronousStartEf A%l is calle#9</p> <p>Current &e*a ior'</p> <p>C*eck t*e pro&lem #escription</p>	<UG	(%)! ISSU)
2@2=2	%B,	/%164/%B, 4%B, notification is not *an#le# properly	<p>ro&lem Description'</p> <p>19%B, notification null pointer c*ecking is not performing on %-m_?B_Call&ack ISR</p> <p>29! otification - ill sen# for - rong %B, c*annels7c*annels - *ic* are not configure#9</p> <p>Actual &e*a iour'</p> <p>B it* in %-m_?B_Call&ack ISR: c*annel i# is incrementing - it* in a for loop9! otification c*ecking an# sen#ing is #oing outsi#e t*is for loop9 After t*e e6ecution of t*at for loop c*annel i# - ill &e: e6act c*annel i# ^ 1 ^ num&er of sla e c*annels9 So t*e notification - ill sen# for - rong c*annels or try to sen# notification for t*e c*annels - *ic* are not configure#9</p> <p>6ample' -e *a e configure# 7 c*annels out of - *ic* A are sla e c*annels9</p> <p>an# t*e interrupt is coming for 2t* master c*annel9</p> <p>So at t*e en# of t*e #for+loop: c*annel i# - ill &e @9 T*is - ill cause out of array access an# if t*e alue of t*at memory location is one: it - ill try to sen# notification: - *ic* is not configure#9</p> <p>T*is - ill cause t*e controller to reset9</p> <p>6pecte# &e*a iour'</p> <p>19%B, notification null pointer c*ecking s*oul# &e performe# in %-m_?B_Call&ack ISR</p> <p>29%B, notification c*ecking s*oul# &e - it* proper c*annel i#9</p>	<UG	(%)! ISSU)
2300A	RamTst	/%164/RA, TST4/R2914 T*e test result is not RA, TST_R)SU>T_U! D) "I!)D: if a , arc* Test on t*is &lock is running9	RamTst_GetTestResult%er<lockEf #oes not return RA, TST_R)SU>T_U! D) "I!)D: - *en , arc* test on t*e specific &lock is running9	<UG	(%)! ISSU)
23002	RamTst	/%164/RA, TST4/R2914 RamTst_"ill%attern not getting up#ate# in t*e RA, location - *en RamTstTest%olicy is RA, T)ST_D)STRUCTI8)9	B *en t*e configuration parameter RamTstTest%olicy for a &lock is set to RA, T)ST_D)STRUCTI8): t*e test algorit*m #oes not fill t*e teste# cells after t*e test - it* t*e &it pattern #efine# for t*is &lock &y parameter RamTst_"ill%attern e6cept for t*e test algorit*m RA, TST_A<RA?A, _T)ST_A%%9	<UG	(%)! ISSU)

202@2	RamTst	Dem e ent parameter name not generate# correctly	<p>%ro&lem Description'</p> <p>If t*e s*ort name of Dem) ent%arameter in file Dem_RamTst0ar6ml is not appen#e# - it* any num&er t*en t*e Dem e ent parameter name is not getting generate# correctly9</p> <p>)6pecte# <e*a iour'</p> <p>D), e ent parameters s*oul# generate# correctly as follo-s</p> <p>I#efine RA, TST_)_RA, _"AI>UR) J</p> <p>DemConf_Dem) ent%arameter_Dem) ent%arameter</p> <p>Actual <e*a iour'</p> <p>D), e ent parameters are generate# as follo-s</p> <p>I#efine RA, TST_)_RA, _"AI>UR) J</p> <p>DemConf_Dem) ent%arameter_</p> <p>T*is results in compilation issues as Mt*e i#entifier MDemConf_Dem) ent%arameter_M is un#efine#M</p>	<UG	(%)! ISSU)
2@012	RamTst	Issues in)U, 9	<p>%ro&lem Description'</p> <p>T*is ticket is to report t*e #effects foun# in)U, 9</p> <p>19In Section @ MSoft--are Generation ToolM an# MDri er Generation ToolM are use# in parallel: - *ic* is mislea#ing9</p> <p>/Dri er Generation Tool4 s*oul# &e use# *ere9</p> <p>29In Re ision ?istory SI9 ! o9 2 MAs part of %16 82911912 acti ity follo-ing c*anges are ma#e'M s*oul# &e remo e#9</p> <p>A9In Section 293 +; +is not marke# for user mo#e for RamTst A%ls: e en - it* kno-n limitations liste# in Ta&le 2519 T*is is not in line - it* ot*er , CA> mo#ules9 User mo#e is supporte# &y</p> <p>RamTst_Run"ullTest: RamTst_Run%artialTest: RamTst_, ain"unction: &ut - it* precon#ition t*at t*e critical section s*oul# &e #isa&le#9</p> <p>)6pecte# &e*a ior</p> <p>! 7A</p> <p>Actual &e*a ior</p> <p>! 7A</p>	<UG	(%)! ISSU)
2@013	RamTst	RamTst_Ram%c an# RamTst_Ram9* files are missing9	<p>%ro&lem Description '</p> <p>Unlike ot*er , CA> mo#ules t*ere are no #e#icate# files: ie9 RamTst_Ram%c an# RamTst_Ram9*: to a##ress glo&al aria&les9</p> <p>In ot*er , CA> mo#ules C, S! D_Ram%c an# C, S! _Ram9*D use# to a##ress glo&al aria&les9</p> <p>Actual <e*a ior '</p> <p>! o #e#icate# file is e6ist to a##ress glo&al aria&les9</p> <p>)6pecte# <e*a ior '</p> <p>To maintain consistent file structures across all t*e , CA> mo#ules t*ese files s*oul# &e a##e# to a##ress glo&al aria&les9</p>	<UG	(%)! ISSU)

2017	RamTst	Autosar requirement RamTst1AA is not implemented properly	<p>Problem Description</p> <p>As per AUTOSAR requirement RamTst1AA</p> <p>If the DUT is enabled and the execution status of the RA, Test is not RA, TST_); CUTI(! _RU! !! G or RA, TST_); CUTI(! _SUS%)! D): the function RamTst_Stop shall report the error value RA, TST_)_STATUS_"AI>UR) to the DUT: and then immediately return</p> <p>Actual case for</p> <p>In the current implementation execution status is checked against ST(%)D as follows -</p> <p>else if ERA, TST_); CUTI(! _ST(%)D LL RamTst_)_executionStatusF in RamTst_Stop A%I</p> <p>Expected case for</p> <p>! 7A</p>	<UG	(%)! ISSU)
2018	RamTst	Autosar requirement RamTst11A is not implemented properly	<p>Problem Description</p> <p>As per the requirement RamTst9* shall include St#_Types* directly</p> <p>In current implementation St#_Types* is included via RamTst_Types*9</p> <p>Actual case for</p> <p>St#_Types* is included RamTst_Types* and RamTst_Types* is included RamTst9* - which is not correct as per Autosar requirement RamTst11A9</p> <p>Expected case for</p> <p>RamTst9* shall include St#_Types* directly</p>	<UG	(%)! ISSU)
2311=	SPI	SPI-related notification functions are not generated correctly: - when two or more RSCs share the same RSC notification function	<p>Problem Description</p> <p>SPI-related notification functions are not generated correctly: - when two or more RSCs share the same RSC notification function</p> <p>Some RSC notification functions are ! U>> after the generation: in spite they are not configured as ! U>>9</p> <p>There is no information or warning in the generator's manual: that the configuration - out# not &e allowed - e#9</p> <p>Expected case for</p> <p>If the following SPI-related notifications are in one configuration'</p> <p>Ts-Spi_AsyncRSC)n#! otif</p> <p>Ts-Spi_AsyncRSC)n#! otif</p> <p>! U>></p> <p>! U>></p> <p>Ts-Spi_%rioC*eckRSC1)n#! otif</p> <p>Ts-Spi_%rioC*eckRSC2)n#! otif</p> <p>Ts-Spi_%rioC*eckRSCA)n#! otif</p> <p>Ts-Spi_%rioC*eckRSC2)n#! otif</p> <p>Ts-Spi_%rioC*eckRSC3)n#! otif</p> <p>! U>></p> <p>! U>></p> <p>the same should &e expected to &e generated in Spi_<%cfg%</p> <p>Actual case for</p> <p>Instead in Spi_<%cfg% - e share the following'</p> <p>! U>>_%TR</p> <p>Ts-Spi_AsyncRSC)n#! otif</p>	<UG	(%)! ISSU)

20A@=	S%l	S*ort name: "ile name an# %at* generate# for error i#)RR1@A13@ is incorrect	<p>%ro&lem Description'</p> <p>)RR1@A13@ message is generating as follo – s</p> <p>T*e reference pat* C7AUT (SAR7)cucDefs7Dem17DemConfigSet17Dem) ent%arameteD pro i#e# for t*e parameter :\$%l_)_?ARDBAR)_RR (R+in t*e container</p> <p>\$SpiDem) ent%arameterRefs: *a ing s*ortname C?AS?E16A1@2=acfS*ort! ameU is incorrect9</p> <p>"ile ! ame' ?AS?E16A1@2=acf T"ile! ameU</p> <p>%at*' ?AS?E16A1@2=acfS*ort! ameU</p> <p>)6pecte# <e*a iour'</p> <p>Correct S*ort name: "ile name an# %at* s*soul# &e generate# for error i#)RR1@A13@9</p> <p>Actual <e*a iour'</p> <p>S*ort name: "ile name an# %at* generate# for error i#)RR1@A13@ are – rong9</p>	<UG	(%)! ISSU)
20221	S%l)6ecution stuck in Spi_?BTransmitSync& function	<p>%ro&lem Description'</p> <p>B*en a se\$uence is transmitte# sync*ronously: t*e e6ecution *angs in Spi_?BTransmitSync&9</p> <p>)6pecte# <e*a iour'</p> <p>Se\$uence s*soul# &e transmitte# – it*out *ang9</p> <p>Actual <e*a iour'</p> <p>Calling Spi_SyncTransmit A%l results *anging in Spi_?BTransmitSync& A%l 9</p>	<UG	(%)! ISSU)
20702	S%l	T*e S%l #ri er #oes not c*ange its status in case of #ata consistency error occurs #uring sync transmission9	<p>%ro&lem #escription'</p> <p>In case of #ata consistency error flag ECSI?nDC)F is set #uring S%l sync transmission: t*e ongoing se\$uence is a&orte#9</p> <p>T*e pro&lem is t*at after t*e ongoing se\$uence – as a&orte#: t*e glo&al aria&le Spi_Gus? – Status is not c*ange# &y t*e Spi_SyncTransmit A%l9 T*is &locks all t*e ne6t S%l communication9</p> <p>)6pecte# result'</p> <p>In case of consistency error #etecion #uring S%l Sync transmission: t*e ongoing se\$uence must &e cancelle#9</p> <p>Actual result'</p> <p>After consistency error #etecion #uring S%l Sync transmission: – *ole furt*er communication is &locke#9</p> <p>A – orkaroun# is not to ena&le t*e CSI?nCT>1%CSI?nDCS &it: until t*is issue is not fi6e#9</p>	<UG	(%)! ISSU)
270@@	S%l	Illegal , emory access in Spi_Dri er% in A%l Spi_TransmitISREF	<p>%ro&lem #escription'</p> <p>If t*e if con#ition'</p> <p>if ES%l_"l" (_<U""")R_"U>> bL Spi_Guc?B"ifo<ufferSts/S%l_"l" (_R; _! D); 4ff fails:</p> <p>t*e pointer >p%<C*annelConfig – ill not &e initialise# Esince it is – ritten in t*e if con#ition at >ine 301@f</p> <p>T*is – oul# lea# to an illegal memory access Eat >ine'3717f – *ere >p%<C*annelConfig is use#9</p> <p>)6pecte# <e*a ior'</p> <p>T*e aria&le >p%<C*annelConfig s*all &e initialiXe# &efore use#</p> <p>Actual &e*a ior'</p> <p>If t*e if con#ition EEIF ES%l_"l" (_<U""")R_"U>> bL Spi_Guc?B"ifo<ufferSts/S%l_"l" (_R; _! D); 4fff fails: >p%<C*annelConfig is not initialiXe#9</p>	<UG	(%)! ISSU)

27717	S%l	Illegal , emory access in Spi_Dri er%c	<p>%ro&lem Description' In Spi_Dri er%c: A%l Spi_TransmitISR: t*e local aria&le >pRo&Config is initialiXe# in t*e part of co#e as &elo-9</p> <pre> if ES%l_"I"(_<U"")R_U! I! IT LL Spi_Guc?B"ifo<ufferSts/S%l_"I"(_R;_! D);4F T 9999999999990 9999999999990 >pRo&Config L Spi_Gp"irstRo& ^>##Ro&In#e60 U </pre> <p>If t*e con#ition S%l_"I"(_<U"")R_U! I! IT is not e\$ual to Spi_Guc?B"ifo<ufferSts/S%l_"I"(_R;_! D);4F: t*e aria&le >pRo&Config - ill not &e initialiXe#9 T*is coul# lea# to illegal memory access since t*e aria&le is also use# else- *ere9)g' In t*e #o - *ile loop &elo- t*e if loop mentione# in t*e #escription:</p> <pre> I if ES%l_D, A_, (D)_! A<>) LL STD_(! F 7W , ISRA 8iolation' START , sgE2'2=02F51@ W7 if EES%l_I! T)RRU%T_, (D) LL Spi_G##Async, o#eF PP ES%l_I! 8A>ID_D, AU! IT LL >pRo&Config5DucR6DmaDe icelIn#e6ff 7W)! D , sgE2'2=02F51@ W7 I en#if </pre> <p>)6pecte# <e*a ior' ! one</p> <p>Actual <e*a ior' Illegal , emory access coul# *appen if t*e if con#ition mentione# in t*e pro&lem #escription fails9</p>	<UG	(%)! ISSU)
27=7@	S%l	Spi_SyncTransmit A%l is not - orking properly9	<p>%ro&lem Description'</p> <p>- *en calling Spi_SyncTransmitE an e6ception is occurring from pri ate A%l Spi_?BTransmitSyncRo&FF9 (n analysis - e foun# t*at t*is is &ecause of improper *an#ling of a - *ile loop e6it criteria: resulting in illegal memory access9</p> <p>)6pecte# &e*a ior'</p> <p>Spi_SyncTransmitE e6ecute - it* out any e6ception9</p> <p>Actual &e*a ior'</p> <p>An e6ception is occurring - *ile e6ecution of Spi_SyncTransmitE A%l9</p>	<UG	(%)! ISSU)
2@2AA	S%l	Improper pre5compiler s- itc* for t*e Spi_, ainfunction_?an#ling function #efinition	<p>%ro&lem Description'</p> <p>Spi_, ainfunction_?an#ling function s*outl# &e in oke# only - *en polling mec*anism is selecte# &y Spi_SetAsync, o#e A%l9 T*is mo#e can &e set only - *en t*e S%l_>)8)>_D)>I8)R)D is t-o9&ut t*e pre compiler s- itc* for t*e function #efinition is as follo-s</p> <pre> I if EES%l_>)8)>_D)>I8)R)D LL S%l_(!)F VV ES%l_>)8)>_D)>I8)R)D LL S%l_TB (ff J PP ES%l_?BU! IT_ASH! C?R(! (US LL STD_(! ff </pre> <pre> I #efine S%l_START_S)C_%U<>IC_C(D) I inclu#e M, em, ap9*M </pre> <pre> "U! CE oi#: S%l_%U<>IC_C(D)F Spi_, ain"unction_?an#ling E oi#F T 999 </pre> <p>)6pecte# <e*a iour'</p> <p>Spi_, ainfunction_?an#ling function s*all &e a aila&le in >e el 2 only9</p> <p>Actual <e*a iour'</p> <p>Spi_, ainfunction_?an#ling function is a aila&le in >e el 1 an# 2 also9</p>	<UG	(%)! ISSU)
2@22=	S%l	Spi"ifoTime(ut parameter is man#atory	<p>%ro&lem #escription'</p> <p>Spi"ifoTime(ut parameter is ma#e man#atory &ut it is only ali# for CSI?</p> <p>)6pecte# &e*a iour'</p> <p>Spi"ifoTime(ut parameter s*outl# &e optional {multiplicity 1991F an# a##itionally t*ere s*outl# &e a generator error c*eck in case it is not configure# for CSI? ?B units in "I" (mo#e9</p>	<UG	(%)! ISSU)

2@231	S%l	T*e information pro i#e# a&out user mo#e an# super isor mo#e is not correct in t*e user manual	<p>%ro&lem Description'</p> <p>In t*e user manual Ta&le 253: it in#icates t*at Spi_, ain"unction_?an#lingF re\$uires Super isor mo#e access – *en Interrupt mo#e is acti e ESI9 ! o9 12F: t*oug*</p> <p>Spi_, ain"unction_?an#lingF is not necessary in interrupt mo#e9</p> <p>Also: Spi_AsyncTransmitF: SI9 ! o9 2 in Ta&le 253: is missing any mark in t*e Interrupt , o#e7user mo#e column99</p> <p>)6pecte# <e*a iour'</p> <p>Spi_, ain"unction_?an#ling s*all &e remo e# in interrupt mo#e an# Spi_AsyncTransmitF s*all &e correcte# for applica&le mo#es9</p> <p>Actual <e*a iour'</p> <p>Spi_, ain"unction_?an#ling is marke# for &ot* interrupt an# %olling mo#es9 an# Spi_AsyncTransmitF is missing any mark in t*e Interrupt , o#e7user mo#e column9</p>	<UG	(%)! ISSU)
2@A02	S%l)rror)RR1@A121 is generate# – *en Spi)na&leCs is configure# as false	<p>%ro&lem Description'</p> <p>B*en t*e %arameter Spi)na&leCs is configure# as false Spi%ort%inSelect s*oul# not &e configure#9 <ut – *en Spi%ort%inSelect is not configure# tool is generating error)RR1@A1215 t*e parameter <Spi%ort%inSelect+ alue in t*e container <SpiRo&C6D+ s*oul# &e configure# as CS>CnD since <CSI?C6D+ is configure#9</p> <p>)6pecte# <e*a iour'</p> <p>Tool s*oul# not generate error9</p> <p>Actual <e*a iour'</p> <p>)RR1@A121 is generate#9</p>	<UG	(%)! ISSU)
2@230	S%l	8aria&les are uninitialiXe# – *en t*e certain con#ition #oes not meet9	<p>%ro&lem #escription'</p> <p>Some of t*e 8aria&les are uninitialiXe# – *en t*e follo–ing con#itions are not met9</p> <p>– *en t*e S%l_DIR)CT_ACC)SS_, (D) is STD_('"</p> <p>)6pecte# &e*a ior'</p> <p><efore using t*e aria&les: 8aria&les s*oul# &e initialiXe#9</p> <p>Actual &e*a ior'</p> <p><efore using t*e aria&les: 8aria&les are not initialiXe# – *en S%l_DIR)CT_ACC)SS_, (D) is STD_('"</p>	<UG	(%)! ISSU)
2@070	S%l	Calling of Spi_, ain"unction_?an#ling possi&le in interrupt mo#e	<p>Description'</p> <p>If interrupt mo#e is selecte# ESpi_SetAsync, o#eES%l!! T)RRU%T_, (D)FF</p> <p>a call to Spi_, ain"unction_?an#lingF is possi&le9 "unctions Spi_TransmitISR an# Spi_Recei elSR are calle# t*ere – it*out furt*er c*ecks9 T*is can cause corrupte# #ata transmission9</p> <p>Actual <e*a ior'</p> <p>! o error &ut corrupte# #ata9</p> <p>)6pecte# <e*a ior'</p> <p>In interrupt mo#e a call to Spi_, ain"unction_?an#ling s*all &e reflecte#: e9g9 &y D)T9</p>	<UG	(%)! ISSU)

2@1==	BDG	<p>B#g_Set, o#e function reports D), error if BDGI"_(""_, (D) is selecte#</p>	<p>%ro&lem #escription' As per AUT (SAR specification /BDG1014: B#g_Set, o#e function supports BDGI"_(""_, (D)9 An# t*e user sets B#gDisa&leAllo-e# parameter +true+ in Configuration tool9 ?o-e er: in co#e B#g_3=_Dri erA_Set, o#e function reports a Dem)rror in case BDGI"_(""_, (D) is selecte#9</p> <p>B#g_3=_Dri erA_Set, o#e function in B#g_3=_Dri erA%' if E, o#e LL BDGI"_(""_, (D)F T 7W Report)rror to D), W7 Dem_Report)rrorStatusE(BDG_3=_DRI8)RA_)_DISA<>)_R)R)CT)D: D), _)8)! T_STATUS_"Al>)DF0</p> <p>BDG #ri er #oes not allo- B#g_Set, o#e function to translate t*e state into ("" &y M292 BDG State DiagramM in BDG Dri er Component)m&e##e# User#s , anual Re 91911 ! o 211A9</p> <p>Customer nee# to kno- t*e &ackgroun# for t*is9</p> <p>)6pecte# &e*a iour' B#g_Set, o#e function s*all report D), error only if re\$uire# mo#e is +BDGI"_(""_, (D)+an# +B#gDisa&leAllo-e#+ is false9</p> <p>Actual &e*a iour' B#g_Set, o#e function is reporting D), error only if re\$uire# mo#e is +BDGI"_(""_, (D)+an# +B#gDisa&leAllo-e#+ is true9</p>	<UG	(%)! ISSU)
-------	-----	--	---	-----	---------------