

Exp No: 4

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Code Conversion

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Aim:

To write assembly language programs to perform the following code conversions:

1. BCD to Hexadecimal Code Conversion
2. Hexadecimal to BCD Code Conversion

Programs:

(i) BCD to Hexadecimal Code Conversion

Algorithm:

- Move the data segment to DS register through AX register.
- Move the extra segment to ES register through AX register.
- Clear AH register.
- Load the BCD value to AL.
- Load 10H to BL.
- Divide the value at AL by BL.
- Load the LSB at AH to DL.
- Multiple AL by 10 and add it to value at DL.
- Move the result at AL to HEX. 16.

Program	Comments
ASSUME <i>CS:CODE,DS:DATA</i>	Using assume directive to declare data,extra and code segment
DATA SEGMENT BCD DB 12H HEX DB ? DATA ENDS	Declaring and initialising variables in data segment
CODE SEGMENT <i>ORG 0100h</i>	Set location for code segment at 0100h

STARTt:	
<i>MOV AX,data</i>	Move the content of Data segment to AX register
<i>MOV DS,AX</i>	Move the content of AX register to DS register
<i>MOV AL,BCD</i>	Move the content of BCD to AL register
<i>MOV AH,0H</i>	Move the 0H to AH register..
<i>MOV BL,10H</i>	Assign the offset of source to SI register.
<i>DIV BL</i>	Divide AX by BL. (Quotient in AL, Remainder in AH)
<i>MOV BL,0AH</i>	Transfer 10 to BL.
<i>MOV DL,AH</i>	Copy the contents of AH to DL.
<i>MOV AH,0H</i>	Clear AH register.
<i>MUL BL</i>	AX = AL * BL (Multiply MSB by 10)
<i>ADD AL,DL</i>	AL = AL + DL (Add LSB to the hex result)
<i>MOV AH,4CH</i>	Moves the hexadecimal value 4c to ah.
<i>INT 21H</i>	When Software interrupt 21 is called with AH=4C, then current process terminates
CODE ENDS	Ending the code segment
END START	Ending start segment

Unassembled Code:

```

-u
076B:0000 B86A07      MOV     AX,076A
076B:0003 8ED8          MOV     DS,AX
076B:0005 A00000      MOV     AL,[0000]
076B:0008 B400          MOV     AH,00
076B:000A B310          MOV     BL,10
076B:000C F6F3          DIV     BL
076B:000E B30A          MOV     BL,0A
076B:0010 8AD4          MOV     DL,AH
076B:0012 B400          MOV     AH,00
076B:0014 F6E3          MUL     BL
076B:0016 02C2          ADD     AL,DL
076B:0018 A20100      MOV     [0001],AL
076B:001B B44C          MOV     AH,4C
076B:001D CD21          INT     21
076B:001F 5E          POP     SI

```

Snapshot of sample input and output:

INPUT: Packed BCD No- 12

```

-d 076a:0000
076A:0000 12 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0010 BB 6A 07 8E D8 A0 00 00-B4 00 B3 10 F6 F3 B3 0A .j.....
076A:0020 8A D4 B4 00 F6 E3 02 C2-A2 01 00 B4 4C CD 21 5E .....L.?!^
076A:0030 F9 B7 00 D1 E3 8B 87 AE-16 3B 46 FE 77 09 89 46 .....;F.w..F
076A:0040 FE 8A 46 F9 88 46 F8 FE-46 F9 EB C9 8A 5E F8 B7 ..F..F..F....^..
076A:0050 00 8A 87 48 2F D0 D8 73-17 EB B6 00 8A 5E F8 B7 ...H/.s.....^..
076A:0060 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01 ...H/.s..S..P.s.
076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8 ...:F.t~.F....F.

```

OUTPUT: Hexadecimal equivalent- 0C

```

-d 076a:0000
076A:0000  12 0C 00 00 00 00 00 00 00-00 00 00 00 00 00 00  .....
076A:0010  B8 6A 07 8E D8 A0 00 00-B4 00 B3 10 F6 F3 B3 0A  .j.....
076A:0020  8A D4 B4 00 F6 E3 02 C2-A2 01 00 B4 4C CD 21 5E  .....L.?!^
076A:0030  F9 B7 00 D1 E3 8B 87 AE-16 3B 46 FE 77 09 89 46  .....F.w..F
076A:0040  FE 8A 46 F9 88 46 F8 FE-46 F9 EB C9 8A 5E F8 B7  ..F..F..F....^..
076A:0050  00 8A 87 48 2F D0 D8 73-17 E8 B6 00 8A 5E F8 B7  ...H/.s.....^..
076A:0060  00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01  ...H/.s.S..P.s.
076A:0070  A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8  ...:F.t~.F....F.

```

(ii) Hexadecimal to BCD Code Conversion

Algorithm:

- Move the data segment to DS register through AX register.
- Move the extra segment to ES register through AX register.
- Load 100(64H) to BL.
- Divide the value at AX by BL.
- Move the MSB at AL to CL.
- Move the LSBs at AH to AL.
- Clear AH register.
- Load the 10(0AH) to BL.
- Dive the value at AX by BL.
- Move the second bit of BCD to CH.
- Move the LSB of BCD to DL.
- Apply $[CL]*100 + [CH]*10 + [DL]$ and store the result at AX.
- Move the result at AX to BCD.

Program	Comments
ASSUME CS:CODE,DS:DATA,	Using assume directive to declare data, and code segment
DATA SEGMENT HEX DB0FFH BCD DW ? DATA ENDS	Declaring and initialising variables in data segment
CODE SEGMENT ORG 0100H	Set location for code segment at 0100h

START:	
<i>MOV AX,DATA</i>	Move the content of Data segment to AX register
<i>MOV DS,AX</i>	Move the content of AX register to DS register
<i>MOV AL, HEX</i>	Transfer the given BCD byte to AL.
<i>MOV AH, 0H</i>	Clear AH register. mov bl, 64h Transfer 100 to BL.
<i>MOV BL, 64H</i>	Transfer 100 to BL.
<i>DIV BL</i>	Divide AX by BL. (Quotient in AL, Remainder in AH)
<i>MOV CL, AL</i>	Transfer the quotient to CL register. (MSB of BCD)
<i>MOV AL, AH</i>	Transfer the remainder to AL register.
<i>MOV AH, 0H</i>	Clear AH register.
<i>MOV BL, 0AH</i>	Transfer 10 to BL.
<i>DIV BL</i>	Divide AX by BL.
<i>MOV CH, AL</i>	Transfer the quotient to CH register. (2nd MSB of BCD)
<i>MOV DL, AH</i>	Transfer the remainder to DL register. (LSB of BCD)
<i>MOV BL, 10H</i>	Transfer 16 to BL.
<i>MOV AL, CL</i>	Transfer the MSB of BCD to AL register.
<i>MUL BL</i>	$AX = AL * BL$ (Multiply MSB by 10)
<i>ADD AL, CH</i>	$AL = AL + CH$ (Add 2nd MSB to the BCD result)
<i>MUL BL</i>	$AX = AL * BL$ (MSB * 100 + 2nd MSB * 10)
<i>ADD AL, DL</i>	$AL = AL + DL$ (MSB * 100 + 2nd MSB * 10 + LSB)
<i>MOV BCD, AX</i>	Store the value in AX as the final BCD converted code
<i>MOV AH,4CH</i>	Moves the hexadecimal value 4c to ah.
<i>INT 21H</i>	When Software interrupt 21 is called with AH=4C, then current process terminates
CODE ENDS	Ending the code segment
END START	Ending start segment

Unassembled Code:

```

-u
076B:0100 B86A07      MOV     AX,076A
076B:0103 BED8        MOV     DS,AX
076B:0105 A00000      MOV     AL,[0000]
076B:0108 B400        MOV     AH,00
076B:010A B364        MOV     BL,64
076B:010C F6F3        DIV     BL
076B:010E 8AC8        MOV     CL,AL
076B:0110 8AC4        MOV     AL,AH
076B:0112 B400        MOV     AH,00
076B:0114 B30A        MOV     BL,0A
076B:0116 F6F3        DIV     BL
076B:0118 8AE8        MOV     CH,AL
076B:011A 8AD4        MOV     DL,AH
076B:011C B310        MOV     BL,10
076B:011E 8AC1        MOV     AL,CL

```

Snapshot of sample input and output:

INPUT: Hexadecimal value-FF

```
-d 076a:0000
076A:0000  FF 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0010  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0020  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0030  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0040  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0050  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0060  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0070  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
```

OUTPUT: BCD output in packed form- location1 = 02, location2 = 55

```
-d 076a:0000
076A:0000  FF 55 02 00 00 00 00 00-00 00 00 00 00 00 00 00 .U.....
076A:0010  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0020  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0030  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0040  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0050  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0060  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
076A:0070  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 .....
```

Result:

The assembly level programs were written to perform the above specified code conversions and the output was verified.