# **Taylor Kim**

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#### **Education**

University of Washington, Seattle, WA

Bachelor of Science in Electrical and Computer Engineering

Graduation: May 2024 | GPA: 3.80/4.0

Relevant Coursework: Analog Circuits, Power Electronics, Control Systems, Electromagnetics

## **Experience**

Power Electronics Intern | Eaton Corporation | Summer 2023

- Designed power supply circuits and simulated converters using LTspice
- Performed testing on high-voltage components for industrial applications
- Assembled lab setups and documented efficiency measurements

Lab Assistant | UW Electrical Lab | Jan 2023 - May 2023

- Helped students debug analog circuit designs during lab sessions
- Maintained lab equipment and calibrated measurement tools

#### **Technical Skills**

- Circuit Design & Analysis
- LTspice, Multisim, MATLAB/Simulink
- Power Systems, Analog Filters
- Oscilloscopes, Function Generators

### **Projects**

Solar Battery Charger with MPPT

- Designed a DC-DC converter with MPPT algorithm on a TI MSP430
- Measured and optimized energy efficiency under varying load conditions

#### Soft Skills

- Precision and Accuracy
- Hands-on Troubleshooting
- Technical Communication
- Initiative