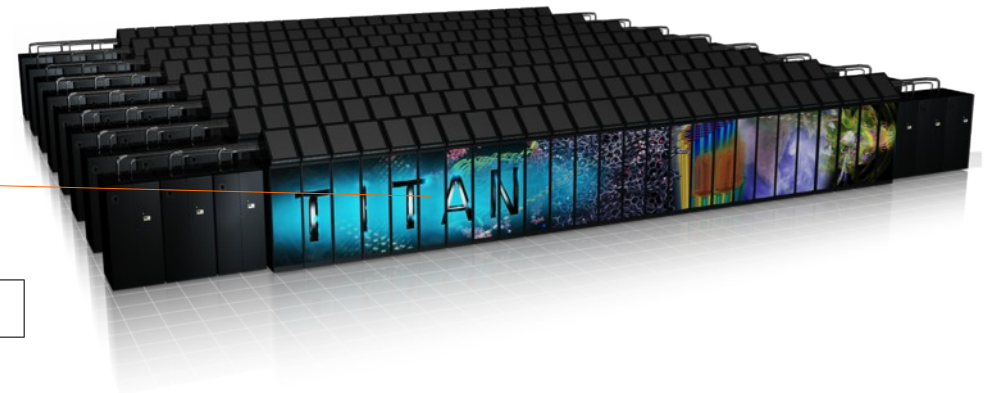
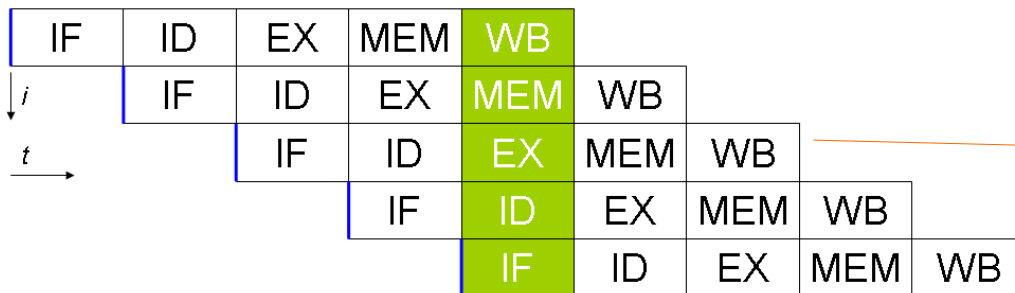


# Many cores



## Pipeline

- Instructions divided in steps



## Memory hierarchy

