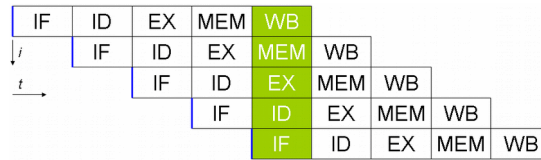


Pipeline

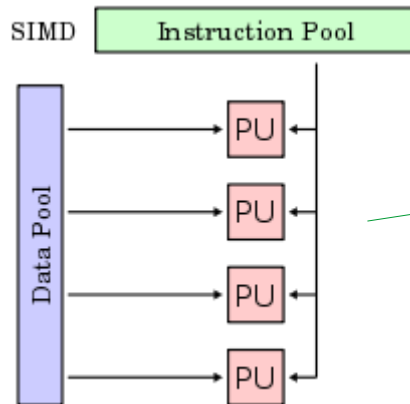


Many cores



Vector support

- Same Instruction Multiple Data
- Save decoding phase



Memory hierarchy

