Pipeline

 Instructions devided in stepsMultiple instructions in the pipeline but at different steps

• Importance of the order of the instructions

| IF | ID | EX | MEM | WB | | | | |
|------------|----|----|-----|-----|-----|-----|-----|----|
| ↓ <i>i</i> | IF | ID | EX | MEM | WB | | | 40 |
| <i>t</i> → | | IF | ID | EX | MEM | WB | | |
| | | | IF | ID | EX | MEM | WB | |
| | | | | IF | ID | EX | MEM | WB |



Memory hierarchy

