## **Pipeline**

IF	ID	EX	MEM	WB				
j i	IF	ID	EX	MEM	WB			
<u>t</u>		IF	ID	EX	MEM	WB		
			IF	ID	EX	MEM	WB	
				IF	ID	EX	MEM	WB

## Many cores

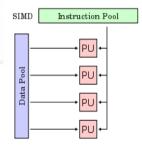


## **GPUs**

- Powerfull for scientific computation
- Mapping computation on on pipeline suited to graphics
- Different cache size and organization.



**Vector support** 



## **Memory hierarchy**

