Pipeline

IF	ID	EX	MEM	WB				
i	IF	ID	EX	MEM	WB			
<u>t</u>		IF	ID	EX	MEM	WB		
		. 1. 4. juli	IF	ID	EX	MEM	WB	
				IF	ID	EX	MEM	WB

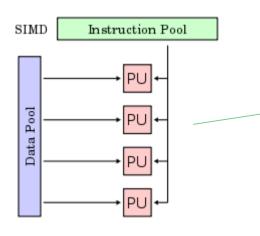
Many cores

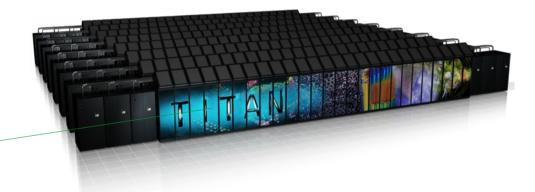


Vector support

• Same Instruction Multiple Data

Save decoding phase





Memory hierarchy

