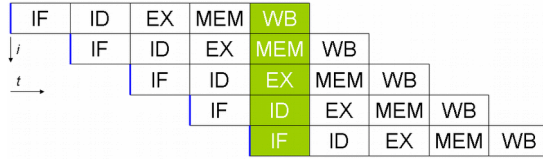


# Pipeline

Take care of the order of the instructions



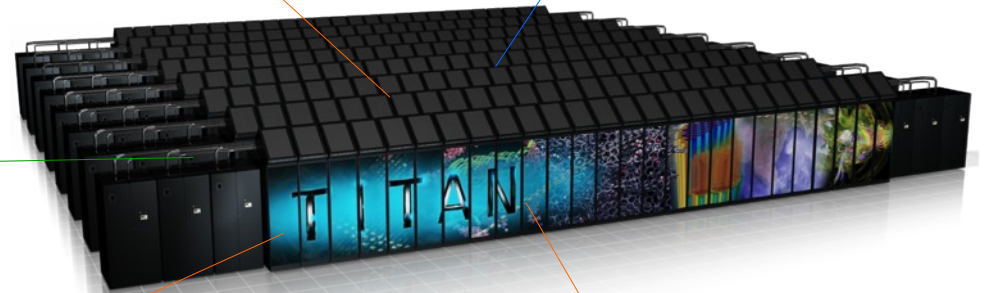
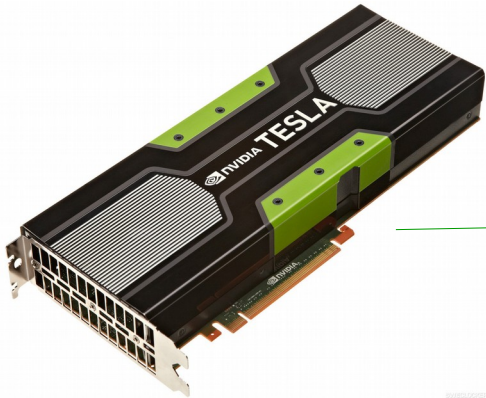
# Many cores

Efficient parallelism is complicated



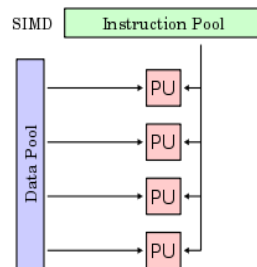
# GPUs

Different architecture and way of programming than CPUs



# Vector support

Manipulate vector instead of scalar variables



# Memory hierarchy

Data locality → memory access patterns

