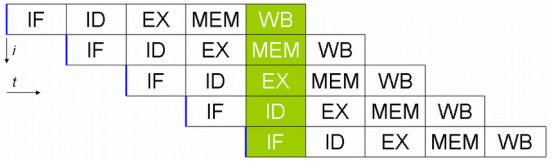
Pipeline

Take care of the order of the instructions



Many cores

Efficient parallelism is complicated

Memory hierarchy

Data locallity →memory access patterns