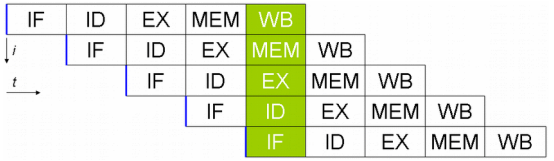
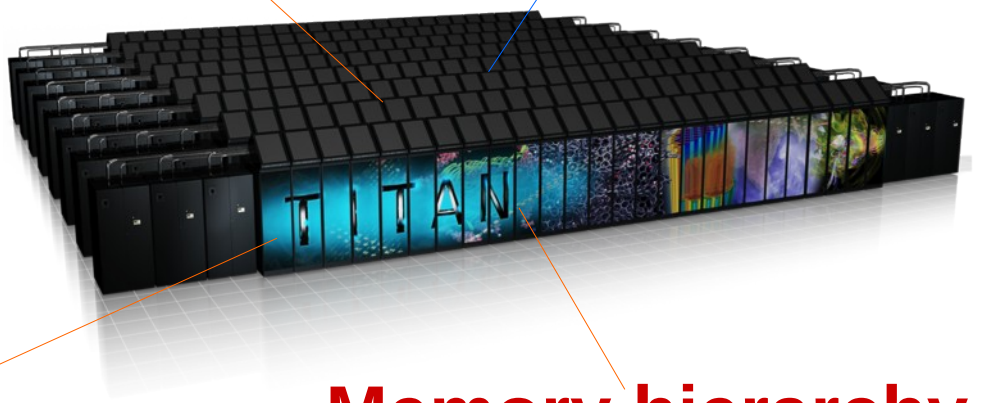
Pipeline

Take care of the order of the instructions



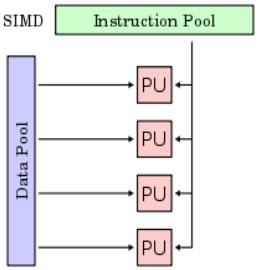
Many cores

Efficient parallelism is complicated



Vector support

Manipulate vector instead of scalar variables



Memory hierarchy

Data locallity →memory access patterns