

## SH68F881 BANK0

	Bit-addressable	Non bit-addressable								
	0/8	1/9	2/A	3/B	4/C	5/D	6/E	7/F		
F8H				IB_OFFSET	IB_DATA				FFH	USB
F0H	B	AUXC	IB_CON1	IB_CON2	IB_CON3	IB_CON4	IB_CON5	XPAGE	F7H	FLASH
E8H	EXF0	P0PCR	P1PCR	P2PCR	P3PCR	P4PCR			EFH	GPIO
E0H	ACC	P0CR	P1CR	P2CR	P3CR	P4CR			E7H	TIMER
D8H	EXF1	PWM0DL	PWM0DH	PWM1DL	PWM1DH	PWM2DL	PWM2DH		DFH	PWM
D0H	PSW	PWM0PL	PWM0PH	PWM1PL	PWM1PH	PWM2PL	PWM2PH		D7H	INTERRUPT
C8H	T2CON	T2MOD	RCAP2L	RCAP2H	TL2	TH2	TCON1		CFH	CLOCK
C0H	P4	BTCON	SEC	MIN		PWM0CON	PWM1CON	PWM2CON	C7H	UART
B8H	IPL0	IPL1	IENC			CLKL0	CLKRC0	CLKRC1	BFH	WDT
B0H	P3	RSTSTAT	CLKCON	PLLCON	IPH0	IPH1	SFINE		B7H	SPI
A8H	IEN0	IEN1							AFH	8051
A0H	P2	REGCON			SPCON	SPSTA	SPDAT	FLASHCON	A7H	NONE
98H	SCON	SBUF	SADDR	SADEN	SBRTH	SBRTL			9FH	WHICH_BANK
90H	P1								97H	
88H	TCON	TMOD	TL0	TL1	TH0	TH1	SUSL0		8FH	
80H	P0	SP	DPL	DPH	DPL1	DPH1	INSCON	PCON	87H	
	0/8	1/9	2/A	3/B	4/C	5/D	6/E	7/F		

ISP SFR

SAME SFR

Which bank though?

## SH68F881 BANK1

	Bit-addressable	Non Bit addressable								
	0/8	1/9	2/A	3/B	4/C	5/D	6/E	7/F		
F8H	USBIF2								FFH	USB
F0H	B	AUXC						XPAGE	F7H	FLASH
E8H	USBIF1	P5PCR	P6PCR	P7PCR	P8PCR				EFH	GPIO
E0H	ACC	P5CR	P6CR	P7CR	P8CR				E7H	TIMER
D8H	EP2CON								DFH	PWM
D0H	PSW								D7H	INTERRUPT
C8H	T4CON				TL4	TH4			CFH	CLOCK
C0H	EP1CON								C7H	UART
B8H	IPL0	IPL1							BFH	WDT
B0H	P8				IPH0	IPH1			B7H	SPI
A8H	IEN0	IEN1							AFH	8051
A0H	P7					0EP0CNT	0EP1CNT	0EP2CNT	A7H	NONE
98H	EP0CON	USBCON	USBIE1	USBIE2	USBADDR	IEP0CNT	IEP1CNT	IEP2CNT	9FH	WHICH_BANK
90H	P6								97H	
88H	T3CON				TL3	TH3	SUSLO		8FH	
80H	P5	SP	DPL	DPH	DPL1	DPH1	INSCON	PCON	87H	
	0/8	1/9	2/A	3/B	4/C	5/D	6/E	7/F		

ISP SFR

SAME SFR

Which bank though?