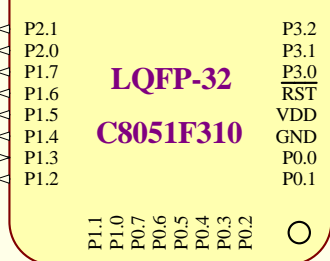


U6 C8051F310

SELECT_LED_DAC6 17
SELECT_LED_DAC5 18
SELECT_LED_DAC4 19
SELECT_LED_DAC3 20
SELECT_LED_DAC2 21
SELECT_LED_DAC1 22
ADC_ReadyN 23
DAC_csN 24

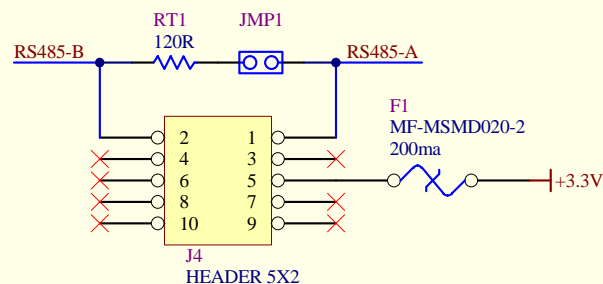
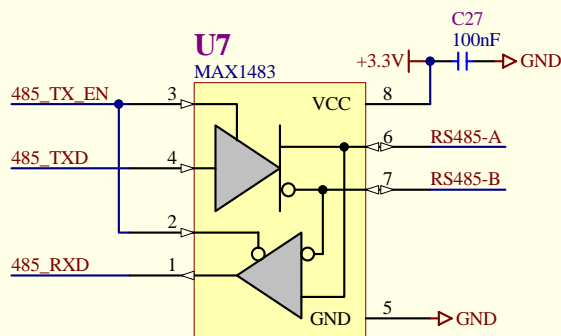
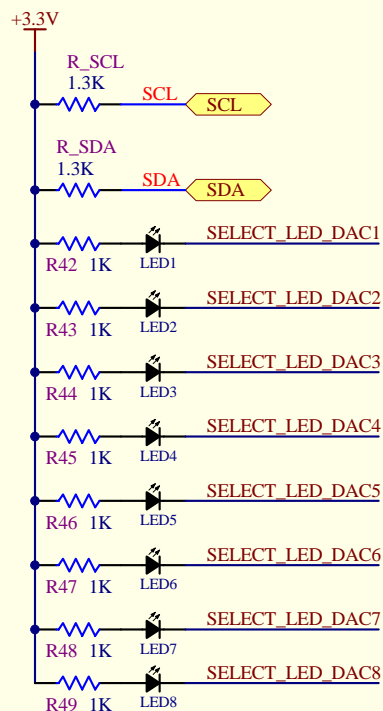


P2.2 8 ADC_ResetN
P2.3 7 ADC_csN
P2.4 6 uP_C2D
P2.5 5 uP_C2CK
P2.6 4
P2.7 3
P3.4 2
P3.3 1

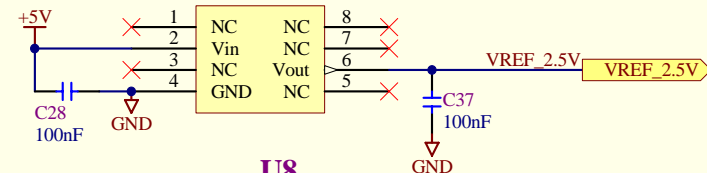
+3.3V
C26 100nF
GND

CEX0_CHG_PMP
Int_BIAS_SelN
Ext_BIAS_SelN

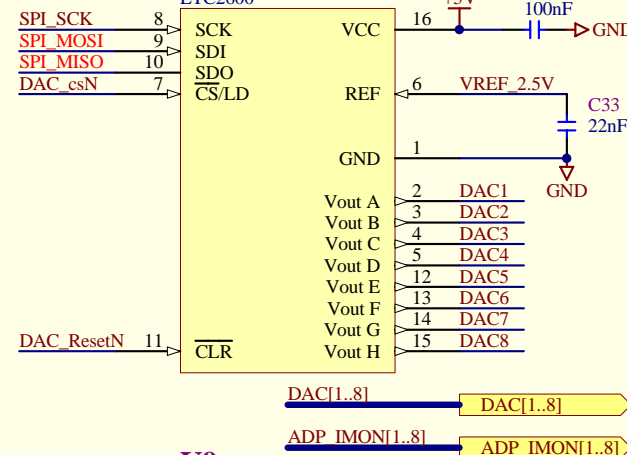
CEX0_CHG_PMP
Int_BIAS_SelN
Ext_BIAS_SelN



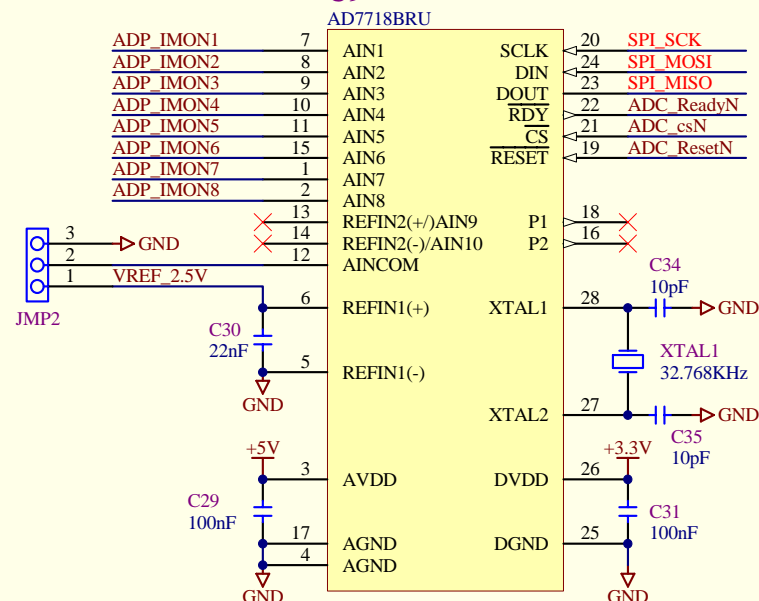
U10 AD680JRZ



U8 LTC2600



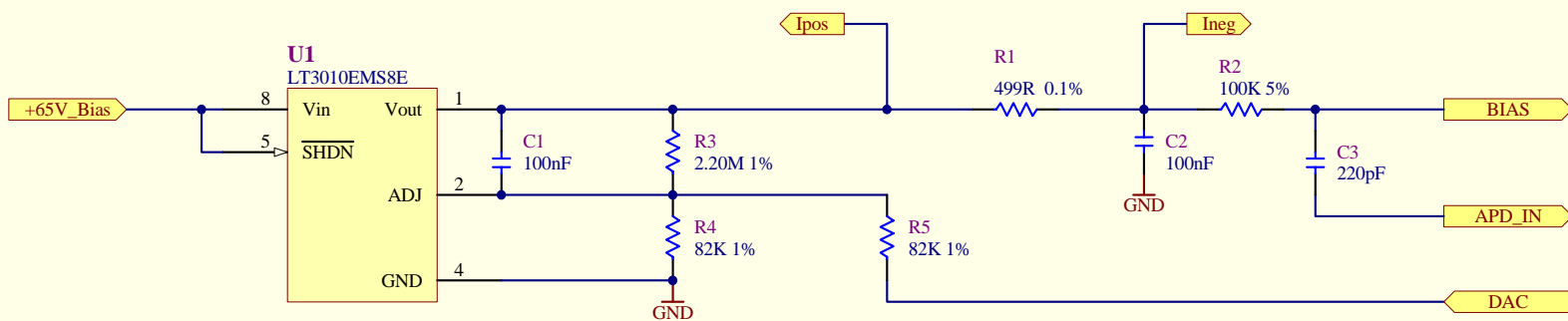
U9 AD7718BRU




T2K 8 Channel Bias - CPU / DAC / ADC

Revision	Drawing #:	TRIUMF
0	Sheet #: 2 of 8	4004 Wesbrook Mall
	Size: A	Vancouver, B.C.
	Drawn by: D. Bishop	Canada
	Date: 8/31/2006	V6T 2A3

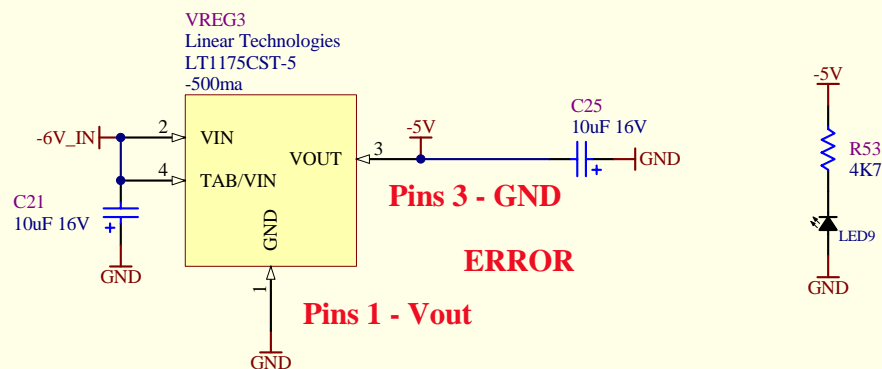
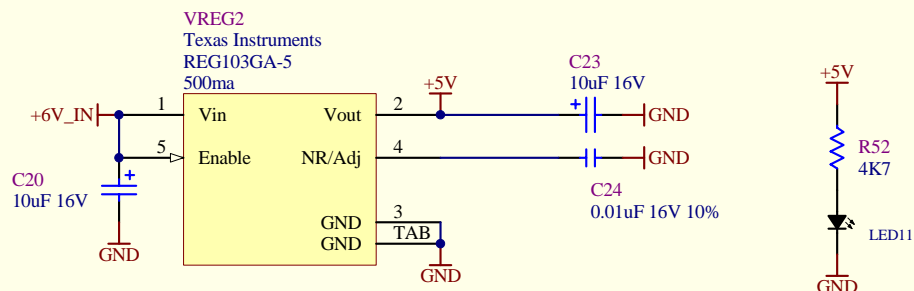
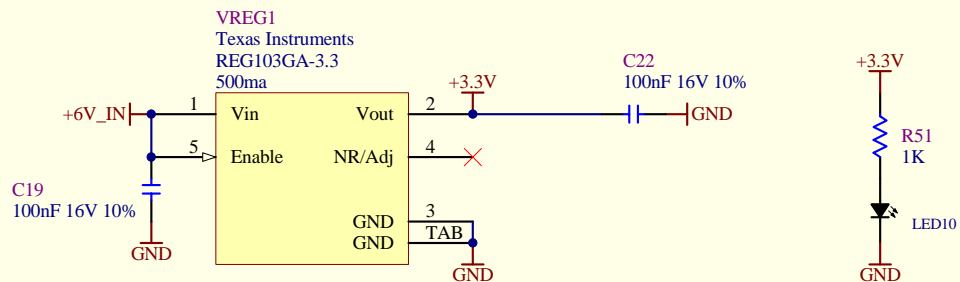
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T2K - 8 Channel Bias: APD Voltage Regulator

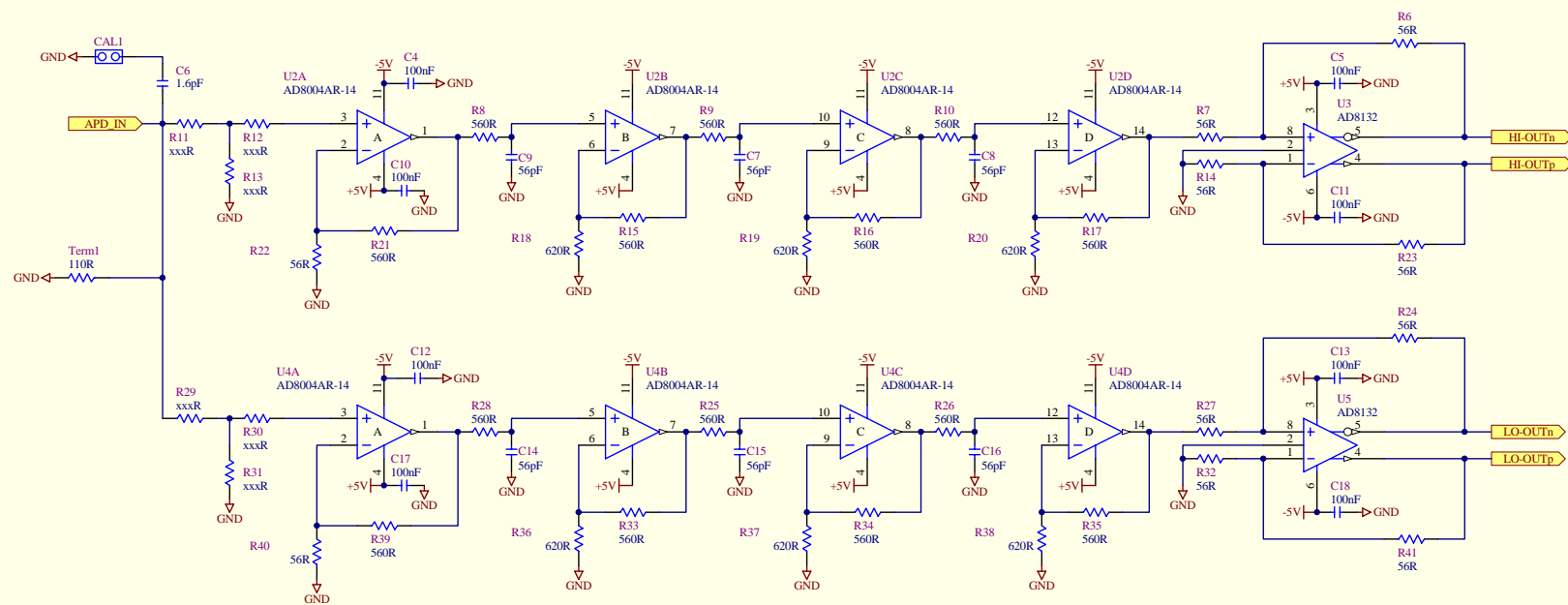
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	Drawn by: D. Bishop	Date: 6/28/2006	

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T2K 8 Channel Bias - Regulators

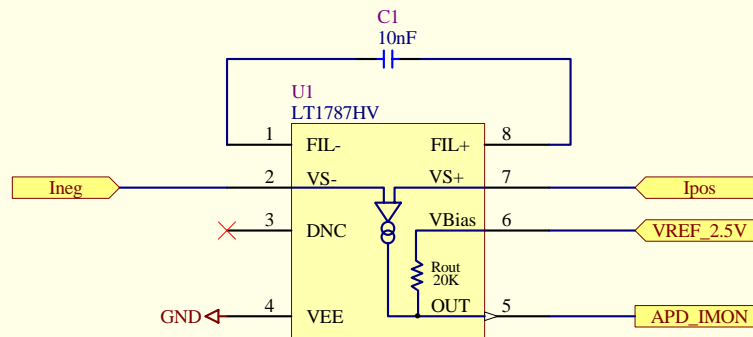
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	Drawn by:	D. Bishop	Date: 8/31/2006	
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T2K 8 Channel Bias - OP Amps

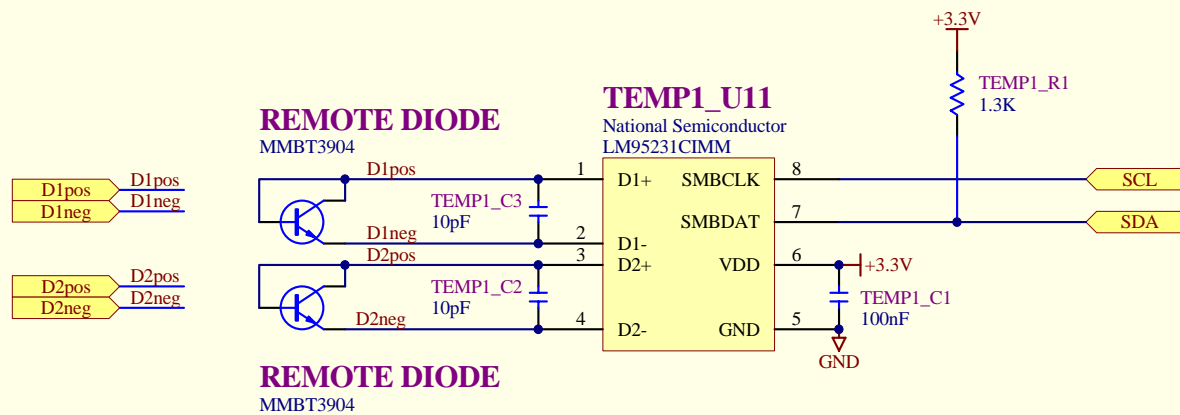
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Drawn by: D. Bishop	Date: 6/28/2006		Vancouver, B.C.
			Canada
			V6T 2A3
File: G:\AHW\T2K\T2K 8 Channel Bias\T2K 8 Channel Bias - Op Amps.SCHDOC			9:03:54 PM





T2K 8 Channel Bias - Current Sense

Revision 0	Drawing #:		<i>TRIUMF</i> 4004 Wesbrook Mall Vancouver, B.C. Canada V6T 2A3	
	Sheet #: 5 of 8	Size: A		
	Drawn by: D. Bishop	Date: 6/28/2006		
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7-bit Slave Address Determined by part number:

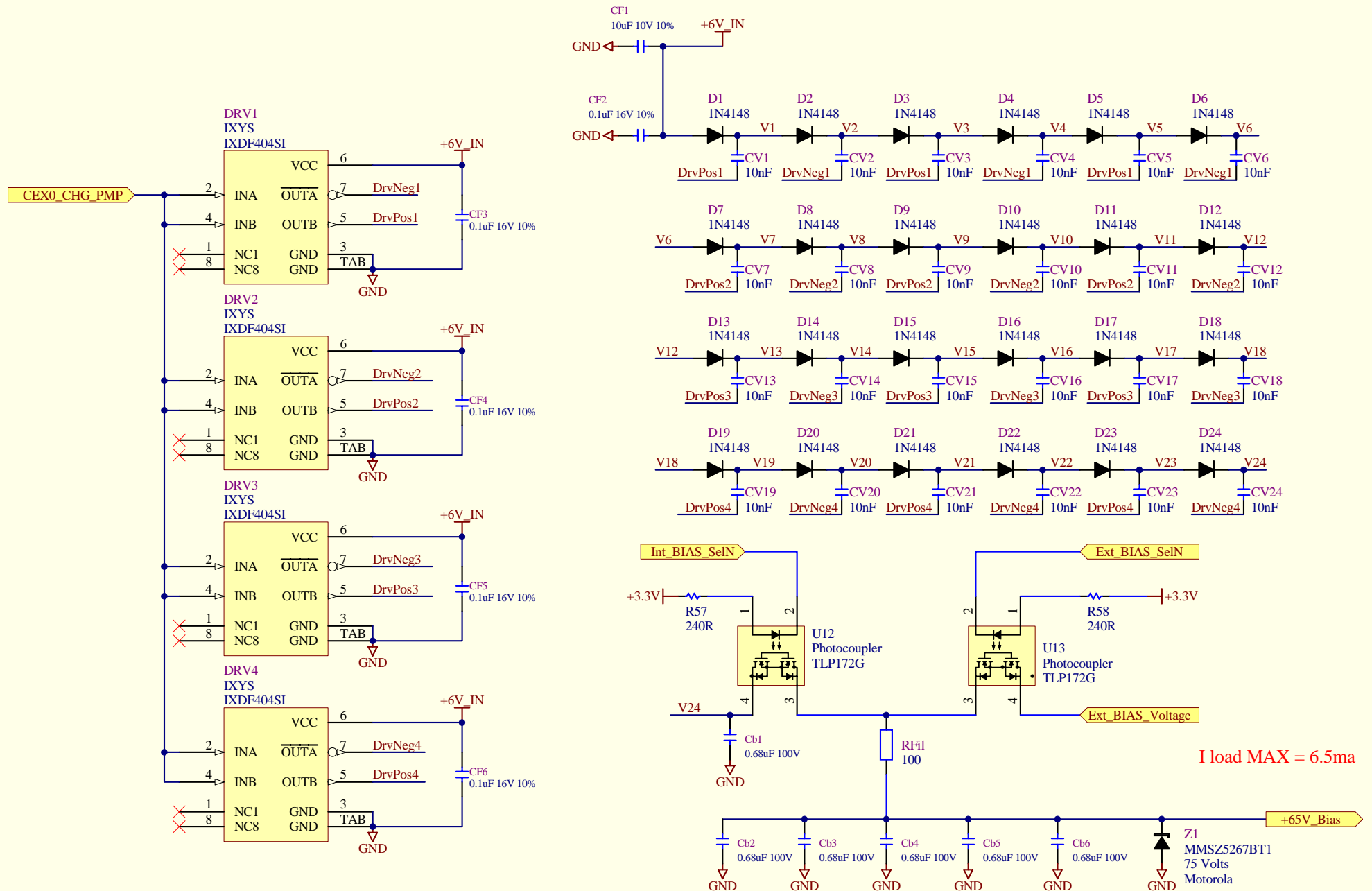
LM9523BIMM > 0101011

LM9523BIMM-1 > 0011001


LM9523BIMM-2 > 0101010

T2K 8 Channel Bias - Temperature Sense

Revision 0	Drawing #:		<i>TRIUMF</i> 4004 Wesbrook Mall Vancouver, B.C. Canada V6T 2A3	
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	Drawn by: D. Bishop	Date: 6/28/2006		
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T2K 8 Channel Bias: 65 Volt Charge Pump

Revision 0	Drawing #:		TRIUMF	
	Sheet #: 3 of 8	Size: A	4004 Westbrook Mall Vancouver, B.C. Canada V6T 2A3	
	Drawn by: D. Bishop	Date: 8/31/2006		
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