



VIT[®]

UNIVERSITY

(Estd. u/s 3 of UGC Act 1956)

Vellore-632 014, Tamil Nadu, India.

www.vit.ac.in

School of Electronics Engineering

VLSI Division

Certificate of Participation


This is to certify that Mr/ Ms / Mrs ... **S. VIJAY MURUGAN** ... of

... **ADHILAMAN COLLEGE OF ENGINEERING** has undergone a 3 Day Hands-on training in **"Digital IC Design"**


held at VIT University from 23rd Feb to 25th Feb 2012, organized by VLSI Division, School of Electronics Engineering,

VIT University, Vellore.




Course Coordinator


Division Leader


Director

