

## Priyadarshini Engineering College

Chettiyappanur Village & Fost, Vaniyambadi-635751, Vellore District, Tamil Nadu, India.

Listed in 2(f) & 12(B) Sections of UGC.



International Conference on Electrical, Electronics, Computers, Communication,

Mechanical and Computing (EECCMC) - 2018

This is to certify that Miss A. Chitra has published a paper entitled: Low-power Stuck Open and Short Fault Analysis Built-In Self-Test Architecture for SRAM based FPGA with paper code: 01-2018-193 in International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC) - 2018, with catalog "CFP18037-PRT: 978-1-5386-4303-7", organized by Priyadarshini Engineering College, Vellore District, Tamil Nadu, India during 28th & 29th January 2018.

Certificate Proof

Dr. Siva Ganesh Malla Director, CPGC Dr. P. Natarajan
Principal, PEC

