Lab 3. K-map, Multi-Level, Multi-Output Logic

Prelab

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4.2 가

Diagram, engineering drawing

Description automatically generated

4.2 나

Using De Morgan’s Theorem,

We can use AOI22 to replace AND-AND-OR gates from above.

AOI22 is expressed as two 2-input AND gates with outputs going into a 2-input NOR gate.

Diagram, engineering drawing

Description automatically generated

4.3 Pulse Shaper Circuit 가

Diagram

Description automatically generated