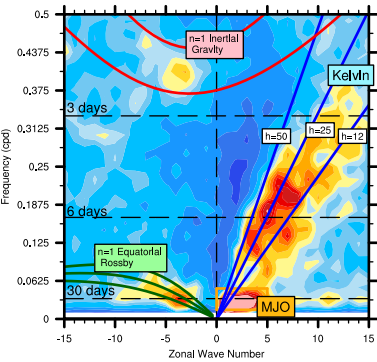
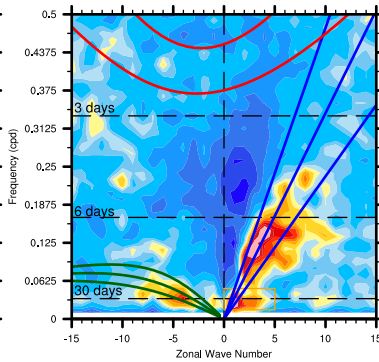


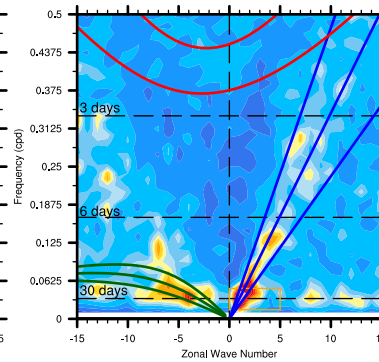
(a) TRMM



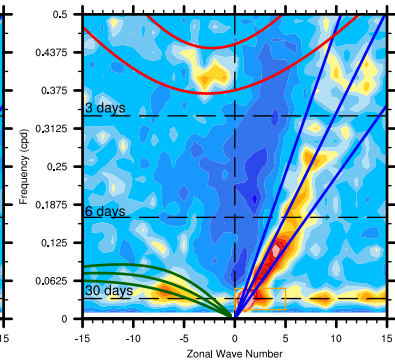
(b) v0atm



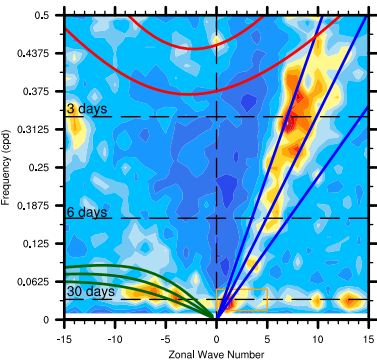
(c) v0atm_C



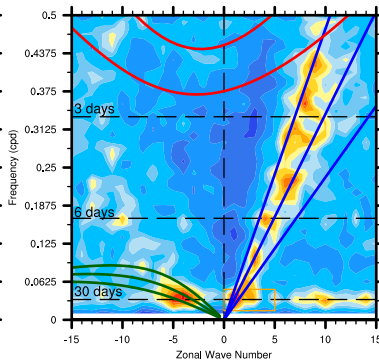
(d) v0CL72



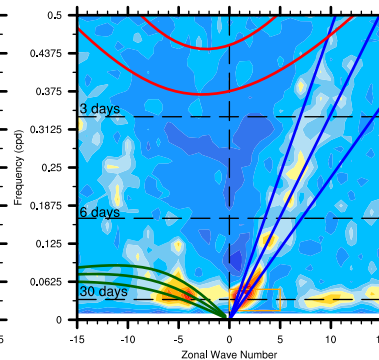
(e) v0CL72p



(f) v1L30



(g) v1L30p



(h) v1L72

