# **Equivalence Semantics of CCS**

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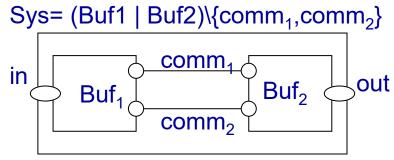
#### **Oultline**

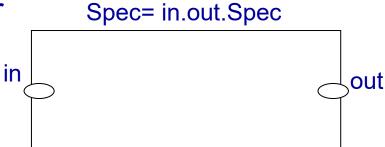
- Trace Equivalence
- Observational Trace Equivalence
- Bisimulation Equivalence
- Observational Bisimulation Equivalence
- May Preorder and Must Preorder
- Example
- Usage of Concurrent Workbench



#### **Trace Equivalence**

- Sys is a design for buffer with separated input/output ports
  - Sys= (Buf<sub>1</sub> | Buf<sub>2</sub>)\{comm<sub>1</sub>,comm<sub>2</sub>}
    - Buf<sub>1</sub> = in.comm<sub>1</sub>'.Buf<sub>1</sub>', Buf<sub>1</sub>' = comm<sub>2</sub>.Buf<sub>1</sub>
    - Buf<sub>2</sub> = comm<sub>1</sub>.Buf<sub>2</sub>',Buf<sub>2</sub>'= out'.comm<sub>2</sub>'.Buf<sub>2</sub>
- Spec is a requirement for the buffer design
  - Spec = in.Spec', Spec'=out'.Spec





- Question: Sys == Spec?
  - ♣ Let us consider trace equivalence (i.e. language equivalence) =<sub>T</sub>
    - T(P) = { s ∈ Act\* | s is an execution trace of P}
    - $P =_T Q \text{ iff } T(P) = T(Q)$





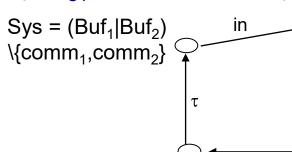
### **Observational Trace Equivalence**

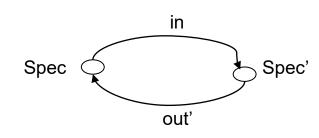
- Sys =<sub>T</sub> Spec?
  - **♣** No. Sys has τ which Spec does not
    - $T(Sys) = \{in, in.\tau, in.\tau.out', in.\tau.out'.\tau,...\}$
    - T(Spec) = {in , in.out' ...}

```
Sys= (Buf1 | Buf2)\{comm1,comm2}
Buf1 = in.comm1.Buf1', Buf1' =
comm2.Buf1
Buf2 =
comm1' Buf2' Buf2'=out comm2' Buf2
```

comm1'.Buf2',Buf2'=out.comm2'.Buf2 Spec = in.out.Spec

- + Yes. τ is an internal hidden action not visible outside (not observable). Thus, τ should not be included in an execution
  - If s∈Act\*, then ŝ ∈(Act –{τ})\* is the action sequence obtained by deleting all occurrences of τ from s.
    - Ex> s =  $a.\tau.b.\tau.c$ , then  $\hat{s}$  = a.b.c
  - A set of observable execution traces: T'(P) = {ŝ | s ∈ T(P)}
  - $P =_{OT} Q \text{ iff } T'(P) = T'(Q)$
  - Sys =<sub>OT</sub> Spec because T'(Sys) = {in, in.out',...}, T'(Spec) = {in, in.out', ...}





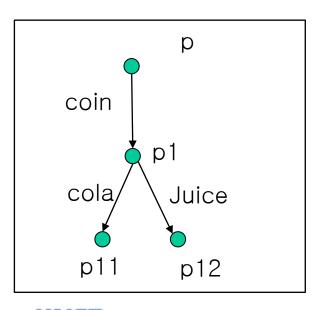


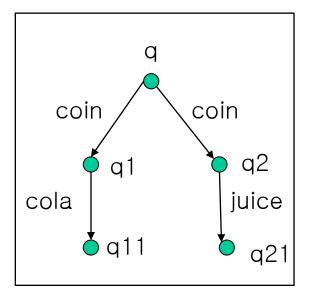
out'(Buf<sub>1</sub>'|Buf<sub>2</sub>')\{comm<sub>1</sub>,comm<sub>2</sub>}

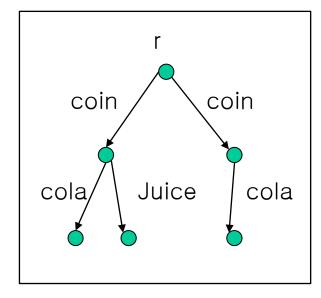
## Importance of Branching Behavior

Which vending machine do you prefer? p? q? r?







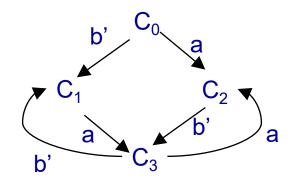


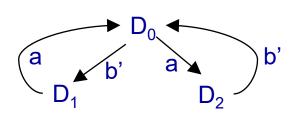




### **Bisimulation Equivalence**

- $P =_{BS} Q \text{ iff for all } \alpha \in Act$ 
  - ♣ Whenever P - $\alpha$ -> P', then for some Q', Q - $\alpha$ -> Q' and P' =<sub>BS</sub> Q'
  - ♣ Whenever Q -α-> Q', then for some P', P -α-> P' and P' =<sub>BS</sub> Q'
- Note
  - ♣ =<sub>BS</sub> is an equivalence relation (reflexive, transitive, symmetric)
  - $\blacksquare$  P =<sub>RS</sub> Q implies P =<sub>T</sub> Q, but not vice versa
- Example>
  - $C_0 = b'.C_1 + a.C_2, C_1 = a.C_3, C_2 = b'.C_3, C_3 = b'.C_1 + a.C_2$
  - $\bot$  D<sub>0</sub> = b'.D<sub>1</sub> +a.D<sub>2</sub>, D<sub>1</sub>=a.D<sub>0</sub>, D<sub>2</sub>=b'.D<sub>0</sub>
  - $\blacksquare$  A binary relation R proves that  $C_0 =_{BS} D_0$ 
    - $R = \{(C_0, D_0), (C_1, D_1), (C_2, D_2), (C_3, D_0)\}$





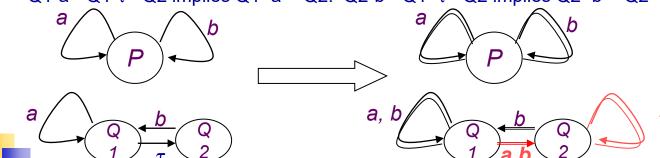


### **Observational Bisimulation Equivalence**

- We cannot simply ignore  $\tau$  for observational bisimulation equivalence. Thus, we define a new observational transition = $\alpha$ =>
- $P =_{OBS} Q$  iff for all  $\alpha \in Act$

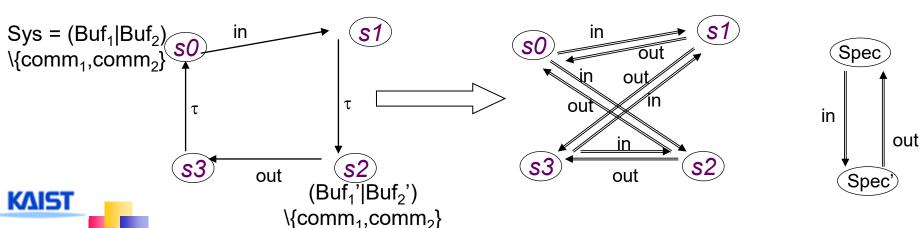
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- ♣ Whenever P =  $\alpha$  => P', then for some Q', Q =  $\alpha$  => Q' and P' =  $_{OBS}$  Q'
- ♣ Whenever Q = $\alpha$ => Q', then for some P', P = $\alpha$ => P' and P' =<sub>OBS</sub> Q'
- P =  $\alpha$  => Q iff P (- $\tau$ ->)\*- $\alpha$ ->(- $\tau$ ->)\* Q where  $\alpha \in Act$ -{ $\tau$ }
  - **↓** Let  $s \in (Act \{\tau\})^*$ . Then q = s = p if there exists s' s.t. q s' p and s = s
  - $\blacksquare$  P = a.P + b.P, Q1=a.Q1 + τ.Q2, Q2=b.Q1
    - Suppose that 'a' means pushing button 'a'. Similarly for 'b'
      - P always allows a user to push any buttons.
      - Q1 allows a user to push button 'a' sometimes, button 'b' sometimes.
    - Thus, we need to distinguish P from Q1 (P and Q1 are not observationally bisimilar), which can be done using = $\alpha$ => instead of - $\alpha$ ->
      - Q1-a->Q1 implies Q1=a=>Q1. Similary Q2-b->Q1 implies Q2=b=>Q1
      - Q1-a->Q1-τ->Q2 implies Q1=a=>Q2. Q2-b->Q1- τ->Q2 implies Q2=b=>Q2



#### Observational Bisimulation Equivalence (cont)

- $\blacksquare$  Sys =<sub>RS</sub> Spec? (see slide 3)
  - **4** No. Sys has τ which Spec does not (i.e. not strongly bisimilar)
- Sys = OBS Spec?
  - Yes. Sys is observationally bismilar to Spec
    - Proof: R = { (s0,Spec), (s1,Spec'),(s3,Spec),(s2,Spec')}
      - s0 -in->s1 implies s0=in=> s1. Similarly, s2-out->s3 implies s2=out=>s3
      - s0 -in->s1 - $\tau$ ->s2 implies s0=in=>s2.
      - s2-out->s3- $\tau$ -> s0 implies s2=out=>s0





#### **CWB-NC Commands**

- load <ccs filename>
- help <command>
- S
- cat crocess>
- compile compile compile
- es <script file> <output file>
- eq -S <trace|bisim|obseq> proc1>
- le –S may <proc1> <proc2> /\* Trace subset relation \*/
- sim process>
  - semantics <bisim|obseq>
  - ♣ random <n>
  - ♣ back <n>
  - break <act list>
  - history
  - **4** quit
- quit



