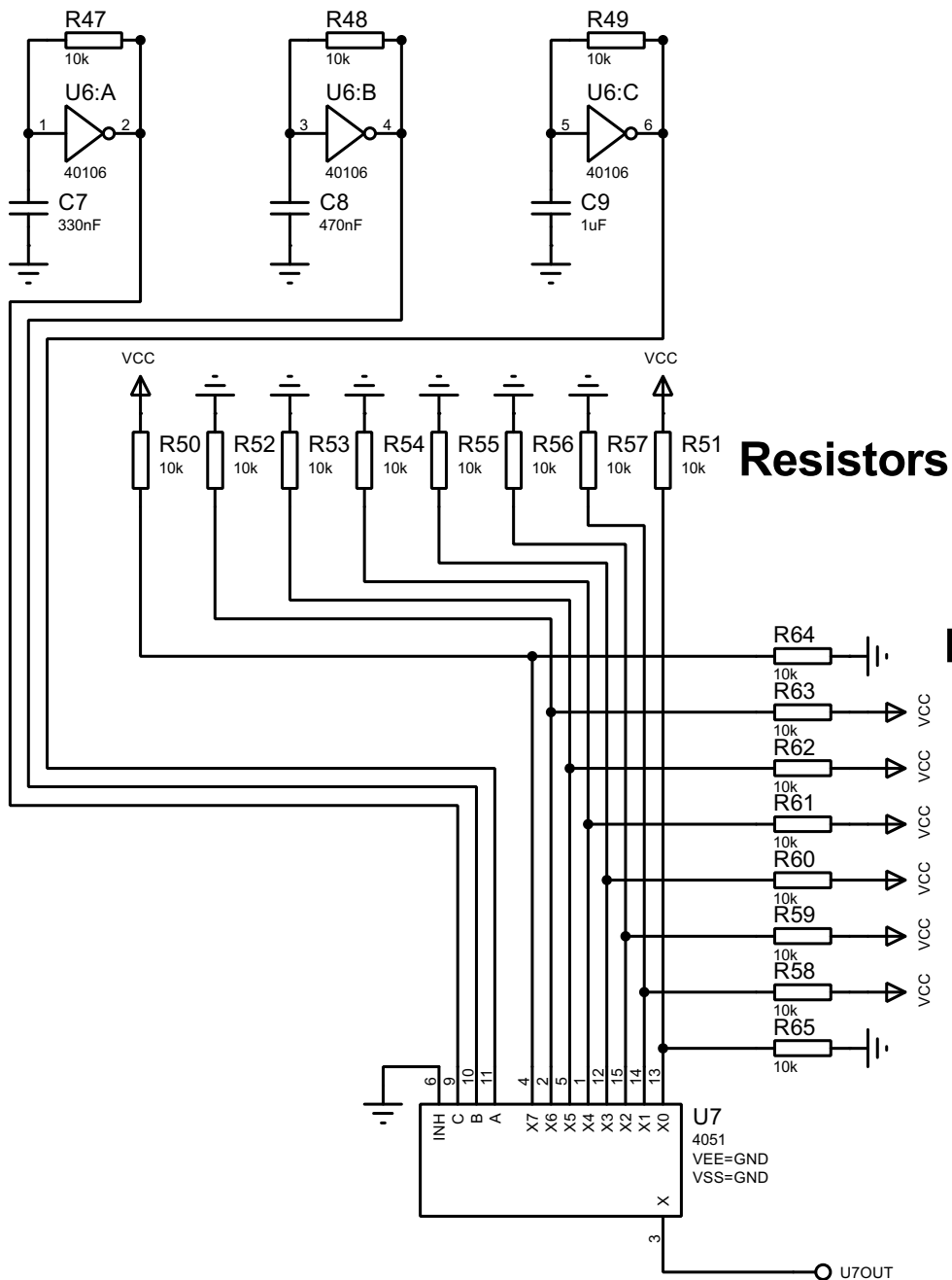


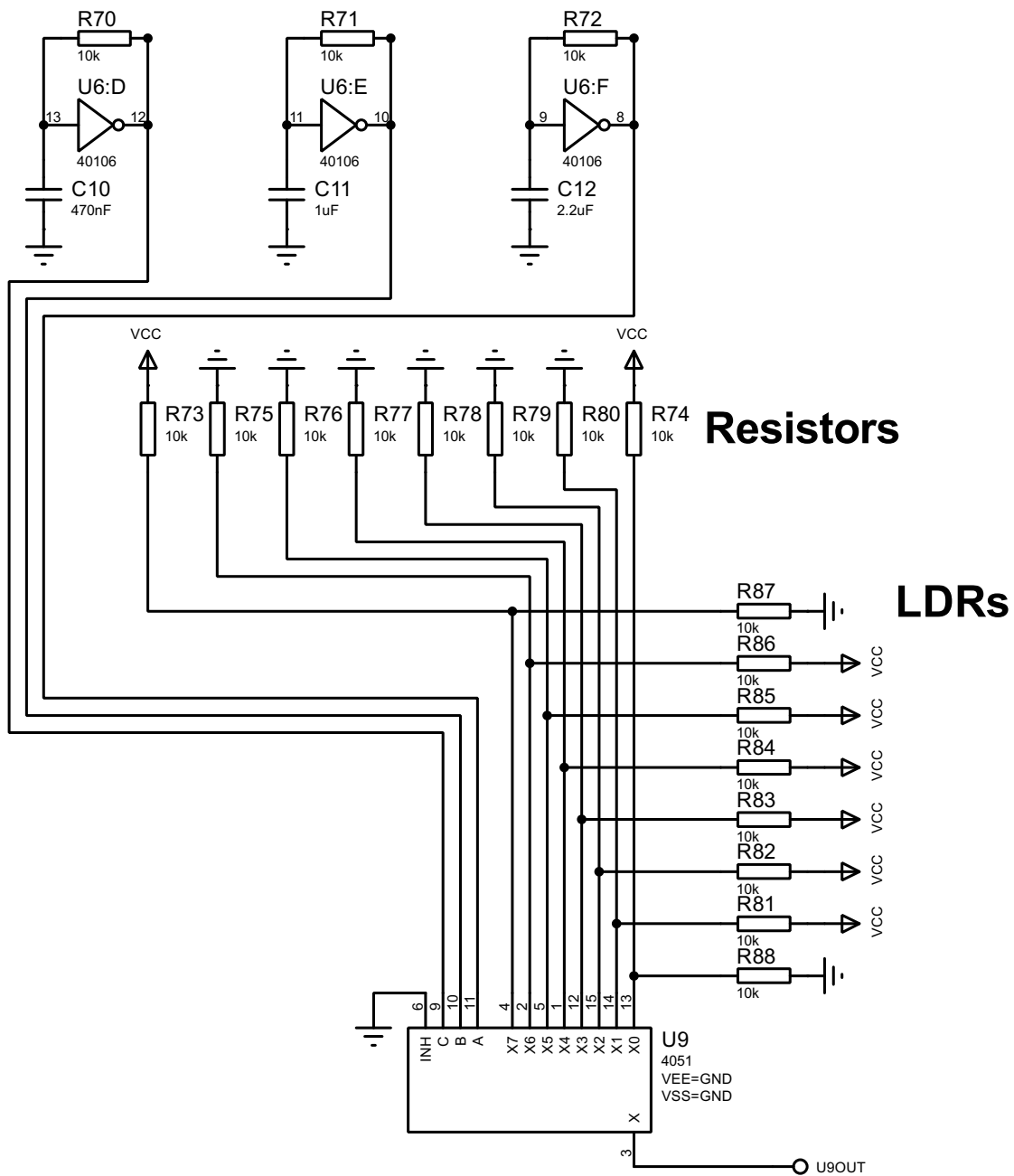
**Resistors**

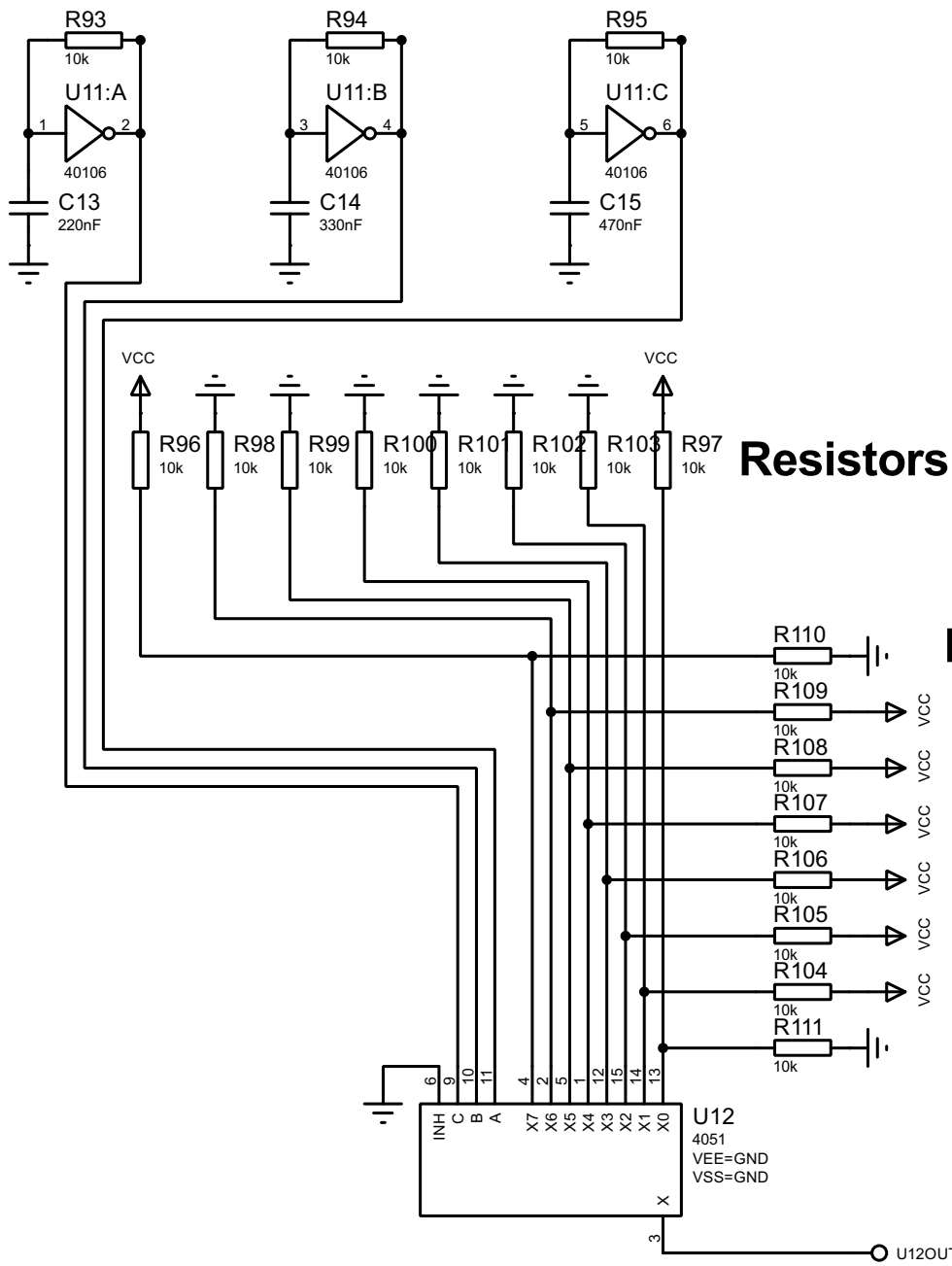
**LDRs**

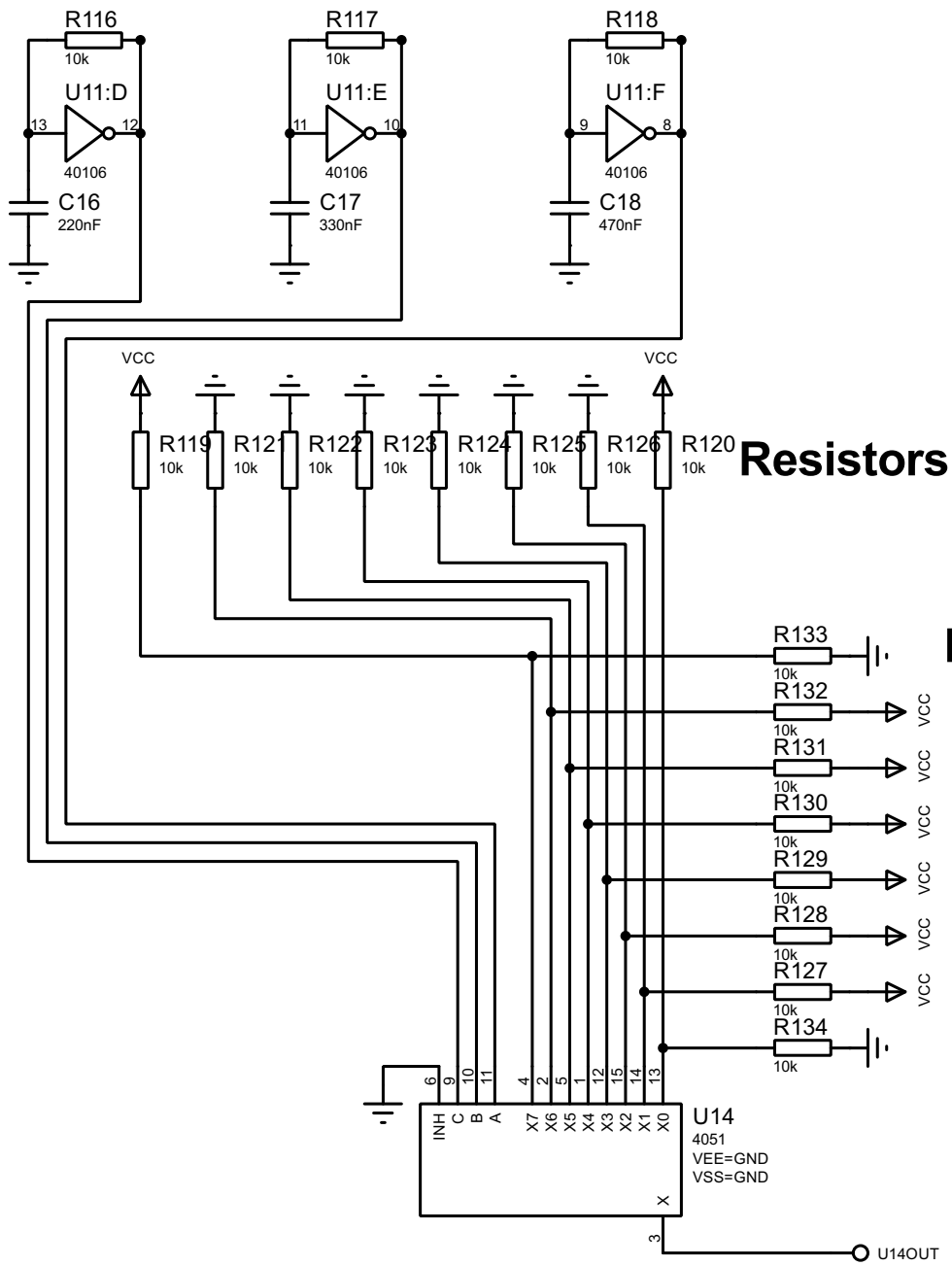


**Resistors**

**LDRs**







**Resistors**

**LDRs**

**Register order from top:**  
U4OUT, U2OUT, U9OUT, U7OUT, U14OUT, U12OUT

