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NPTEL (https://swayam.gov.in/explorer?ncCode=NPTEL)   »   Compiler Design (course)

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Course outline

Week 1 : Assignment 1

The due date for submitting this assignment has passed.

Due on 2025-02-05, 23:59 IST.

Assignment submitted on 2025-02-05, 22:15 IST

1) Which phase of compiler does NOT use symbol table?   **1 point**

    a) Code generation

    b) Syntax Analysis

    c) Lexical Analysis

    d) None of the other options

⦿ a

○ b

○ c

○ d

No, the answer is incorrect.
Score: 0

Accepted Answers:
d

2) Which phase of compiler is Syntax Analysis? **1 point**

   a) First

   b) Second

   c) Third

   d) None of the mentioned

○ a
◉ b
○ c
○ d

Yes, the answer is correct.
Score: 1

Accepted Answers:
b

3) Output of the syntax analysis is called **1 point**

   a) Parse tree
   b) Keyword tree
   c) Binary tree
   d) All of the other options

◉ a
○ b
○ c
○ d

Yes, the answer is correct.
Score: 1

Accepted Answers:
a

4) **1 point**

A programming language does not allow integer division operation. This is generally detected in the phase of

   a) Lexical Analysis

   b) Syntax Analysis

   c) Semantic Analysis

   d) None of the other options

○ a

○ b

◉ c

○ d

Yes, the answer is correct.
Score: 1
Accepted Answers:
c

5) Which of these is not true about Symbol Table   *1 point*

    a) All the labels of the instructions are symbols

    b) Table has entry for symbol name address value

    c) Perform the processing of the assembler directives

    d) Created during pass 1

○ a

○ b

◉ c

○ d

Yes, the answer is correct.
Score: 1
Accepted Answers:
c

6) A compiler can check   *1 point*

    a) Logical error

    b) Syntax error

    c) Both logical error and syntax error

    d) Not logical and syntax error

○ a

◉ b

○ c

○ d

Yes, the answer is correct.
Score: 1
Accepted Answers:
b

7) Error recovery helps to   *1 point*

    a) Report multiple errors

    b) Rectify multiple errors

    c) Both report and rectify multiple errors

    d) None of the other options

    ⦿ a

    ○ b

    ○ c

    ○ d

Yes, the answer is correct.
Score: 1

Accepted Answers:
a

8)   *1 point*

Converting a hardware description into actual circuitry is known as

a) Silicon Compilation

b) HDL Compilation

c) Circuit Compilation

d) None of the other options

    ⦿ a

    ○ b

    ○ c

    ○ d

Yes, the answer is correct.
Score: 1

Accepted Answers:
a

9) Loops are the major targets for optimization since   *1 point*

    a) Loop may go to infinite execution

    b) Loop body is repeated to several times

    c) Condition check takes exceedingly large time

    d) None of the other options

    ○ a

    ⦿ b

    ○ c

    ○ d

Yes, the answer is correct.

Score: 1
Accepted Answers:
b

10)   *1 point*

For maximum speed of execution of target code , temporary variables be best allocated to

a) Swap space

b) Main memory

c) CPU registers

d) None of the other options

○ a

○ b

◉ c

○ d

Yes, the answer is correct.
Score: 1
Accepted Answers:
c

11) Intermediate code helps in   *1 point*

a) Program Analysis

b) Code optimization

c) Retargeting code

d) Code check

○ a

○ b

◉ c

○ d

Yes, the answer is correct.
Score: 1
Accepted Answers:
c

12) Output file of Lex is _____ the input file is Myfile   *1 point*

a) Myfile.e
b) Myfile.yy.c
c) Myfile.lex
d) Myfile.obj

○ a

◉ b

○ c

○ d

Yes, the answer is correct.
Score: 1

Accepted Answers:
b