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NPTEL (https://swayam.gov.in/explorer?ncCode=NPTEL) » Compiler Design (course)



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Course outline

About NPTEL ()

How does an **NPTEL** online course work? ()

Week 0: ()

Week 1 ()

- O Lecture 01: Introduction (unit? unit=18&lesso n=19)
- O Lecture 02: Introduction (Contd.) (unit?

exam Week 1: Assignment 1 (https://examform.nptel.ac.in/2025_br/exam_form/dashboard.gnment 1

The due date for submitting this assignment has passed.

Due on 2025-02-05, 23:59 IST.

Assignment submitted on 2025-02-05, 22:15 IST

- 1) Which phase of compiler does NOT use symbol table?
- 1 point

- a) Code generation
- b) Syntax Analysis
- c) Lexical Analysis
- d) None of the other options
- a
- O b
- Ос
- \bigcirc d

No, the answer is incorrect. Score: 0

Accepted Answers:

unit=18&lesso n=20)	Which phase of compiler is Syntax Analysis? 1 point 2) Finet.
Lecture 03 : Introduction (Contd.) (unit? unit=18&lesso n=21)	a) First b) Second
Lecture 04 : Introduction (Contd.) (unit? unit=18&lesso n=22)	c) Third d) None of the mentioned
Lecture 05 : Introduction (Contd.) (unit? unit=18&lesso n=23)	b c d Yes, the answer is correct. Score: 1
Lecture 06 : Introduction (Contd.) (unit? unit=18&lesso n=24)	Accepted Answers: b 3) Output of the syntax analysis is called 1 point
Lecture Materials (unit? unit=18&lesso n=25)	a) Parse tree b) Keyword tree c) Binary tree d) All of the other options
Feedback Form (unit? unit=18&lesso n=26)	 a b c
Week 01 : Assignment Solution (unit? unit=18&lesso n=159)	Yes, the answer is correct. Score: 1 Accepted Answers:
Quiz: Week 1: Assignment1(assessment?name=181)	A programming language does not allow integer division operation. This is generally detected in the phase of A Lexical Analysis
Week 2 ()	b) Syntax Analysis
Week 3 ()	c) Semantic Analysis
Week 4 ()	d) None of the other options
Week 5 ()	a a

Week 6 ()	○ b	
Week 7 ()	© c	
Week 8 ()	Yes, the answer is correct. Score: 1	
Week 9 ()	Accepted Answers:	
Week 10 ()	5) Which of these is not true about Symbol Table	1 point
Week 11 ()	a) All the labels of the instructions are symbols	
Week 12 ()	b) Table has entry for symbol name address value	
DOWNLOAD VIDEOS ()	c) Perform the processing of the assembler directives	
Text Transcripts	d) Created during pass 1	
0	Оа	
Books ()	O b	
	© c○ d	
	Yes, the answer is correct.	
	Score: 1	
	Accepted Answers:	
	6) A compiler can check	1 point
	a) Logical error	
	b) Syntax error	
	c) Both logical error and syntax error	
	d) Not logical and syntax error	
	Оа	
	o b	
	С	
	O d	
	Yes, the answer is correct. Score: 1	
	Accepted Answers:	
	b	

7) Error recovery helps to	1 point
a) Report multiple errors	
b) Rectify multiple errors	
c) Both report and rectify multiple errors	
d) None of the other options	
a	
○ b	
С	
O d	
Yes, the answer is correct. Score: 1	
Accepted Answers:	
a	
8)	1 point
Converting a hardware description into actual circuitry is known as	
a) Silicon Compilation	
b) HDL Compilation	
c) Circuit Compilation	
d) None of the other options	
a	
○ b	
С	
O d	
Yes, the answer is correct. Score: 1	
Accepted Answers:	
а	
9) Loops are the major targets for optimization since	1 point
a) Loop may go to infinite execution	
b) Loop body is repeated to several times	
c) Condition check takes exceedingly large time	
d) None of the other options	
Оа	
b	
○ c	
O d	
Yes, the answer is correct.	

Score: 1 Accepted Answers:
ь
10) 1 point
For maximum speed of execution of target code , temporary variables be best allocated to
a) Swap space
b) Main memory
c) CPU registers
d) None of the other options
Оа
○ b
○ d
Yes, the answer is correct. Score: 1
Accepted Answers:
С
11) Intermediate code helps in 1 point
a) Program Analysis
b) Code optimization
c) Retargeting code
d) Code check
Оа
○ b
© c
O d
Yes, the answer is correct. Score: 1
Accepted Answers:
C
12) Output file of Lex is the input file is Myfile 1 point
a) Myfile.e
b) Myfile.yy.c
c) Myfile.lex d) Myfile.obj
uj Piylile.ooj
Оа

\bigcirc c	С		
\bigcirc c	d		
Yes, t Score	the answer is correct. e: 1		
Accep	pted Answers:		
b			