

Concatenate codes, save qubits

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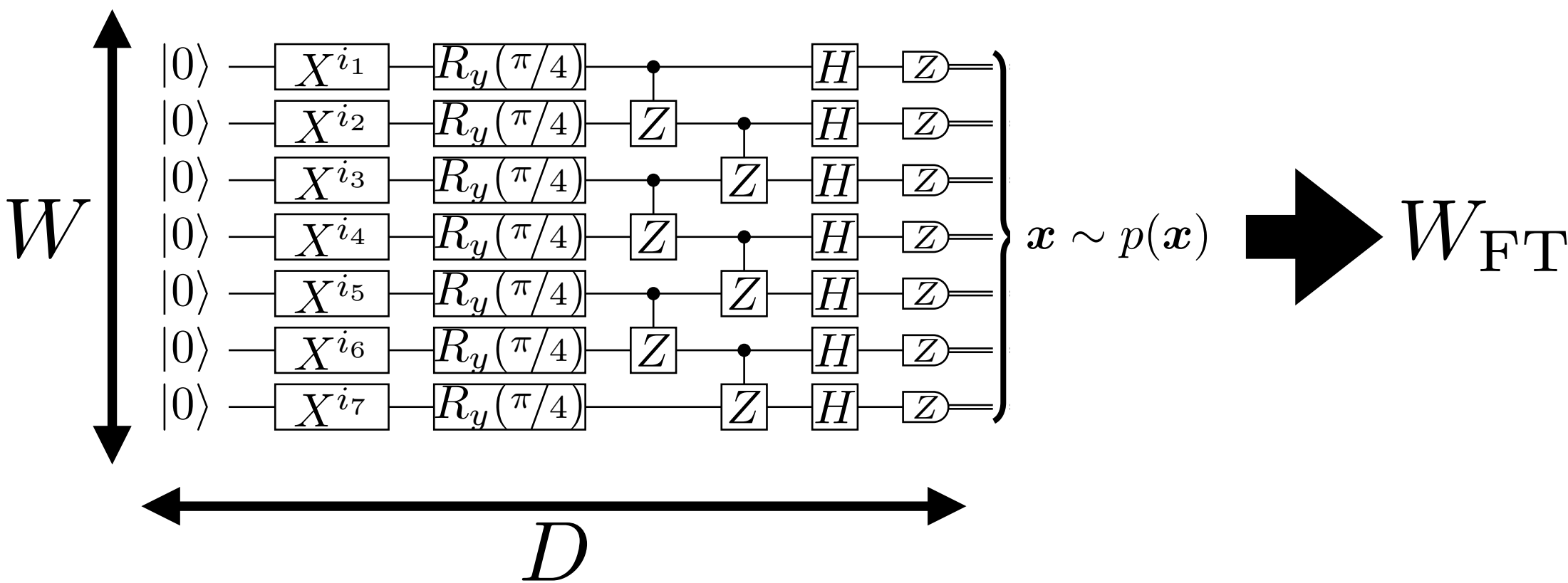
Based on [arXiv:2402.09606](https://arxiv.org/abs/2402.09606)

TQC2024, 13th September

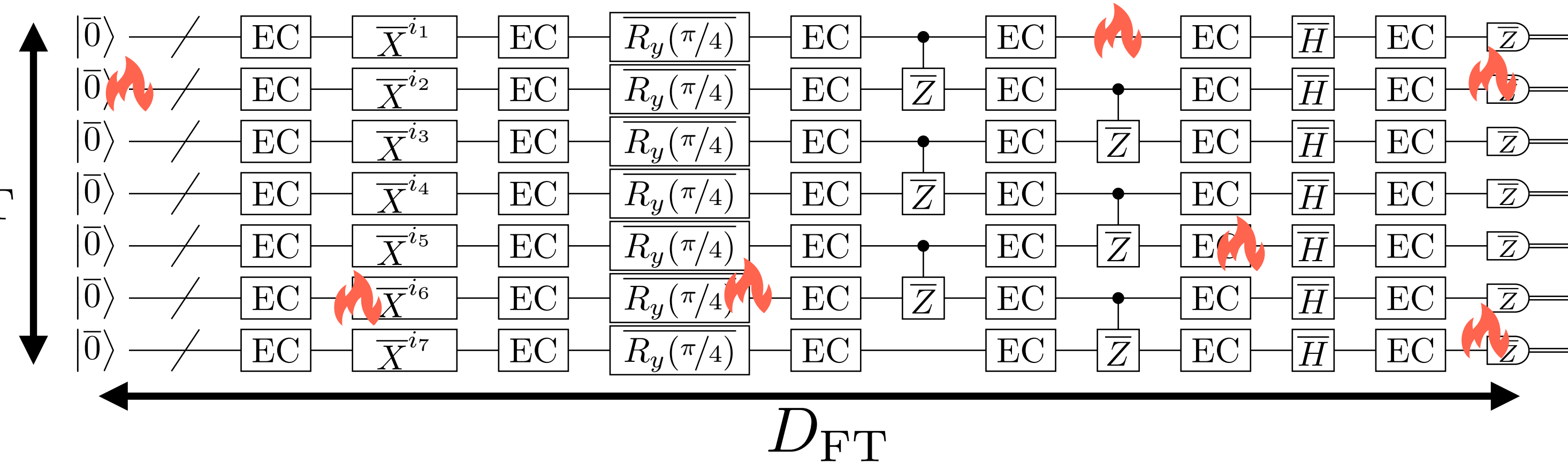


Fault-Tolerant Quantum Computation (FTQC)

Original circuit



Noisy circuit with **physical error rate** $p_0 > 0$



Task: Given ϵ and an original circuit, perform **the noisy circuit** to output $x \sim \tilde{p}(x) \approx_{\epsilon} p(x)$ (the fault-tolerant circuit)

→ Use **quantum error-correcting codes** that can suppress **logical error rate** arbitrarily

$$p_0 < p_{\text{th}} \Rightarrow p_L \lesssim \frac{\epsilon}{WD} \text{ for achieving the overall error } O(\epsilon)$$

Below threshold

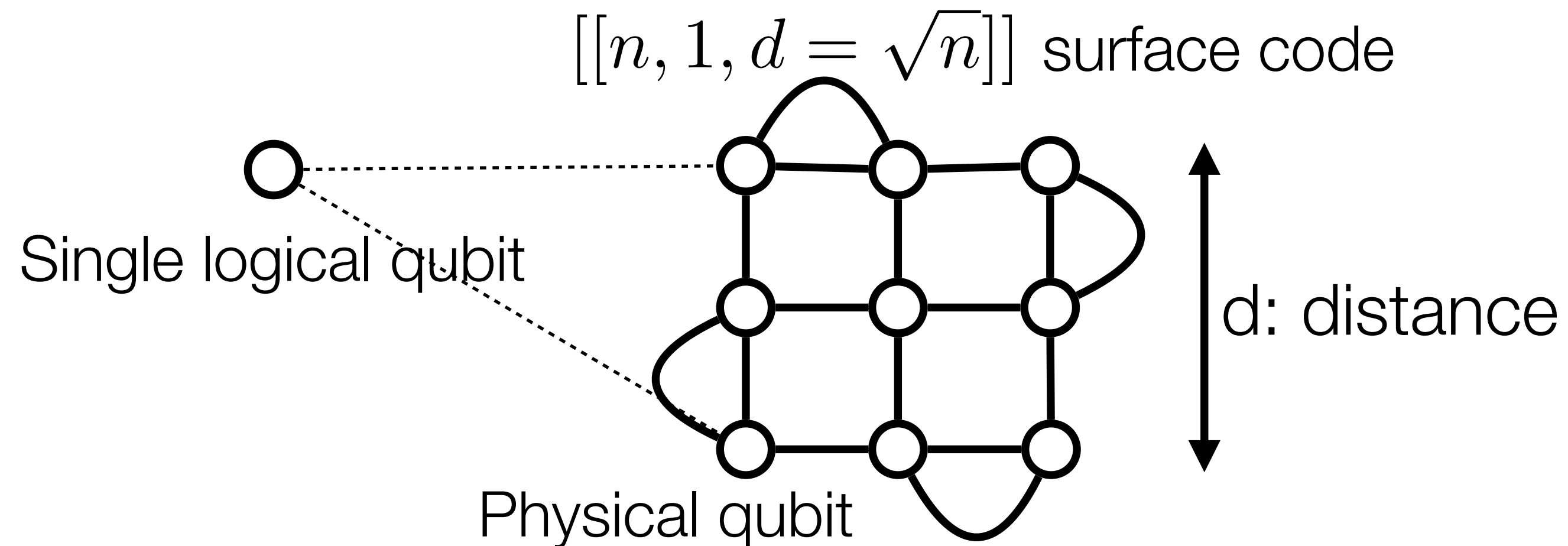
Width \times Depth = Circuit size

$$\text{Space overhead} := \frac{W_{\text{FT}}}{W} \quad \text{Time overhead} := \frac{D_{\text{FT}}}{D}$$

Obstacle: Overheads of FTQC

Two conventional approaches for FTQC to achieve $p_0 < p_{\text{th}} \Rightarrow p_L \lesssim \frac{\epsilon}{WD}$ $[[n, k, d]]$ n #physical qubits
 k #logical qubits
 d distance

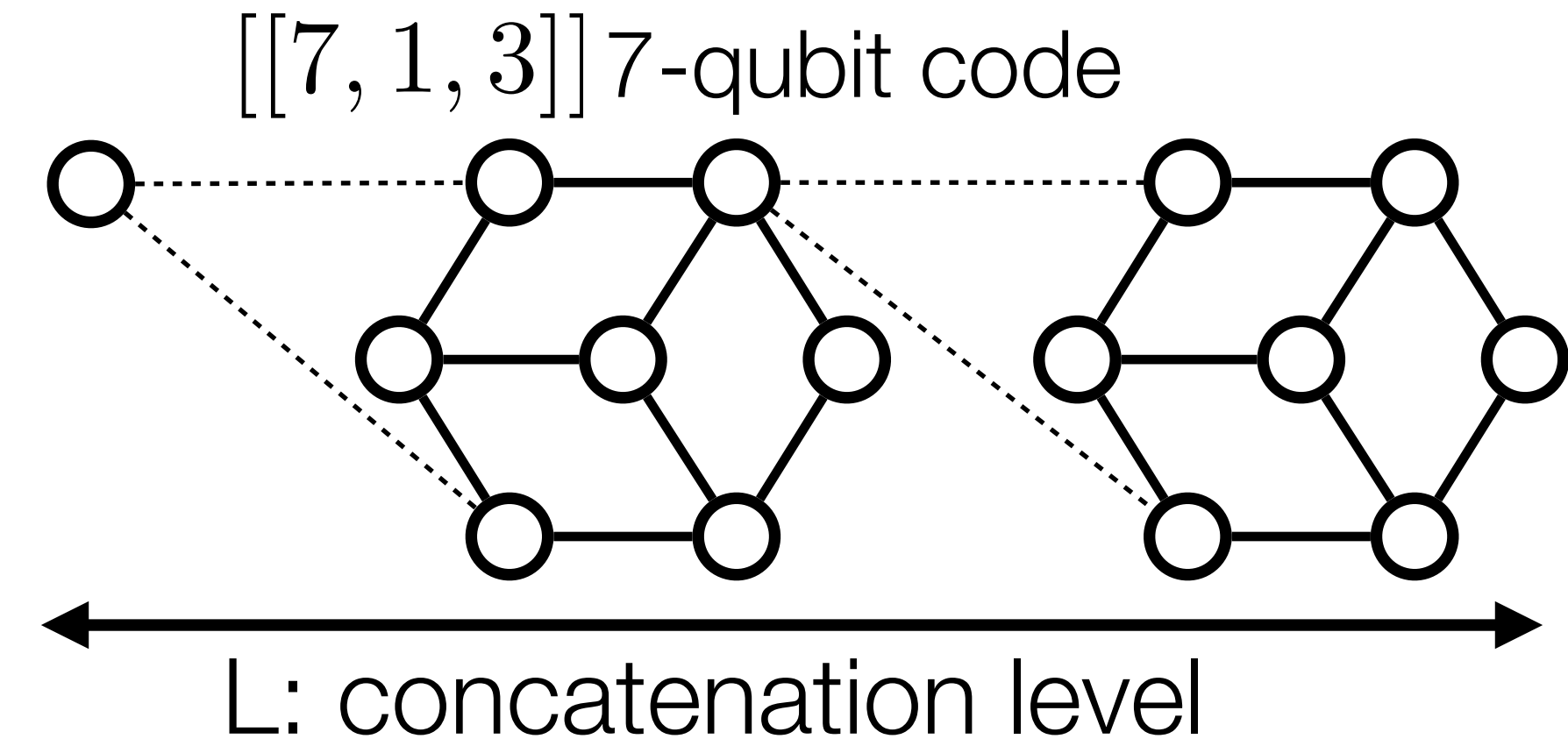
1: Quantum low-density parity-check (LDPC) code



Increase **distance d**

$$n = d^2, p_L \lesssim \left(\frac{p_0}{p_{\text{th}}}\right)^d$$

2: Concatenated code



Increase **concatenation level L**

$$n = 7^L, p_L \lesssim \left(\frac{p_0}{p_{\text{th}}}\right)^{2^L}$$

Obstacle in realizing FTQC: Polylog overhead \rightarrow diverging to infinity on large scales

Space $\frac{W_{\text{FT}}}{W} \approx \text{polylog}\left(\frac{WD}{\epsilon}\right)$

Time $\frac{D_{\text{FT}}}{D} \approx \text{polylog}\left(\frac{WD}{\epsilon}\right)$

Solution: Constant-overhead FTQC

Constant-overhead protocols

1: Quantum LDPC code

$[[n, k = \Theta(n), d = \Theta(n^\alpha)]]$
+ efficient, robust decoder

✓ Constant space overhead ✗ Large time overhead

$$\longrightarrow \frac{W_{\text{FT}}}{W} = O(1)$$

$$\frac{D_{\text{FT}}}{D} = \text{poly} \left(\frac{WD}{\epsilon} \right)$$



✓ Short time overhead

$$\frac{D_{\text{FT}}}{D} = \text{polylog} \left(\frac{WD}{\epsilon} \right)$$

Kovalev and Pryadko, PRA 87, 020304 (2013), Gottesman, Quantum Info. Comput. 14, 1338–1372 (2014).

Fawzi, Grospellier, and Leverrier, FOCS2018, Krishna and Poulin, PRX 11, 011023 (2021),

Cohen et al. Sci. Adv. 8, eabn1717 (2022), Tremblay, Delfosse, and Beverland, PRL 129, 050504 (2022).

Tamiya, Yamasaki, Koashi, AQIS 2024 (2024); Tamiya, PhD thesis (2024).

2: Concatenated quantum Hamming code

Concatenation of

$$[[N_r = 2^r - 1, N_r = 2^r - 2r - 1, 3]]$$

$$\frac{N}{K} = \prod_{l=1}^{\infty} \frac{N_{r_l}}{K_{r_l}} \rightarrow O(1)$$

✓ Constant space overhead ✓ Short time overhead

$$\longrightarrow \frac{W_{\text{FT}}}{W} = O(1)$$

$$\frac{D_{\text{FT}}}{D} = \exp(O(\text{polylog}(\log(WD/\epsilon))))$$

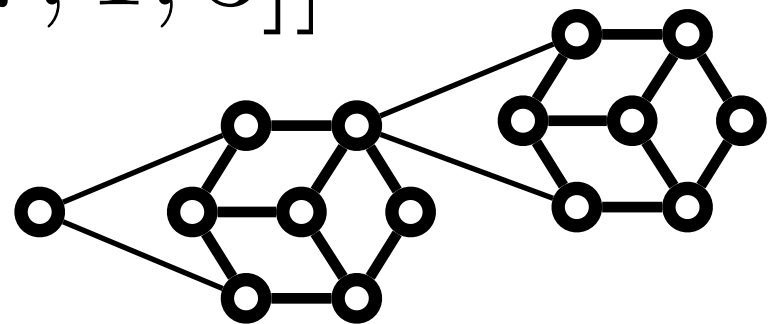
Yamasaki and Koashi, Nature Physics 20, 247 (2024).

Concatenated Hamming Codes

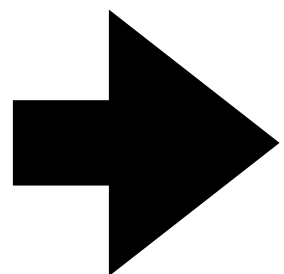
H. Yamasaki and M. Koashi, Nature Physics 20, 247 (2024).

Steane's 7-qubit code

$[[7, 1, 3]]$



Generalization

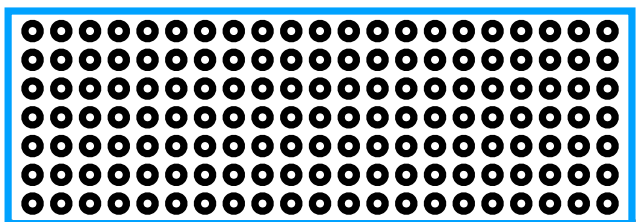


Quantum Hamming codes

$[[2^r - 1, 2^r - 2r - 1, 3]]$ Many logical qubits

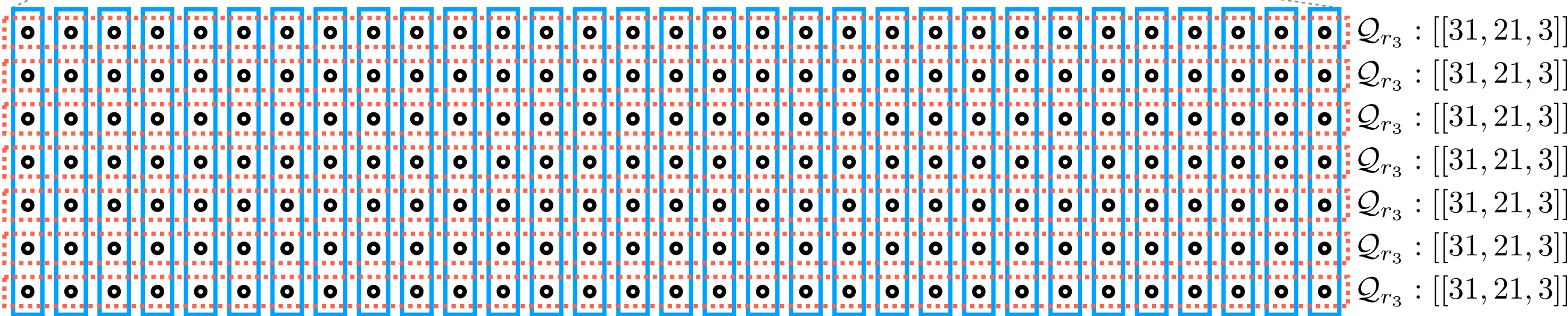
$[[7, 1, 3]], [[15, 7, 3]], [[31, 21, 3]], [[63, 51, 3]], \dots$

Level-3 register
(Logical qubits)

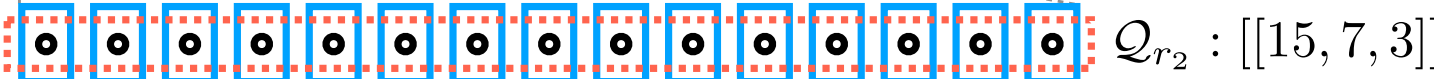


Encode

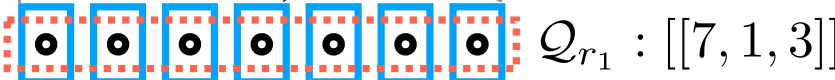
Level-2 registers



Level-1 registers

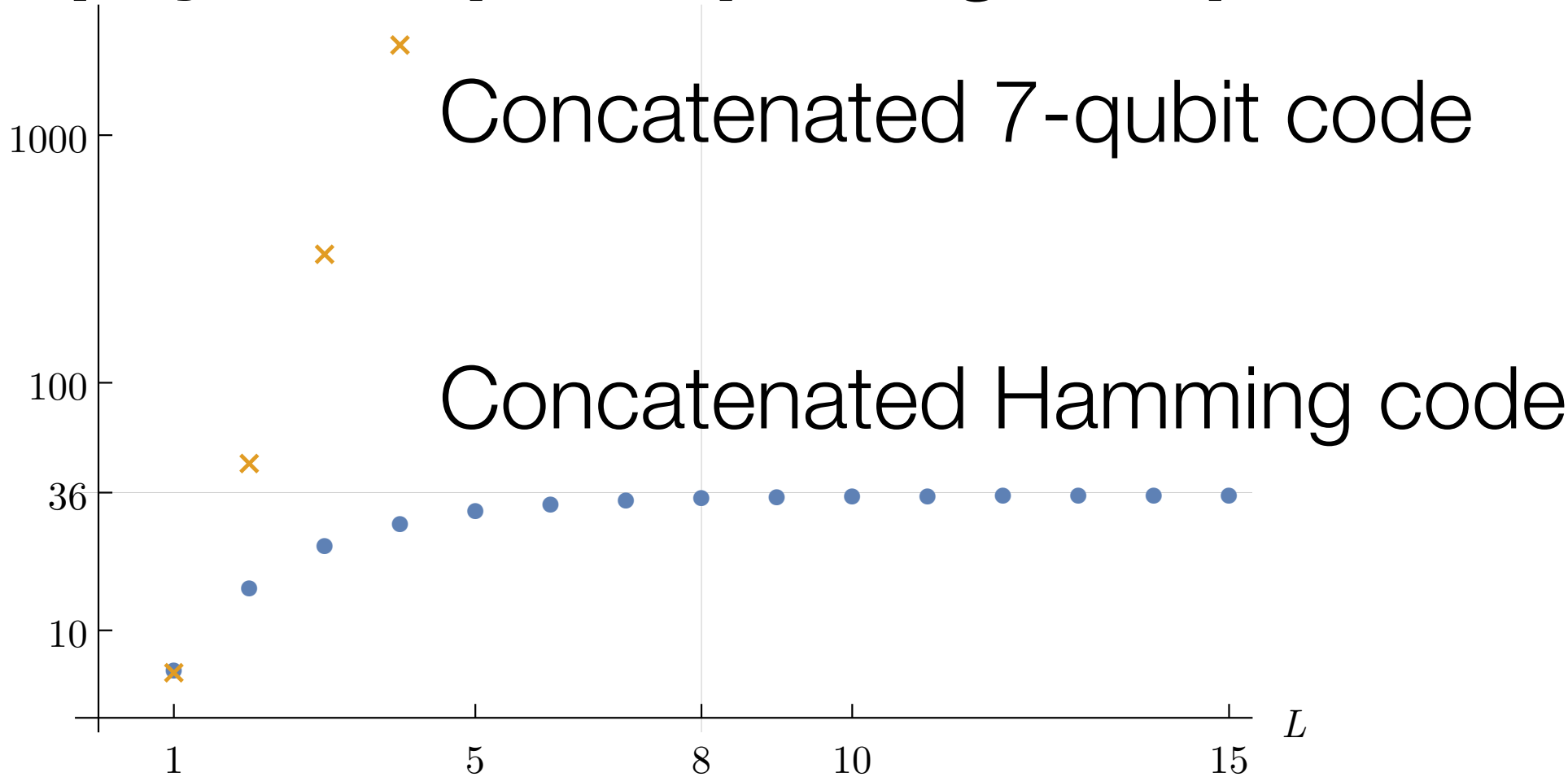


Level-0 registers
(Physical qubits)



Idea: Use higher-rate codes
at higher concatenation levels

#physical qubits per logical qubit



Concatenating quantum Hamming codes at growing rate yields a **non-vanishing overall rate**

Practical requirements for FTQC

Concatenated Q Hamming code

This work

- Low space overhead
- High threshold
- Modularity
- Quantitative comparison



This work

- **High threshold & low space overhead** protocol
- Quantitative comparison with **surface code**
 - **Reduction of space overhead** at the practical regime

Outline of this talk

- **Backgrounds**
- Main results
- Discussions

Underlying code to improve threshold

Concatenated Hamming code

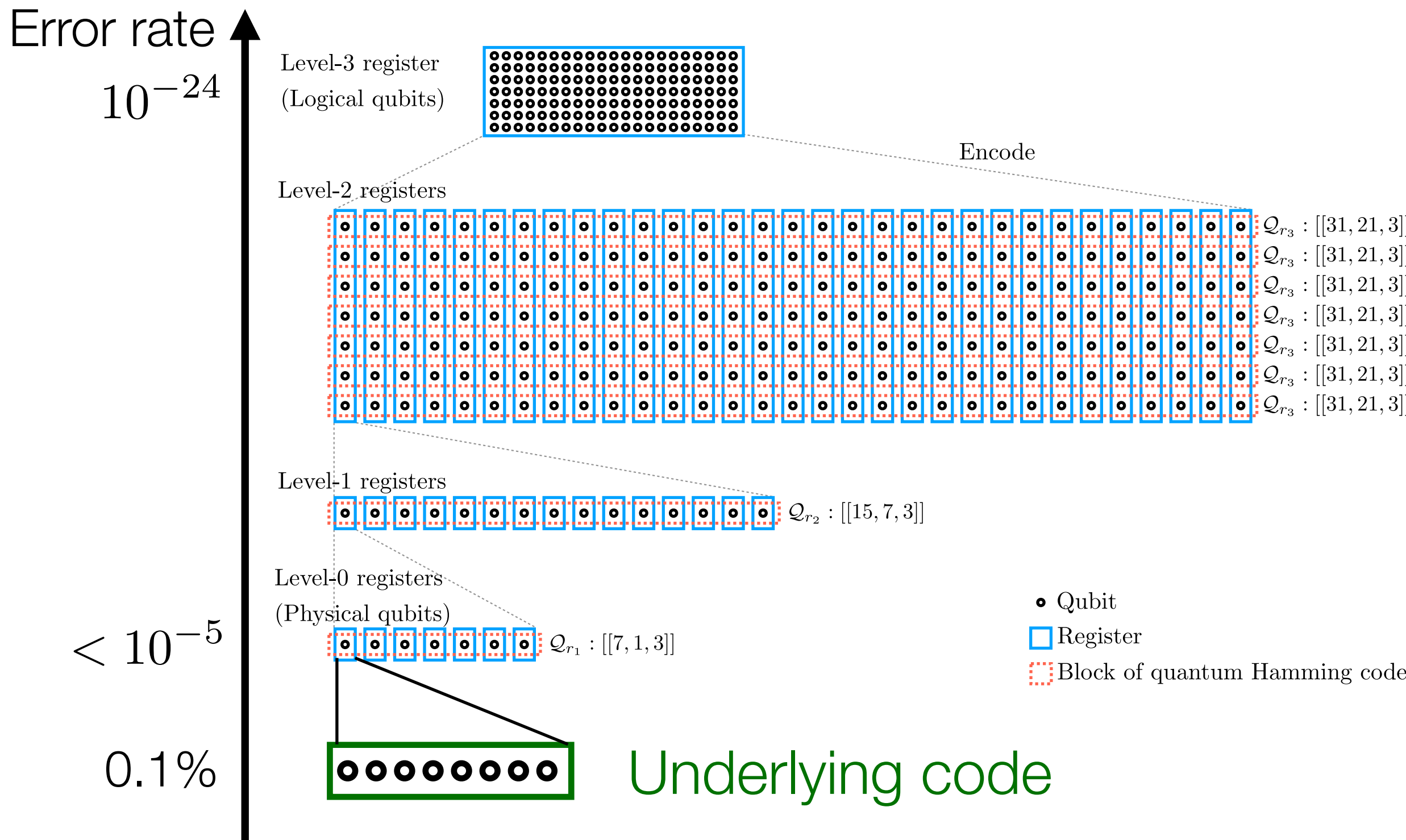
✓ Small space overhead: $\frac{W_{\text{FT}}}{W} = O(1)$

✗ Low threshold: $\sim 10^{-5}$

Surface code

✗ Large space overhead: $\frac{W_{\text{FT}}}{W} \approx \text{polylog}\left(\frac{WD}{\epsilon}\right)$

✓ High threshold: $\sim 10^{-2}$



Error suppression

Underlying code:

$$0.1\% \rightarrow 10^{-5}$$

Concatenated Hamming code:

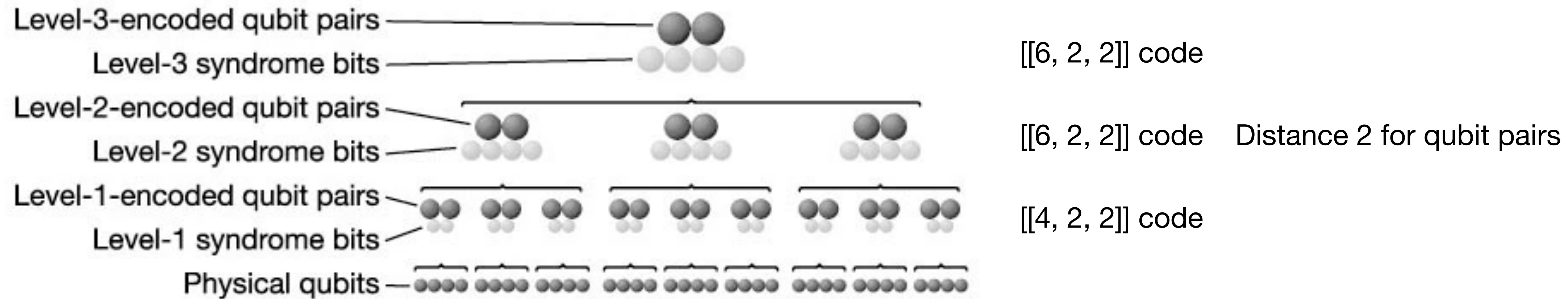
$$10^{-5} \rightarrow 10^{-24}$$

Put high threshold code on the bottom layer **to improve the threshold**

High threshold code: C4/C6 code

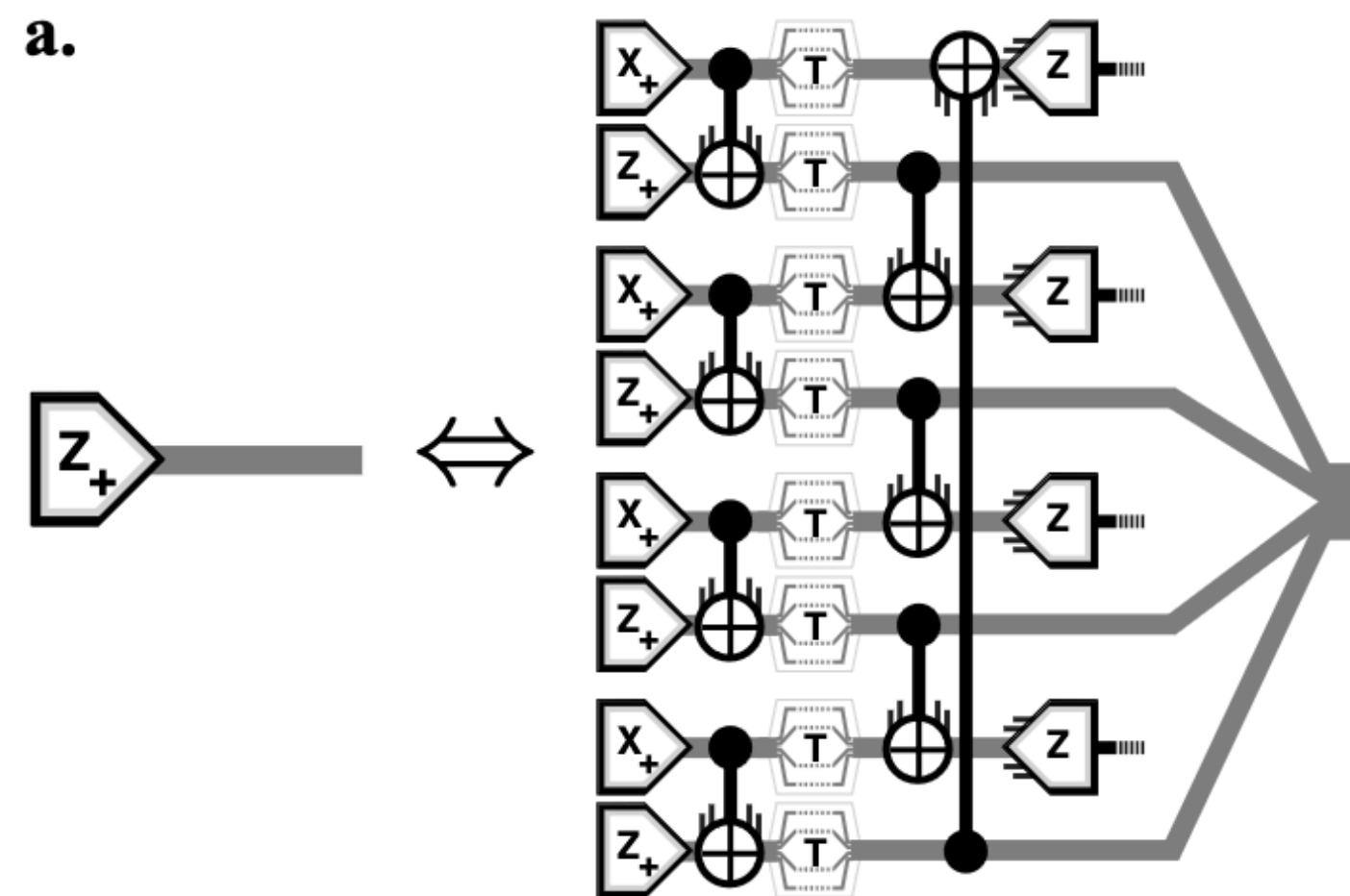
E. Knill, Nature 434, 39-44 (2005).

Merit: Threshold for protocol based on C4/C6 code : 2.4% vs Threshold for surface code: 0.3%



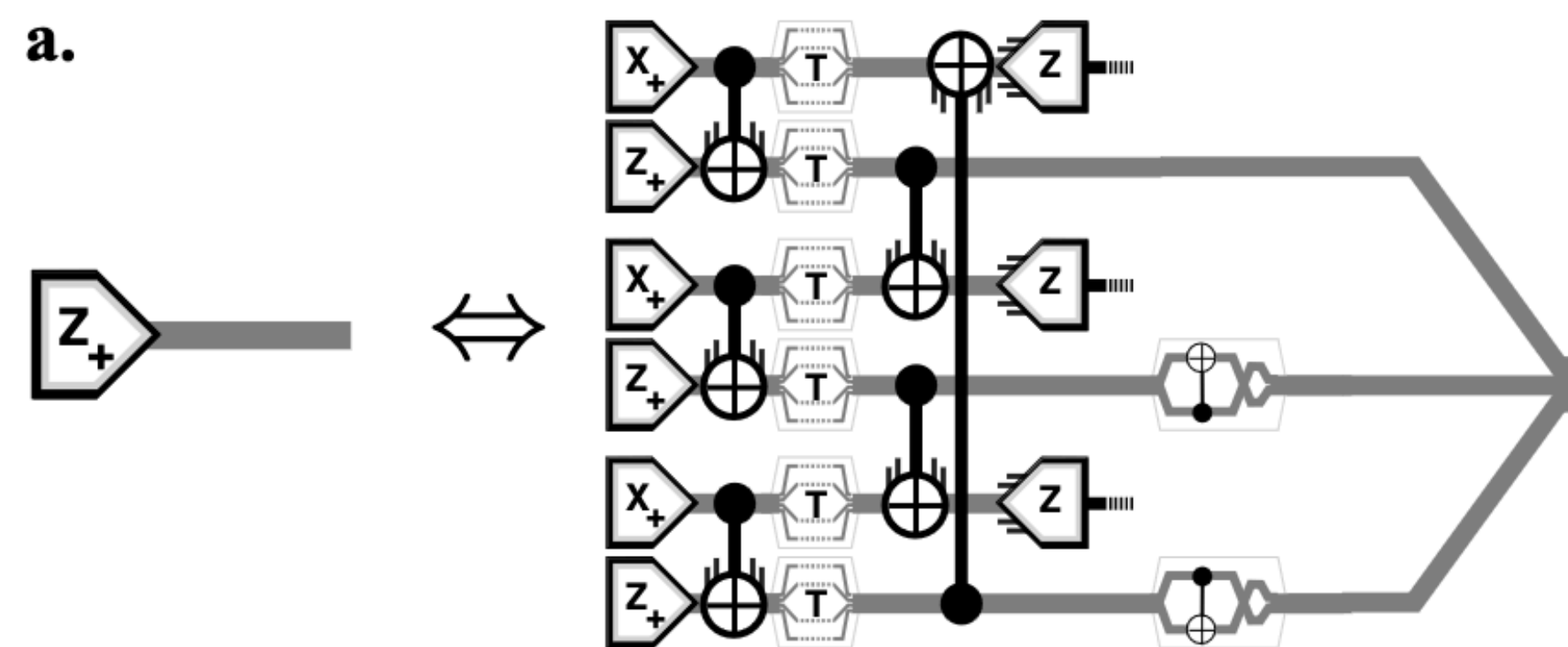
Protocol

8 qubits in 1D periodic arrangement

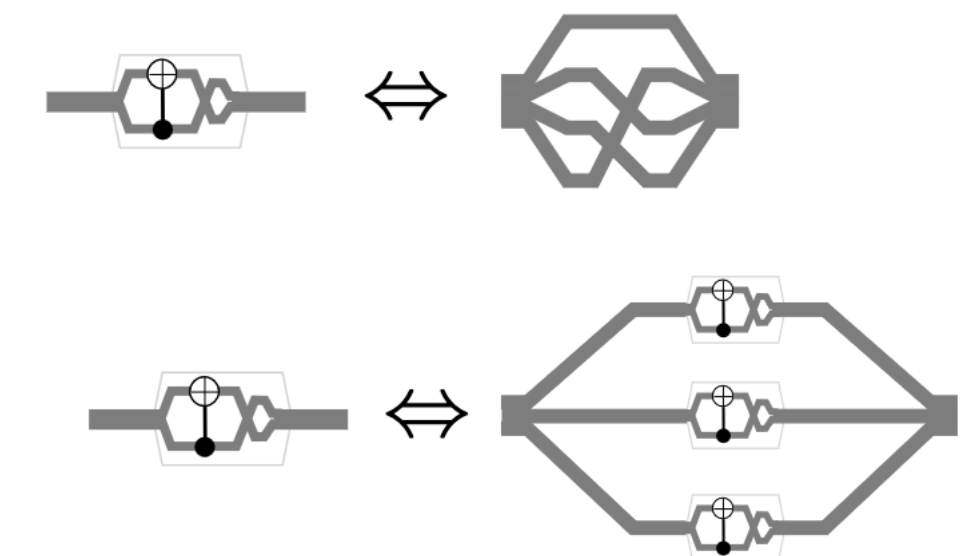


Preparation of $[[4, 2, 2]]$ code

6 level-1 registers in 1D periodic arrangement



Preparation of $[[6, 2, 2]]$ code



Outline of this talk

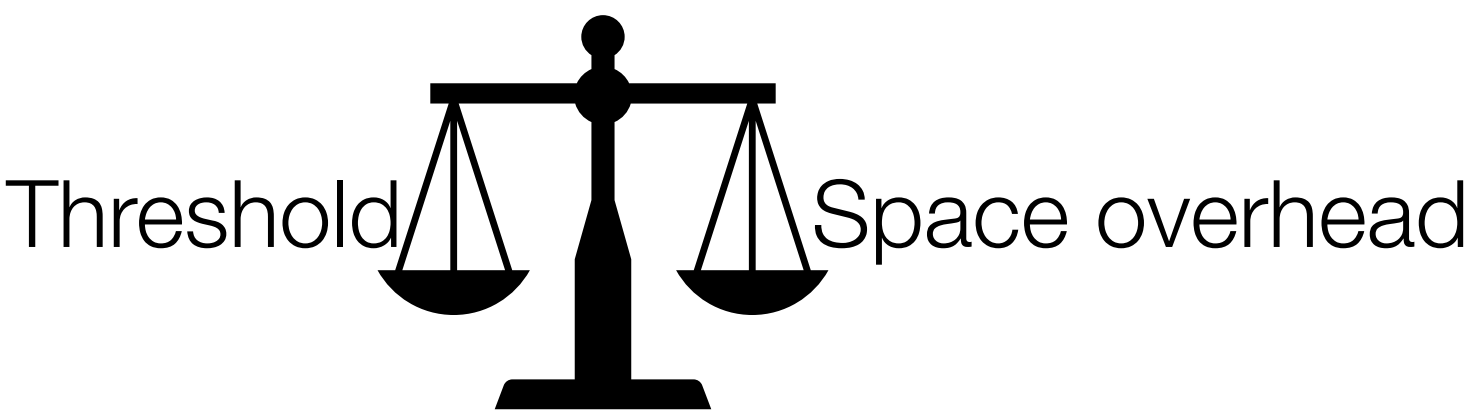
- Backgrounds
- **Main results**
- Discussions

Code construction

Guiding principle

Underlying code: High threshold (but many qubits)

Higher-level code: Few qubits (but low threshold)



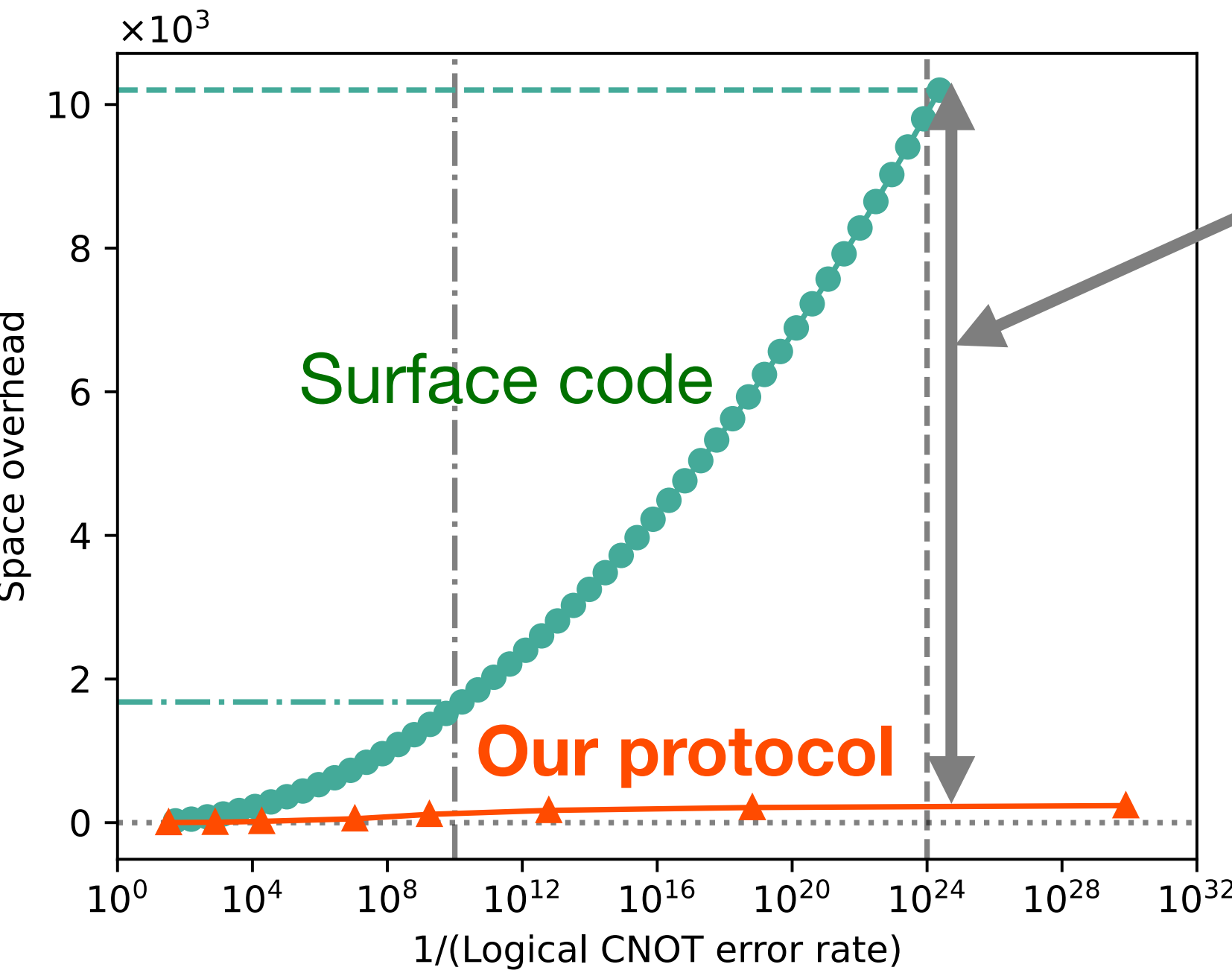
Optimization

- Underlying code → C4/C6 code
- Higher-level code → Concatenation of Q Hamming codes

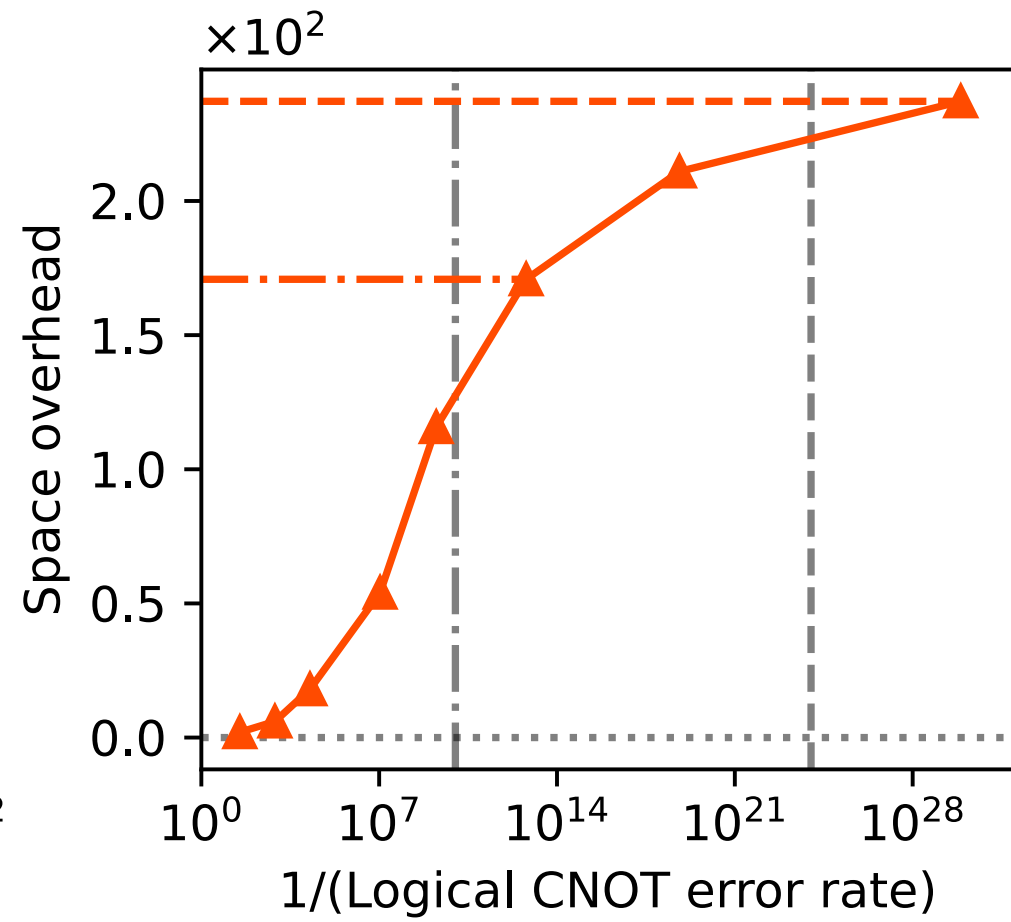
	Quantum code	N	K	N/K	
level-1	$C_4(= [[4, 2, 2]])$	4	2	2	C4/C6 code
level-2	$C_6(= [[6, 2, 2]])$	12	2	6	
level-3	$C_6(= [[6, 2, 2]])$	36	2	18	
level-4	$C_6(= [[6, 2, 2]])$	108	2	54	
level-5	$Q_4(= [[15, 7, 3]])$	1.6×10^3	14	1.2×10^2	Q Hamming codes
level-6	$Q_5(= [[31, 21, 3]])$	5.0×10^4	2.9×10^2	1.7×10^2	
level-7	$Q_6(= [[63, 51, 3]])$	3.2×10^6	1.5×10^4	2.1×10^2	
level-8	$Q_7(= [[127, 113, 3]])$	4.0×10^8	1.7×10^6	2.4×10^2	

Main result: Comparison of our protocol with surface code

Space overhead: physical error rate = 0.1 %



Saving of space overhead



Threshold:

	Threshold
C_4/C_6 code	2.4%
Surface code	0.31%
Steane code	0.030%
$C_4/\text{Steane code}$	0.15%

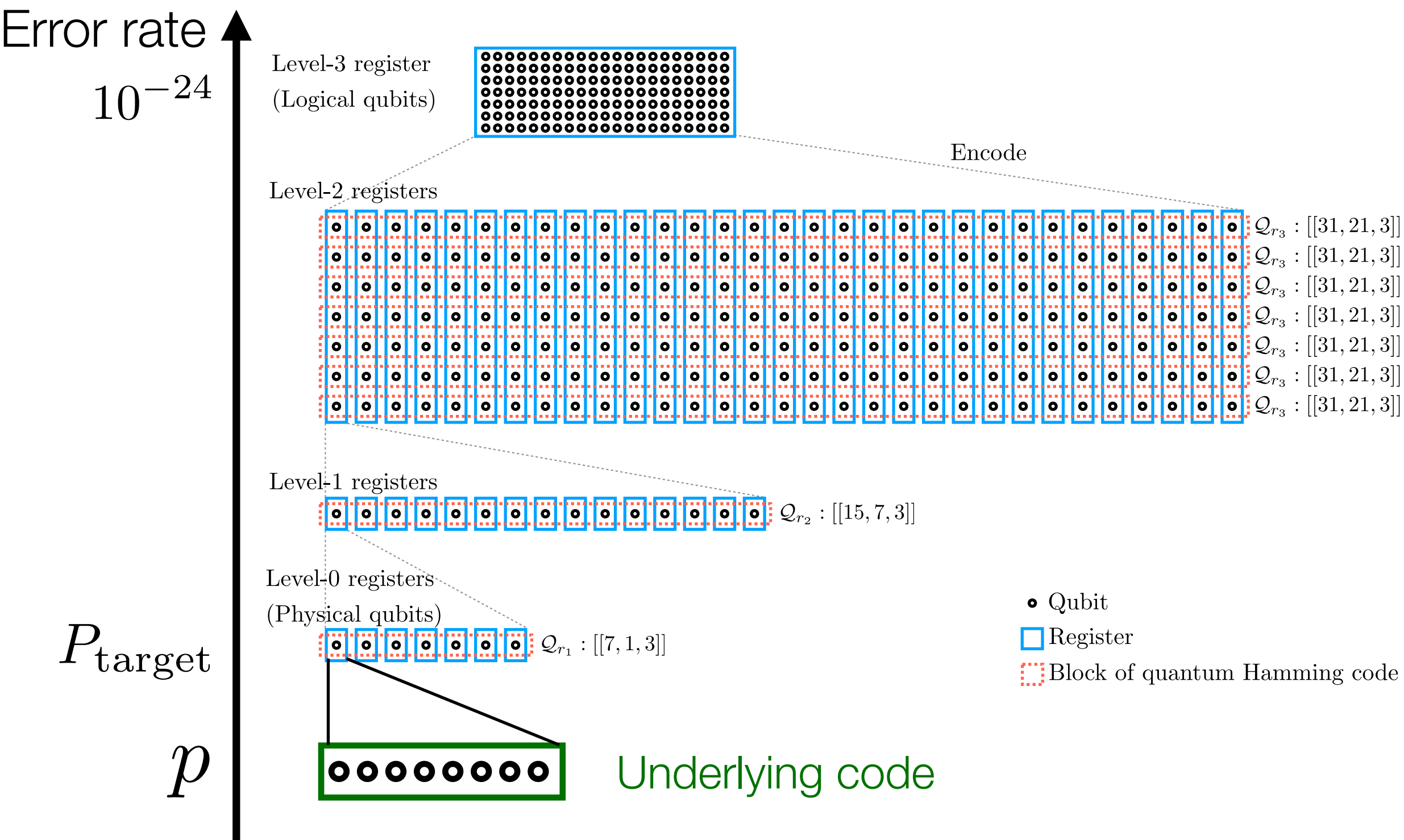
- Circuit-level error model
- Logical CNOT error rate

Space overhead = $\#(\text{Logical qubits})/\#(\text{Physical qubits})$ per code block

Higher threshold, lower space overhead than surface code

Remark: Requires all-to-all connectivity → Suitable for neutral atoms, ion traps, and optics

Optimization over underlying code



H. Yamasaki and M. Koashi, Nature Physics 20, 247 (2024).

Space overhead to achieve P_{target}

	Threshold	Space overhead		
		$p = 0.01\%$	$p = 0.1\%$	$p = 1\%$
C_4/C_6 code	2.4%	18	54	1458
Surface code	0.31%	121	841	-
Steane code	0.030%	343	-	-
C_4 /Steane code	0.15%	14	4802	-

Change underlying code to **optimize space overhead for each physical error rate**

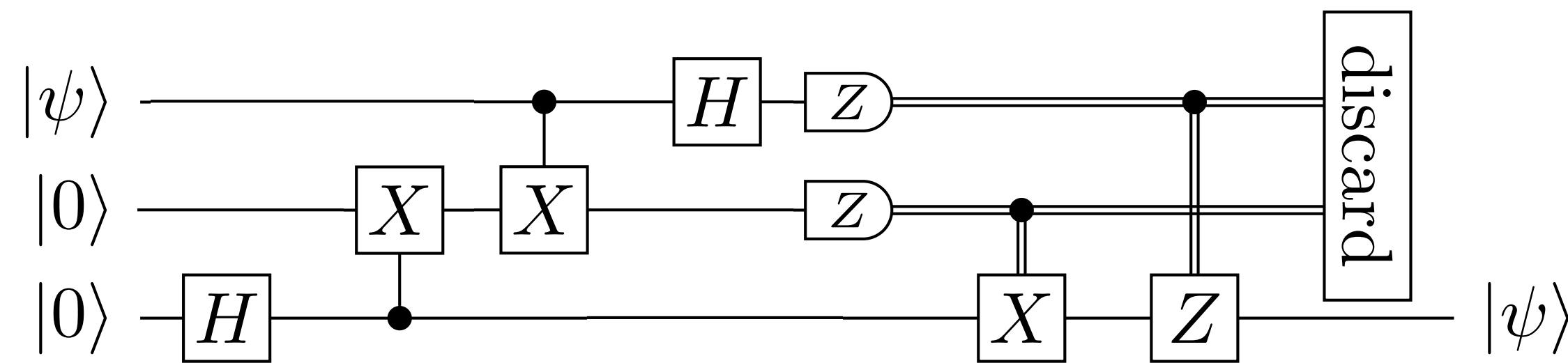
Details of the protocol

H. Yamasaki and M. Koashi, Nature Physics 20, 247 (2024).

Fault-tolerant gate operation = gate gadget + error correction gadget

Knill's EC gadget: based on teleportation

Conventional: Parallel use of auxiliary qubit

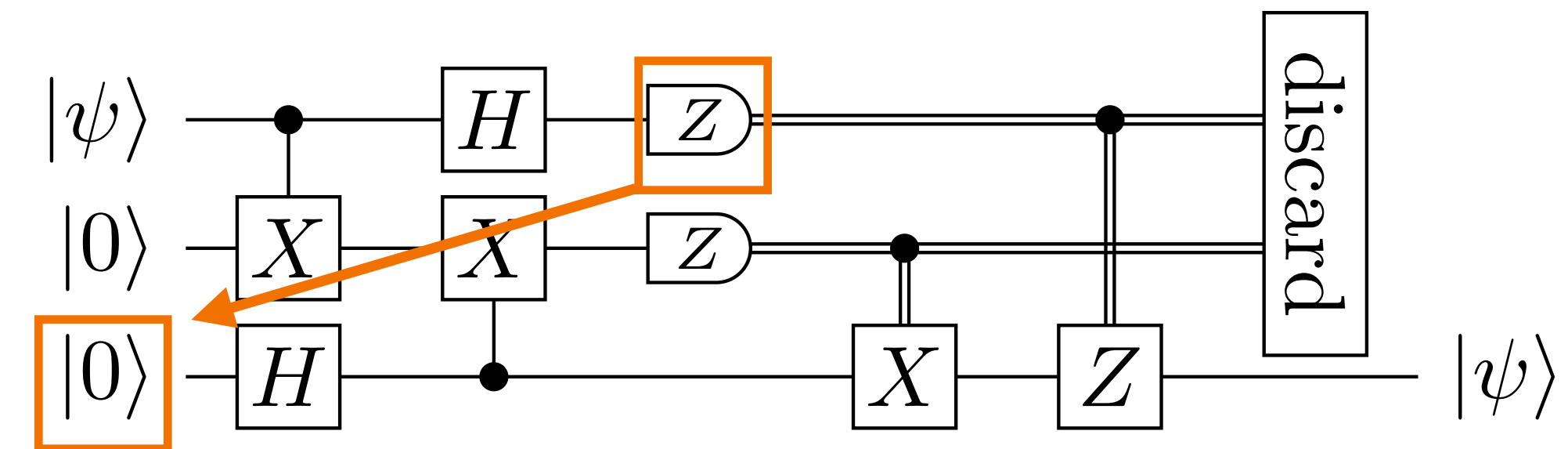


✗ Large space overhead

✓ Less errors

→ Underlying codes

Modified: Sequential use of auxiliary qubit



✓ Small space overhead

✗ More errors

→ Q Hamming codes

Parallel use of auxiliary qubit at underlying code → **High threshold**

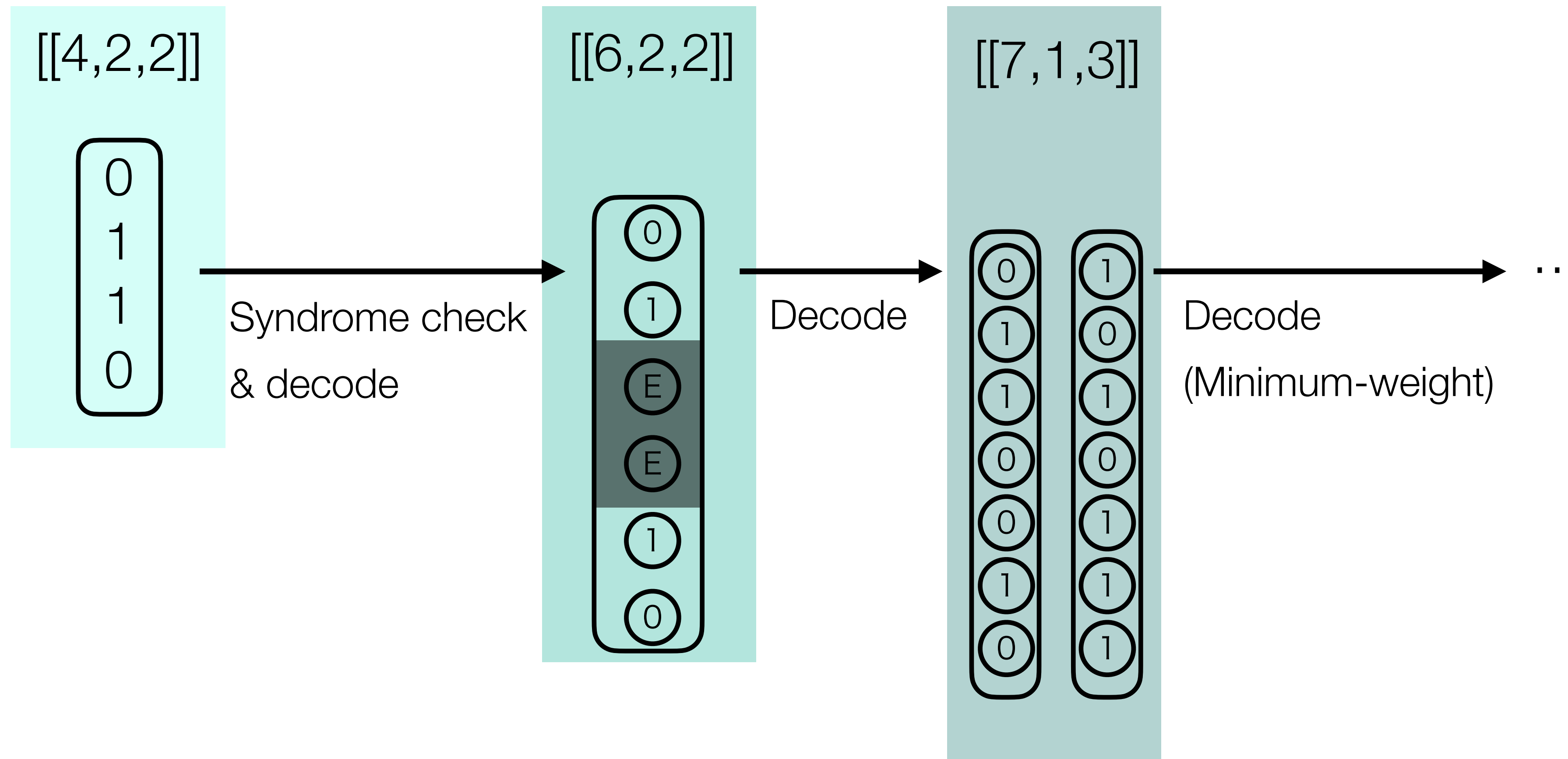
Sequential use at higher-level codes → **Small space overhead**

Similar strategy for initial state preparation

Details of the protocol

H. Goto and H. Uchikawa, Sci. Rep. 3, 2044 (2013).
H. Yamasaki and M. Koashi, Nature Physics 20, 247 (2024).

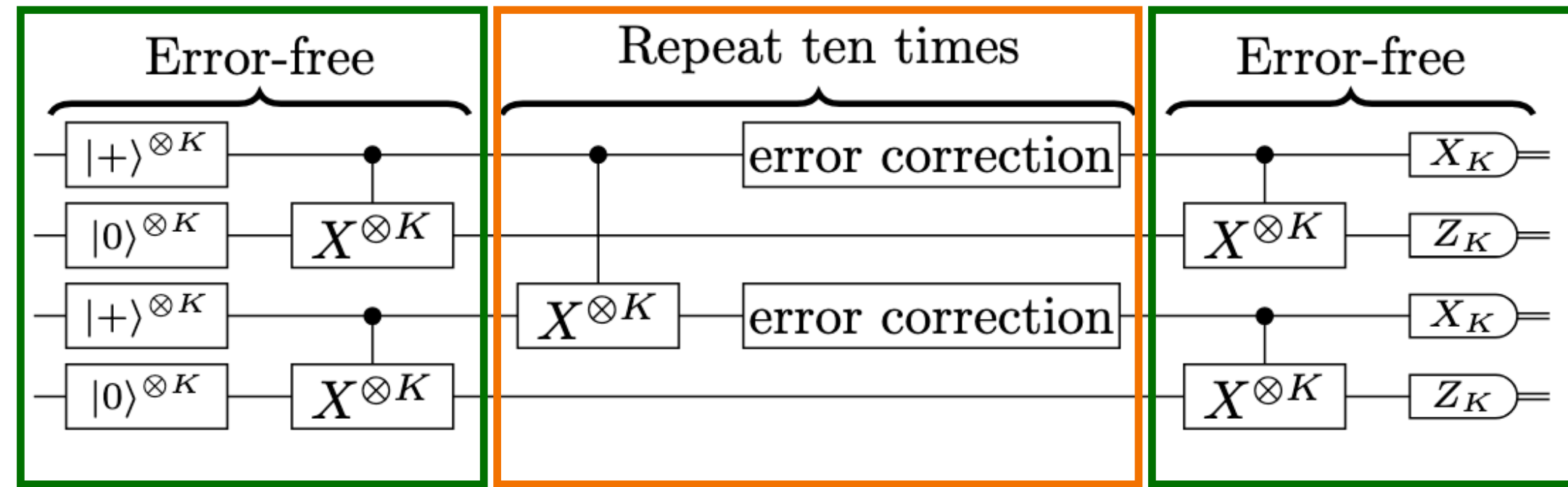
Hard-decision decoder



Details of the protocol

H. Goto, Sci. Rep. 6, 19578 (2016).
D. Horsman et al. NJP 14, 123011 (2012).
C. Vuillot et al. NJP 21, 033028 (2019).
C. Gidney, Quantum 7, 786 (2022).

Numerical simulation



Logical Bell pair

Noisy CNOT + EC

Logical Bell
measurement

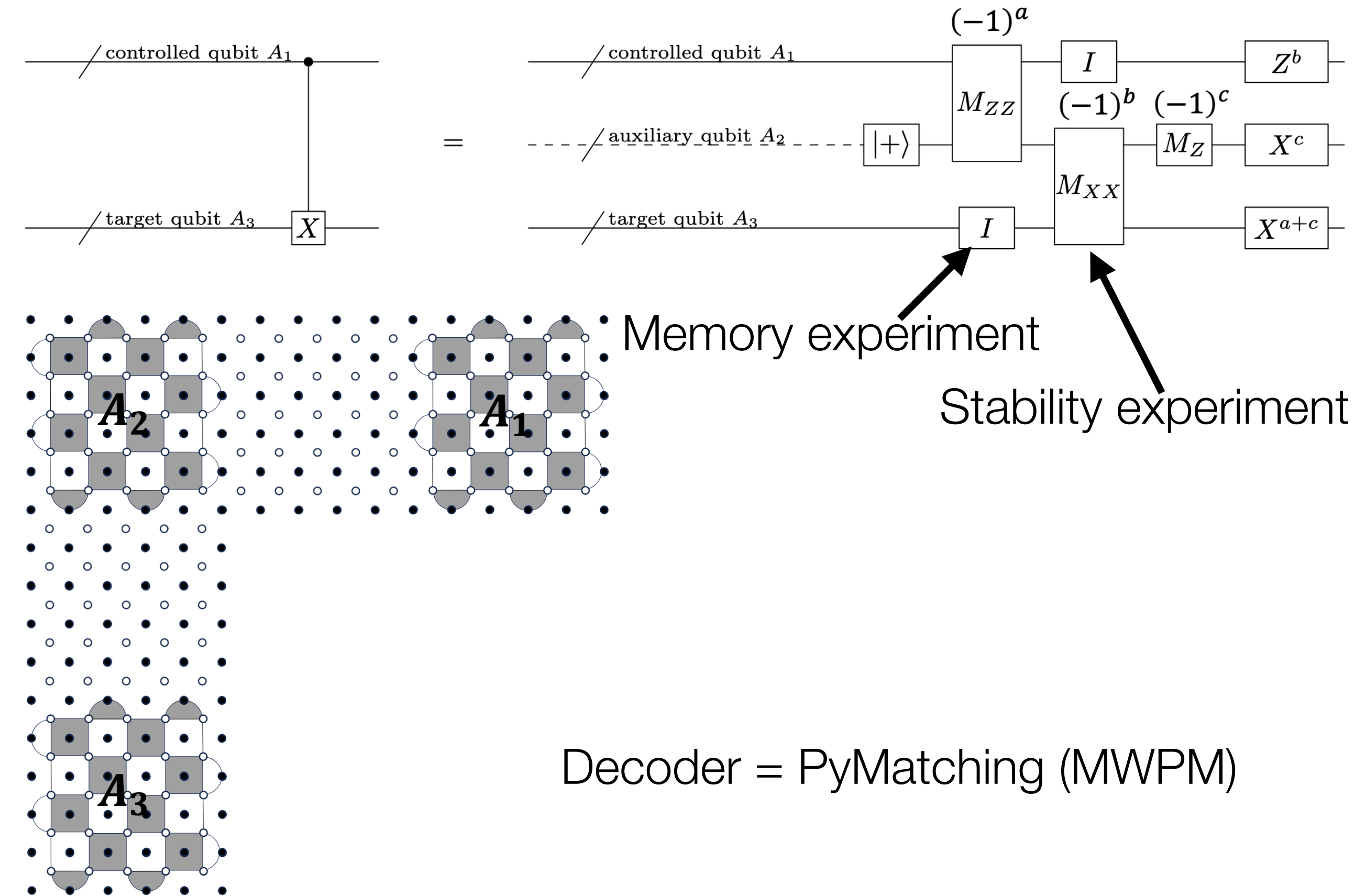
Transversal CNOT
+ Knill's EC gadget

C4/C6, Hamming

Lattice surgery

Surface code

Logical CNOT for surface code



Quantitative comparison with the surface code at **the same condition**

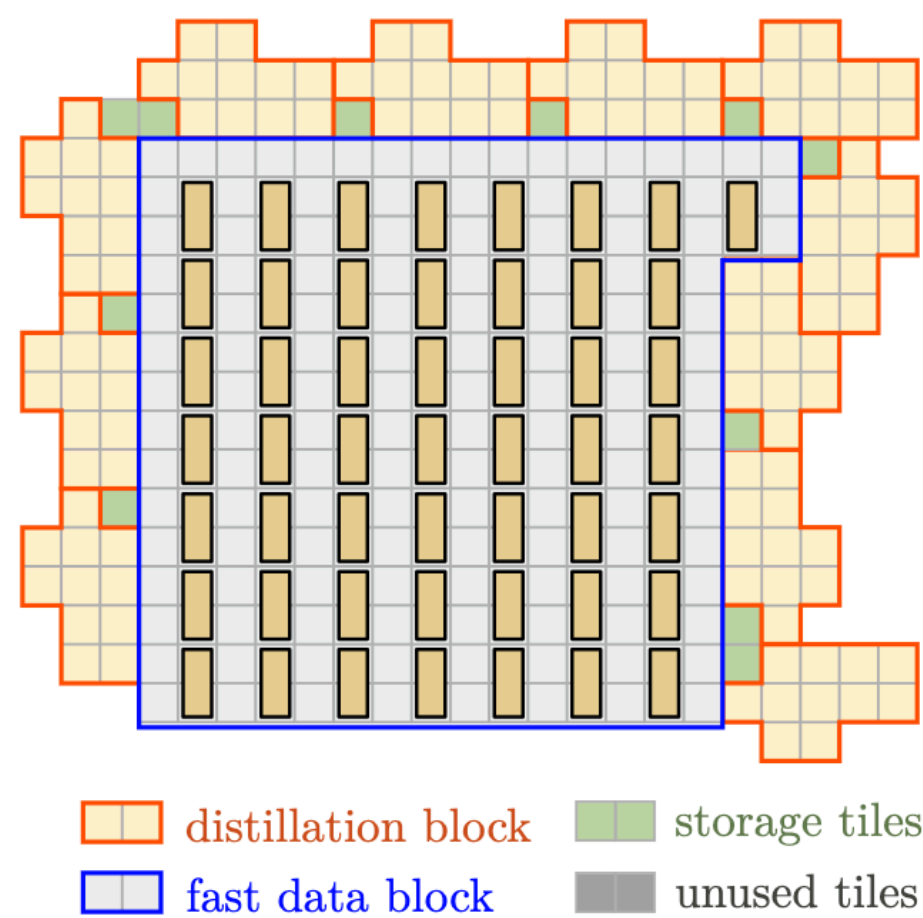
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Comparison with QLDPC codes

Modularity

QLDPC

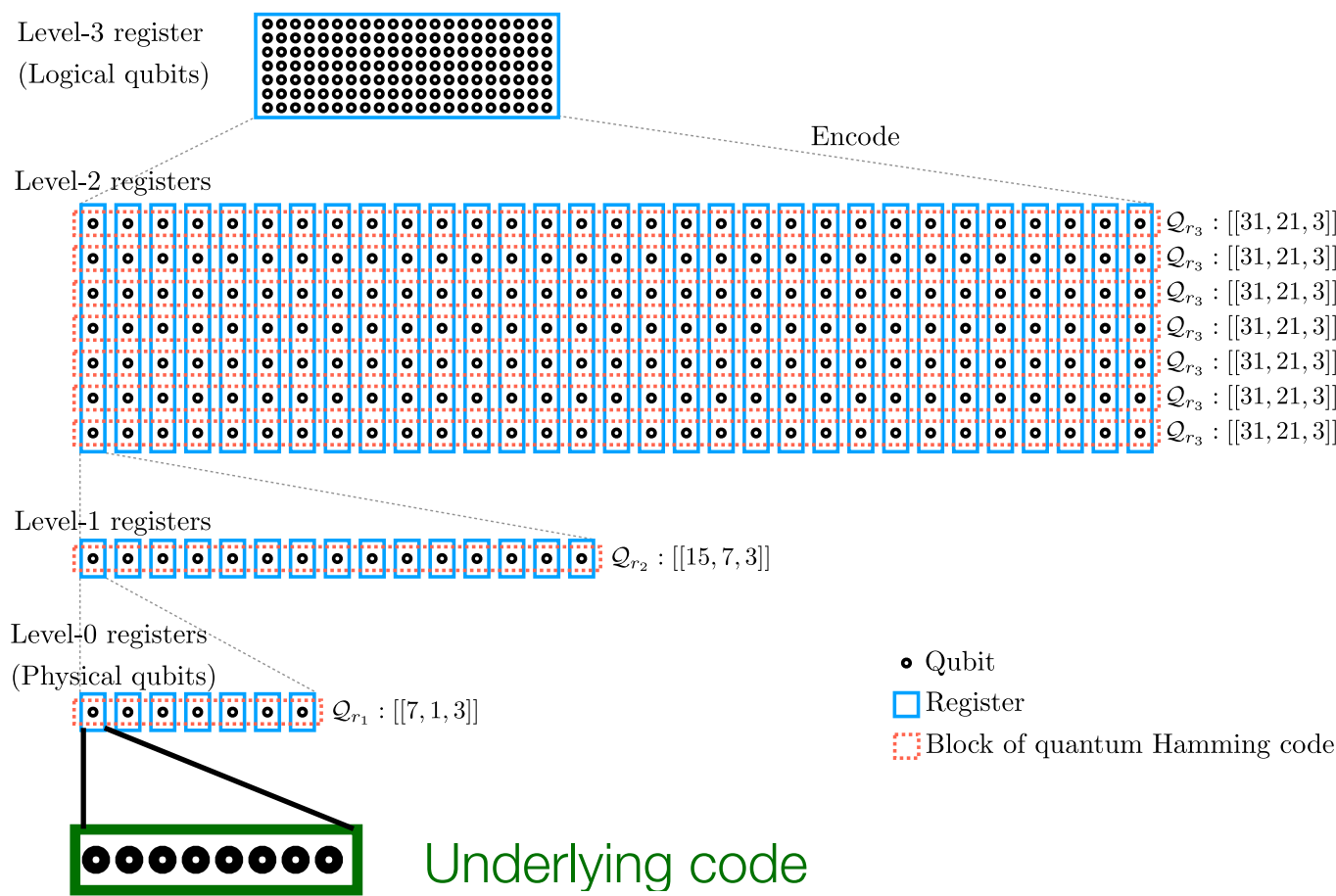


distillation block storage tiles
fast data block unused tiles

Litinski, arXiv:1808.02892

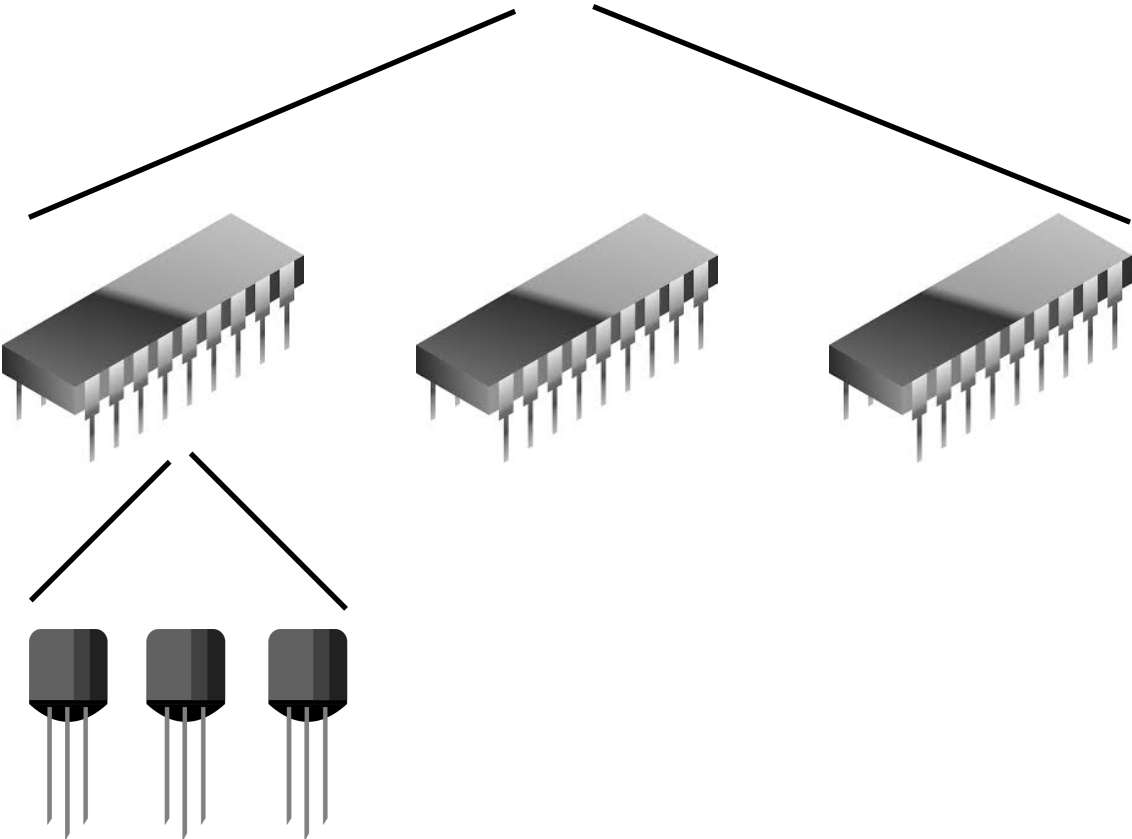
✗ Large code at once

Concatenated code



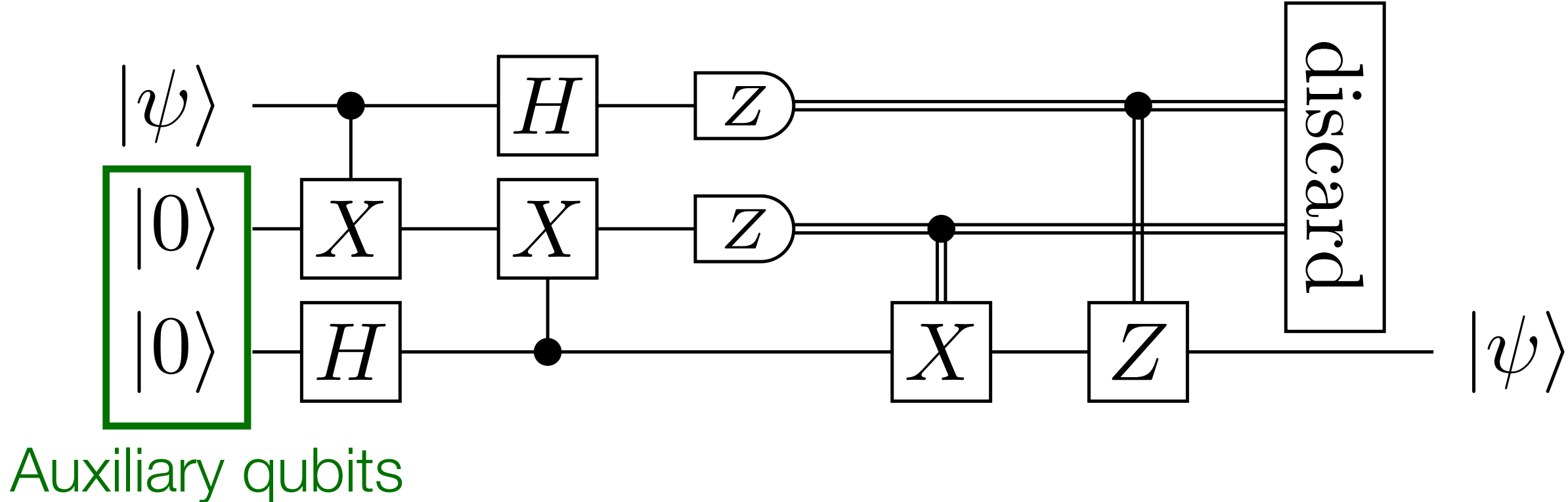
✓ Abstraction layers

https://commons.wikimedia.org/wiki/File:Summit_Supercomputer_2018.jpg



Future works

- Optimization of space overhead including auxiliary qubits



Space overhead $3 \times \frac{N}{K}$
On underlying codes

- Different error model other than depolarizing error model

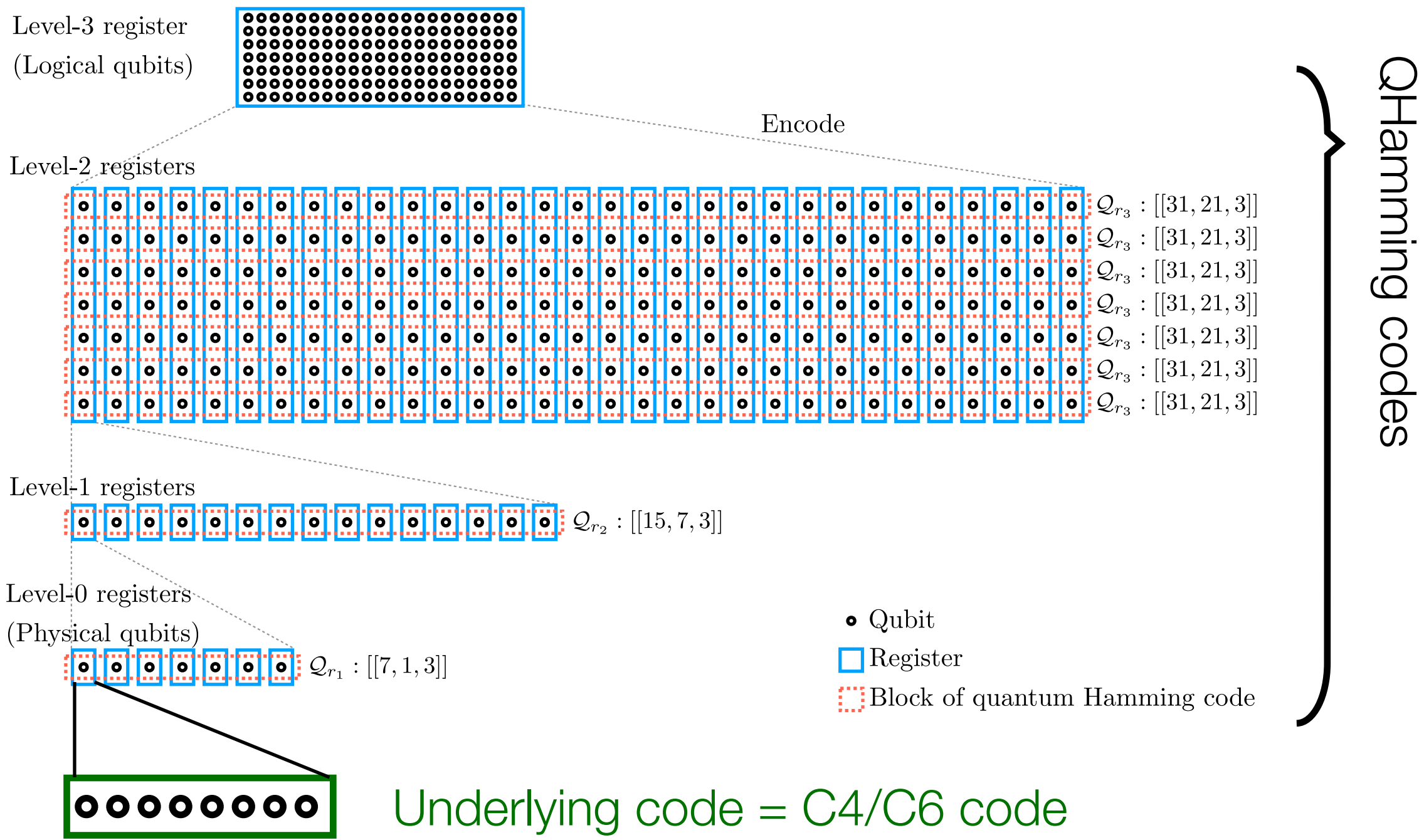
Abbreviation	SD6	SI1000	EM3
Name	Standard Depolarizing	Superconducting Inspired	Entangling Measurements
Noisy Gateset	$CX(p)$ $AnyClifford_1(p)$ $Init_Z(p)$ $M_Z(p)$ $Idle(p)$	$CZ(p)$ $AnyClifford_1(p/10)$ $Init_Z(2p)$ $M_Z(5p)$ $Idle(p/10)$ $ResonatorIdle(2p)$	$M_{PP}(p)$ $M_{PI}(p)$ $Init_Z(p/2)$ $M_Z(p/2)$ $Idle(p)$
Measurement Ancillae	Yes	Yes	No
Honeycomb Cycle Length	6 time steps	9 time steps ($\approx 1000ns$)	3 time steps

C. Gidney, M. Newman, M. McEwen, Quantum 6, 813 (2022).

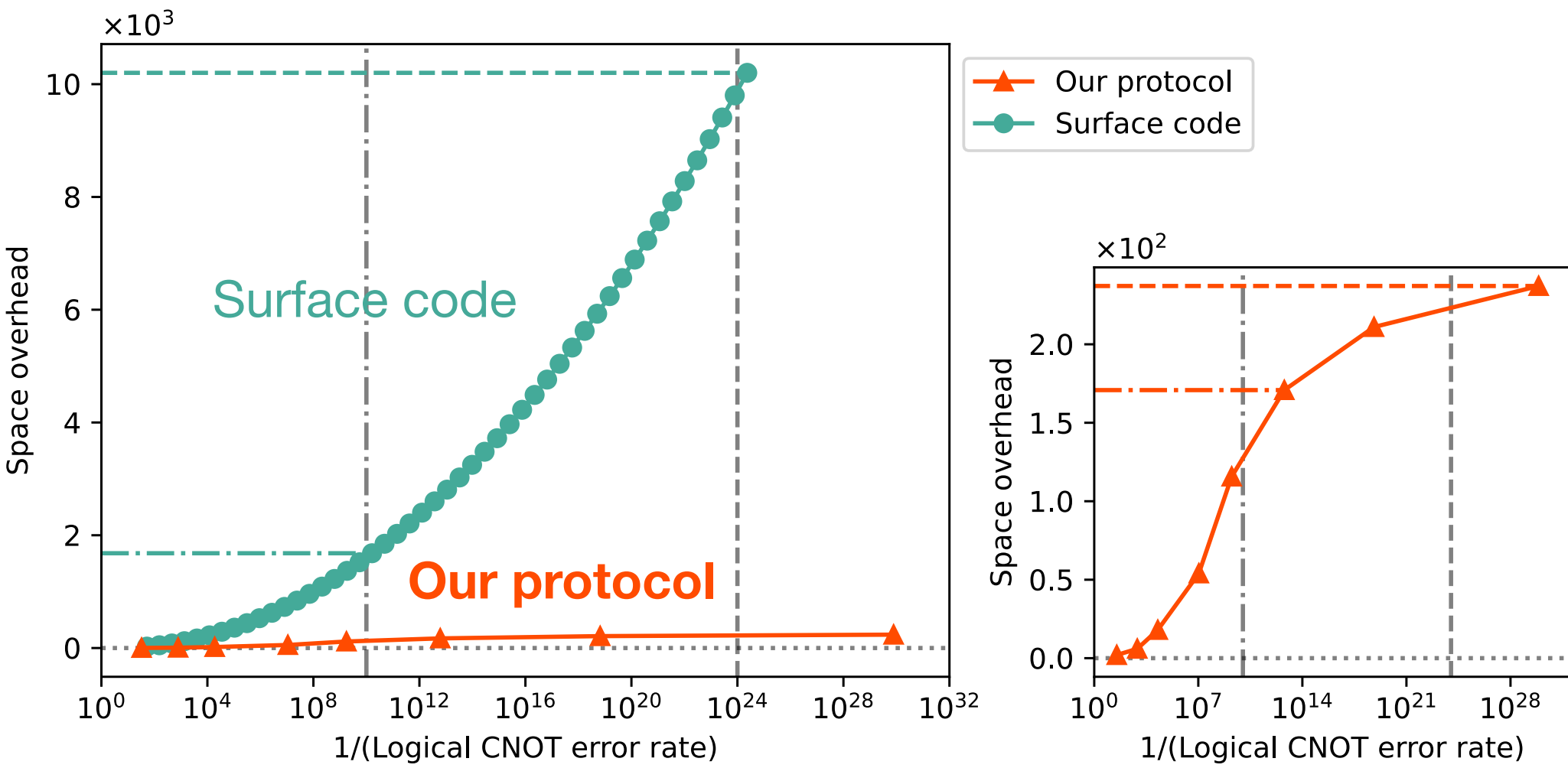
Do we still have an advantage over surface code including auxiliary qubits/on other error models? → Ongoing

Conclusion

Code construction



Space overhead



Threshold

Our protocol: 2.4% > Surface: 0.3%

- **Lower space overhead, higher threshold** than the surface code
- Underlying code can be optimized for each physical error rates

Concatenate codes, save qubits!

Thank you!