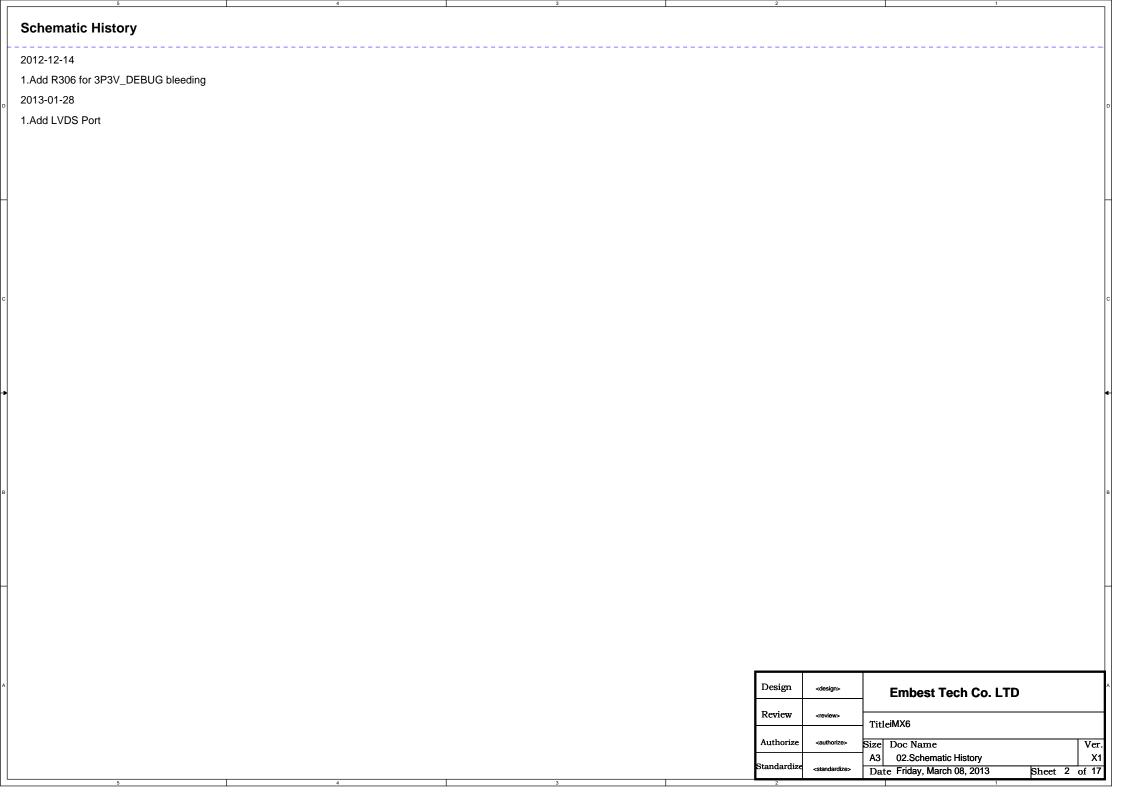
# **MarS Board**

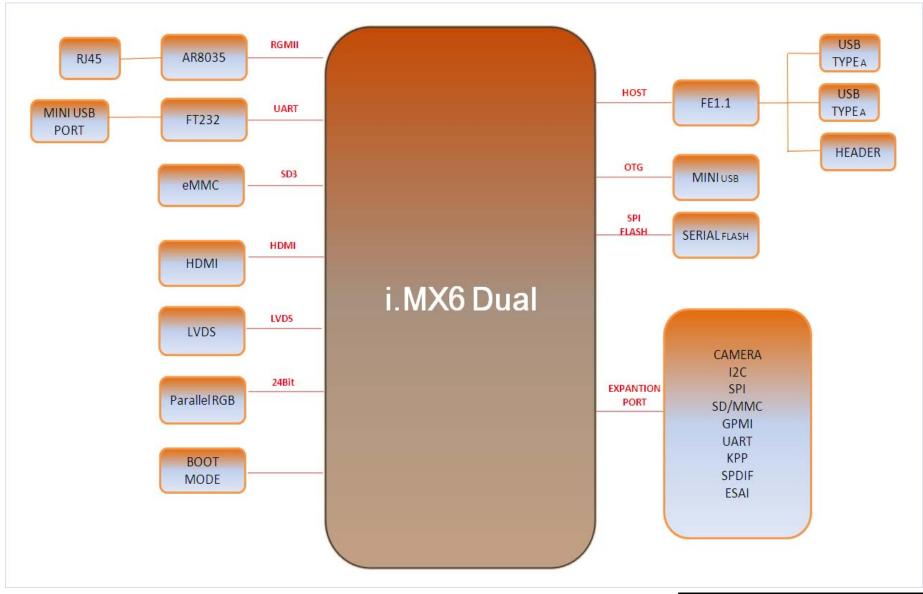
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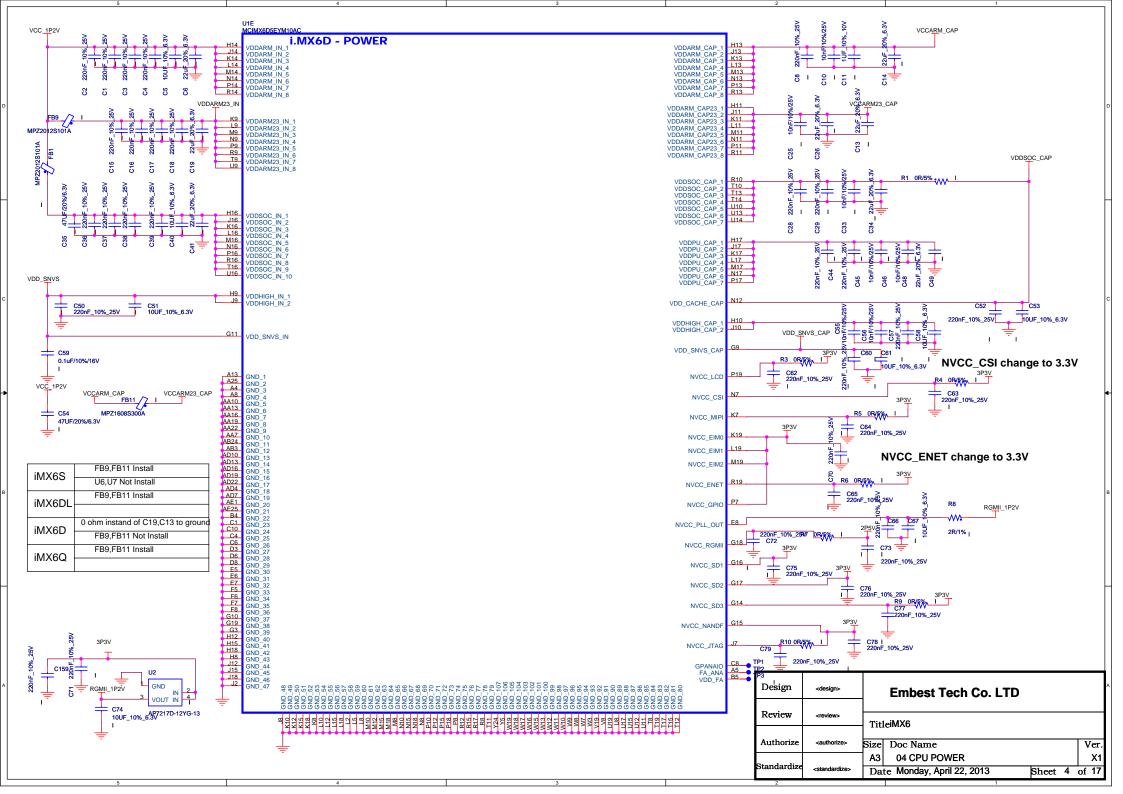
Design	<design></design>	Embest Tech Co. LTD			
Review	<review></review>	TitleiMX6			
Authorize	<authorize></authorize>	Size Doc Name		Ver.	
n. 1 1:		A3 01.CONVER PAGE		X1	
Standardize	<standardize></standardize>	Date Friday, March 08, 2013 Sheet	1	of 17	

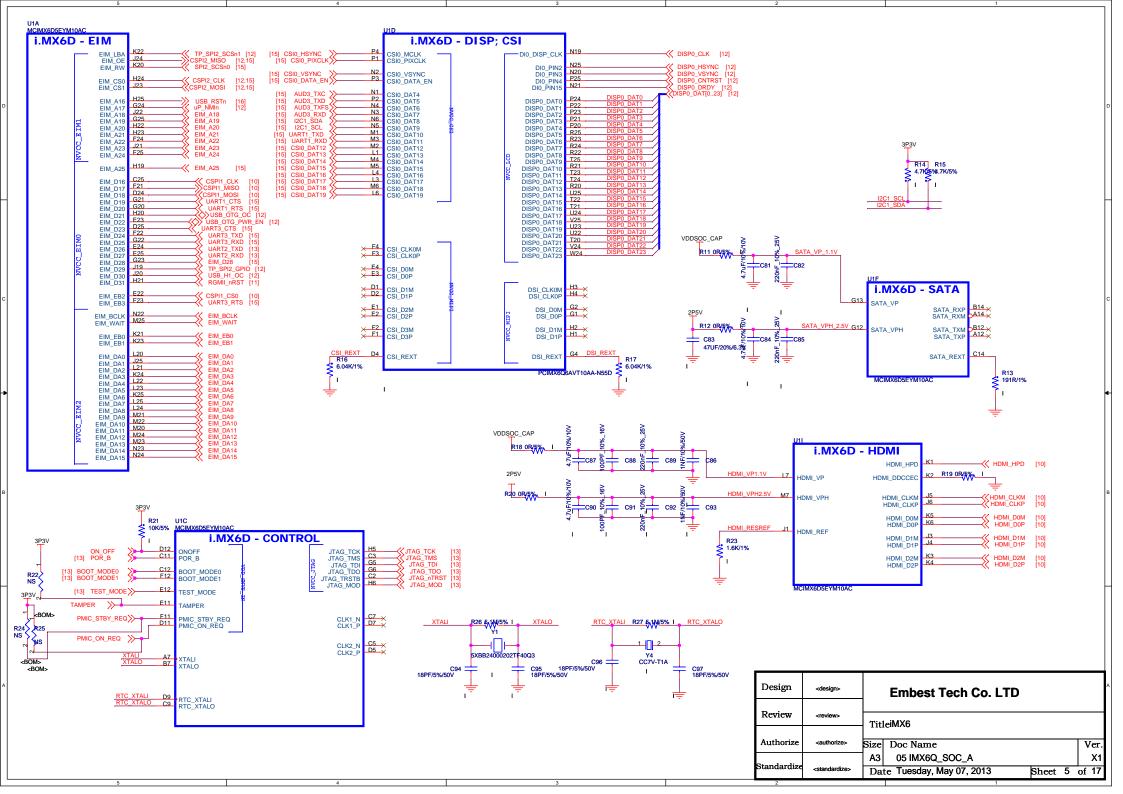


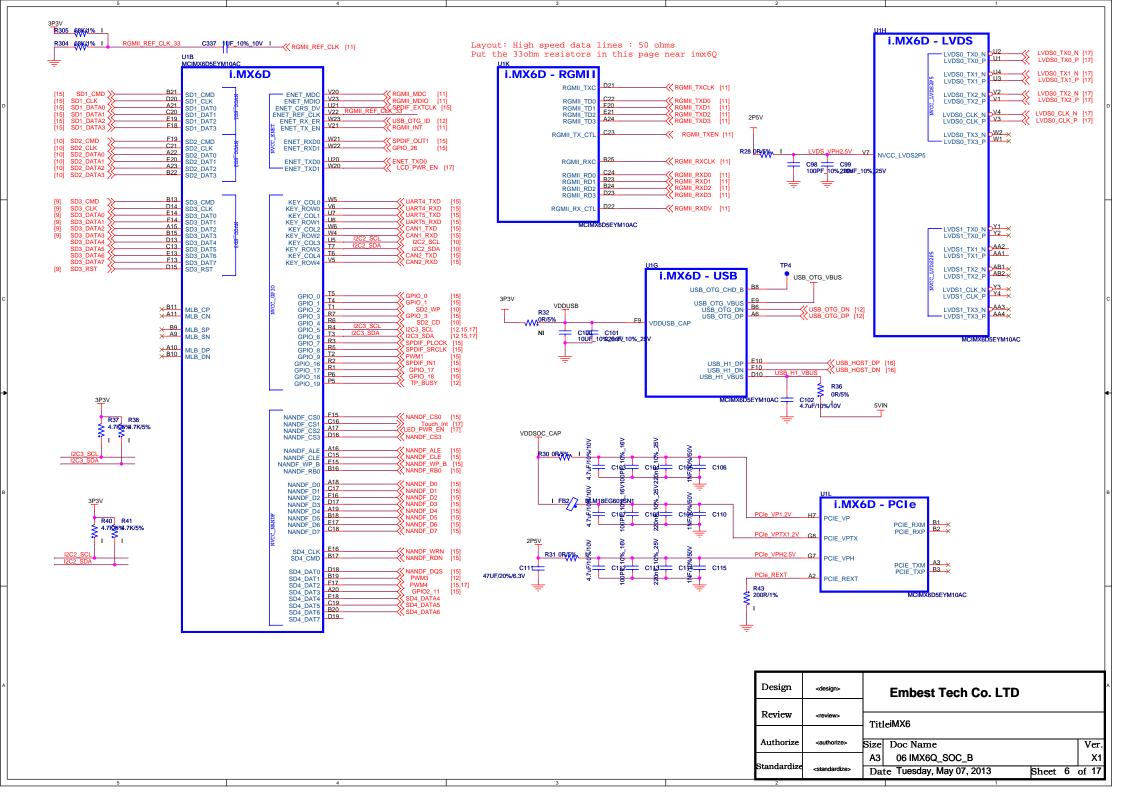
### **MarS Board Block**

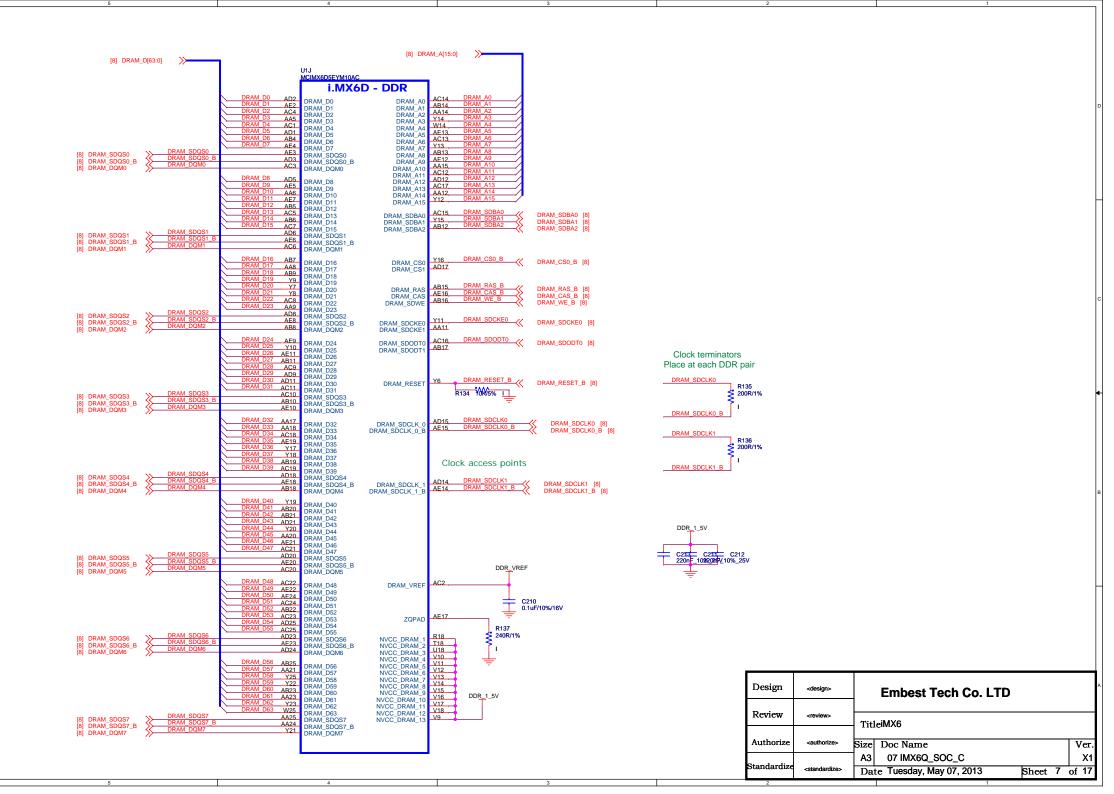


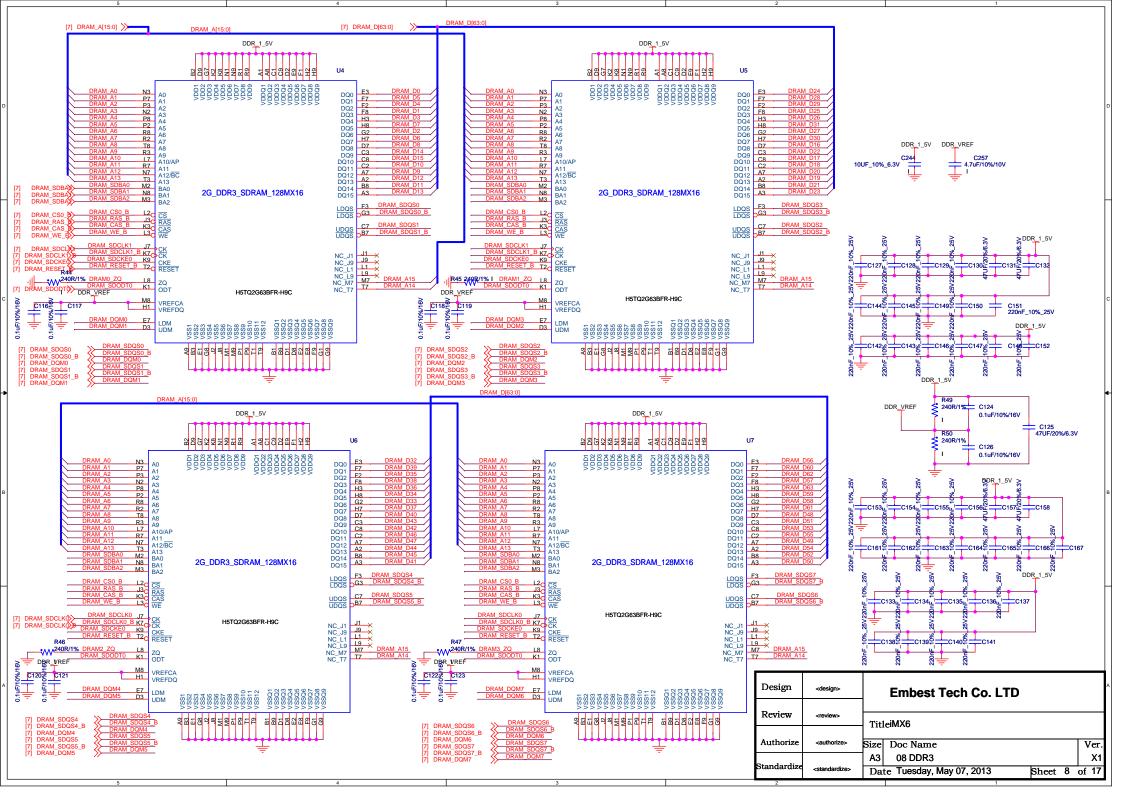
Design	<design></design>	Embest Info & Tech Co. LTD					
Review	<review></review>	myd iAAVC					
Authorize	<authorize></authorize>	TitleiMX6 Size Doc Name					Ver.
G, 1 1:		-	03.SYSTEM BLOCK				X1
Standardize	<standardize></standardize>	Date	Friday, March 15, 2013		Sheet	3	of 17

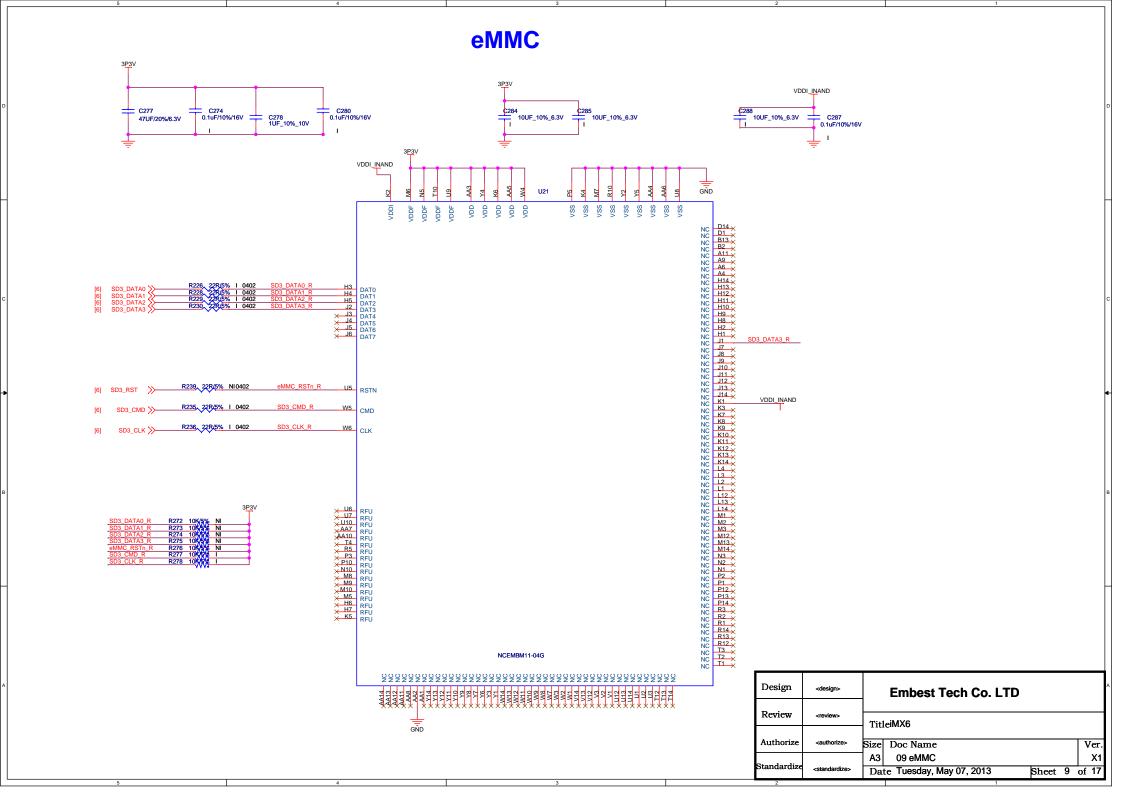


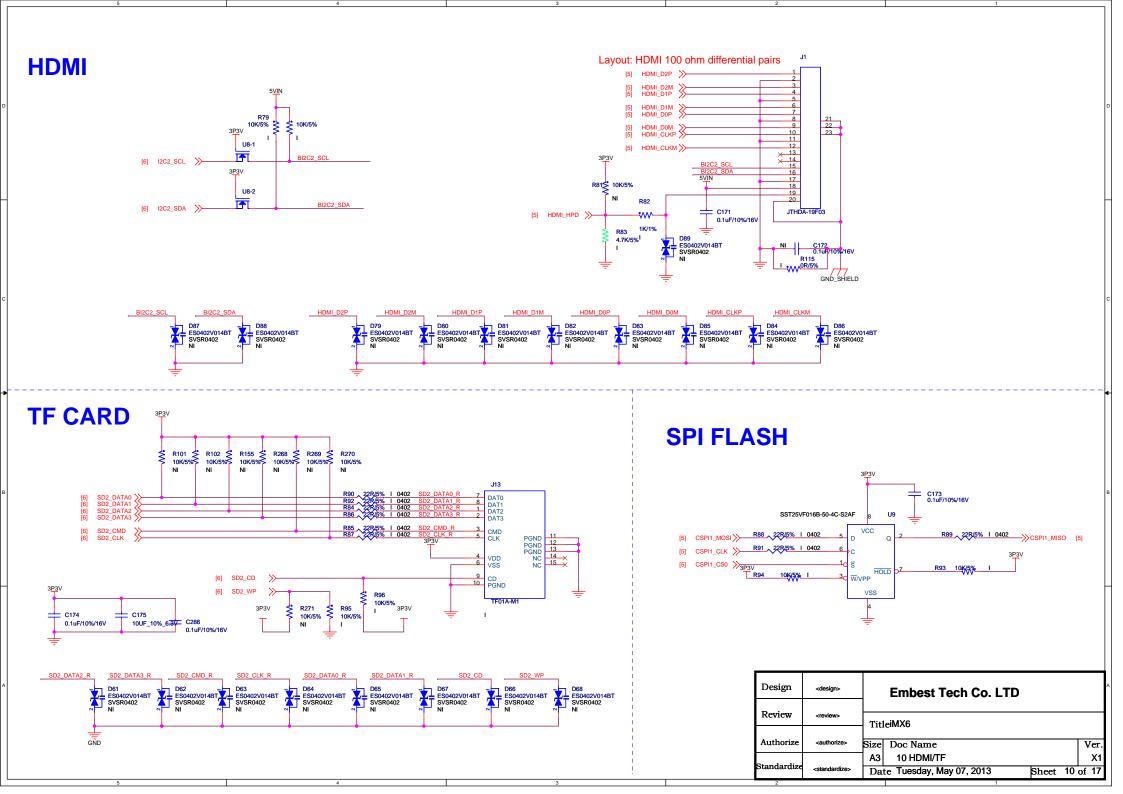






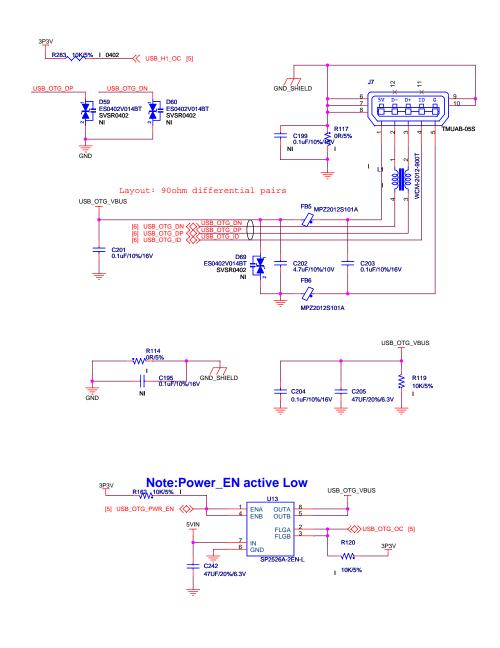


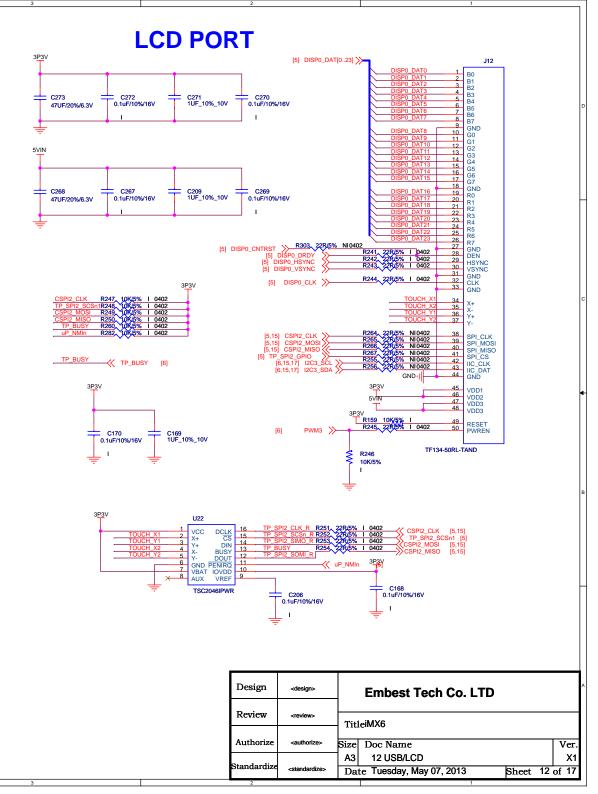


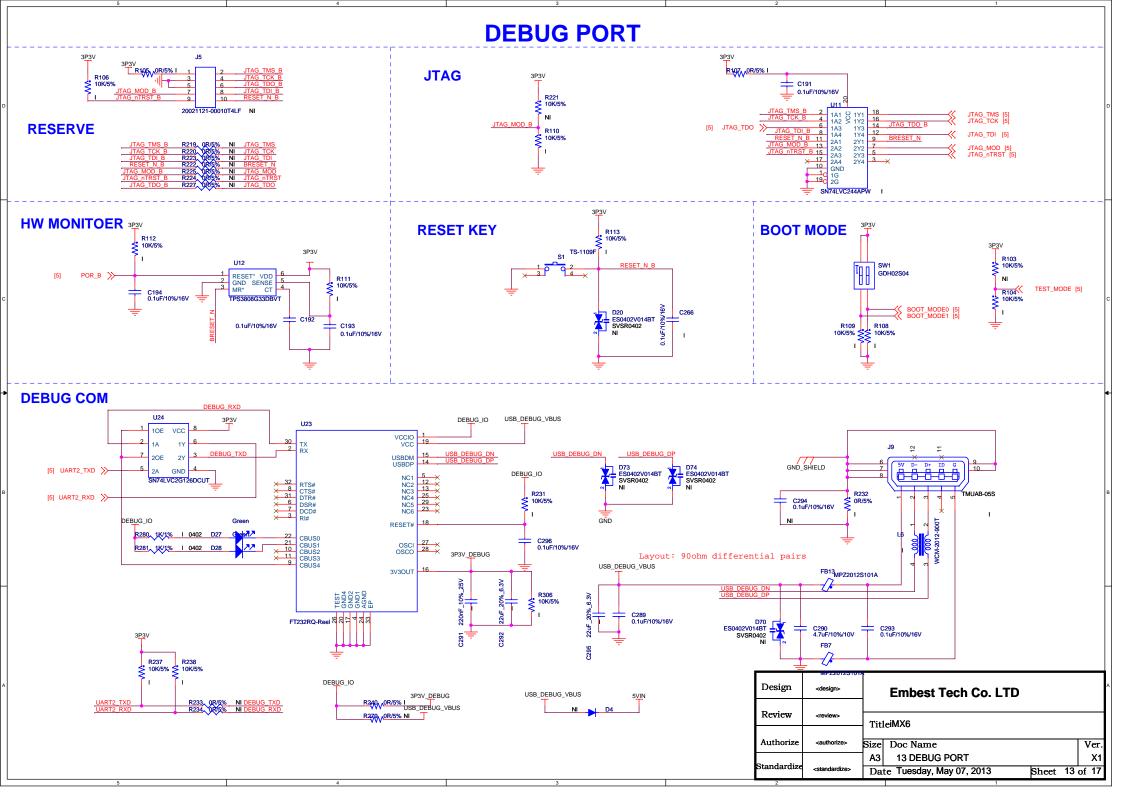


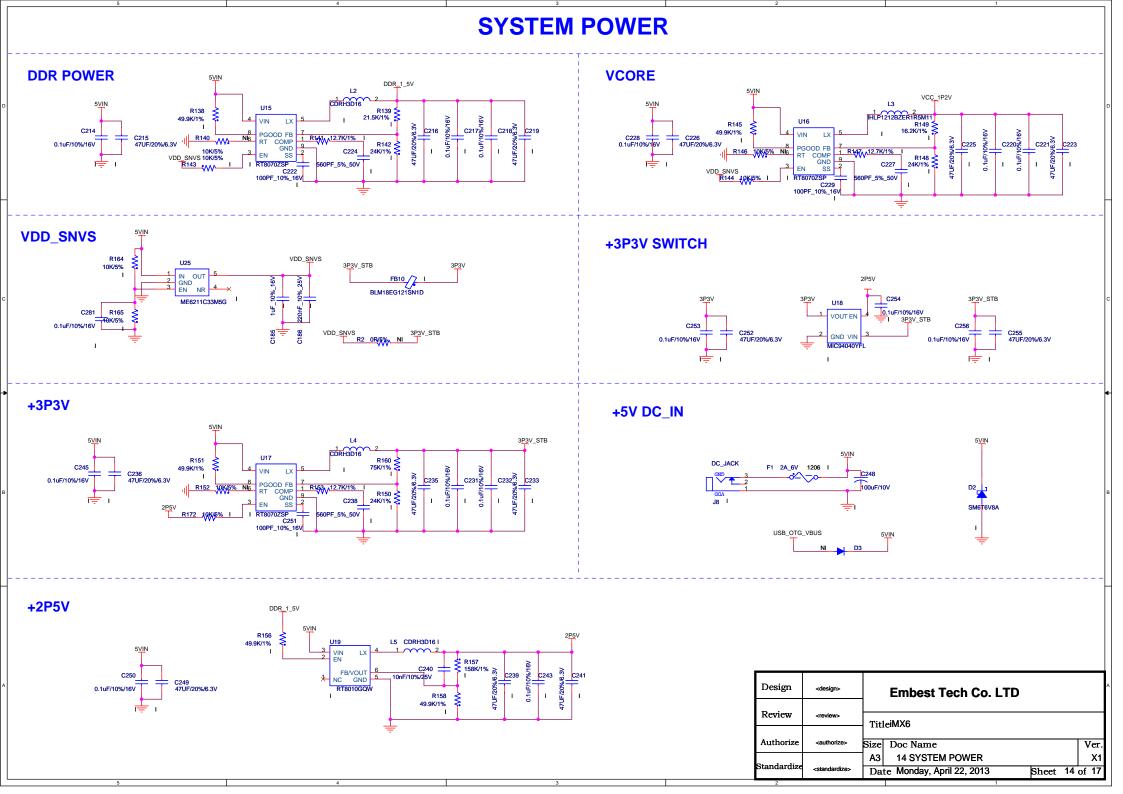
LAN VDD\_RMII AVDD33 RGMII\_AVDDL VDD\_RMII I FB167 BLM18EG601SN1 C259 0.1uF/10%/16V 4.7uF/10%/10V [5] RGMII\_nRST >>-U20 VDD\_RMII\_2.5V VDD\_RMII 22R/5% RESTn TRXP0 TRXNO BLM18EG601SN1 FB17 I AVDD33 AVDD33 TD1+ TD1-R261 1K/1% L7 TRXP1 TD2+ TD2-TRXN1 R263 0402 1K/1% 4.7UH/20% YELC 8 RD1-9 RD2+ YELA 6 AVDDL 11 AVDDL 17 AVDDL 0402 BLM18EG601SN1 FB18 I RGMII A TRXP2 GND GND GND GND 16 17 18 × TCT RCT TRXN3 R121 22R/5% I 0402 R122 22R/5% I 0402 R123 22R/5% I 0402 R124 22R/5% I 0402 VDD\_RMII\_2.5V GND\_SHIELD 29 28 RXD1 48F-01GY2DPL2NL 26 RXD2 RXD3 R262\_ 0R/5% [6] RGMII\_RXD2 [6] RGMII\_RXD3 C207 C208 R125 22R/5% I 0402 RX\_DV 30 RX\_DV 0.1uF/10%/16V 10UF\_10%\_6.3V [6] RGMII\_RXDV <<-R183 2,37K\_1% NI [6] RGMII\_RXCLK 23 CLK\_25MR171, 22R/5% I 0402 RGMII\_REF\_CLK [6] R128, 22R/5% I 0402 [6] RGMILTXEN < DEFAULT: LED\_10\_100: LED\_1000: [6] RGMII\_TXD0 [6] RGMII\_TXD1 [6] RGMII\_TXD2 TXD0 TXD1 TXD2 TXD3 LED\_ACT : LED\_10\_100 LED\_1000 LED\_ACT 21 R284, 10K6% | 0402 R285, 10K6% | 0402 0402 | 122R6%, R133 | MDIO 39 0402 | 122R6%, R169 | MDC 40 0402 | 122R6%, R170 | INT 20 INT LED\_ACT R298 VDD\_RMII\_2.5V 10K/5% [6] RGMII\_MDIO [6] RGMII\_MDC [6] RGMII\_INT VDDH REG VDDIO\_REG 0402 € C315 C312 R302, 10K/5% 0402 AR8035-AL1A RGMII\_XO VDD\_RMII\_2.5V 18PF/5%/50V VDD\_RMII\_2.5V C260 R168 Y5 5XBB25000202TF50Q3 R286 R288 R289 R291 C261 10K/5% 10K/5% 10K/5% 10K/5% 10K/5% 0402 0402 0402 0402 0402 PHY address: 100 18PF/5%/50V mode[0..3]: 1110:RGMII, PLLON, INT; Q12 VDD\_RMII\_2.5V SI2301BDS RX\_CLK: RX\_CLK 0 :1.5V I/O; Q11 1 :1.8V I/O; 2.5V I/O: PULL UP/DOWN SI2301BDS Design <design> **Embest Tech Co. LTD** R297 LED\_LINK 10K/5% 10K/5% 10K/5% 0K/5% 10K/5% NI NI Review LED\_1000 <review> 0402 0402 0402 0402 0402 TitleiMX6 LED\_10\_100 ÷ ÷ Authorize <authorize> Size Doc Name Ver 11 RGMII Χí Standardize <standardize> Sheet 11 of 17 Date Tuesday, May 07, 2013

## **USB OTG**

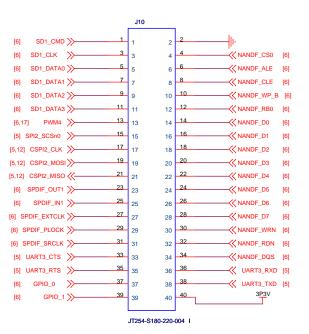


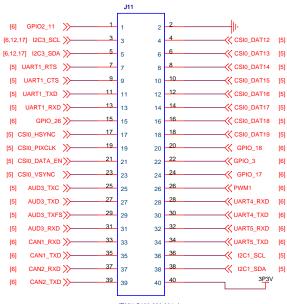






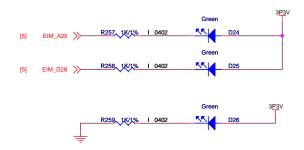
#### **EXPANTION PORT**

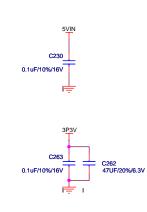


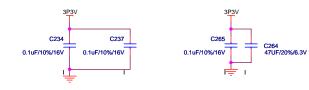


JT254-S180-220-004

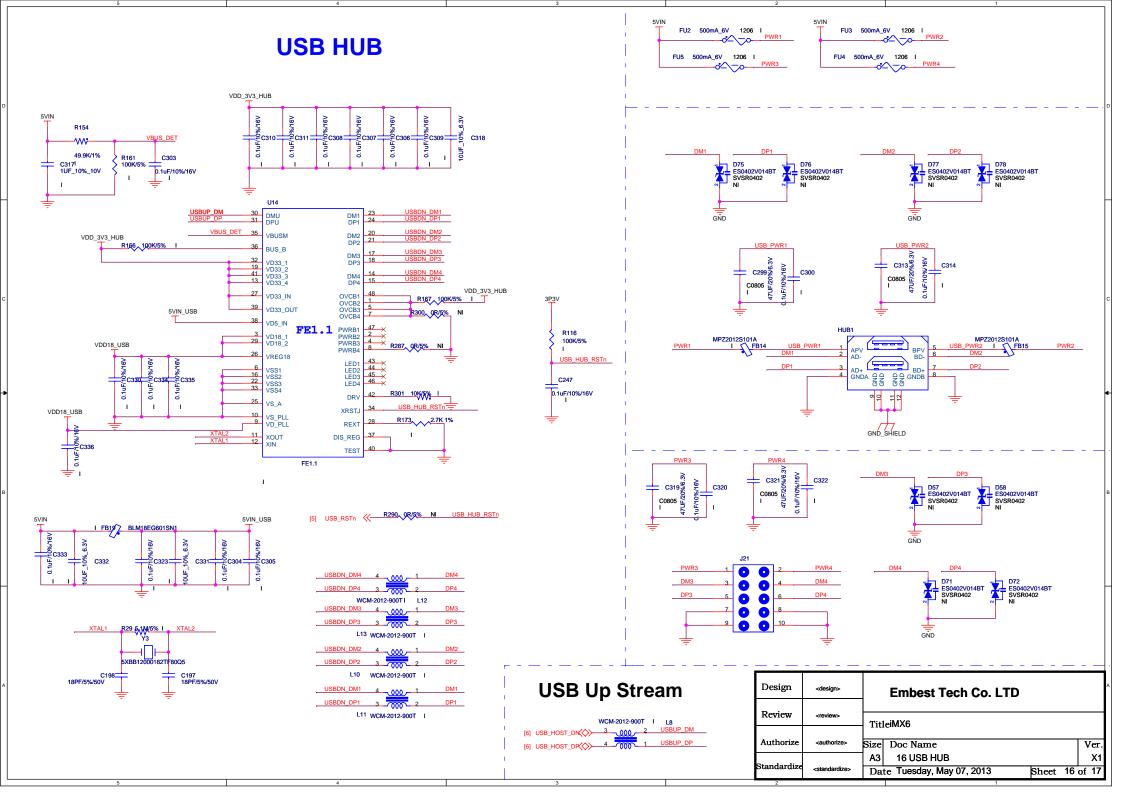
## **USER LED**



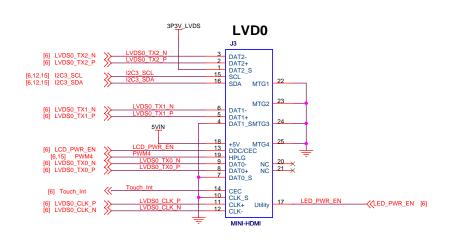


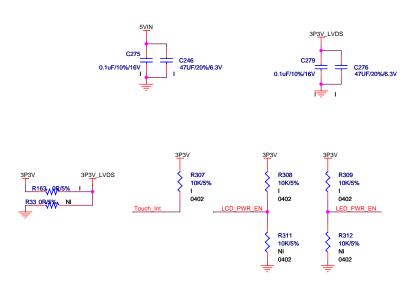


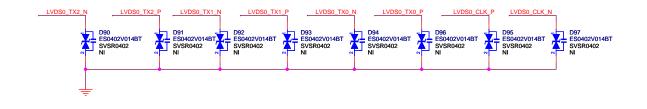
Design	<design></design>	Embest Tech Co. LTD	
Review	<review></review>	TitleiMX6	
Authorize	<authorize></authorize>	Size Doc Name	Ver.
Gr. 1 1:		A3 15 EXPANSION PORT	X1
Standardize	<standardize></standardize>	Date Tuesday, May 07, 2013 Sheet 15	of 17



## LVDS INTERFACE







	Design	<design></design>	Embest Tech Co. LTD			
	Review	<review></review>	TitleiMX6			
	Authorize	<authorize></authorize>	Size Doc Name	Ver.		
	04 44:		A3 17 LVDS	X1		
	Standardize	<standardize></standardize>	Date Tuesday, May 07, 2013 Sheet 17	of 17		