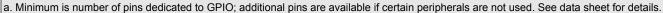
TEXAS INSTRUMEN

June 28, 2010



Product Selector Guide

		Me	emo	ry			Core	•				Т	imer	's							Se	erial	Inte	rfac	es					A	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle \mathbb C}$	UART	l²C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S101	8	2	_	_	_	20	_	_	✓	2	1	1	_	_	_	1	_	_	_	-	_	_	_	1	_	1	_	_	-	_	_	_	2/0	2-18	_	✓	I/E	28SOIC 48QFP 48QFN	Р
LM3S102	8	2	-	-	-	20	-	_	1	2	1	1	_	-	-	2	-	1	1	-	ı	-	-	1	1	1	-	-	-	-	-	_	1/0	0-18	-	1	I/E	28SOIC 48QFP 48QFN	Р
LM3S300	16	4	_	_	-	25	-	1	1	3	1	1	_	-	_	6	_	_	-	-	_	_	_	2	1	1	-	_	-	-	_	_	3/0	8-36	_	1	I/E	48QFP 48QFN	Р
LM3S301	16	2	-	-	-	20	-	1	1	2	1	1	2	1	1	2	_	_	-	-	_	-	-	1	-	1	-	1	10	3	250K	1	2/0	12-33	-	1	I/E	48QFP 48QFN	Р
LM3S308	16	4	-	-	-	25	-	1	1	3	✓	1	-	-	-	6	-	_	-	-	-	-	-	2	1	1	-	1	10	8	500K	1	1/0	5-28	-	1	I/E	48QFP 48QFN	Р
LM3S310	16	4	-	-	-	25	-	✓	✓	3	1	1	6	1	1	6	-	1	1	-	-	-	-	2	1	1	-	-	-	-	_	-	3/0	3-36	-	1	I/E	48QFP 48QFN	Р
LM3S315	16	4	_	_	-	25	_	✓	✓	3	1	1	2	1	1	6	-	ı	-	-	ı	_	_	2	_	1	_	1	10	4	250K	✓	1/0	7-32	_	✓	I/E	48QFP 48QFN	Р
LM3S316	16	4	-	-	-	25	-	✓	1	3	1	1	4	1	1	6	-	-	-	-	-	-	-	2	1	1	-	1	10	4	250K	✓	1/0	3-32	-	✓	I/E	48QFP 48QFN	Р
LM3S317	16	4	_	_	_	25	_	✓	✓	3	✓	1	6	1	1	6	_	-	-	-	-	-	-	1	_	1	_	1	10	6	250K	✓	1/0	3-30	-	✓	I/E	48QFP 48QFN	Р



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



		Me	emo	ry			Core)				Т	imer	'S							Se	erial	Inte	rfac	es					A	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et									DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\rm c}$	UART	1 ₂ C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S328	16	4	-	-	-	25	-	✓	1	3	✓	1	-	-	_	6	-	_	ı	_	-	-	-	2	1	1	-	1	10	8	500K	1	0/0	7-28	-	1	I/E	48QFP 48QFN	Р
LM3S600	32	8	-	-	-	50	-	✓	✓	3	✓	1	-	_	_	6	-	-	١	-	-	-	-	2	1	1	-	-	-	-	_	-	3/0	8-36	-	✓	I/E	48QFP 48QFN	Р
LM3S601	32	8	-	-	-	50	-	1	1	3	1	1	6	1	1	6	1	_	-	_	-	-	-	2	1	1	-	_	-	-	_	-	3/0	0-36	-	1	I/E	48QFP 48QFN	Р
LM3S608	32	8	-	-	-	50	-	1	1	3	1	1	-	-	-	6	-	-	-	-	-	-	-	2	1	1	-	1	10	8	500K	1	1/0	5-28	-	1	I/E	48QFP 48QFN	Р
LM3S610	32	8	-	-	-	50	-	1	1	3	1	1	6	1	1	6	_	_	-	_	_	-	-	2	1	1	-	1	10	2	500K	1	0/0	6-34	-	1	I/E	48QFP 48QFN	Р
LM3S611	32	8	-	-	-	50	-	1	1	3	1	1	6	1	1	6	-	_	_	_	-	-	-	2	1	1	-	1	10	4	500K	1	0/0	4-32	-	1	I/E	48QFP 48QFN	Р
LM3S612	32	8	-	-	-	50	_	1	1	3	1	1	2	1	1	6	_	_	-	_	_	-	-	2	1	1	_	1	10	2	500K	1	1/0	7-34	-	1	I/E	48QFP 48QFN	Р
LM3S613	32	8	-	-	-	50	-	1	1	3	✓	1	4	1	✓	6	_	-	-	_	-	-	-	2	1	1	-	1	10	4	500K	1	1/0	3-32	-	✓	I/E	48QFP 48QFN	Р
LM3S615	32	8	_	_	_	50	_	1	1	3	√	1	6	1	1	6	_	_	ı	_	_	_	-	2	1	1	-	1	10	2	500K	1	3/0	0-34	_	1	I/E	48QFP 48QFN	Р
LM3S617	32	8	-	-	_	50	-	✓	✓	3	✓	1	6	1	✓	6	-	-	_	-	-	_	-	2	_	1	-	1	10	6	500K	✓	1/0	1-30	_	✓	I/E	48QFP 48QFN	Р

a. Minimum is number of pins dedicated to GPIO; additional pins are available if certain peripherals are not used. See data sheet for details.

b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

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		Me	emo	ry			Core	•				Т	imeı	's							Se	erial	Inte	rfac	es					A	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et								Α	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle \mathbb C}$	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S618	32	8	-	-	-	50	-	1	1	3	✓	1	6	1	✓	4	1	ı	-	-	ı	-	-	2	-	1	-	1	10	6	500K	✓	1/0	0-30	-	✓	I/E	48QFP 48QFN	Р
LM3S628	32	8	_	_	-	50	-	1	1	3	1	1	-	_	_	4	_	ı	-	-	ı	_	_	2	1	1	_	1	10	8	1M	1	0/0	9-28	-	1	I/E	48QFP 48QFN	Р
LM3S800	64	8	-	-	-	50	-	1	1	3	1	1	-	-	_	6	-	ı	1	-	ı	-	-	2	1	1	-	_	1	-	_	-	3/0	8-36	-	1	I/E	48QFP 48QFN	Р
LM3S801	64	8	-	-	-	50	-	1	✓	3	1	1	6	1	✓	6	1	-	1	-	-	-	-	2	1	1	-	-	1	-	_	-	3/0	0-36	-	1	I/E	48QFP 48QFN	Р
LM3S808	64	8	-	-	-	50	-	1	1	3	1	1	-	-	_	6	-	-	-	-	-	-	-	2	1	1	-	1	10	8	500K	1	1/0	5-28	-	1	I/E	48QFP 48QFN	Р
LM3S811	64	8	-	-	-	50	-	1	✓	3	1	1	6	1	1	6	-	-	-	-	-	-	-	2	1	1	-	1	10	4	500K	1	1/0	1-32	-	1	I/E	48QFP 48QFN	Р
LM3S812	64	8	-	-	-	50	-	1	1	3	1	1	2	1	1	6	-	-	1	-	-	-	-	2	1	1	-	1	10	2	250K	1	1/0	7-34	-	1	I/E	48QFP 48QFN	Р
LM3S815	64	8	-	-	-	50	-	1	✓	3	1	1	6	1	✓	6	-	ı	-	-	ı	-	-	2	1	1	-	1	10	2	500K	1	3/0	0-34	-	1	I/E	48QFP 48QFN	Р
LM3S817	64	8	_	_	-	50	-	1	1	3	1	1	6	1	1	6	_	ı	-	_	ı	_	-	2	-	1	-	1	10	6	1M	1	1/0	1-30	_	1	I/E	48QFP 48QFN	Р
LM3S818	64	8	_	_	_	50	_	1	1	3	✓	1	6	1	✓	4	1	-	-	-	-	-	_	2	-	1	_	1	10	6	1M	✓	1/0	0-30	_	✓	I/E	48QFP 48QFN	Р

a. Minimum is number of pins dedicated to GPIO; additional pins are available if certain peripherals are not used. See data sheet for details.

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		Me	mo	ry			Core	9				Т	imer	'S							Se	erial	Inte	rfac	es					A	nalog								
													M	lotio	n C	ontr	ol		Et	hern	net									DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\rm c}$	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S828	64	8	-	-	-	50	-	✓	✓	3	1	1	-	-	-	6	-	-	-	_	-	-	-	2	1	1	-	1	10	8	1M	1	0/0	7-28	-	1	I/E	48QFP 48QFN	Р
LM3S1110	64	16	_	_	-	25	_	1	1	3	1	1	_	-	_	2	-	-	-	_	_	_	_	2	-	1	_	-	-	-	_	-	2/0	20-41	1	1	I/E	100LQFP 108BGA	Р
LM3S1133	64	16	-	_	-	50	-	1	1	4	1	1	2	1	1	8	_	-	-	_	-	-	-	3	1	2	-	1	10	2	250K	1	1/0	9-44	1	1	I/E	100LQFP 108BGA	Р
LM3S1138	64	16	-	-	-	50	-	1	1	4	1	1	-	-	-	6	-	_	-	-	-	-	-	3	2	2	-	1	10	8	1M	1	3/0	9-46	1	1	I/E	100LQFP 108BGA	Р
LM3S1150	64	16	-	_	-	50	-	1	1	4	1	1	6	1	1	6	1	ı	-	_	_	-	-	3	1	2	-	-	-	-	-	-	3/0	7-52	1	1	I/E	100LQFP 108BGA	Р
LM3S1162	64	16	-	-	-	50	-	1	1	4	1	1	6	1	1	6	-	-	-	-	-	-	-	3	1	2	-	1	10	2	500K	1	3/0	4-46	1	1	I/E	100LQFP 108BGA	Р
LM3S1165	64	16	-	_	-	50	_	1	1	4	1	1	6	1	1	8	_	-	-	_	_	-	-	3	1	2	_	1	10	4	500K	1	1/0	4-43	1	1	I/E	100LQFP 108BGA	Р
LM3S1332	96	16	-	-	-	50	-	1	1	4	1	1	-	_	_	8	_	-	-	_	-	-	-	2	-	1	-	1	10	3	250K	1	3/0	29-57	1	✓	I/E	100LQFP 108BGA	Р
LM3S1435	96	32	_	_	-	50	-	1	1	3	1	1	2	1	1	4	_	-	-	_	_	-	-	2	1	1	_	1	10	2	500K	1	1/0	21-46	1	1	I/E	100LQFP 108BGA	Р
LM3S1439	96	32	-	-	_	50	_	✓	✓	3	✓	1	6	1	✓	6	1	_	_	-	_	_	-	2	1	2	_	1	10	4	500K	✓	1/0	14-52	1	✓	I/E	100LQFP 108BGA	Р

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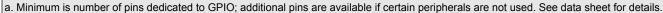
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		Me	emo	ry			Core	9				Т	imer	's							Se	erial	Inte	rfac	es					Aı	nalog								
													M	lotio	n C	ontr	ol		Etl	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	₽WM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or 0°	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S1512	96	64	-	-	-	25	-	1	✓	4	✓	1	-	-	-	8	1	-	-	-	-	-	-	3	2	2	-	1	10	2	250K	✓	3/0	15-58	✓	✓	I/E	100LQFP 108BGA	Р
LM3S1538	96	64	_	-	_	50	_	1	✓	4	1	1	_	_	-	8	1	ı	-	-	-	_	_	3	2	2	_	1	10	8	500K	1	0/0	9-43	1	1	I/E	100LQFP 108BGA	Р
LM3S1601	128	32	-	-	-	50	-	1	1	4	1	1	-	-	-	8	-	-	-	-	-	-	-	3	2	2	-	-	_	-	_	-	2/0	23-60	1	1	I/E	100LQFP 108BGA	Р
LM3S1607	128	32	✓	1	-	50	-	1	1	4	1	1	-	-	-	6	-	-	-	-	-	-	-	3	2	1	-	1	10	8	500K	1	0/0	0-33	1	✓	Ι	64LQFP	Р
LM3S1608	128	32	-	-	-	50	-	1	1	4	1	1	_	-	-	8	-	-	-	-	_	_	-	2	2	2	-	1	10	8	500K	1	2/0	17-52	1	1	I/E	100LQFP 108BGA	Р
LM3S1620	128	32	-	-	-	25	-	1	1	3	1	1	6	1	1	4	1	-	-	-	-	-	-	2	1	2	-	-	-	-	_	-	3/0	11-52	1	1	I/E	100LQFP 108BGA	Р
LM3S1621	128	32	1	1	-	80	1	1	1	4	1	2	-	-	-	8	-	✓	-	-	-	-	-	3	2	2	-	1	10	8	1M	1	2/8	0-67	1	1	I	100LQFP 108BGA	S
LM3S1625	128	32	✓	1	-	50	-	1	✓	4	✓	1	4	1	1	4	_	1	-	-	_	_	_	1	2	1	-	1	10	6	500K	1	1/0	0-33	-	✓	Ι	64LQFP	Р
LM3S1626	128	32	1	1	-	50	-	1	1	4	1	1	4	1	1	4	1	-	-	-	-	-	-	2	1	1	-	1	10	6	500K	1	0/0	0-33	-	✓	I	64LQFP	Р
LM3S1627	128	32	✓	✓	-	50	-	1	✓	4	✓	1	6	1	✓	4	1		-	-	_	_	-	2	1	1	-	1	10	4	500K	1	0/0	0-33	-	✓	I	64LQFP	Р
LM3S1635	128	32	-	-	-	50	-	1	1	4	1	1	6	1	1	8	-	-	-	-	-	_	-	3	2	2	-	1	10	4	500K	1	2/0	12-56	1	1	I/E	100LQFP 108BGA	Р
LM3S1637	128	32	_	-	-	50	_	✓	✓	4	✓	1	6	1	1	6	1	-	-	-	_	-	-	3	1	1	-	1	10	4	1M	1	1/0	7-43	✓	✓	I/E	100LQFP 108BGA	Р



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													M	lotio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle \mathbb C}$	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S1651	128	32	✓	✓	-	80	✓	✓	✓	4	1	2	6	4	✓	8	2	-	-	-	-	-	-	3	2	2	✓	2	10	16	1M	✓	2/16	0-67	✓	✓	1	100LQFP 108BGA	S
LM3S1751	128	64	_	_	-	50	_	1	1	3	1	1	4	1	1	6	-	-	_	-	_	_	-	3	1	2	_	1	10	4	500K	1	1/0	21-56	1	1	I/E	100LQFP 108BGA	Р
LM3S1776	128	64	✓	✓	-	50	_	✓	✓	3	✓	1	8	3	1	2	_	_	-	_	-	_	_	1	1	1	_	1	10	6	1M	1	0/0	1-33	✓	✓	I	64LQFP	Р
LM3S1811	256	32	✓	✓	-	50	✓	✓	✓	4	✓	2	_	_	-	8	_	✓	-	_	-	-	_	3	2	2	_	1	10	8	1M	✓	2/8	0-67	✓	✓	ı	100LQFP 108BGA	s
LM3S1816	256	32	✓	✓	-	50	✓	✓	✓	4	✓	2	_	-	-	8	_	_	-	_	-	_	_	3	2	2	_	1	10	8	1M	1	2/8	0-33	✓	✓	ı	64LQFP	S
LM3S1850	256	32	ı	_	-	50	_	✓	1	3	✓	1	6	1	✓	6	1	_	-	ı	-	_	_	2	1	1	-	_	_	ı	-	_	3/0	17-56	1	✓	I/E	100LQFP 108BGA	Р
LM3S1911	256	64	ı	-	-	50	-	✓	1	4	✓	1	_	-	-	8	-	_	-	ı	-	-	-	3	2	2	1	-	-	ı	-	-	2/0	23-60	1	1	I/E	100LQFP 108BGA	Р
LM3S1918	256	64	ı	_	-	50	_	✓	1	4	✓	1	_	_	_	8	-	_	-	ı	-	_	_	2	2	2	-	1	10	8	500K	✓	2/0	17-52	1	✓	I/E	100LQFP 108BGA	Р
LM3S1937	256	64	_	-	_	50	-	1	1	3	✓	1	6	1	1	4	-	-	-	-	-	-	-	2	1	1	-	1	10	4	1M	1	1/0	27-56	1	✓	I/E	100LQFP 108BGA	Р
LM3S1958	256	64	-	_	_	50	-	✓	1	4	✓	1	_	_	-	8	-	-	-	ı	_	_	-	3	2	2	-	1	10	8	1M	✓	0/0	21-52	1	✓	I/E	100LQFP 108BGA	Р
LM3S1960	256	64	-	-	-	50	-	✓	1	4	✓	1	6	1	✓	8	2	-	-	-	-	-	-	3	2	2	_	_	-	-	-	-	3/0	7-60	1	1	I/E	100LQFP 108BGA	Р

a. Minimum is number of pins dedicated to GPIO; additional pins are available if certain peripherals are not used. See data sheet for details.



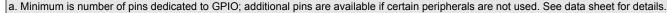
b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



		Me	moi	ry		(Core	•				Т	imer	'S							Se	erial	Inte	rfac	es					Aı	nalog								
													M	otio	n C	ontr	ol		Et	hern	et								Α	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or 0°	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S1968	256	64	-	_	-	50	_	1	1	4	✓	1	6	1	✓	4	2	-	-	-	-	-	_	3	2	2	-	1	10	8	1M	✓	3/0	5-52	✓	1	I/E	100LQFP 108BGA	Р
LM3S1B21	256	96	1	1	-	80	1	1	1	4	✓	2	ı	_	-	8	-	1	-	-	_	-	-	3	2	2	-	1	10	8	1M	1	2/8	0-67	1	✓	1	100LQFP 108BGA	S
LM3S1J11	128	20	✓	1	-	50	1	1	1	3	1	2	_	_	-	6	-	_	-	-	-	-	-	3	2	2	-	1	10	8	1M	1	2/8	0-67	1	1	ı	100LQFP 108BGA	s
LM3S1J16	128	20	✓	1	-	50	✓	✓	✓	3	✓	2	_	-	-	6	_	1	-	_	-	-	_	3	2	2	_	1	10	8	1M	1	2/8	0-33	✓	1	I	64LQFP	S
LM3S1N11	64	12	✓	1	-	50	1	1	1	3	✓	2	-	_	-	6	-	-	-	-	-	-	-	3	2	2	-	1	10	8	1M	1	2/8	0-67	1	✓	ı	100LQFP 108BGA	s
LM3S1N16	64	12	✓	1	-	50	✓	✓	✓	3	✓	2	_	_	-	6	_	_	_	_	_	_	_	3	2	2	_	1	10	8	1M	1	2/8	0-33	✓	1	I	64LQFP	S
LM3S1P51	64	24	✓	1	-	80	1	1	1	4	✓	2	6	4	1	8	2	-	-	-	-	-	-	3	2	2	1	2	10	16	1M	1	2/16	0-67	1	✓	-	100LQFP 108BGA	s
LM3S1R21	256	48	1	1	-	80	1	1	1	4	✓	2	ı	_	-	8	_	1	-	-	_	-	-	3	2	2	-	1	10	8	1M	1	2/8	0-67	1	1	-	100LQFP 108BGA	S
LM3S1R26	256	48	✓	1	-	80	✓	✓	✓	4	✓	2	_	_	<u> </u>	8	_	_	_	-	_	-	_	3	2	2	-	1	10	8	1M	✓	2/8	0-33	1	✓	ı	64LQFP	S
LM3S1W16	32	8	√	1	_	50	✓	✓	1	3	✓	2	_	ı	-	6	-	ı	-	_	ı	_	_	3	2	2	_	1	10	8	1M	✓	2/8	0-33	√	✓	I	64LQFP	S
LM3S1Z16	16	6	✓	1	-	50	✓	✓	✓	3	✓	2	_	-	-	6	-	ı	-	_	ı	_	_	3	2	2	_	1	10	8	1M	1	2/8	0-33	✓	✓	I	64LQFP	S
LM3S2110	64	16	_	-	-	25	-	1	1	3	1	1	2	1	1	4	_	ı	-	-	-	1	_	1	1	1	-	-	-	-	-	_	3/0	11-40	_	1	I/E	100LQFP 108BGA	Р



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

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		Me	mo	ry			Core)				Т	imer	s							Se	erial	Inte	rfac	es					Aı	nalog								
													М	otio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	₽WM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle {\rm C}}$	UART	1²C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S2139	64	16	-	-	-	25	-	✓	✓	3	✓	1	-	-	-	6	-	-	-	_	-	1	-	2	1	1	-	1	10	4	250K	1	3/0	26-56	-	✓	I/E	100LQFP 108BGA	Р
LM3S2276	64	32	✓	✓	_	50	_	✓	✓	3	✓	1	8	3	✓	1	-	_	-	-	-	1	-	1	1	1	-	1	10	6	1M	✓	0/0	0-33	✓	✓	I	64LQFP	Р
LM3S2410	96	32	-	_	-	25	_	✓	✓	3	✓	1	-	-	_	4	-	_	-	_	-	1	-	1	_	1	-	_	_	-	_	-	2/0	37-60	_	✓	I/E	100LQFP 108BGA	Р
LM3S2412	96	32	_	-	-	25	-	1	1	3	1	1	2	1	1	4	-	_	-	ı	-	1	-	2	1	1	-	1	10	3	250K	1	2/0	20-49	_	1	I/E	100LQFP 108BGA	Р
LM3S2432	96	32	_	-	-	50	-	1	1	3	1	1	2	1	1	4	-	-	_	_	_	1	-	2	1	1	-	1	10	3	250K	1	2/0	5-34	-	✓	I/E	100LQFP 108BGA	Р
LM3S2533	96	64	-	_	-	50	-	1	1	4	1	1	6	1	1	6	-	-	-	1	-	1	-	2	1	1	-	1	10	3	250K	1	3/0	11-48	1	1	I/E	100LQFP 108BGA	Р
LM3S2601	128	32	_	-	-	50	-	✓	✓	4	✓	1	-	-	-	8	-	-	-	-	_	1	-	3	2	2	-	-	-	-	-	-	2/0	21-60	1	✓	I/E	100LQFP 108BGA	Р
LM3S2608	128	32	_	_	-	50	-	1	1	4	1	1	_	_	_	8	_	_	_	ı	_	1	-	2	2	2	-	1	10	8	500K	1	2/0	15-52	1	1	I/E	100LQFP 108BGA	Р
LM3S2616	128	16	✓	✓	-	50	-	✓	✓	4	-	1	6	1	✓	-	1	-	-	_	-	1	-	1	1	-	-	1	10	6	1M	✓	2/0	1-33	✓	✓	I	64LQFP	Р
LM3S2620	128	32	-	_	-	25	-	✓	1	4	✓	1	4	1	✓	6	1	-	-	ı	-	2	-	1	1	1	-	-	-	-	-	-	3/0	12-52	1	✓	I/E	100LQFP 108BGA	Р
LM3S2637	128	32	_	_	-	50	_	✓	✓	4	✓	1	-	-	_	6	-	_	_	-	-	1	_	2	1	1	-	1	10	4	500K	√	3/0	15-46	√	✓	I/E	100LQFP 108BGA	Р

a. Minimum is number of pins dedicated to GPIO; additional pins are available if certain peripherals are not used. See data sheet for details.

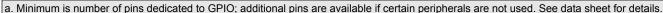
b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

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		Me	mo	ry			Core	Э				Т	ime	rs							Se	erial	Inte	rfac	es					A	nalog								
													N	lotic	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle \mathbb C}$	UART	1 ² C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S2651	128	32	-	_	-	50	-	1	✓	4	✓	1	4	1	✓	6	-	-	-	-	-	1	-	3	1	2	-	1	10	4	500K	✓	1/0	16-53	✓	✓	I/E	100LQFP 108BGA	Р
LM3S2671	128	32	✓	✓	-	50	-	1	✓	4	✓	1	2	1	✓	2	-	-	-	_	_	1	-	1	1	1	-	1	10	4	500K	✓	3/0	3-33	-	✓	I	64LQFP	Р
LM3S2678	128	32	✓	✓	-	50	-	✓	✓	4	✓	1	4	2	✓	2	1	-	-	_	ı	1	-	1	-	1	-	1	10	8	500K	✓	0/0	1-33	-	✓	I	64LQFP	Р
LM3S2730	128	64	-	-	-	50	-	1	✓	3	1	1	-	-	_	4	-	_	-	_	-	1	_	1	-	1	_	-	-	-	_	-	2/0	37-60	-	✓	I/E	100LQFP 108BGA	Р
LM3S2739	128	64	_	_	-	50	-	1	1	3	1	1	6	1	1	6	1	-	-	ı	_	1	-	2	1	1	-	1	10	4	500K	1	1/0	20-56	1	1	I/E	100LQFP 108BGA	Р
LM3S2776	128	64	\	✓	-	50	-	✓	✓	3	✓	1	8	3	✓	1	-	-	-	_	ı	1	_	1	1	1	_	1	10	6	1M	✓	0/0	0-33	✓	✓	I	64LQFP	Р
LM3S2793	128	64	✓	1	-	80	✓	1	1	4	1	2	8	4	✓	8	2	✓	-	-	ı	2	-	3	2	2	✓	2	10	16	1M	✓	3/16	0-67	✓	✓	I	100LQFP 108BGA	S
LM3S2911	256	64	١	_	-	50	-	✓	1	4	1	1	-	-	_	8	-	-	-	ı	١	1	-	3	2	2	-	-	-	-	_	-	2/0	21-60	✓	✓	I/E	100LQFP 108BGA	Р
LM3S2918	256	64	_	_	-	50	-	1	1	4	1	1	-	-	_	8	-	-	-	ı	_	1	-	2	2	2	-	1	10	8	500K	1	2/0	15-52	1	1	I/E	100LQFP 108BGA	Р
LM3S2939	256	64	-	-	-	50	-	✓	1	3	1	1	4	1	1	4	1	-	-	-	-	1	-	3	1	1	-	1	10	3	500K	✓	3/0	18-57	1	✓	I/E	100LQFP 108BGA	Р
LM3S2948	256	64	-	_	-	50	-	✓	1	4	1	1	-	-	_	8	-	-	-	-	ı	2	_	3	1	2	-	1	10	8	1M	✓	3/0	12-52	✓	1	I/E	100LQFP 108BGA	Р



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.



c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

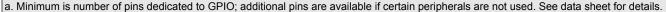
d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.

TEXAS INSTRUME



Product Selector Guide (Continued)

		Me	mo	ry		(Core)				Т	imer	's							Se	erial	Inte	rfac	es					A	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	₽WM ^d	PWM Fault	Dead-Band Generator	ссР	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or O $^{\circ}$	UART	1 ² C	SSI/SPI	l ₂ S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S2950	256	64	-	_	_	50	_	✓	✓	4	✓	1	6	1	✓	6	1	_	-	_	-	2	_	3	1	2	_	-	_	-	_	-	3/0	10-60	1	✓	I/E	100LQFP 108BGA	Р
LM3S2965	256	64	ı	_	-	50	-	✓	✓	4	✓	1	6	1	1	6	2	-	ı	ı	ı	2	-	3	2	2	-	1	10	4	1M	1	3/0	3-56	1	1	I/E	100LQFP 108BGA	Р
LM3S2B93	256	96	√	✓	_	80	✓	1	✓	4	✓	2	8	4	1	8	2	✓	ı	ı	ı	2	_	3	2	2	✓	2	10	16	1M	✓	3/16	0-67	✓	1	ı	100LQFP 108BGA	s
LM3S3634	128	32	\	✓	_	50	_	✓	✓	3	✓	1	ı	_	_	5	ı	_	ı	1	ı	_	Н	2	2	1	_	1	10	8	500K	1	0/0	1-33	✓	✓	I	64LQFP	Р
LM3S3651	128	32	✓	✓	_	50	_	✓	✓	4	✓	1	-	_	_	8	-	_	-	_	-	_	0	1	1	1	_	1	10	4	500K	✓	2/0	0-33	✓	✓	ı	64LQFP	Р
LM3S3739	128	64	✓	✓	-	50	-	✓	✓	4	✓	1	_	_	_	8	-	_	-	-	-	-	Н	3	2	2	_	1	10	8	500K	1	2/0	14-61	✓	✓	ı	100LQFP	Р
LM3S3748	128	64	✓	✓	_	50	_	✓	✓	4	✓	1	8	4	✓	8	1	_	_	_	_	_	Н	2	2	2	_	1	10	8	1M	✓	2/0	3-61	✓	✓	ı	100LQFP	Р
LM3S3749			✓	✓	_	50	_	✓	✓	4	✓	1	8	4	✓	7	1	_	_	_	-	_	Н	3	2	2	_	1	10	8	1M	✓	2/0	0-61	✓	✓	1	100LQFP	
	256		✓	✓	_	50	✓	✓	✓	3	✓	2	_	_	_	6	_	_	_	_	_	_	D	3	2	2	_	1	10	8	1M	✓	2/8	0-33	✓	✓	ı	64LQFP	
			✓	√	-	50	✓	✓	✓	3	✓	2	-	-	_	6	-	_	-	_	-	_	D	3	2	2	_	1	10	8	1M	✓	2/8	0-33	√	✓	1	64LQFP	
LM3S3N26	64	12	√	✓	-	50	✓	✓	✓	3	✓	2	-	_	_	6	_	_	_	_	-	_	D	3	2	2	_	1	10	8	1M	✓	2/8	0-33	√	✓	1	64LQFP	
LM3S3W26	32	8	√	√	-	50	√	√	✓	3	√	2	_	_	_	6	_	_	_	_	_	_	D	3	2	2	_	1	10	8	1M	√	2/8	0-33	V	✓	<u> </u>	64LQFP	
LM3S3Z26	16	6	√	√	_	50	✓	√	√	3	√	2	_	_	-	6	_	_	_	-	_	-	D	3	2	2	_	1	10	8	1M	√	2/8	0-33	√	√		64LQFP	
LM3S5632	128	32	√	✓	_	50	_	✓	✓	3	√	1		-	_	5	_	-	_	_	_	1	Н	2	2	1	_	1	10	6	500K	√	0/0	1-33	\	✓	l	64LQFP	
LM3S5651	128	32	✓	✓	_	80	✓	✓	✓	4	✓	2	6	4	✓	8	2	_	-	_	_	2	0	3	2	2	✓	2	10	16	1M	✓	2/16	0-67	✓	✓	I	100LQFP 108BGA	S



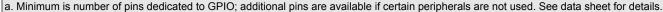
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		Me	emo	ry			Core	•				Т	ime	rs							Se	erial	Inte	rfac	es					A	nalog								
													N	lotio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\rm c}$	UART	l²C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S5652	128		√	√	-	50	-	√	√	3	√	1	-	-	-	6	-	-	-	-	-	1	0	1	1	1	_	1	10	6	500K	√	1/0	0-33	√	✓	1	64LQFP	Р
LM3S5656	128	32	√ 	√	_	80	√	√	√	4	√	2	6	4	√	8	1	_	_	-	_	1	0	3	2	2	_	2	10	8	1M	√	2/16	0-33	√	√		64LQFP	S
LM3S5662	128		√	√	_	50	-	√	√	3	√	1	6	1	√	5	_	_	_	_	_	1	0	1	-	1	_	1	10	4	500K		0/0	0-33	√	√	<u> </u>	64LQFP	P
LM3S5732	128 128	64	√	√	_	50	_	✓ ✓	√	3	√	1	-	_	_	5	_	_	-	_	_	1	H	2	2	1	_	1	10	6 8	500K		0/0	1-33 27-61	√	√	<u> </u>	64LQFP 100LQFP	<u> </u>
LM3S5737	128	64	V	√ √	-	50	-	√ √	✓ ✓	3	✓ ✓	1	-	-	-	8		-	-	-	_	1	Н	3	2	2		1	10	8	500K	✓ ✓	2/0	12-61	√ √	✓ ✓	<u> </u>	100LQFP	
LM3S5747	128	_	V	✓ ✓	_	50	_	✓ ✓	✓	3	✓ ✓	1	6	1	- ✓	2	_	_	_	_	_	1	Н	1	2	2	_	1	10	8	500K		_	27-61	✓	✓ ✓	'	100LQFP	
LM3S5747	128	64	1	√		50	_	√	√	4	√	1	8	4	√	5	1	_	_	_		2	Н	2	2	2		1	10	8	1M	√	2/0	0-61	√	√	<u> </u>	100LQFP	
	128	_	1	√	_	50	_	1	√	3	√	1	_	_	_	6	_	_	_	_	_	1	0	1	1	1	_	1	10	6	500K	-	1/0	0-33	√	√	·	64LQFP	
LM3S5762	128	64	1	1	_	50		√	√	3	1	1	6	1	√	5	_	_	_	_	_	1	0	1	_	1	_	1	10	4	500K	1	0/0	0-33	1	√ -	i	64LQFP	P
LM3S5791			√	1	-	80	1	1	✓	4	1	2	8	4	✓	8	2	1	-	-	-	2	0	3	2	2	1	2	10	16	1M	✓	3/16	0-72	-	✓	ı	100LQFP 108BGA	S
LM3S5951	256	64	1	1	-	80	1	1	1	4	1	2	6	4	✓	8	2	_	-	_	-	2	0	3	2	2	✓	2	10	16	1M	✓	2/16	0-67	✓	✓	ı	100LQFP 108BGA	s
LM3S5956	256	64	1	✓	-	80	✓	✓	1	4	✓	2	6	4	✓	8	1	_	-	_	-	1	0	3	2	2	_	2	10	8	1M	✓	2/16	0-33	✓	✓	I	64LQFP	S
LM3S5B91	256	96	1	✓	-	80	✓	1	✓	4	✓	2	8	4	✓	8	2	✓	-	-	-	2	0	3	2	2	1	2	10	16	1M	✓	3/16	0-72	-	✓	ı	100LQFP 108BGA	s
LM3S5K31			✓	✓	-	80	✓	1	✓	3	✓	2	6	4	✓	6	2	-	-	-	-	1	D	3	2	2	-	2	10	16	1M	✓	2/16	0-67	✓	1	I	100LQFP 108BGA	s



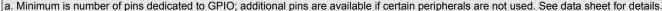
b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



		Me	emo	ry			Core	•				Т	ime	s							Se	erial	Inte	rfac	es					A	nalog								
													N	lotio	n C	ontr	ol		Et	hern	et								Α	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or O ^c	UART	I²C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S5K36	128	24	✓	✓	-	80	✓	✓	✓	3	✓	2	6	4	✓	6	1	_	-	_	-	1	D	3	2	2	_	2	10	8	1M	✓	2/16	0-33	1	✓	ı	64LQFP	S
LM3S5P31	64	24	1	✓	-	80	1	1	1	3	1	2	6	4	✓	6	2	-	ı	_	ı	1	D	3	2	2	-	2	10	16	1M	1	2/16	0-67	✓	✓	1	100LQFP 108BGA	s
LM3S5P36	64	24	✓	✓	_	80	✓	✓	✓	3	✓	2	6	4	✓	6	1	_	-	-	-	1	D	3	2	2	_	2	10	8	1M	✓	2/16	0-33	✓	✓	ı	64LQFP	S
LM3S5P51	64	24	✓	✓	-	80	1	1	1	4	✓	2	6	4	✓	8	2	-	-	_	-	2	0	3	2	2	✓	2	10	16	1M	1	2/16	0-67	✓	✓	1	100LQFP 108BGA	s
LM3S5P56	64	24	✓	✓	-	80	✓	✓	✓	4	✓	2	6	4	1	8	1	_	-	_	-	1	0	3	2	2	_	2	10	8	1M	✓	2/16	0-33	✓	✓	ı	64LQFP	S
LM3S5R31	256	48	1	1	-	80	1	1	1	4	1	2	8	4	1	8	2	1	_	ı	_	1	D	3	2	2	1	2	10	16	1M	1	2/16	0-67	1	1	ı	100LQFP 108BGA	s
LM3S5R36	256	48	1	1	-	80	✓	1	✓	4	✓	2	8	4	✓	8	1	_	-	-	-	1	D	3	2	2	_	2	10	8	1M	1	2/16	0-33	1	✓	Ι	64LQFP	S
LM3S5T36	32	12	✓	✓	_	80	1	✓	✓	3	✓	2	6	4	✓	6	1	_	ı	-	ı	1	D	3	2	2	_	2	10	8	1M	✓	2/16	0-33	1	1	I	64LQFP	S
LM3S5Y36	16	8	✓	✓	_	80	✓	✓	✓	3	✓	2	6	4	✓	6	1	_	-	_	-	1	D	3	2	2	_	2	10	8	1M	✓	2/16	0-33	✓	✓	ı	64LQFP	S
LM3S6100	64	16	-	-	-	25	-	1	1	3	✓	1	-	-	-	4	-	-	✓	-	-	-	-	1	-	1	-	-	-	-	-	-	1/0	10-30	-	✓	I/E	100LQFP 108BGA	Р
LM3S6110	64	16	-	-	-	25	-	1	✓	3	✓	1	2	1	✓	4	ı	_	\	-	ı	_	-	1	-	1	-	-	-	-	_	_	3/0	8-35	-	✓	I/E	100LQFP 108BGA	Р
LM3S6420	96	32	_	_	-	25	_	1	1	3	1	1	_	_	_	4	_	-	>	_	_	_	-	1	_	1	_	_	_	_	-	_	2/0	23-46	_	✓	I/E	100LQFP 108BGA	Р



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

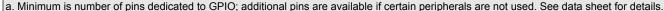


c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



			Me	emo	ry			Cor	Э				Т	imer	'S							Se	erial	Inte	rfac	es					A	nalog								
														M	lotio	n C	ontr	ol		Et	hern	et									DC									
Part Numb	er	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\sf O}^{\scriptscriptstyle \mathbb C}$	UART	1 ² C	SSI/SPI	12S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S6	6422	96	32	_	_	_	25	_	✓	✓	3	✓	1	-	_	_	4	-	_	✓	_	-	_	_	1	_	1	_	1	10	2	250K	✓	2/0	12-34	_	✓	I/E	100LQFP 108BGA	Р
LM3S6	6432	96	32	-	-	-	50	-	1	1	3	✓	1	2	1	1	4	ı	-	>	ı	I	-	-	2	1	1	-	1	10	3	250K	✓	2/0	14-43	_	1	I/E	100LQFP 108BGA	Р
LM3S6	5537	96	64	_	-	-	50	-	✓	✓	4	✓	1	6	1	1	6	-	_	✓	ı	√	-	-	2	1	1	-	1	10	4	500K	✓	2/0	6-41	✓	✓	I/E	100LQFP 108BGA	Р
LM3S6	610	128	32	-	-	-	25	-	✓	✓	4	1	1	4	1	1	6	1	-	✓	ı	-	-	-	3	1	1	-	-	-	-	-	-	3/0	5-46	✓	✓	I/E	100LQFP 108BGA	Р
LM3S6	6611	128	32	-	-	-	50	-	1	✓	4	1	1	-	-	_	6	_	-	✓	ı	-	-	-	3	2	2	-	_	-	-	-	-	2/0	10-46	1	1	I/E	100LQFP 108BGA	Р
LM3S6	618	128	32	_	-	-	50	-	1	1	4	1	1	_	-	-	6	_	-	✓	ı	_	-	-	2	2	2	-	1	10	8	500K	1	2/0	5-38	1	1	I/E	100LQFP 108BGA	Р
LM3S6	6633	128	32	-	-	-	50	-	1	✓	3	1	1	-	-	-	6	-	-	✓	-	-	-	-	2	1	1	-	1	10	3	500K	1	1/0	15-41	1	1	I/E	100LQFP 108BGA	Р
LM3S6	6637	128	32	_	-	-	50	-	1	1	4	1	1	_	_	-	6	-	_	✓	1	-	-	-	2	1	1	_	1	10	4	1M	1	3/0	11-41	1	1	I/E	100LQFP 108BGA	Р
LM3S6	6730	128	64	-	-	-	50	-	1	1	3	1	1	ı	-	-	4	ı	-	>	_	ı	-	-	1	-	1	-	ı	-	-	-	-	2/0	23-46	-	✓	I/E	100LQFP 108BGA	Р
LM3S6	6753	128	64	_	-	-	50	-	1	1	4	✓	1	6	1	1	4	1	-	✓	-	✓	-	_	2	1	1	_	1	10	4	500K	1	2/0	5-41	1	1	I/E	100LQFP 108BGA	Р



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



		Me	emo	ry			Core	Э				Т	imer	's							Se	erial	Inte	rfac	es					Aı	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et									DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or ${\rm O}^{\scriptscriptstyle \mathbb C}$	UART	1 ₂ C	SSI/SPI	12S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S6911	256	64	_	-	-	50	-	✓	1	4	✓	1	-	_	_	6	_	-	✓	-	-	-	_	3	2	2	-	_	-	_	_	-	2/0	10-46	1	1	I/E	100LQFP 108BGA	Р
LM3S6918	256	64	-	-	-	50	-	1	1	4	1	1	_	_	_	6	_	-	✓	ı	-	-	-	2	2	2	-	1	10	8	500K	1	2/0	5-38	1	1	I/E	100LQFP 108BGA	Р
LM3S6938	256	64	-	-	-	50	-	1	1	4	1	1	-	-	_	6	-	_	✓	_	_	-	-	3	1	1	-	1	10	8	1M	1	3/0	7-38	1	1	I/E	100LQFP 108BGA	Р
LM3S6950	256	64	-	-	-	50	-	1	1	4	1	1	6	1	1	6	1	-	✓	ı	✓	-	_	3	1	2	-	_	-	_	-	_	3/0	1-46	1	1	I/E	100LQFP 108BGA	Р
LM3S6952	256	64	-	-	-	50	-	1	1	3	✓	1	4	1	1	4	1	-	✓	-	-	-	-	3	1	1	-	1	10	3	500K	1	3/0	6-43	1	1	I/E	100LQFP 108BGA	Р
LM3S6965	256	64	_	-	-	50	-	1	1	4	1	1	6	1	1	4	2	-	✓	-	-	-	-	3	2	1	-	1	10	4	1M	1	2/0	0-42	1	1	I/E	100LQFP 108BGA	Р
LM3S8530	96	64	-	-	-	50	-	1	1	4	1	1	_	-	-	2	-	-	✓	_	-	3	-	1	1	2	-	-	-	-	-	-	0/0	8-35	-	1	I/E	100LQFP 108BGA	Р
LM3S8538	96	64	-	-	-	50	-	1	1	4	1	1	_	_	_	4	_	-	✓	1	✓	1	-	2	1	1	-	1	10	8	1M	1	3/0	7-36	-	1	I/E	100LQFP 108BGA	Р
LM3S8630	128	32	-	-	-	50	-	1	1	4	✓	1	-	-	-	2	-		✓	-	-	1	-	2	1	1	-	-	-	-	-	-	0/0	10-31	1	1	I/E	100LQFP 108BGA	Р
LM3S8730	128	64	_	-	-	50	-	✓	1	4	1	1	-	-	_	2	-	ı	✓	-	✓	1	-	2	1	1	-	-	-	-	_	-	0/0	11-32	1	1	I/E	100LQFP 108BGA	Р

a. Minimum is number of pins dedicated to GPIO; additional pins are available if certain peripherals are not used. See data sheet for details.

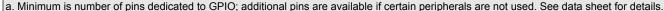
b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



			Ме	mo	ry			Cor	е				Т	imeı	's							Se	erial	Inte	rfac	es					Aı	nalog								
														M	lotio	n C	ontro	ol		Et	hern	et								Δ	DC									
Part Number	(KB)	riasn (NB)		ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	ссР	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or 0°	UART	1²C	SSI/SPI	l ₂ S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S87	33 1	28	64	-	_	_	50	-	✓	✓	4	✓	1	-	_	_	4	ı	ı	✓	-	-	1	-	2	1	1	_	1	10	4	500K	✓	3/0	5-35	✓	✓	I/E	100LQFP 108BGA	Р
LM3S87	38 12	28	64	-	-	-	50	-	1	✓	4	√	1	-	-	-	6	-	-	✓	-	-	1	-	3	1	2	-	1	10	8	500K	1	1/0	4-38	1	1	I/E	100LQFP 108BGA	Р
LM3S89	30 2	56	64	_	_	-	50	_	1	1	4	1	1	_	_	_	2	_	-	✓	-	_	2	_	1	1	1	_	-	-	-	-	-	0/0	13-34	1	1	I/E	100LQFP 108BGA	Р
LM3S89	33 2	56	64	-	-	-	50	-	1	1	4	1	1	-	-	-	4	_	-	✓	-	✓	1	_	2	1	1	-	1	10	4	1M	1	3/0	6-36	1	1	I/E	100LQFP 108BGA	Р
LM3S89	38 2	56	64	-	_	-	50	-	1	1	4	1	1	-	-	_	6	_	_	✓	-	✓	1	-	3	2	1	-	1	10	8	1M	1	3/0	3-38	1	1	I/E	100LQFP 108BGA	Р
LM3S89	32 2	56	64	-	_	_	50	-	1	1	4	1	1	6	1	1	2	2	-	✓	-	✓	1	_	2	1	1	_	1	10	4	500K	1	1/0	5-42	1	1	I/E	100LQFP 108BGA	Р
LM3S89	70 2	56	64	-	-	-	50	-	1	1	4	1	1	-	-	-	2	_	-	✓	-	✓	3	-	2	1	2	-	-	-	-	-	-	0/0	17-46	1	1	I/E	100LQFP 108BGA	Р
LM3S89	71 2	56	64	-	_	-	50	-	1	1	4	1	1	6	1	1	6	1	ı	✓	-	_	1	-	1	-	1	-	1	10	8	1M	1	1/0	4-38	1	1	I/E	100LQFP 108BGA	Р
LM3S97	31 12	28	64	✓	1	-	80	1	1	✓	4	✓	2	-	-	-	8	-	✓	✓	-	_	3	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	1	I	100LQFP 108BGA	s
LM3S97	90 1:	28	64	✓	✓	-	80	1	1	✓	4	1	2	_	_	_	8	-	✓	✓	-	-	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-60	1	1	I	100LQFP 108BGA	S



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.

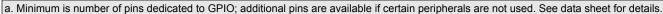


c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.



		Me	mo	ry		(Core	Э				T	imer	s							Se	rial	Inte	rfac	es					Aı	nalog								
													M	lotio	n C	ontr	ol		Et	hern	et								Δ	DC									
Part Number	Flash (KB)	SRAM (KB)	ROM Software Libraries	DMA	SAFERTOS™	Max Speed (MHz)	Internal Precision Oscillator	MPU	SysTick (24-bit)	General-Purpose	Real-Time Clock (RTC)	Watchdog	PWM ^d	PWM Fault	Dead-Band Generator	CCP	QEI Channels	External Peripheral Interface	10/100 MAC+PHY	10/100 MAC with MII Interface	IEEE 1588	CAN MAC	USB D, H, or 0°	UART	I²C	SSI/SPI	l ² S	ADC Units	ADC Resolution (10- or 12-bit)	ADC Channels	ADC Speed (samples per second)	Internal Temp Sensor	Analog/Digital Comparators	GPIOs (5-V tolerant) ^a	Battery-Backed Hibernation	LDO Voltage Regulator	Operating Temperature ^b	Package	Production (P) or Sampling (S)
LM3S979	128	64	✓	1	-	80	1	1	1	4	1	2	8	4	1	8	2	✓	✓	-	ı	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	✓	I	100LQFP 108BGA	s
LM3S999	256	64	✓	1	-	80	1	1	1	4	1	2	6	4	1	8	2	_	✓	-	✓	2	0	3	2	2	✓	2	10	16	1M	1	2/16	0-60	1	1	I	100LQFP 108BGA	S
LM3S9B8	256	96	✓	1	-	80	1	1	1	4	1	2	-	-	-	8	-	✓	✓	-	-	3	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	✓	ı	100LQFP 108BGA	s
LM3S9B9	256	96	✓	1	-	80	1	1	1	4	1	2	-	-	-	8	-	1	✓	-	-	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-60	1	1	I	100LQFP 108BGA	S
LM3S9B9	256	96	✓	1	-	80	1	1	1	4	1	2	8	4	1	8	2	✓	✓	-	-	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	✓	I	100LQFP 108BGA	s
LM3S9B9	256	96	✓	1	-	80	1	1	1	4	1	2	8	4	1	8	2	1	✓	_	✓	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	1	I	100LQFP 108BGA	
LM3S9B9	256	96	√	1	1	80	1	1	1	4	1	2	8	4	1	8	2	1	✓	-	✓	2	0	3	2	2	✓	2	10	16	1M	1	3/16	0-65	-	✓	I	100LQFP 108BGA	s
LM3S9L9	128	48	✓	1	-	80	1	1	1	4	1	2	6	4	1	8	2	-	✓	-	✓	2	0	3	2	2	✓	2	10	16	1M	1	2/16	0-60	1	1	I	100LQFP 108BGA	S



b. Industrial (I) temperature is -40 to +85 °C and Extended (E) temperature is -40 to +105 °C.



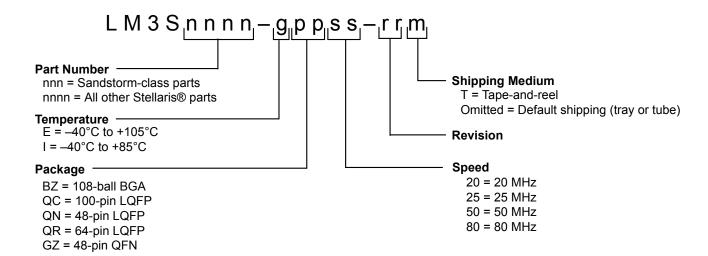
c. USB options for Stellaris microcontrollers include Device Only (D) capability, Host/Device (H) capability, and On-The-Go/Host/Device capability (O).

d. PWM motion-control functionality can be achieved through dedicated motion control hardware (the PWM pins) or through the motion control features of the general-purpose timers (the CCP pins). See data sheet for details.

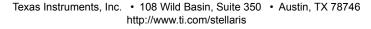
Stellaris® Family



Full Part Number Decoder











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