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02. PM8916 Control and MPP/Clock	31. LTE/W/TD/GSM Antenna Switch	
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10. MSM8916: GPIO	39. QFE1550 DRX Tunner	
11. MSM8916: MIPI and RF Control	40. B7/40/41 DRX Switch	
12. MSM8916: POWER1	41. B39/B1/B3 DRX Switch	
13. MSM8916: POWER2	42. WTR2605_SECONDARY PATH	
14. MSM8916:GND	43. WTR2605_POWER DISTRIBUTION	
15. MEMORY:LPDDR3+EMMC	44. Antenna_SECONDARY PATH	
16. Battery Connector	45. RESERVED	
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18. Mic and Receiver	47. CDMA BC0(Voice) TRX	
19. EARPHONE	48. ET_APT	
20. LCD interface and backlight	49. WCN3620	
21. Main/Slave Camera and Flash	50. WIFI FEM	
22. Sensors	51. GPS/XO DISTRIBUTOR	
23. SIM/TF card	52. NFC	
24. Keypad/LED/Status indicator		
25. Touch Interface		
26. Test Point/GND/Shields		
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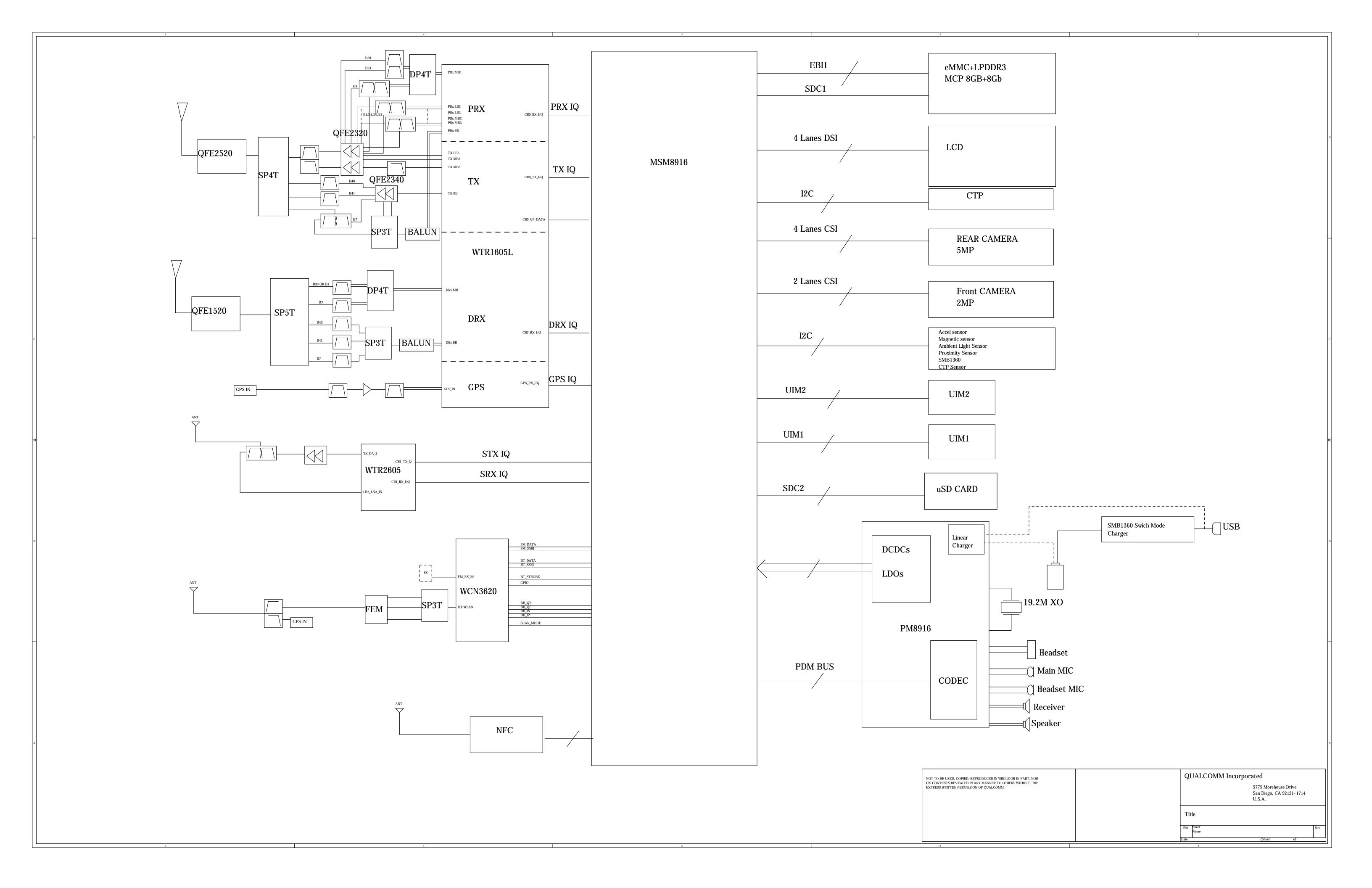
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Title

### **Revision History**

SCHEMATIC MCN	HW VERSION	VERSION	DESCRIPTION OF CHANGE
	EVT1.0	A	EVT1.0 Initial Release

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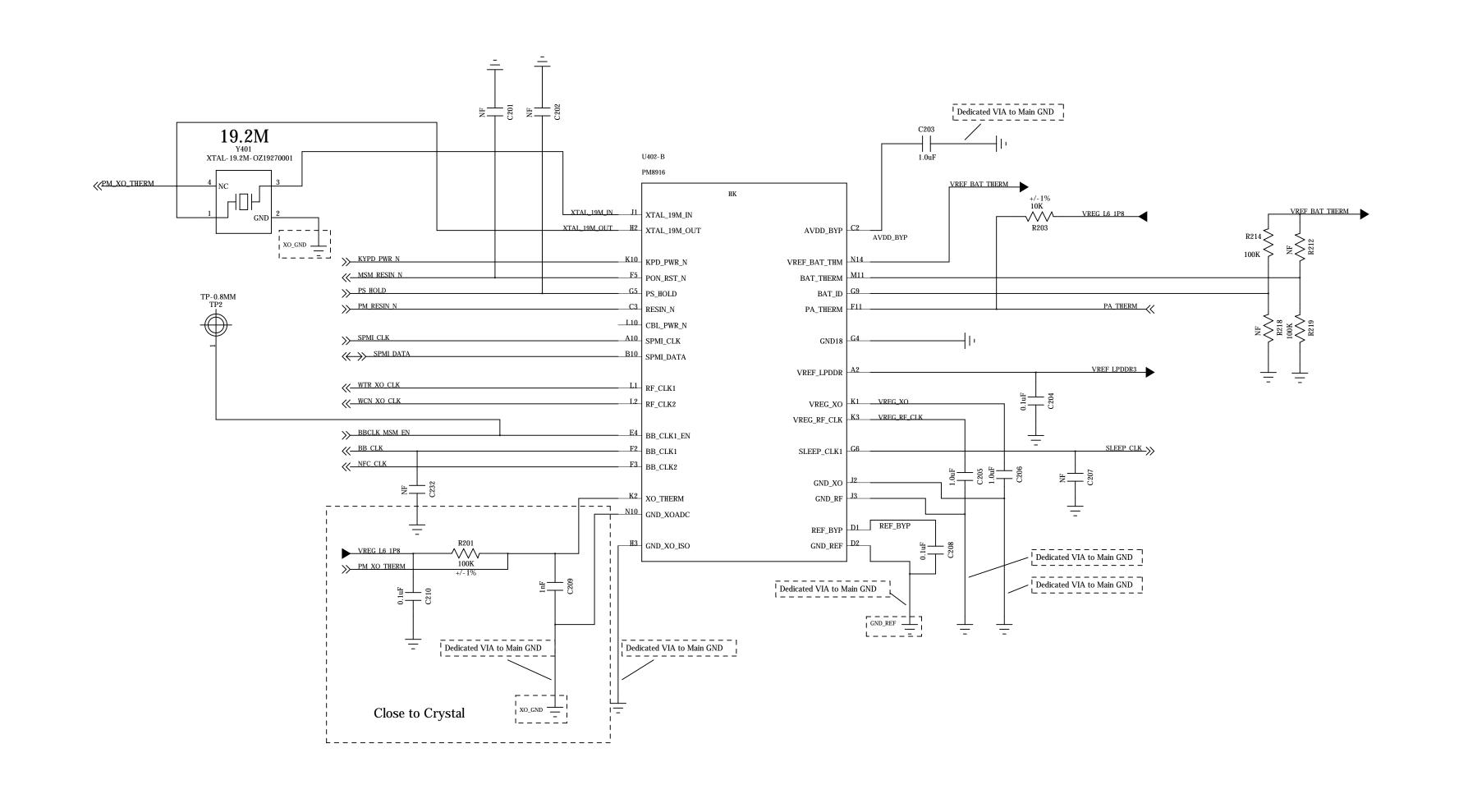


	MSM8916 GPIO Configuration For QRD8916						
GPIO_0	NC	GPIO_41	WCSS_WLAN_DATA_1	GPIO_82	WTR1_SWITCH_CTL3		
GPIO_1	NC	GPIO_42	WCSS_WLAN_DATA_0	GPIO_83	WTR1_GSM_PA_HB_EN		
GPIO_2	NC	GPIO_43	WCSS_WLAN_SET	GPIO_84	WTR1_SWITCH_CTL0		
GPIO_3	NC	GPIO_44	WCSS_WLAN_CLK	GPIO_85	DRX_MODE_SEL2		
GPIO_4	UART_MSM_TX	GPIO_45	WCSS_FM_SSBI	GPIO_86	NC		
GPIO_5	UART_MSM_RX	GPIO_46	WCSS_FM_SDI	GPIO_87	WTR0_PA_R1		
GPIO_6	SENSOR_I2C_SDA	GPIO_47	WCSS_BT_CTRL	GPIO_88	WTR1_PA_R1		
GPIO_7	SENSOR_I2C_SCL	GPIO_48	WCSS_BT_DAT_STB	GPIO_89	WTR1_PA_R0		
GPIO_8	NC	GPIO_49	LCD_ID	GPIO_90	DRX_MODE_SEL0		
GPIO_9	NC	GPIO_50	CTP_ID	GPIO_91	GRFC_17		
GPIO_10	NC	GPIO_51	MCAM_ID	GPIO_92	WTR1_PA_BC0_EN		
GPIO_11	NC	GPIO_52	NC	GPIO_93	DRX_MODE_SEL1		
GPIO_12	TP_RST_N	GPIO_53	UIM2_DATA	GPIO_94	WTR0_PA_R0		
GPIO_13	TP_INT_N	GPIO_54	UIM2_CLK	GPIO_95	WTR1_SWITCH_CTRL2		
GPIO_14	BUCK_SMB_I2C_SDA	GPIO_55	UIM2_RESET	GPIO_96	EXT_GPS_LNA_EN		
GPIO_15	BUCK_SMB_I2C_SCL	GPIO_56	NC	GPIO_97	NC		
GPIO_16	NC	GPIO_57	UIM1_DATA	GPIO_98	NC		
GPIO_17	NC	GPIO_58	UIM1_CLK	GPIO_99	CH0_GSM_TX_PHASE_D0		
GPIO_18	TP_I2C_SDA	GPIO_59	UIM1_RESET	GPIO_100	CH0_GSM_TX_PHASE_D1		
GPIO_19	TP_I2C_SCL	GPIO_60	NC	GPIO_101	CH1_GSM_TX_PHASE_D0		
GPIO_20	NFC_DISABLE	GPIO_61	UIM_BATT_ALM	GPIO_102	CH1_GSM_TX_PHASE_D1		
GPIO_21	NFC_INT_N	GPIO_62	SMB_INT	GPIO_103	CH0_SSBI_WTR_RX		
GPIO_22	NFC_I2C_SDA	GPIO_63	CDC_PDM_CLK	GPIO_104	CH0_SSBI_WTR_TX		
GPIO_23	NFC_I2C_SCL	GPIO_64	CDC_PDM_SYNC	GPIO_105	CH1_SSBI_WTR_RX		
GPIO_24	LCD_TE0	GPIO_65	CDC_PDM_TX	GPIO_106	CH1_SSBI_WTR_TX		
GPIO_25	LCD_RST_N	GPIO_66	CDC_PDM_RX0	GPIO_107	KEY_VOL_UP_N		
GPIO_26	CAM0_MCLK	GPIO_67	CDC_PDM_RX1	GPIO_108	NC		
GPIO_27	CAM1_MCLK	GPIO_68	CDC_PDM_RX2	GPIO_109	NC		
GPIO_28	SCAM_RST_N	GPIO_69	NC	GPIO_110	USB_HS_ID		
GPIO_29	CAM_I2C_SDA	GPIO_70	RFFE1_CLK	GPIO_111	NC		
GPIO_30	CAM_I2C_SCL	GPIO_71	RFFE1_DATA	GPIO_112	ACCL_INT1_N		
GPIO_31	CAM_FLASH_TORCH_EN	GPIO_72	RFFE2_CLK	GPIO_113	ALSP_INT_N		
GPIO_32	CAM_FLASH_TORCH_MODE	GPIO_73	RFFE2_DATA	GPIO_114	ACCL_INT2_N		
GPIO_33	SCAM_PWDN	GPIO_74	NC	GPIO_115	GYRO_INT_N		
GPIO_34	MCAM_PWDN	GPIO_75	GRFC_1	GPIO_116	NC		
GPIO_35	MCAM_RST_N	GPIO_76	GRFC_2	GPIO_117	NC		
GPIO_36	NC	GPIO_77	WTR1_GSM_PA_LB_EN	GPIO_118	NC		
GPIO_37	FORCE_USB_BOOT	GPIO_78	TDS_B34/B39_PA_ON	GPIO_119	CTP_LED_EN		
GPIO_38	NC	GPIO_79	WTR1_SWITCH_CTL1	GPIO_120	CAM_VCM_PWDN		
GPIO_39	WCSS_BT_SSBI	GPIO_80	WTR0_RX_ON	GPIO_121	NC		
GPIO_40	WCSS_WLAN_DATA_2	GPIO_81	WTR0_RF_ON				

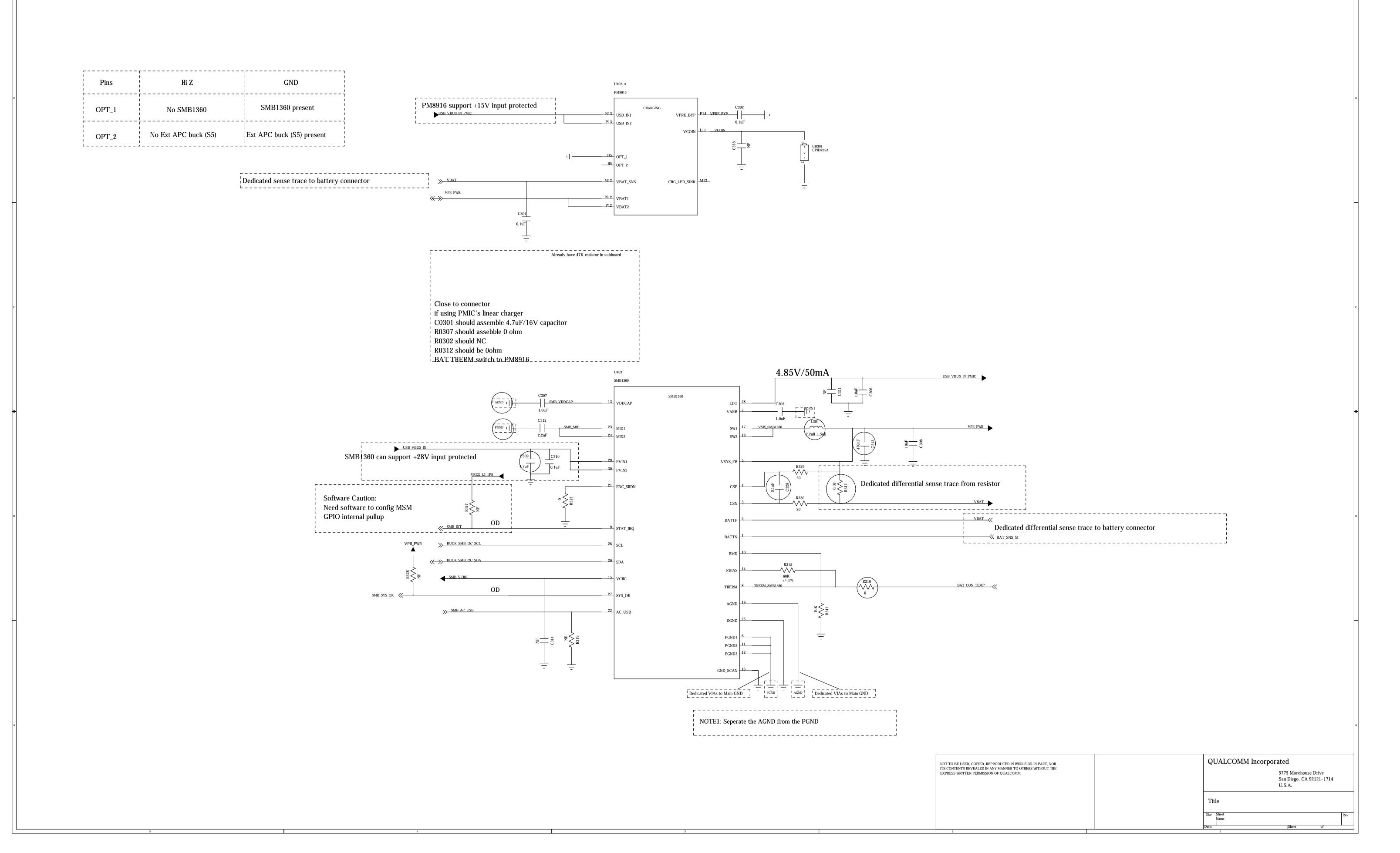
PM8916 GPIO/MPP Configuration For QRD8916							
GPIO_1	UIM_BATT_ALM	MPP_1	VDD_PX_BIAS_MPP				
GPIO_2	NFC_CLK_REQ	MPP_2	SCAM_ID				
GPIO_3	NC	MPP_3	VREF_DAC_MPP				
GPIO_4	NC	MPP_4	PWM_OUT				

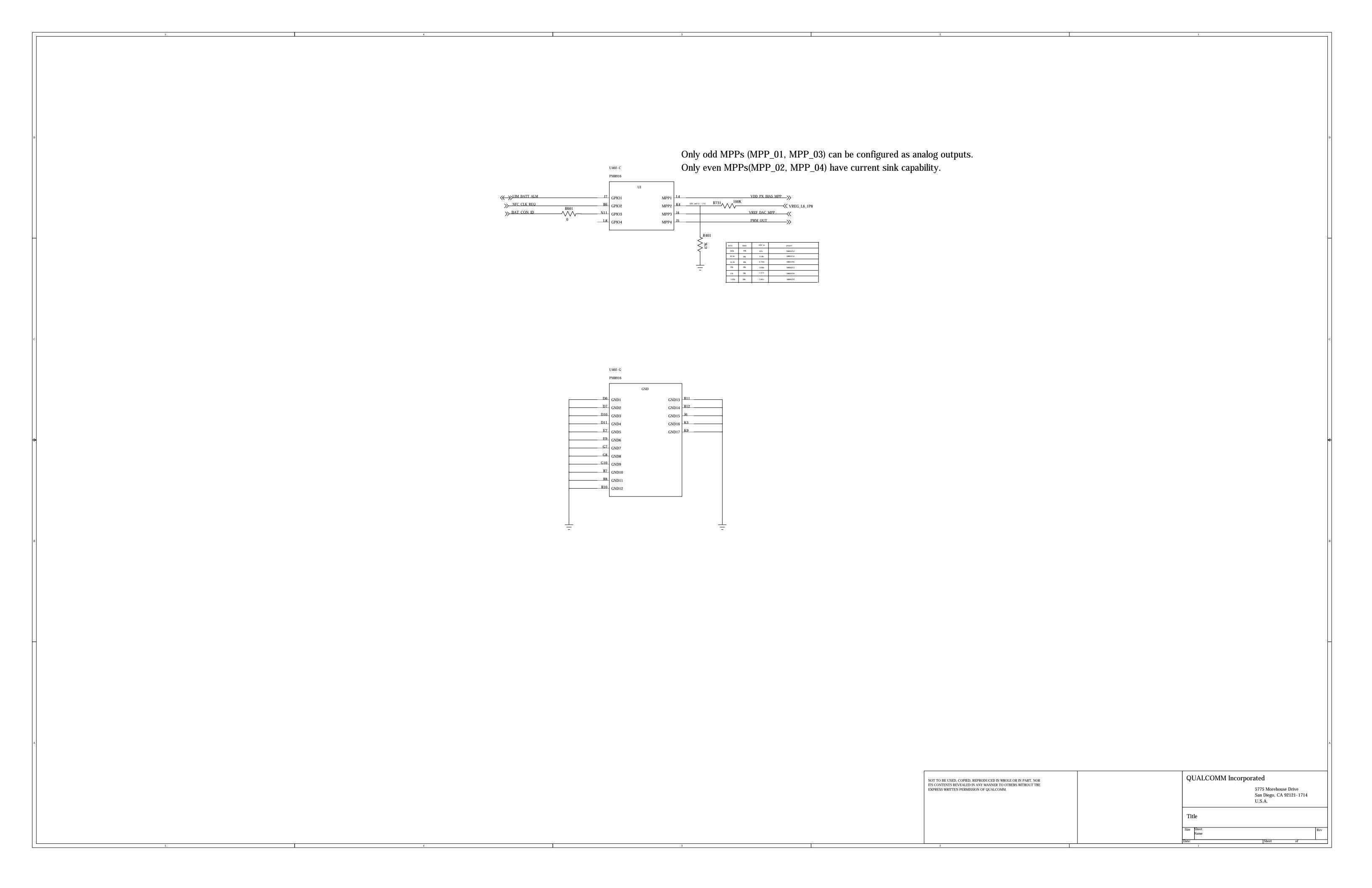
Sensors I2C Address List For QRD8916						
I2C GPIO	Device	MPN	Write Address	Read Address		
	Accelerometer	BMA223	0x30	0x31		
GPIO[6:7]	Magnetometer	MMC34160PJ	0x60	0x61		
	Gyroscope	MPU-6050C	0xD0	0xD1		
	ALS/Proximity	TMD27723	0x72	0x73		
GPIO[18:19]	Touch Screen	FT6346	0x70	0x71		
CDIO[20.20]	Front Camera	OV2680	0x20	0x21		
GPIO[29:30]	Rear Camera	OV5648	0x6C	0x6D		

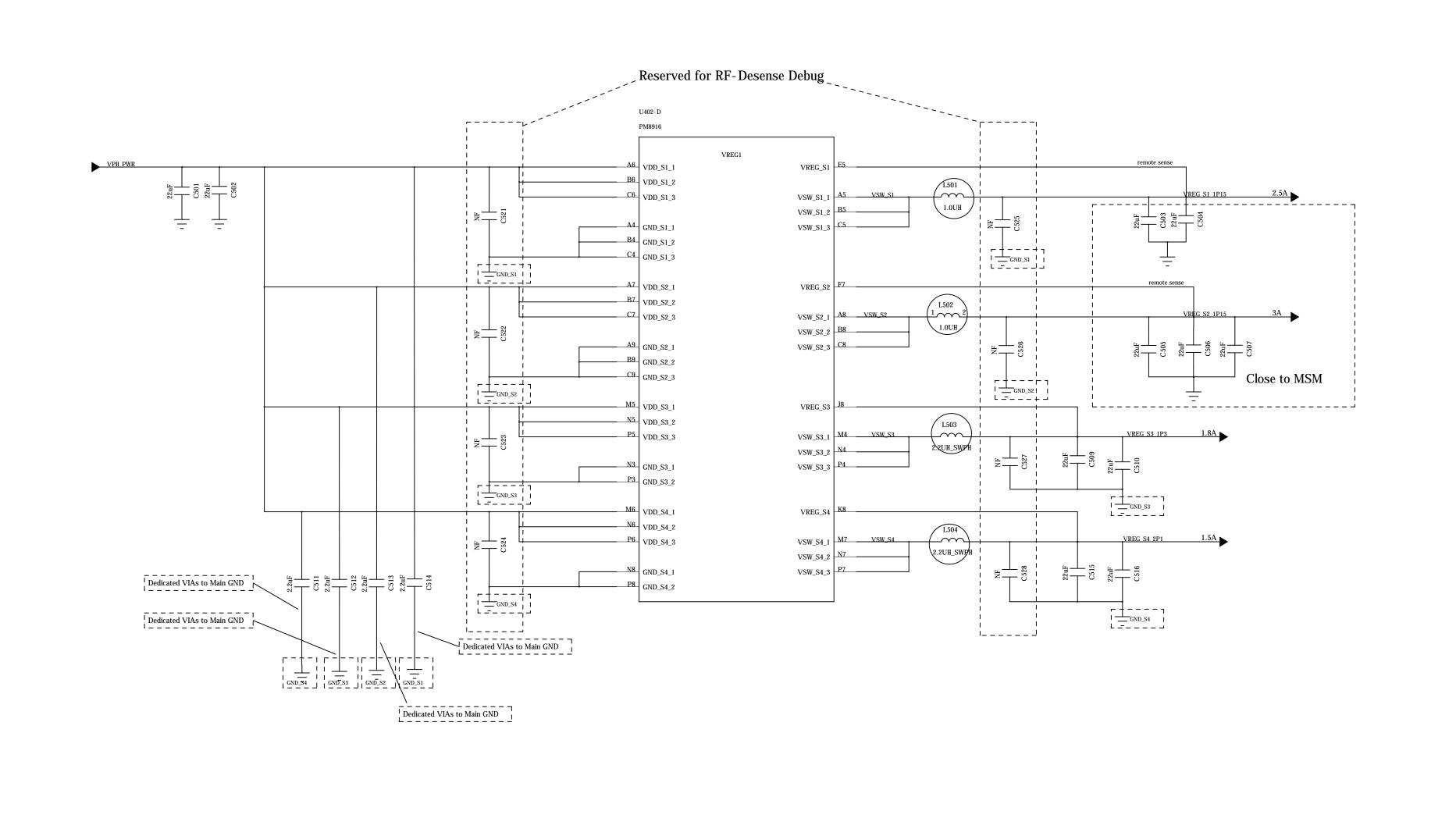
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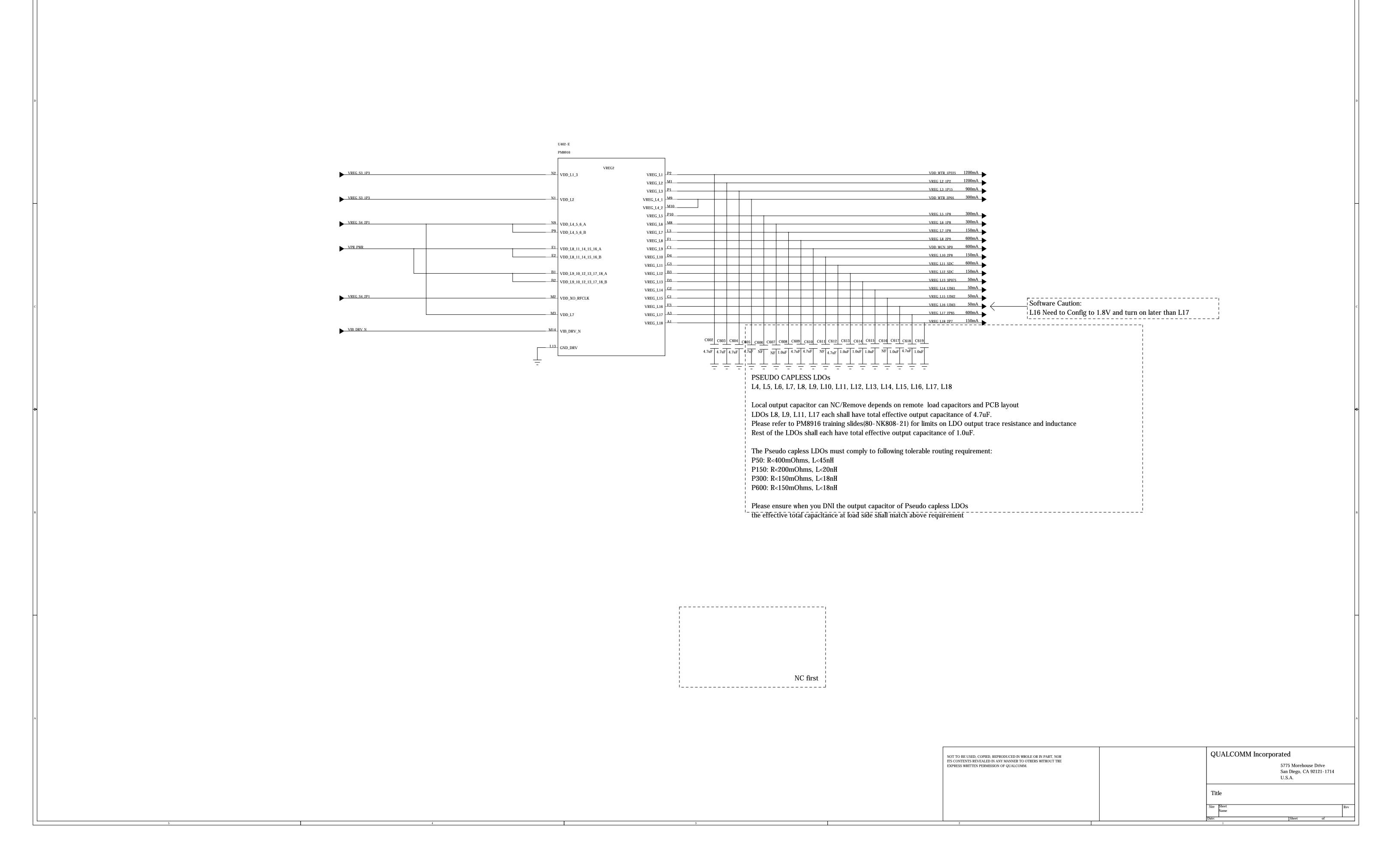
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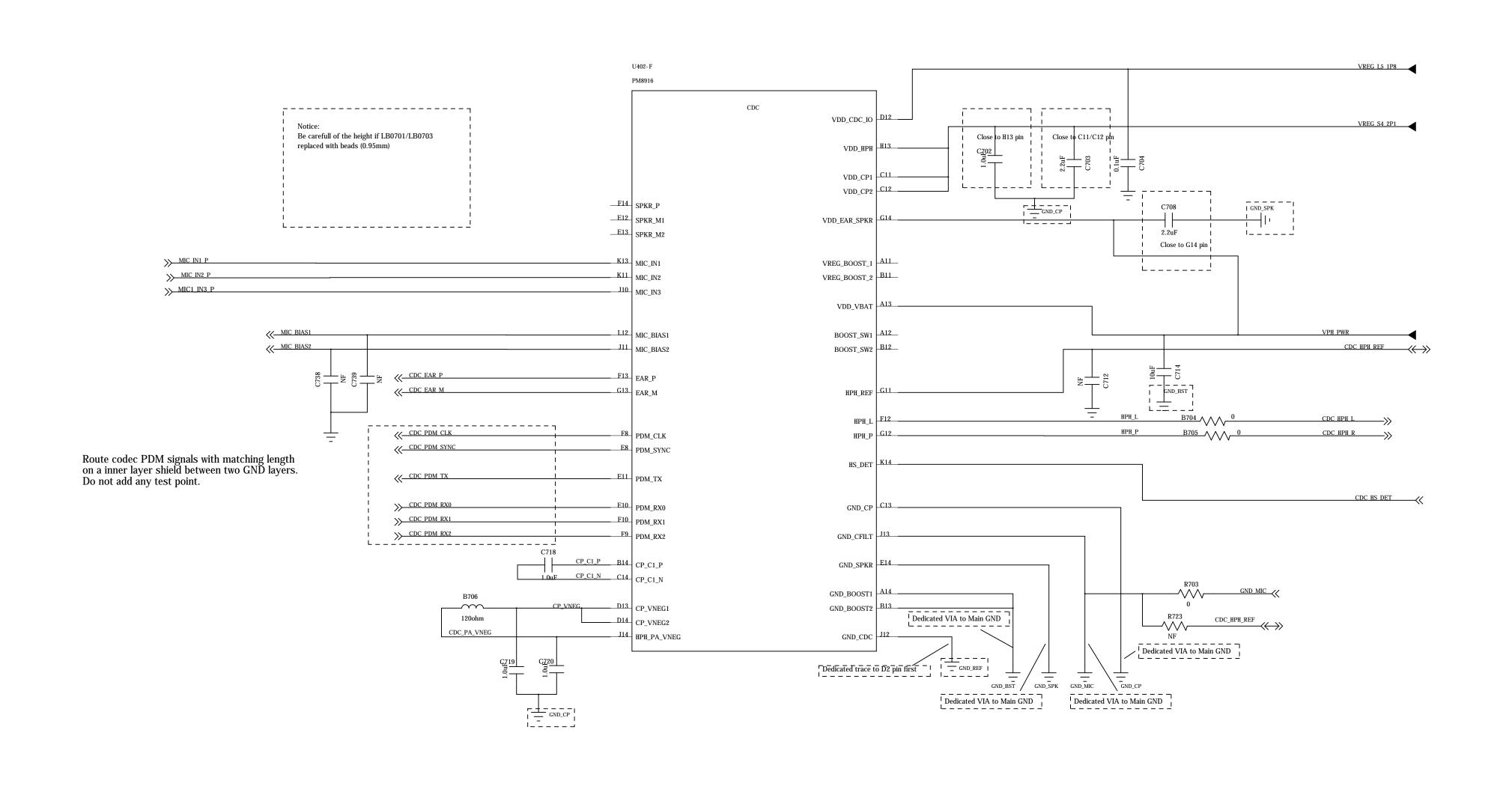
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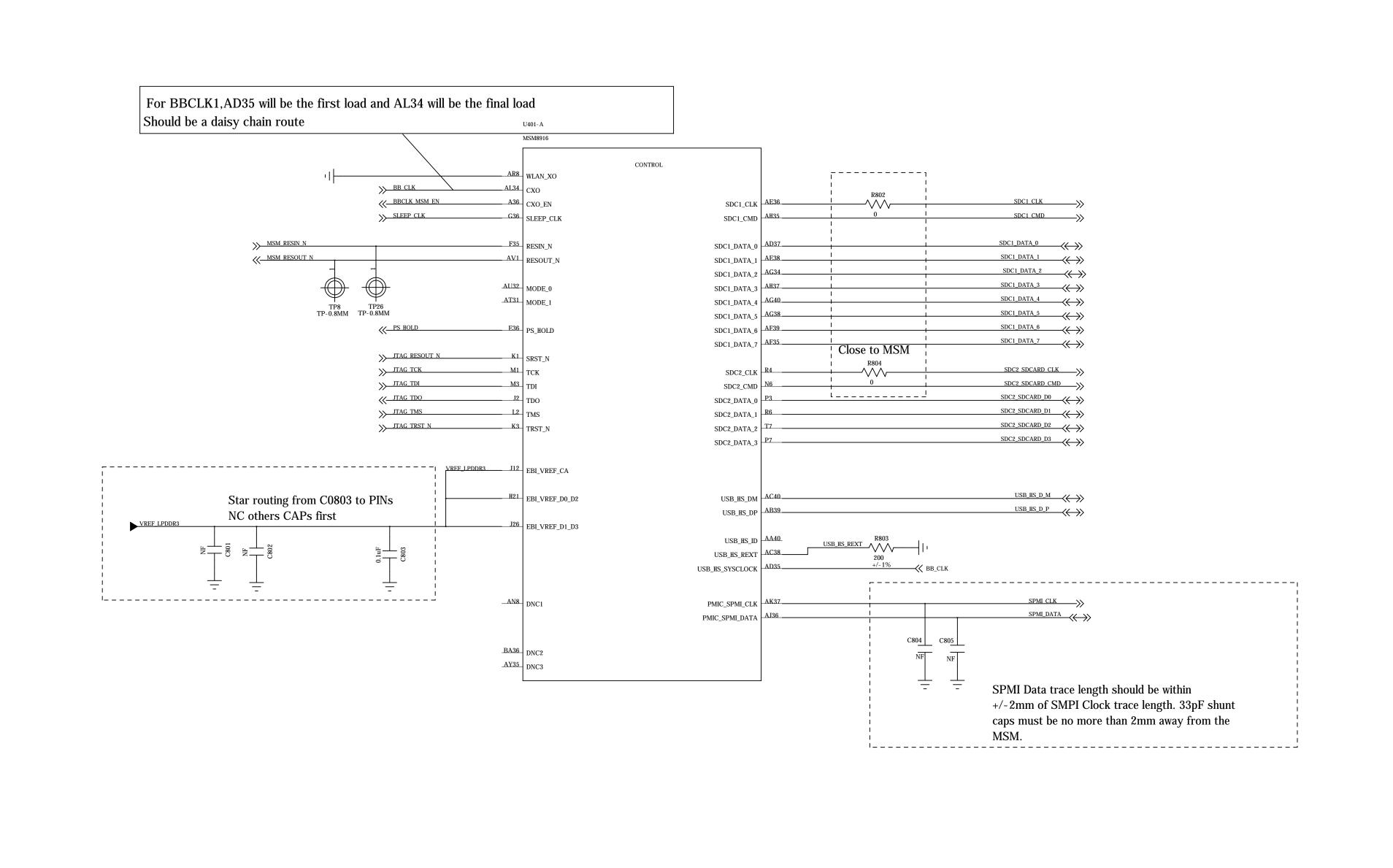
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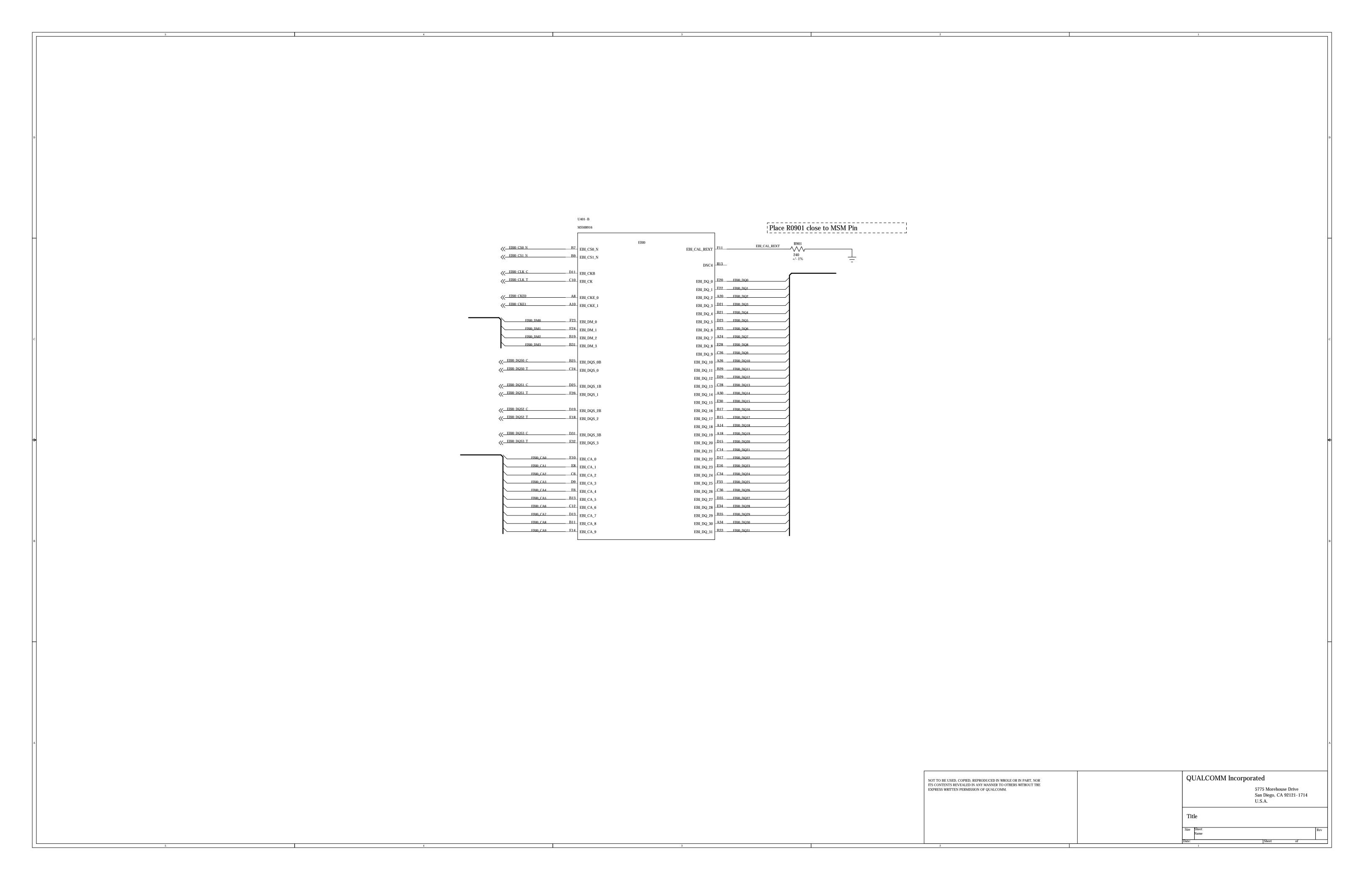
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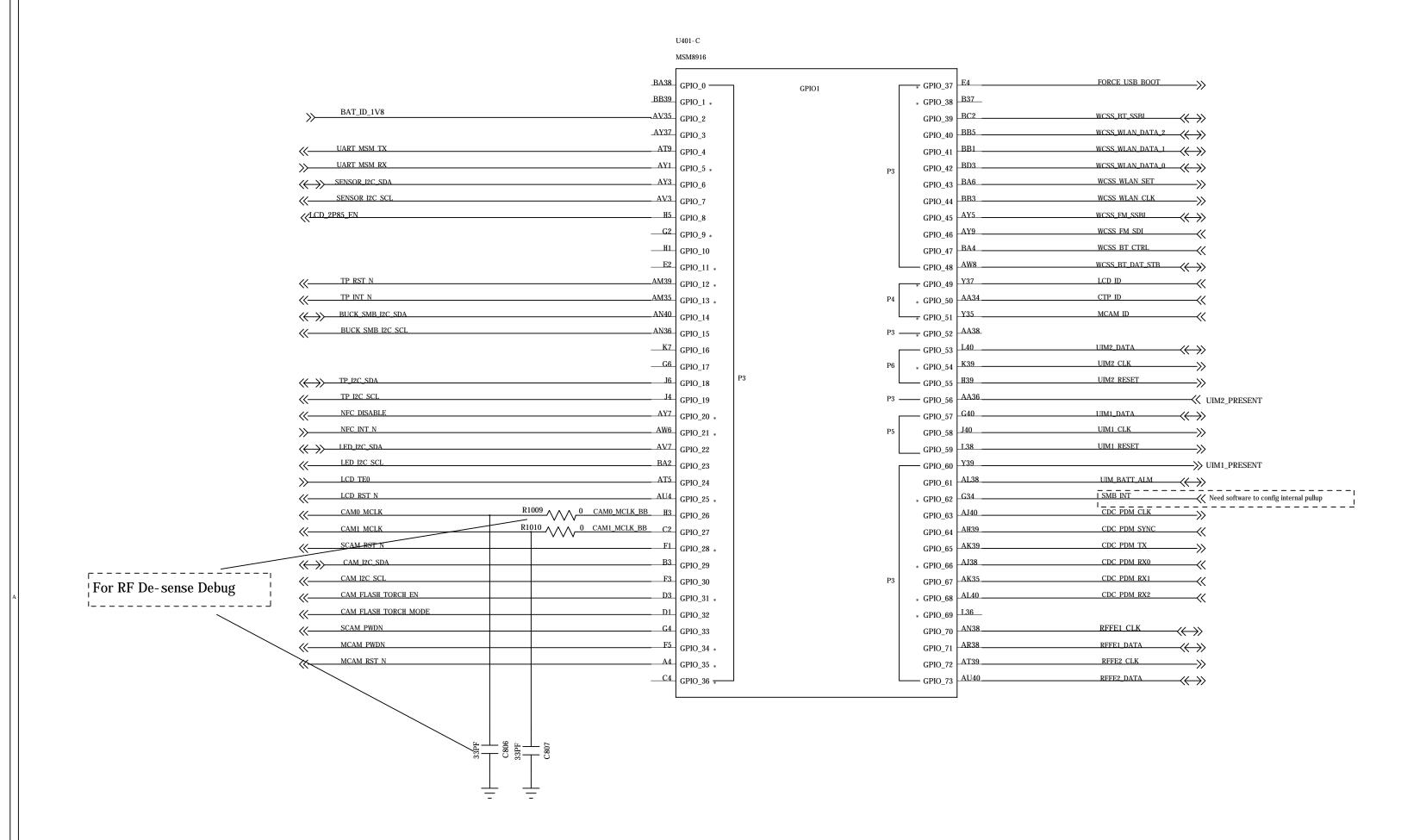


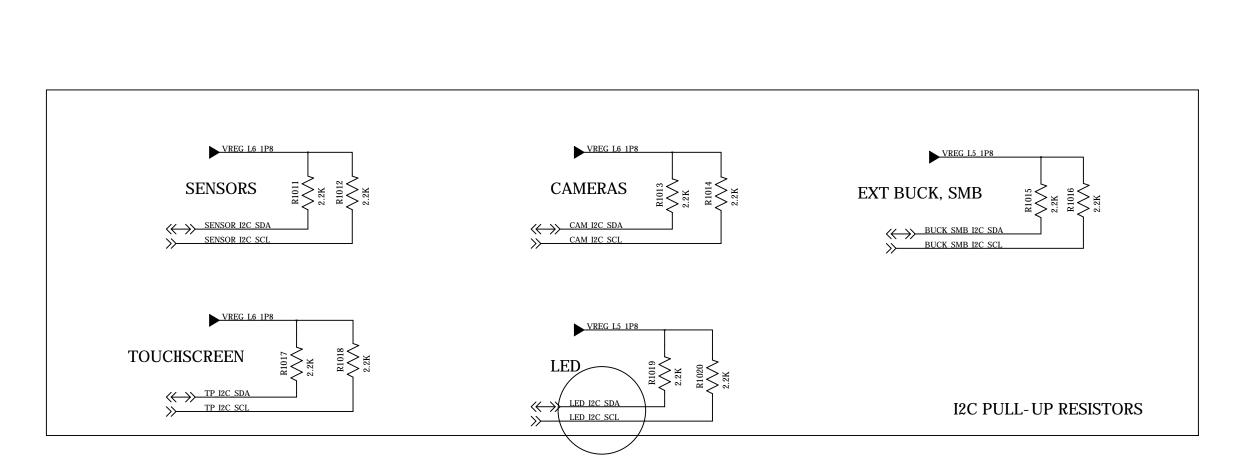


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			U401-D							
			MSM8916							
							7			
		CRECO		GPIO2						
	✓LTE_BPI_OUT74	GRFC0 AP37	GPIO_74 ———	7	Г		B39	USB HS II	<del>)                                    </del>	
	ANT TUNE CTL	GRFC1_AV39	GPIO_75			* GPIO_111	GPIO_111 will be	e in O/P mode on Power Or	Reset, After Power On R	eset, SW can configure it
	<──ANT TUNE EN	GRFC2 AP35	GPIO_76			* GPIO_112	AW36	I2S 2 DI	——————————————————————————————————————	
		GRFC3 _BC4	GPIO_77			* GPIO_113	D39	ALSP INT	N	
	✓ TDS B34/B39 PA ON	GRFC4 BE4	GPIO_78			* GPIO_114	E40	ACCL INT	<sup>2 N</sup> ≪	
	<lte_bpi_out79< td=""><td>GRFC5 BC6</td><td>GPIO_79</td><td></td><td>Р3</td><td>* GPIO_115</td><td>E38</td><td>GYRO INT</td><td><math>\sim</math></td><td></td></lte_bpi_out79<>	GRFC5 BC6	GPIO_79		Р3	* GPIO_115	E38	GYRO INT	$\sim$	
	< ──WTR0 RX ON	GRFC6 BD5	GPIO_80			GPIO_116				
	,  WTR0 RF ON	GRFC7 BD7	GPIO_81			* GPIO_117	AV37			
VREG L5 1P8	LTE BPI OUT82	GRFC8 BC38	GPIO 82			* GPIO_118	1,000			
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		GRFC9 BC40	GPIO_83				D. 40	I2S 2 DO		
120 \$ 7.	LTE BPI OUT84	GRFC10 BC8	GPIO 84			* GPIO_120		CAM VCM		
¦ Internal Use. ŠŽŽŽ	₩ B39 B3 SWITCH SEL	GRFC11 BE6	GPIO 85						,,	
<u> </u>	``	GRFC12 BD39	GPIO 86							
	U WTR0 PA R1	GRFC13_BB37	GPIO 87							
	∠ LTE BPI OUT88	GRFC14_BB35	GPIO 88							
	~	GRFC15 BD35	CPIO 80							
	DRX MODE SEL0	GRFC16 BA32	GPIO_89							
	C DRX MODE SELU	GRFC17 BE38	GF10_90	P3						
	//_ LTE_B7_PA_EN	GRFC18 AY33	GPIO_91							
		GRFC19 BD33	GPIO_92							
		GRFC20 BC34	GPIO_93							
	DRY MODE CELS	GRFC21 BB31	GPIO_94							
	//	BC32	GPIO_95							
	← EXT GPS LNA EN  EXT GPS		GPIO_96							
			GPIO_97 *							
	CHO CSM TV DHASE DO		GPIO_98 *							
	CHO GSM TX PHASE DO		GPIO_99							
	CHO GSM TX PHASE D1	BE36	GPIO_100							
	CH1 GSM TX PHASE DO	BB33	GPIO_101							
	CH1 GSM TX PHASE D1	BE32	GPIO_102							
	CHO SSBI WTR RX	AT35	GPIO_103							
	CHO SSBI WTR TX	AW40	GPIO_104							
	← CH1 SSBI WTR RX  — CH1 SSBI WTR RX  — CH2 SS	AU36	GPIO_105							
		AY39	GPIO_106							
	KEY VOL UP N	Н33	GPIO_107 *							
			GPIO_108 *							
			GPIO_109 *	_						

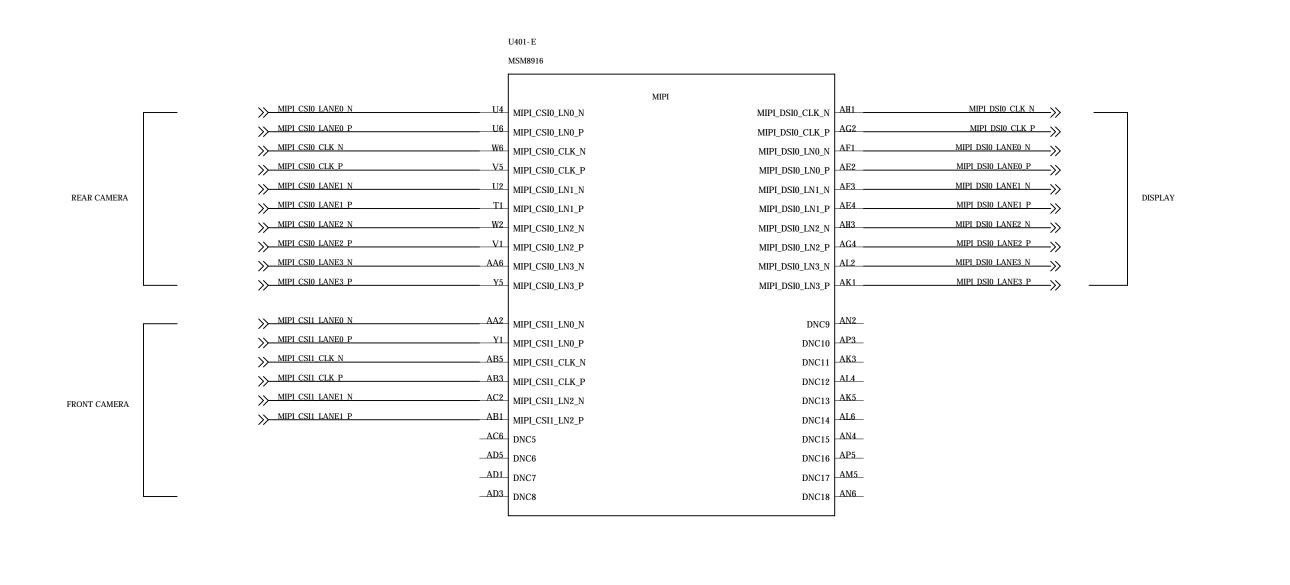
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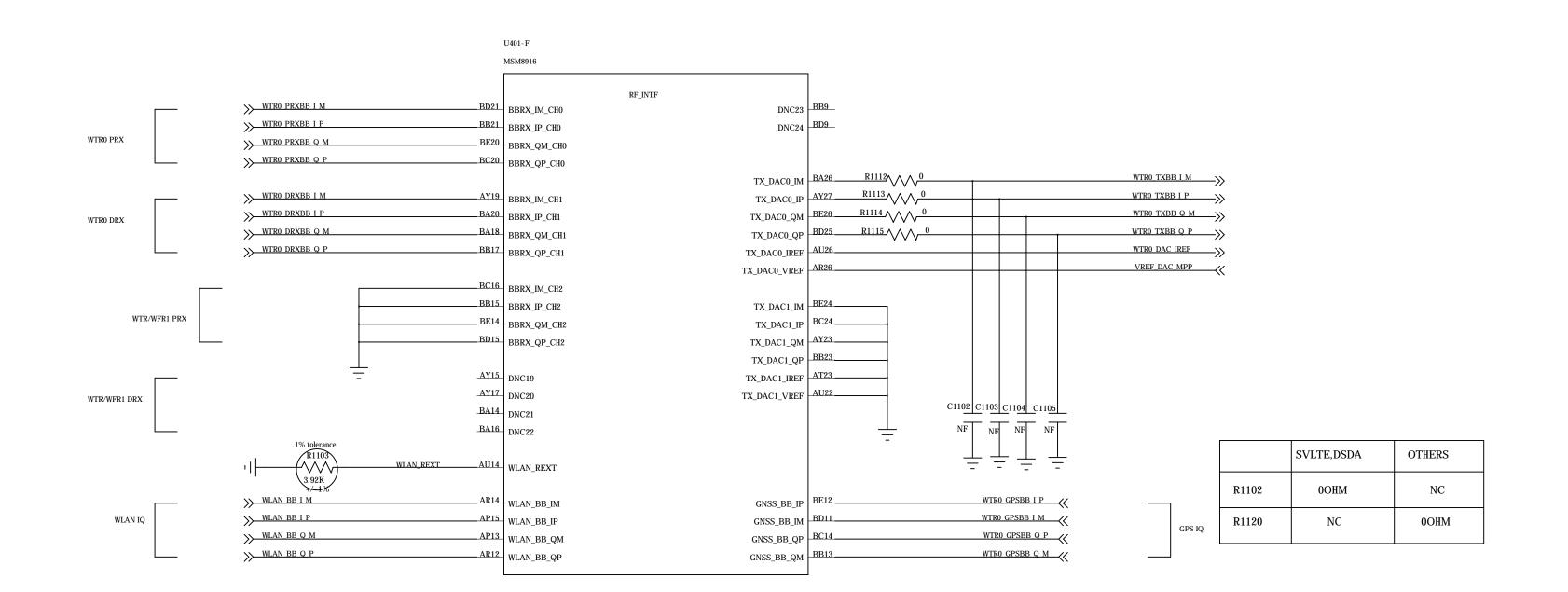
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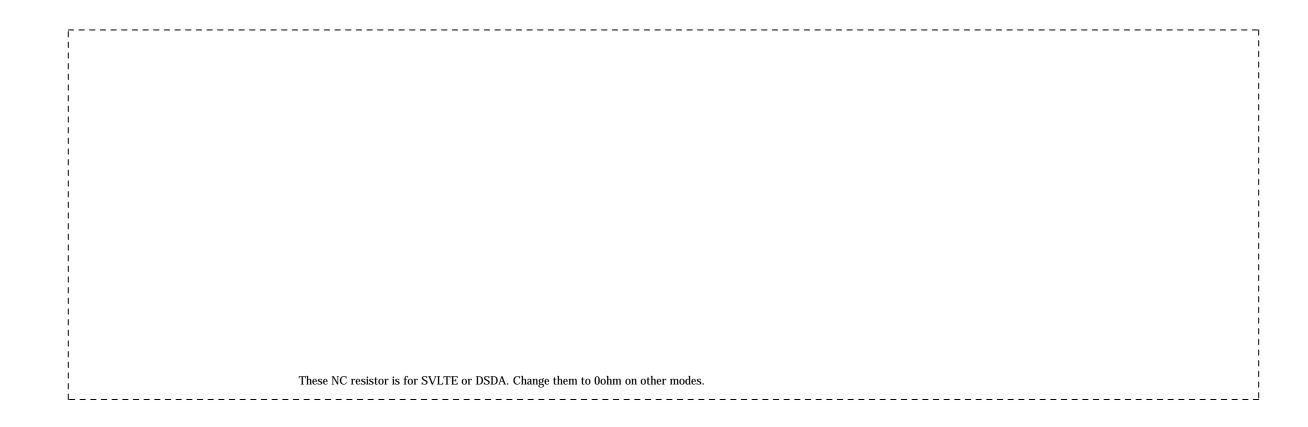
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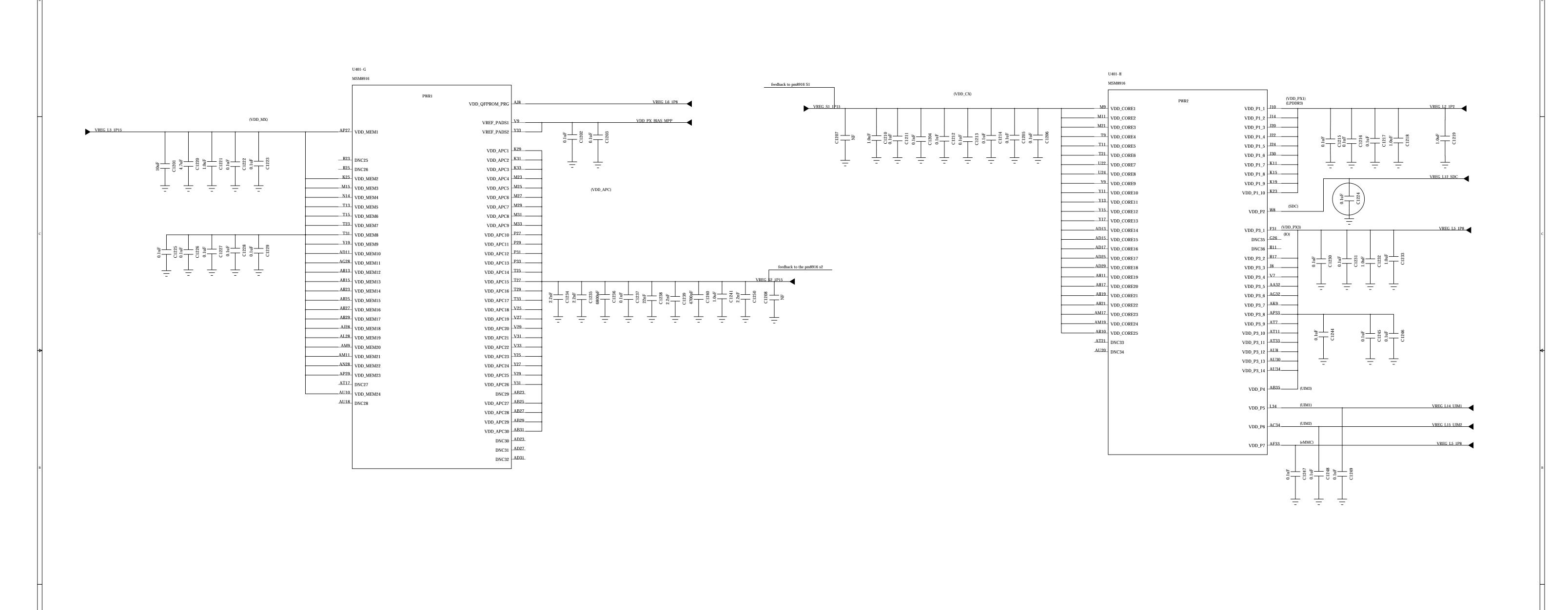


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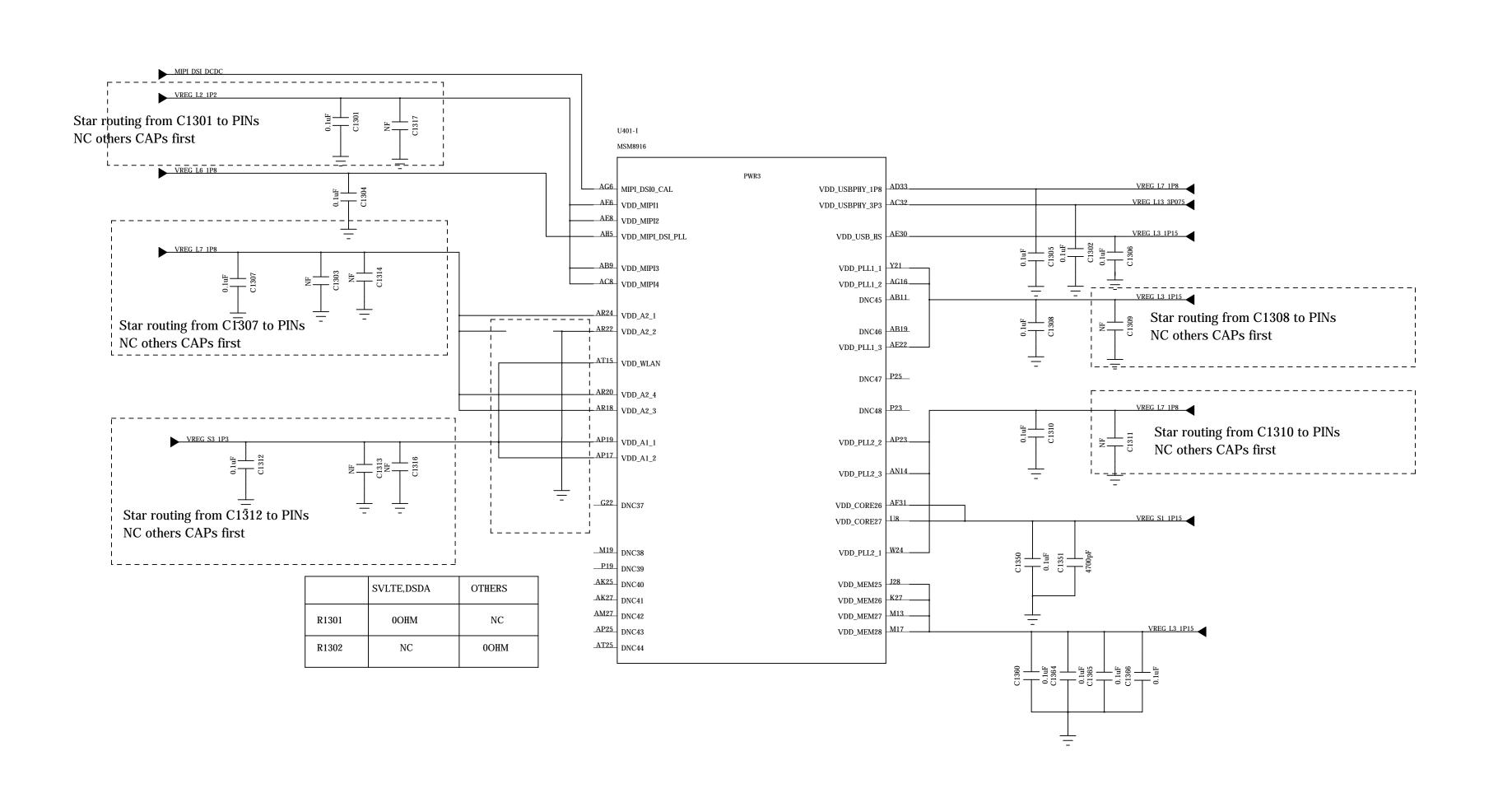
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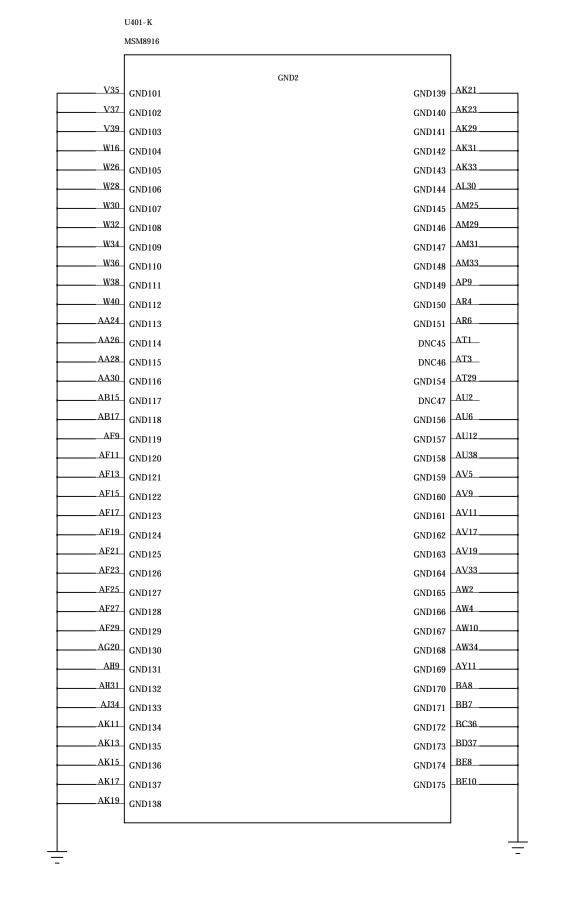


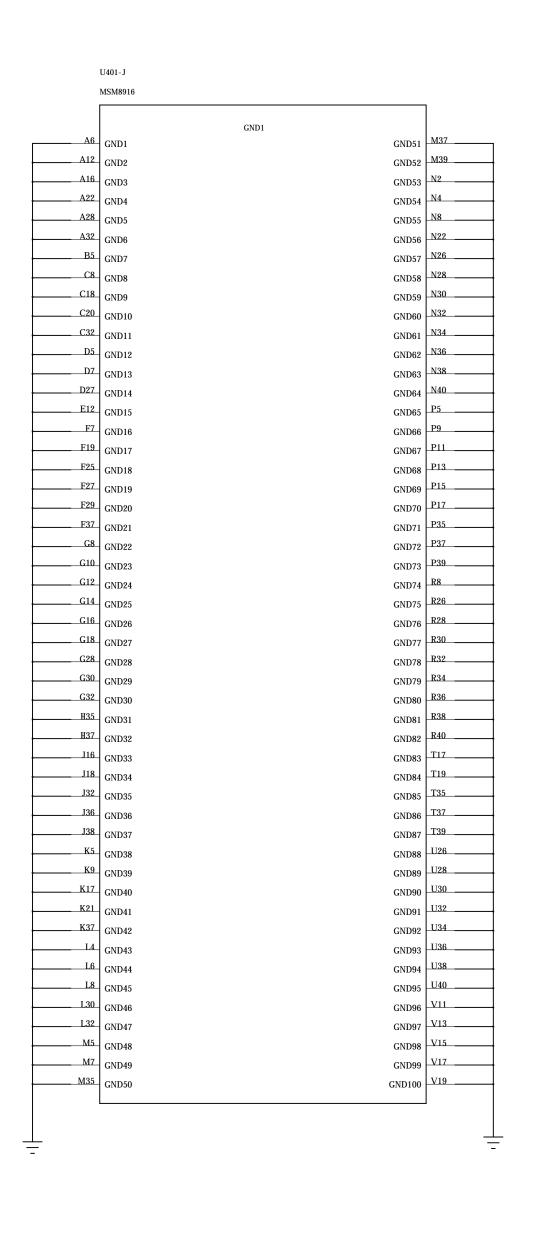
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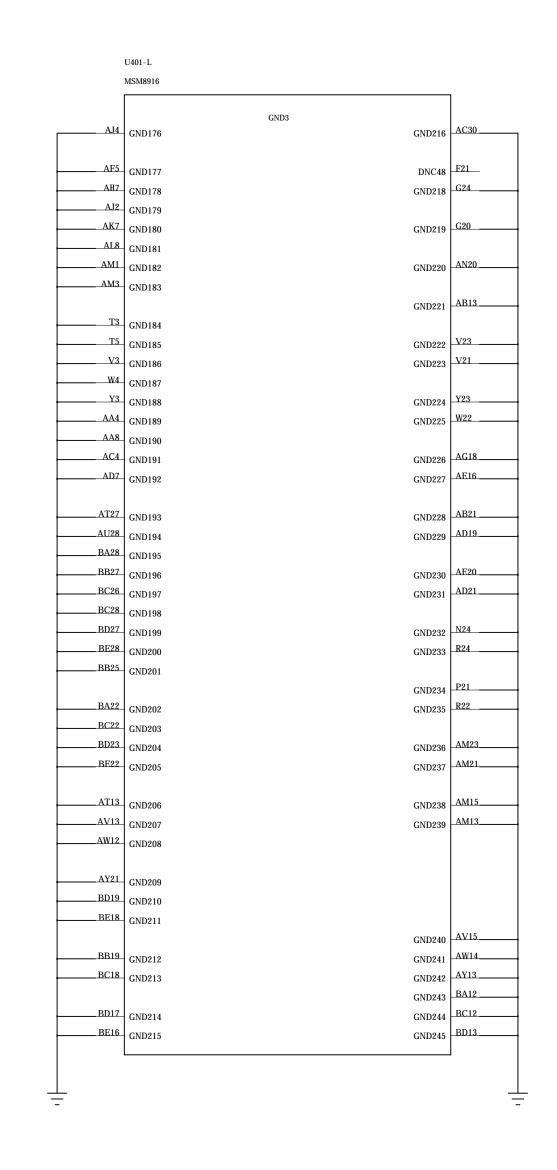
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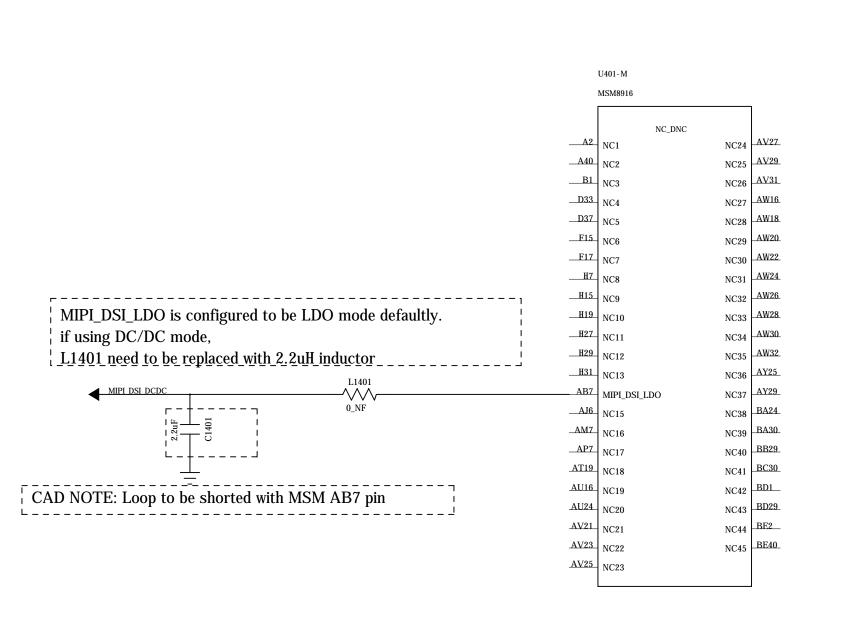
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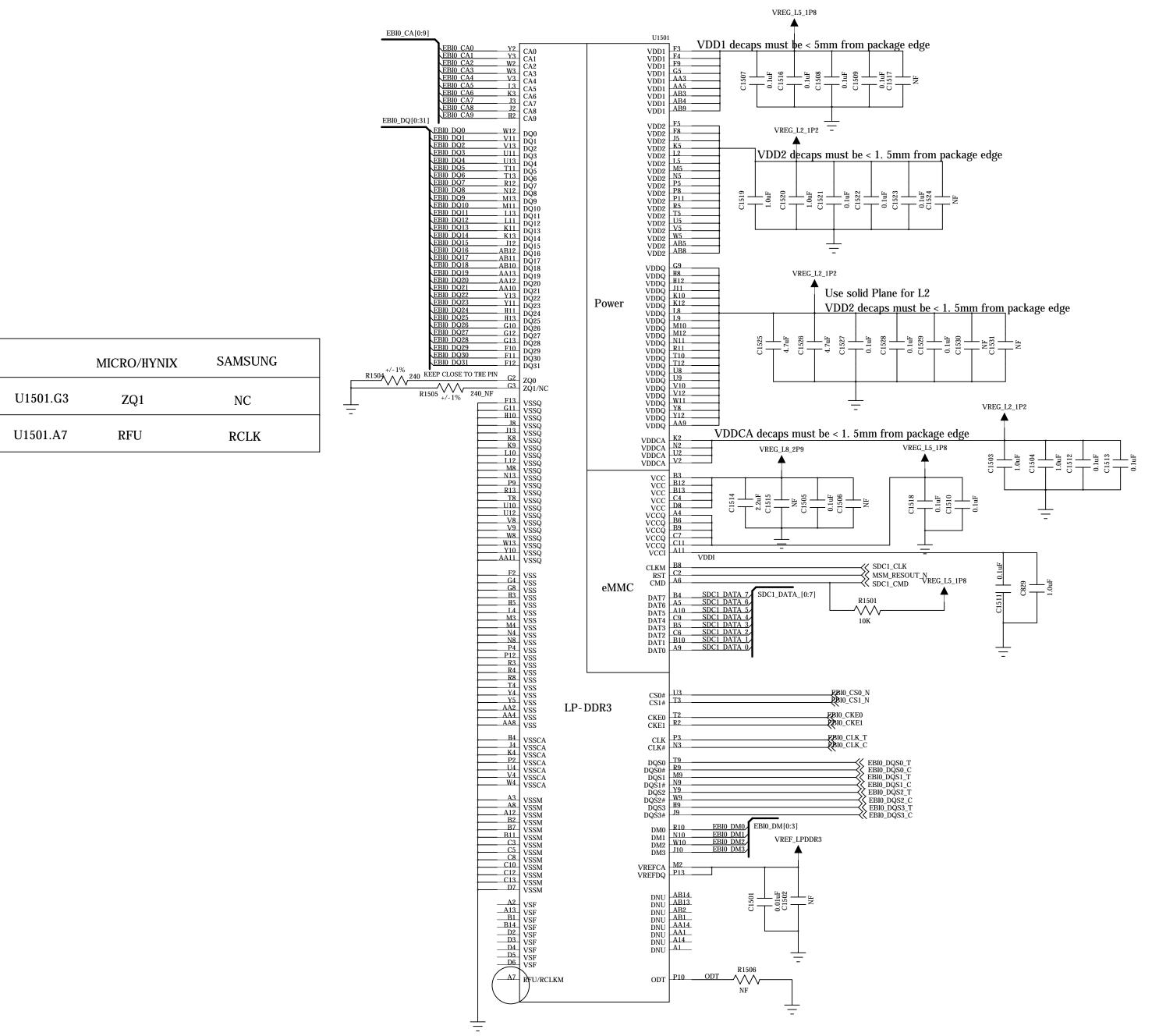






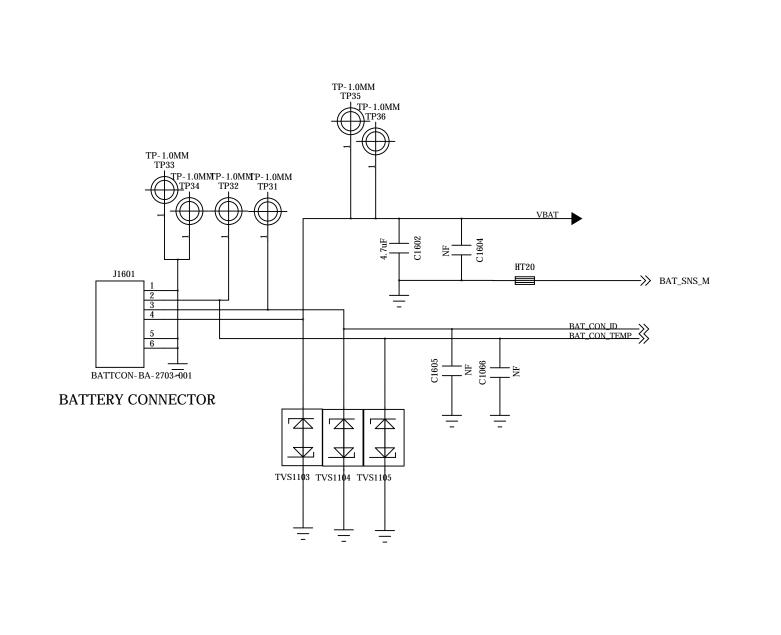


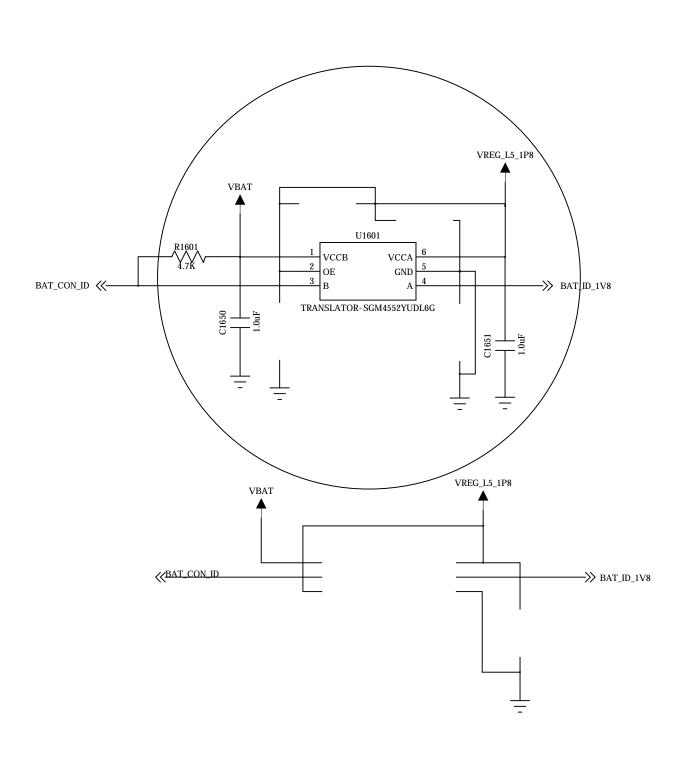
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Note: Pull-up resistors on SDC1\_DATA are PCB and eMMC vender dependent

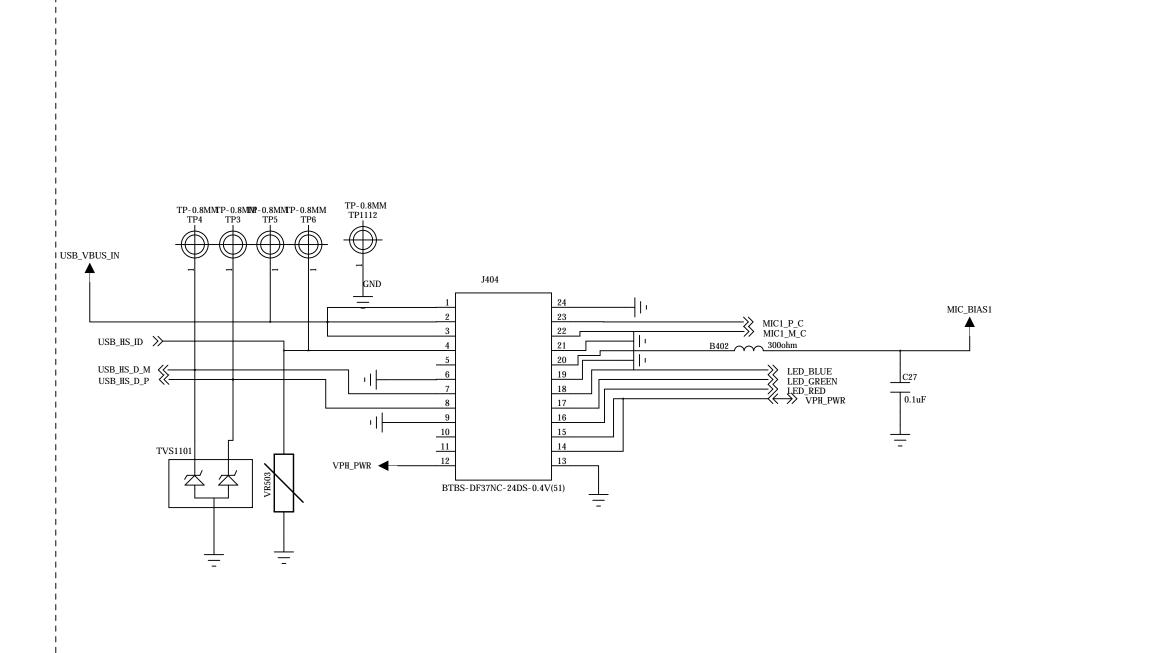
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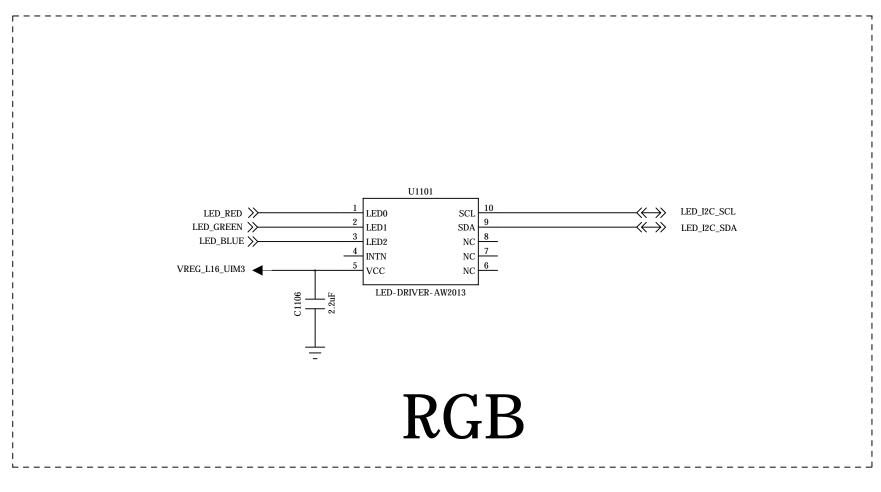


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**Sub Board Connector** 



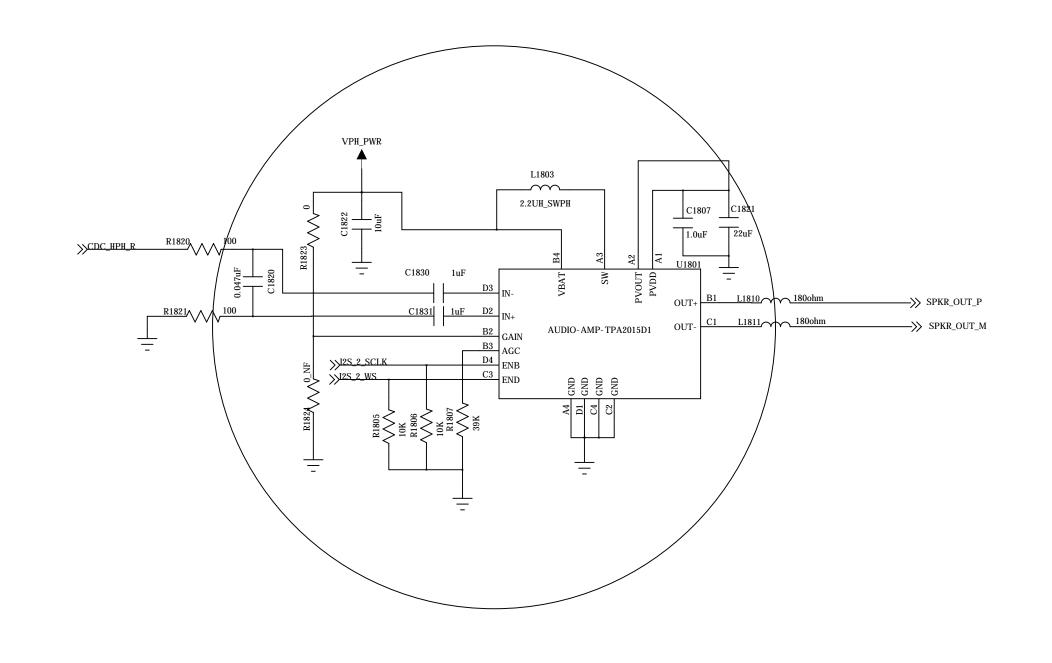
#### MAIN MIC+USB+RGB

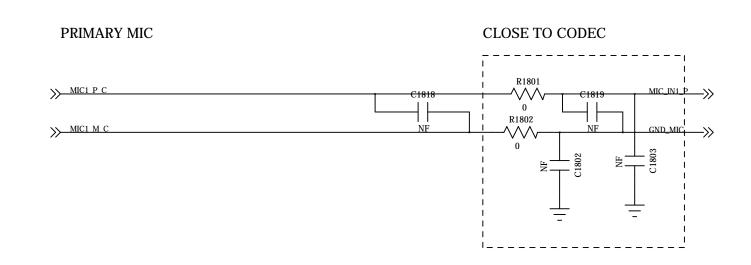


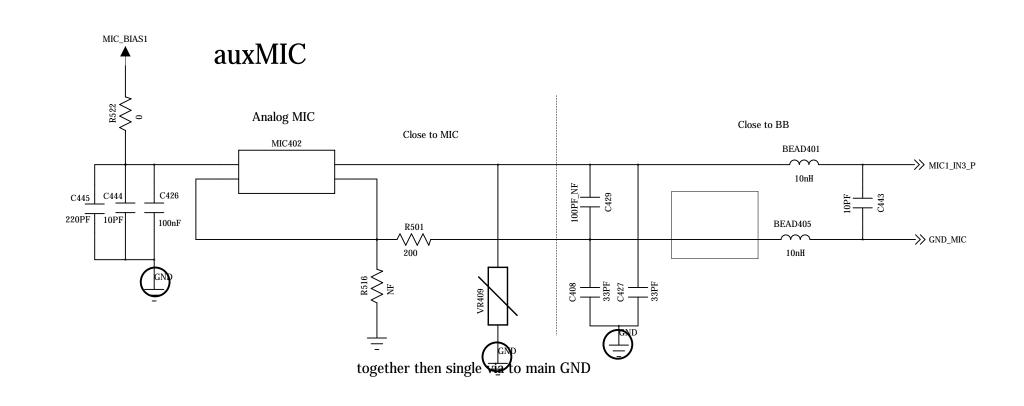
I2C Address
Write 8B0x8A
Read 8B0x8B

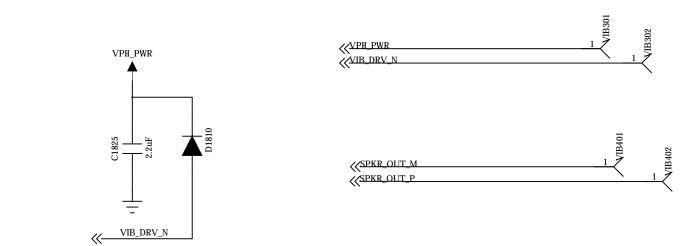
Power on sequence: can be applied in any order

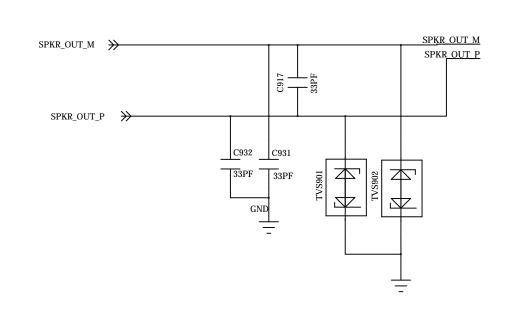
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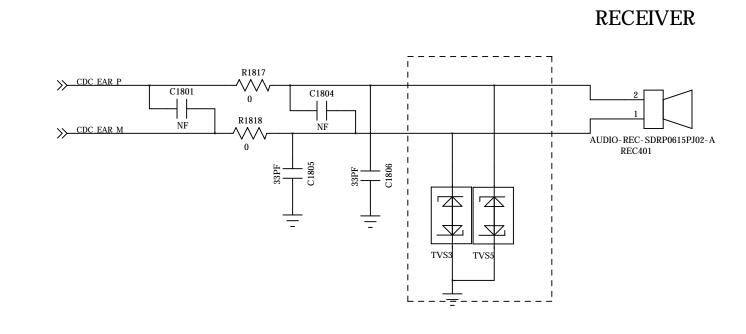




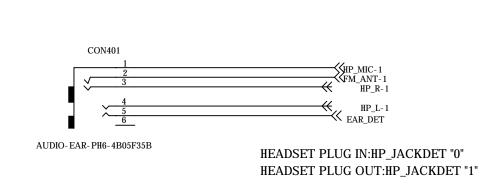


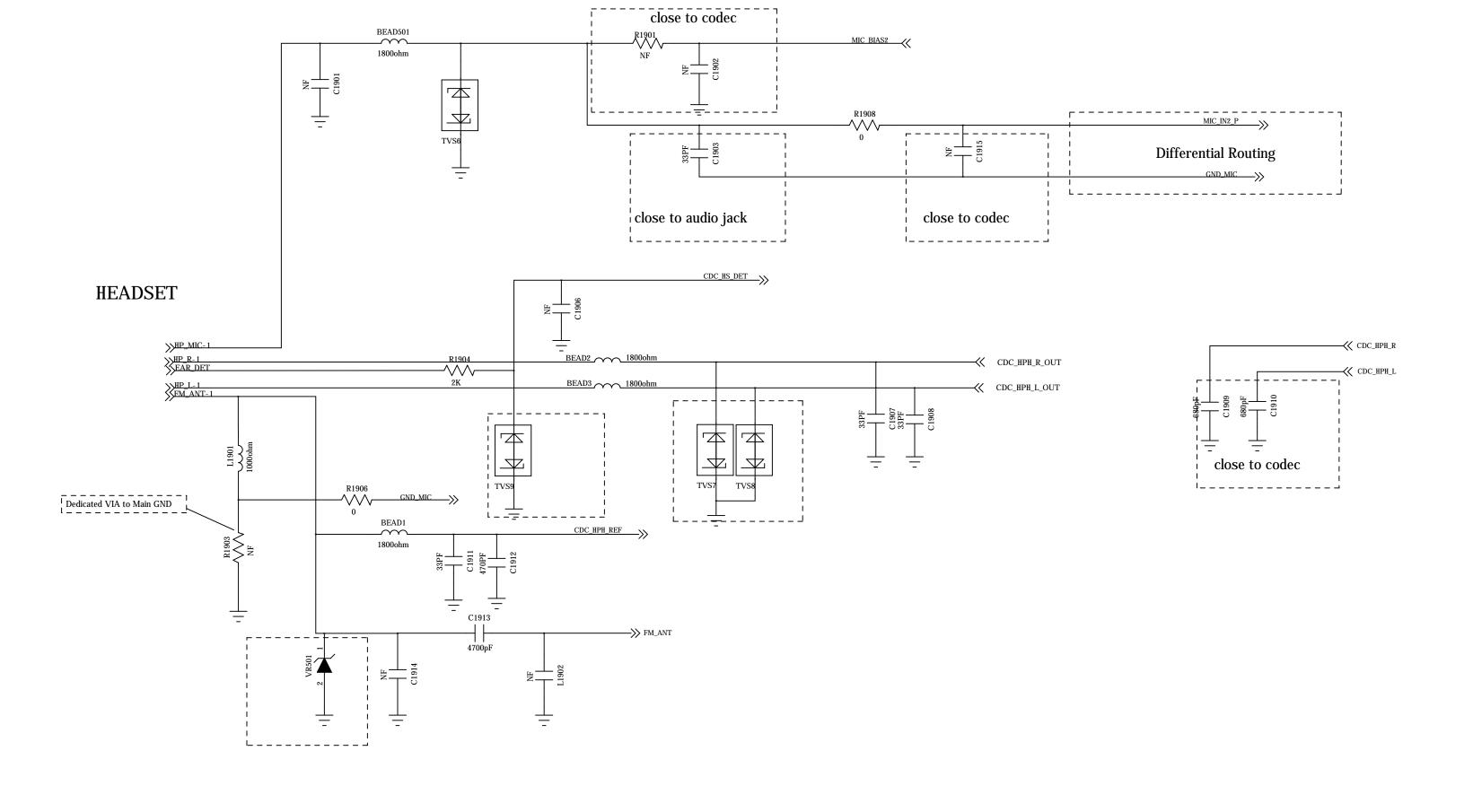


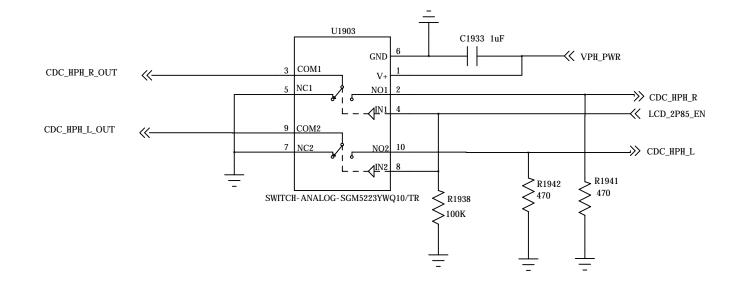




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IN=0, COM Connected to NC; IN=1, COM Connected to NO;

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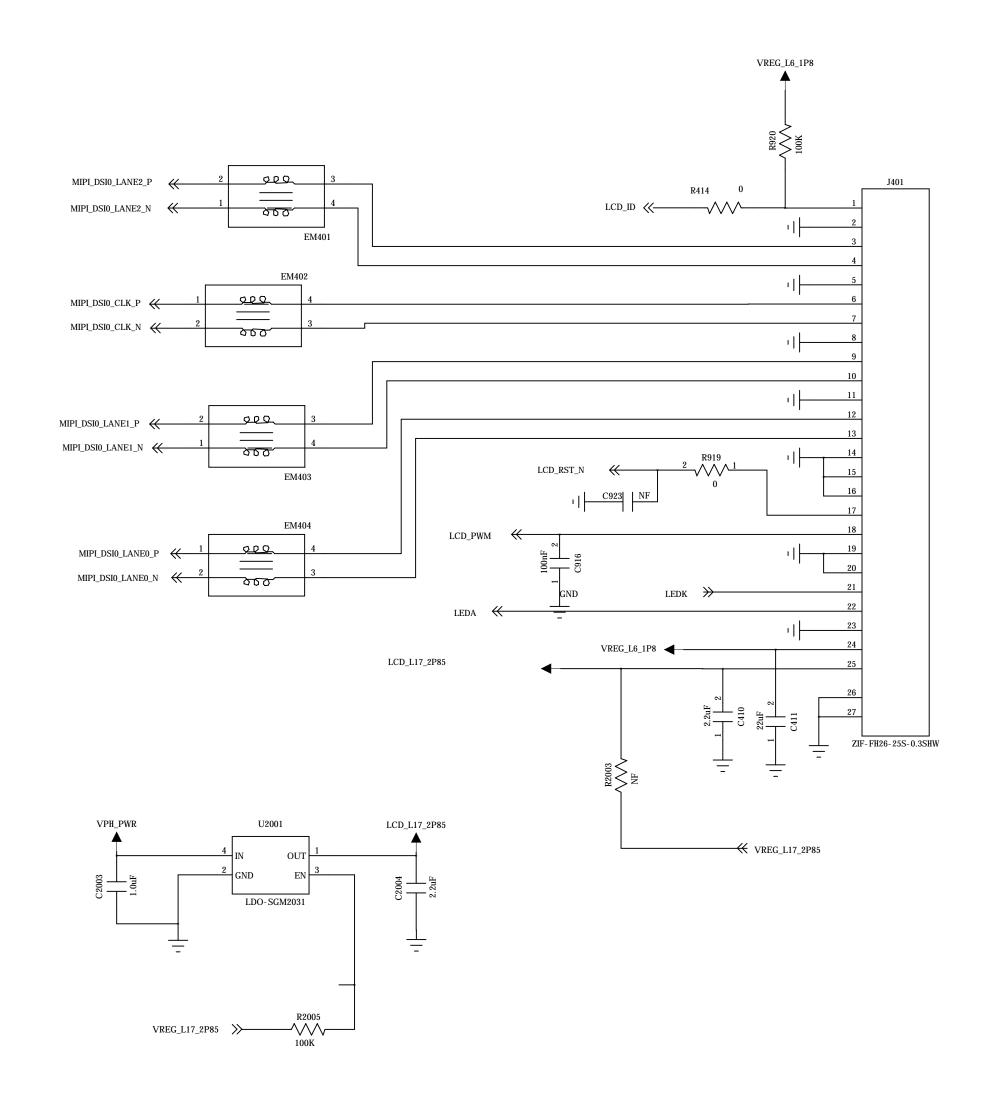
LCD

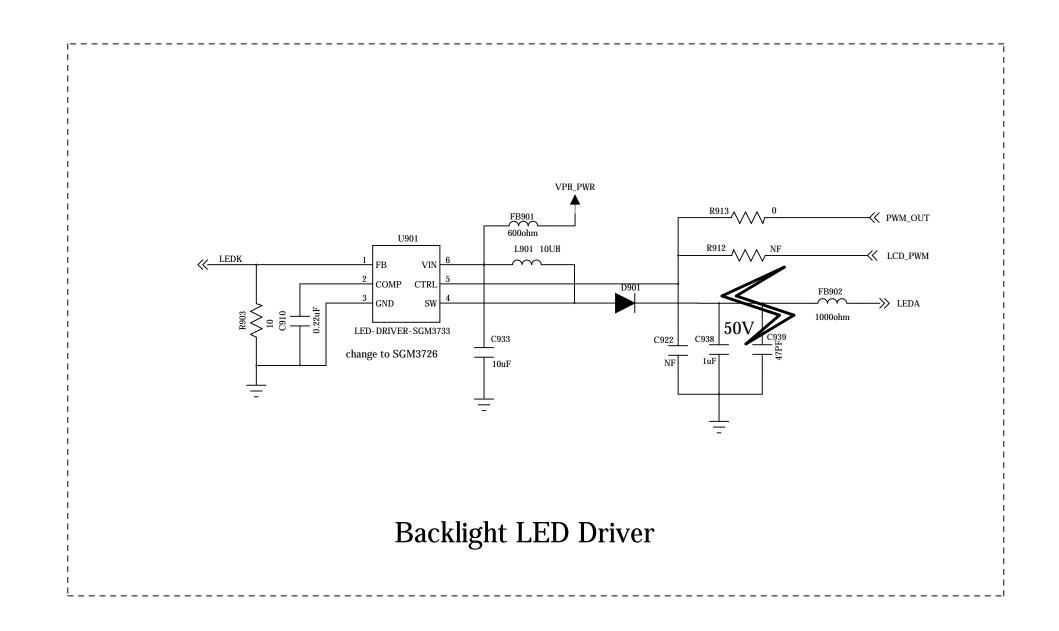
5.0' FWVGA

Module: KTF-050-D5498OF

Driver: OTM8019A

Power on sequence: L6(IOVCC)/L17(VCC) can be applied in any order

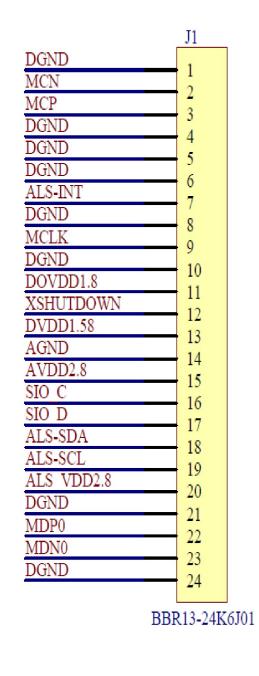


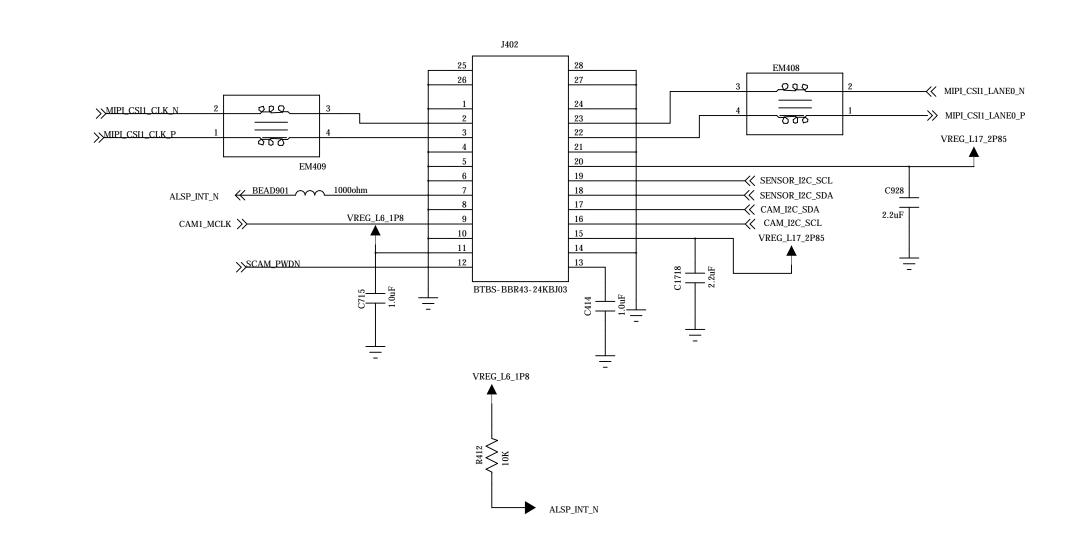


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Sub Camera

#### FRONT\_camera+LP Sensor+SPK





Main Camera / Sub Camera share power domain design should double check the voltage level is compatible

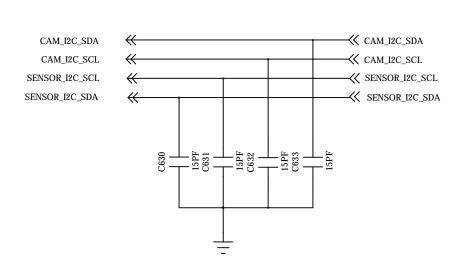
light sensor 0x90 Write(STK3171) light sensor 0x46 Write (LTR-558ALS)

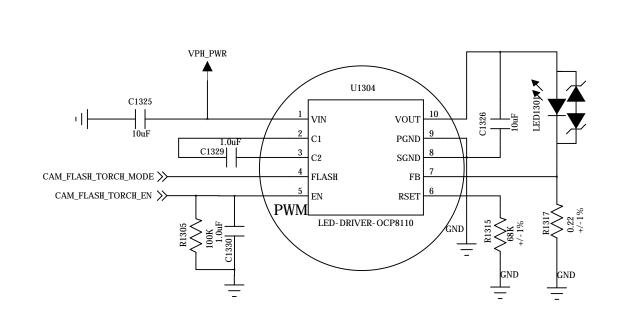
Front Camera: 2M Module:

Sensor: OV2680

I2C Address
Write 0x20
Read 0x21

Power on sequence: L6(DOVDD)/L17(AVDD) can be applied in any order



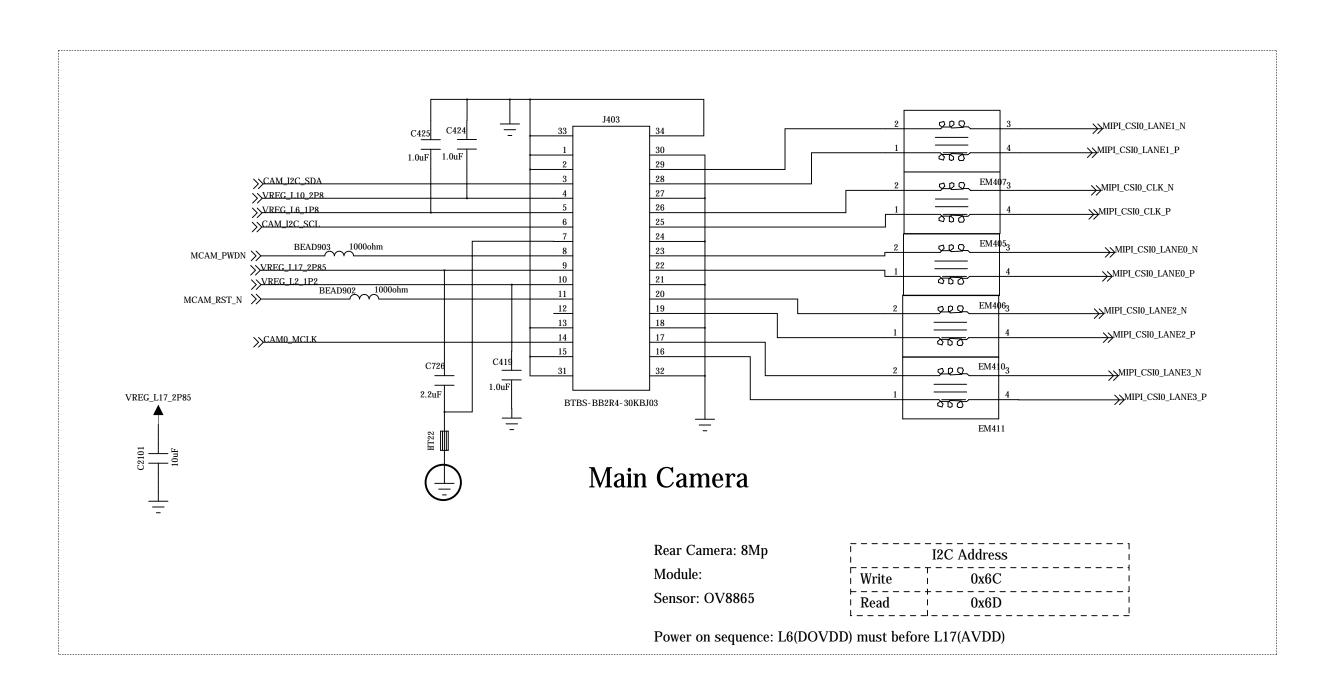


## OCP8111 R1315=68K R1317=0.22 OCP8110 R1315=82K R1317=0.47 FLASH LED

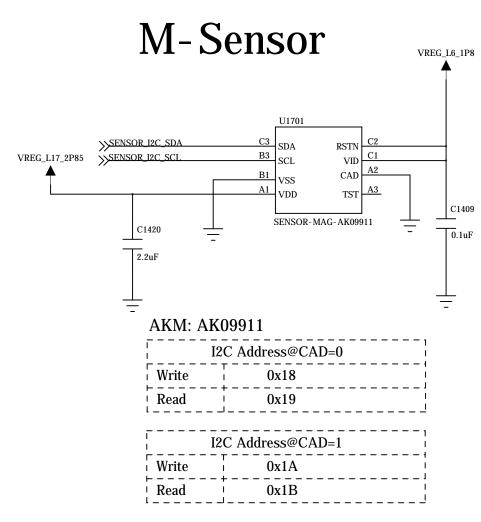
light sensor 0x90 Write(STK3171)

light sensor 0x46 Write (LTR-558ALS)

Main Camera / Sub Camera share power domain design should double check the voltage level is compatible

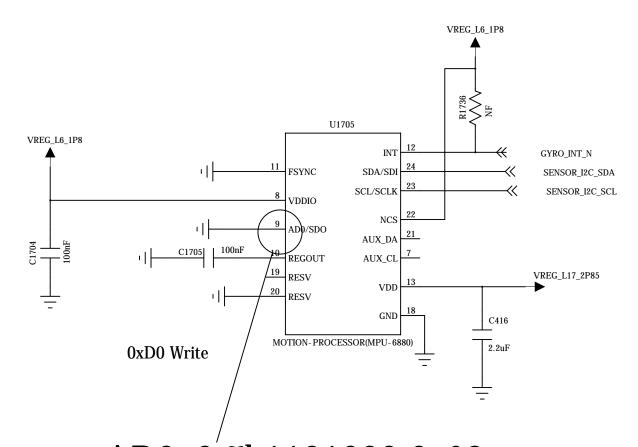


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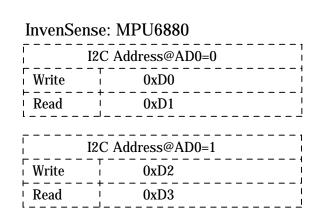


Power on sequence: L6/L17 can be applied in any order

# Gyroscope

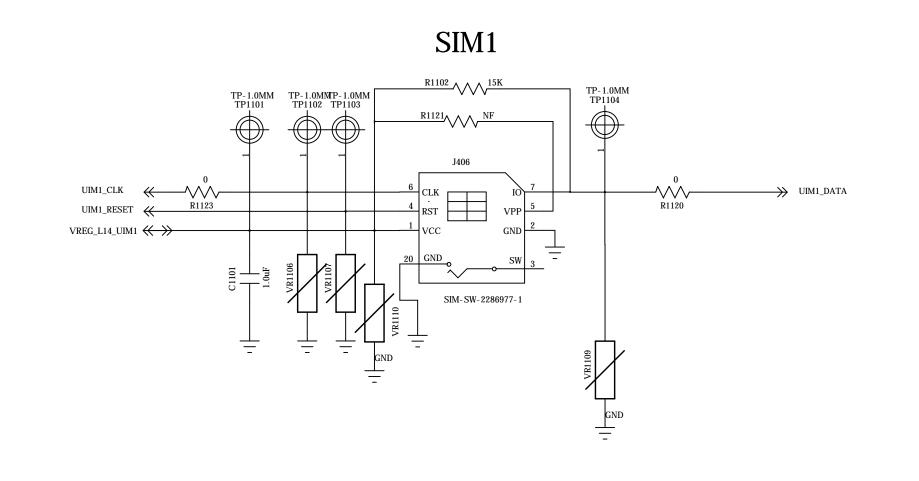


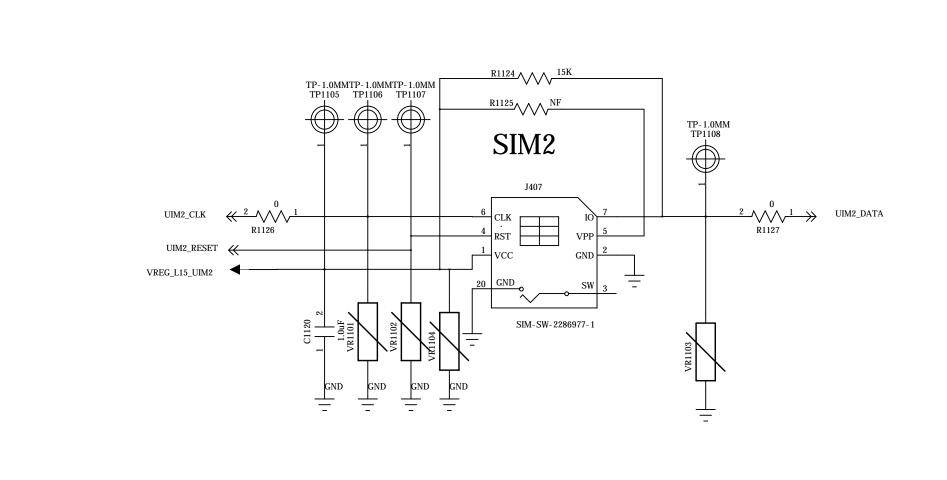
AD0=0 7b1101000 0x68 AD0=1 7b1101001 0x69

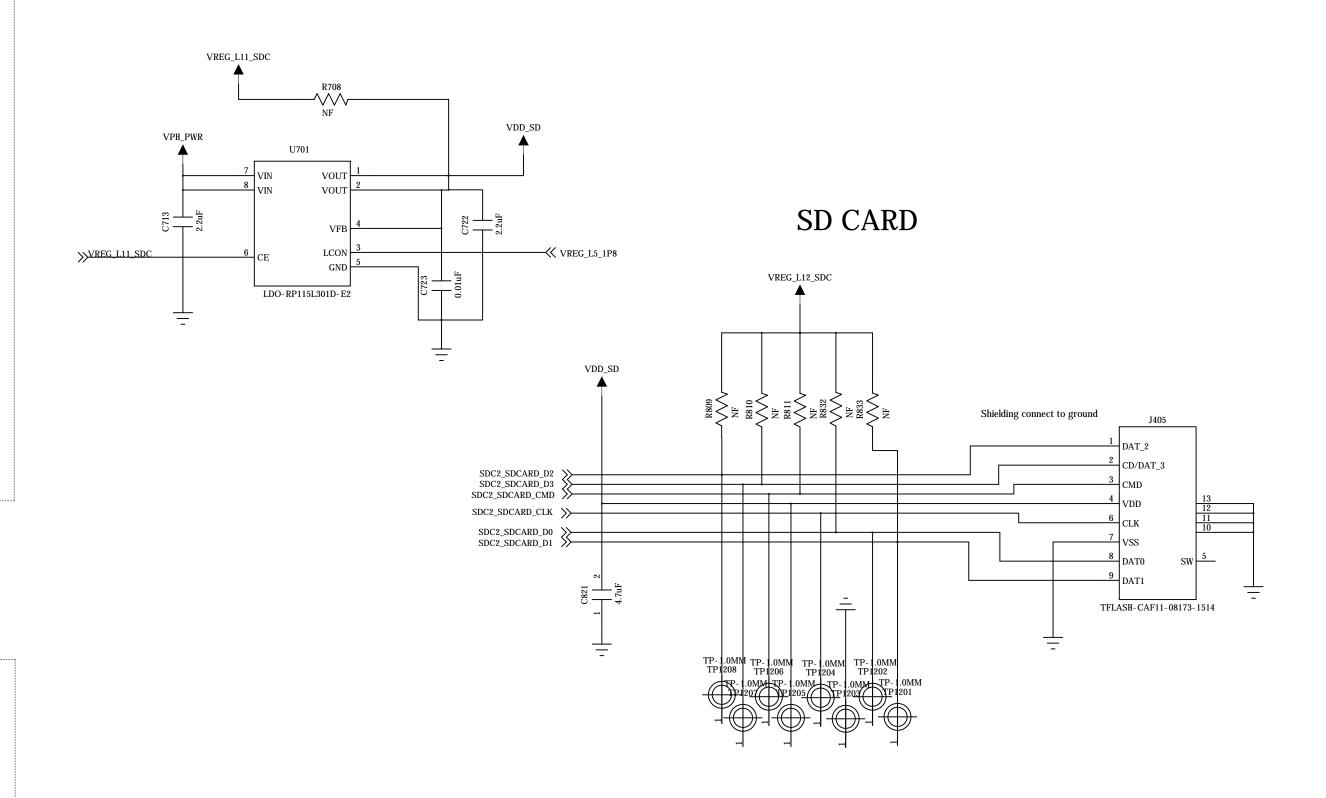


Power on sequence: L6/L17 can be applied in any order

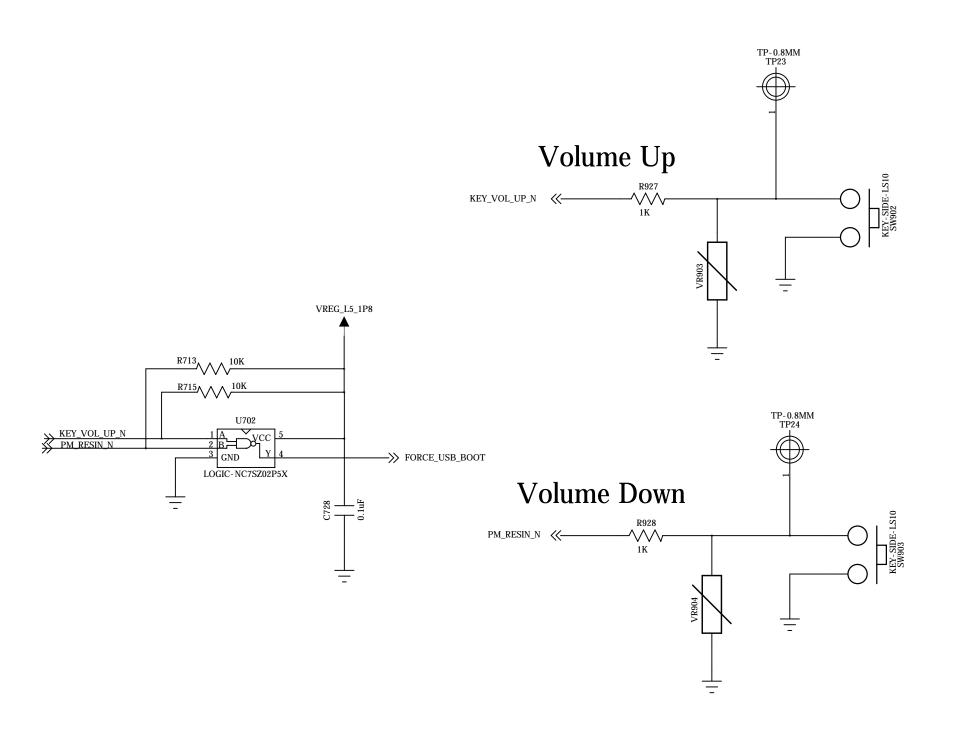
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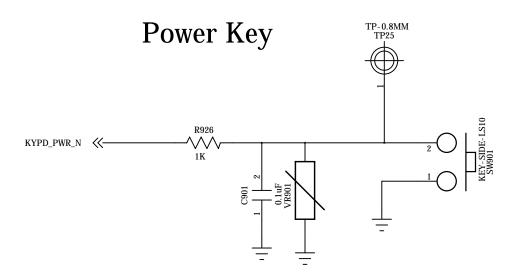




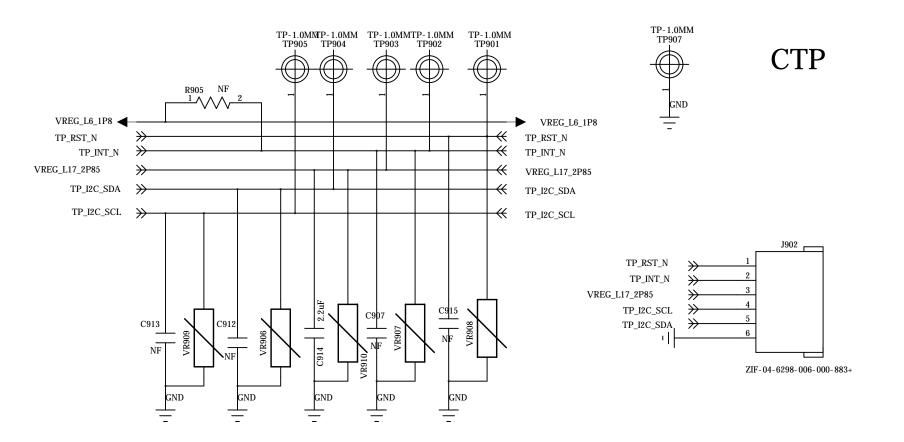


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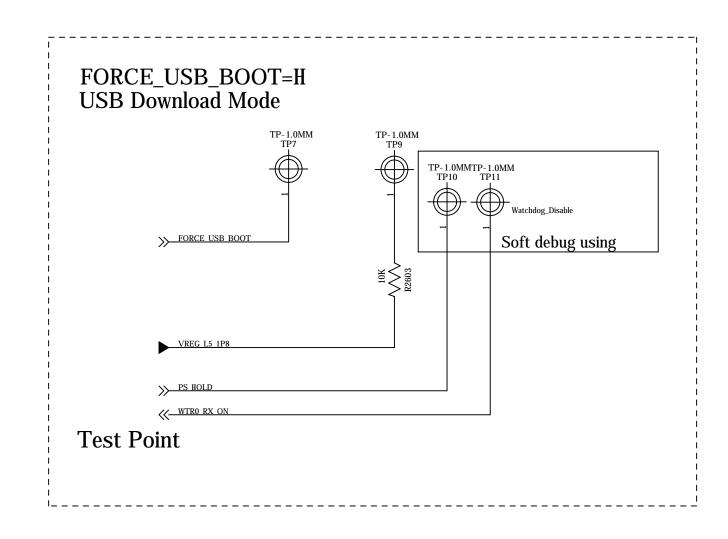


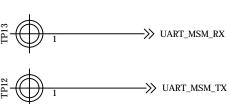
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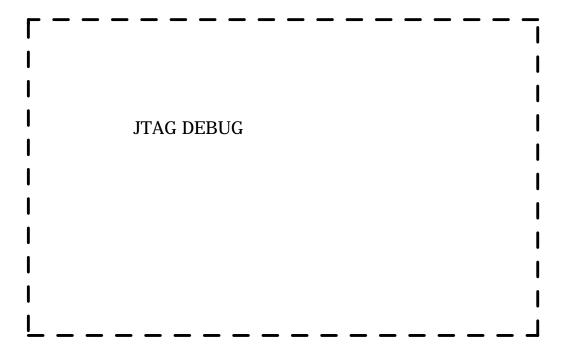


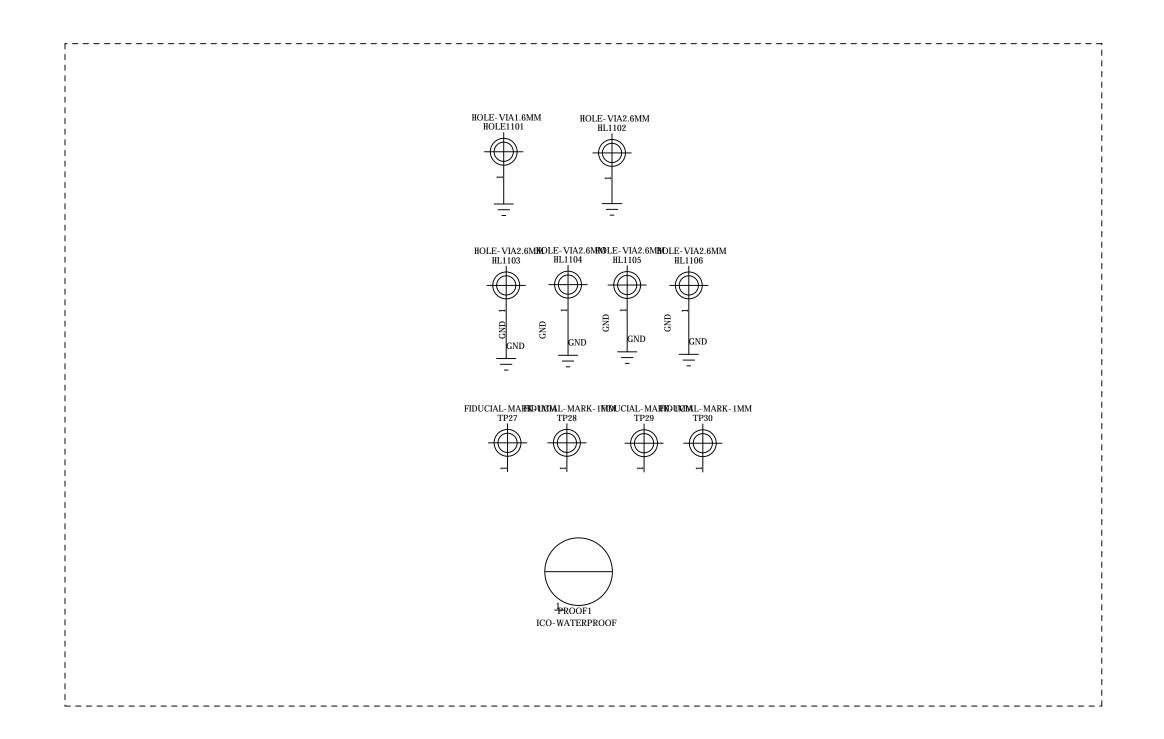
# Power on sequence: Single Supply FocalTech: FT5336 I2C Address Write 0x70 It can be changed by FACTORY. Read 0x71

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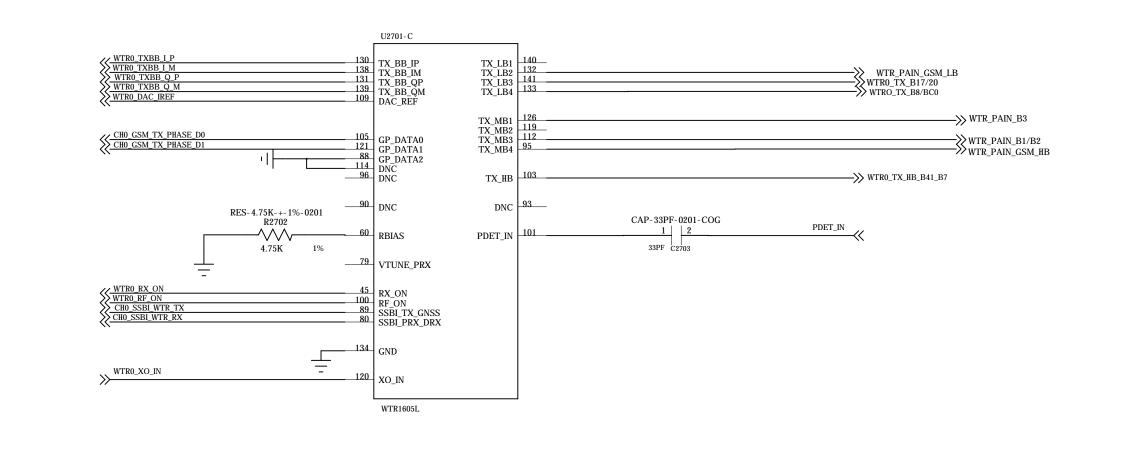


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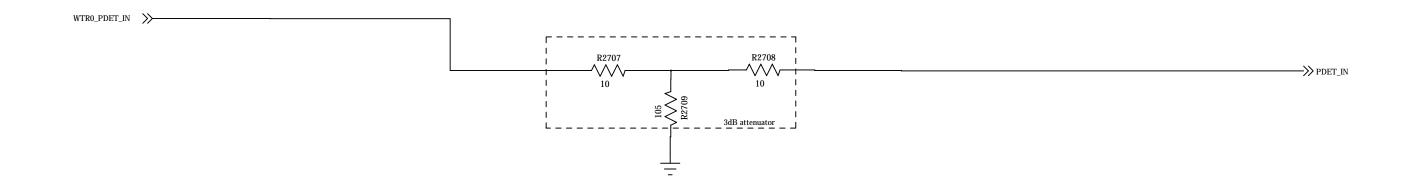
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U.S.A.

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use C2701 for both SPDT power supply, and use C2705 for both SPDT control line



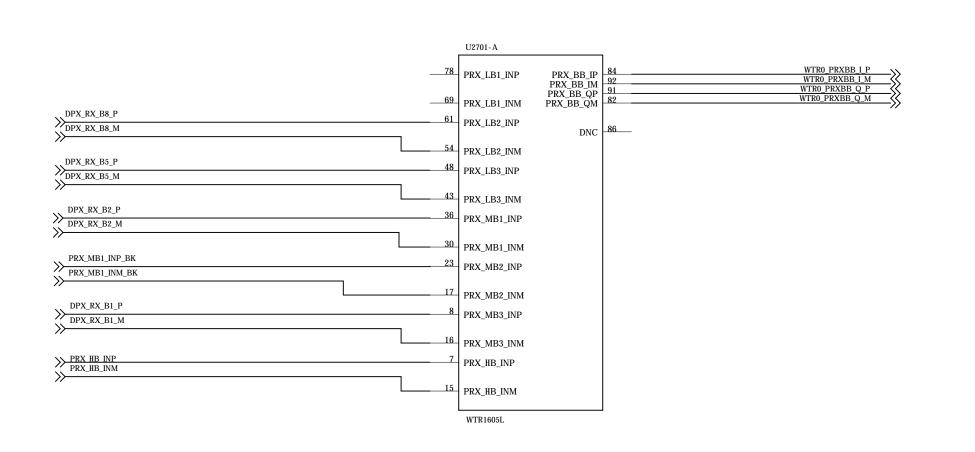
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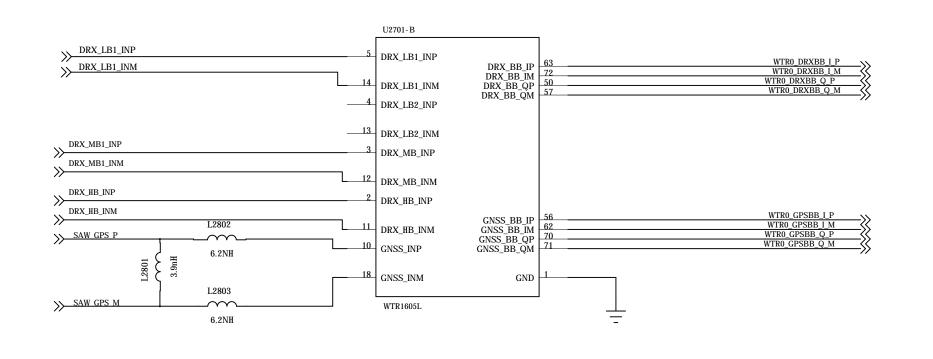
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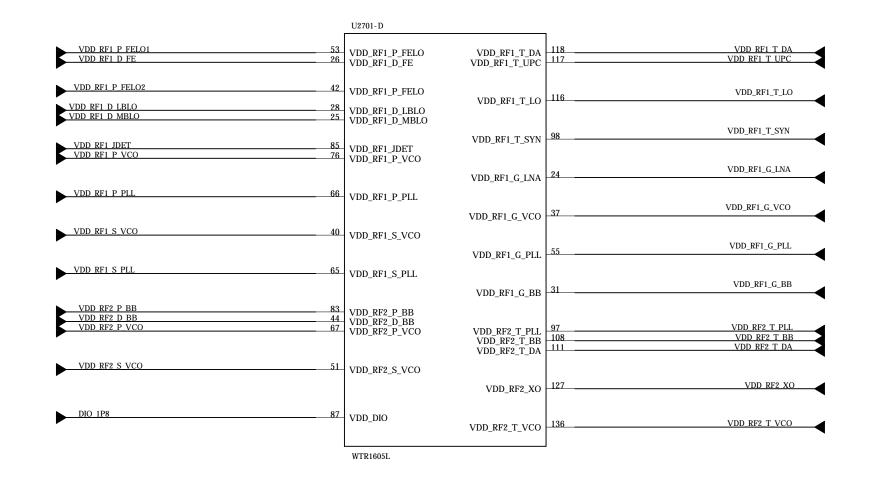


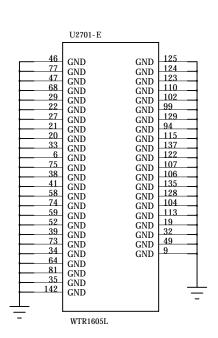
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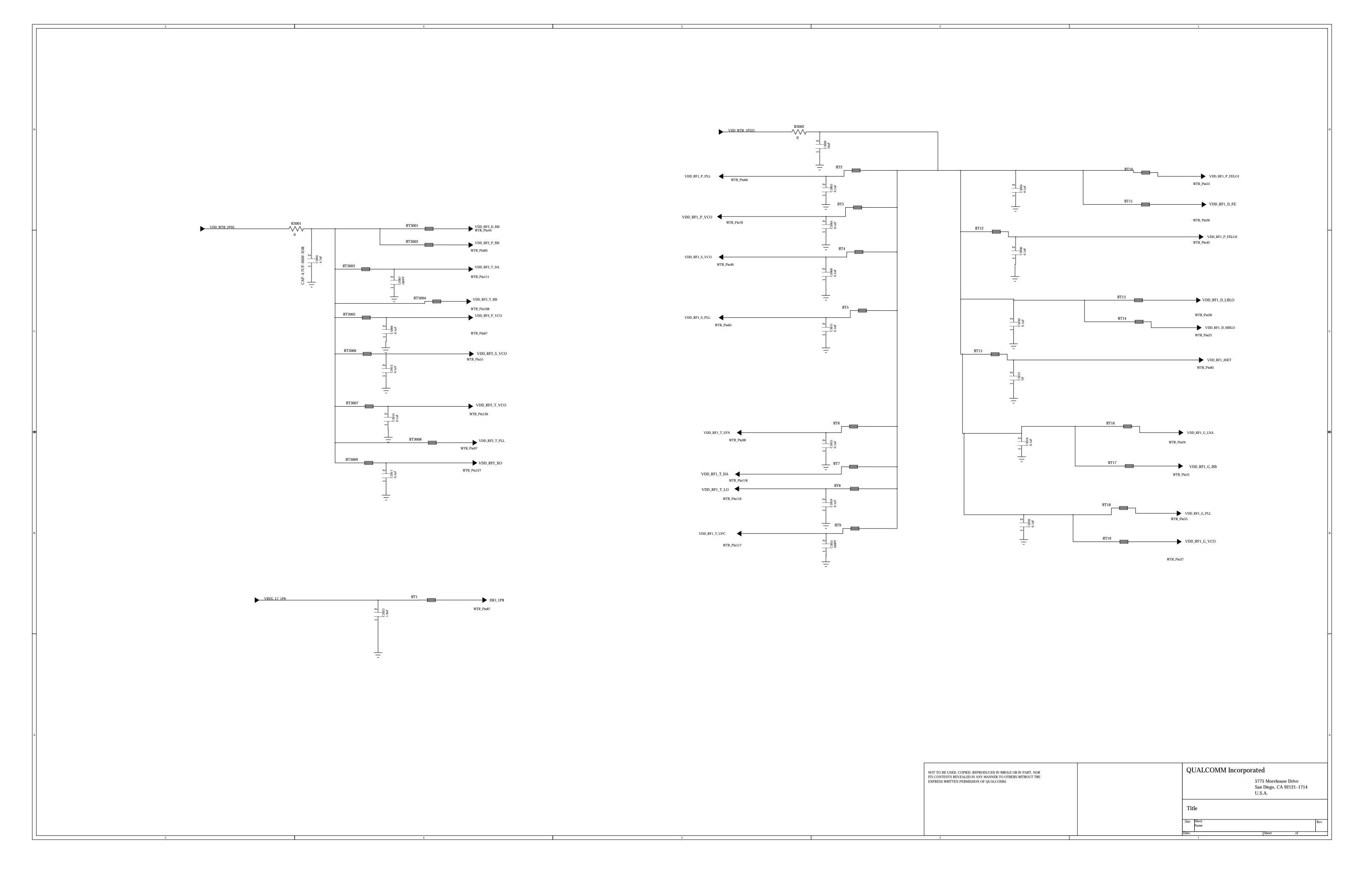


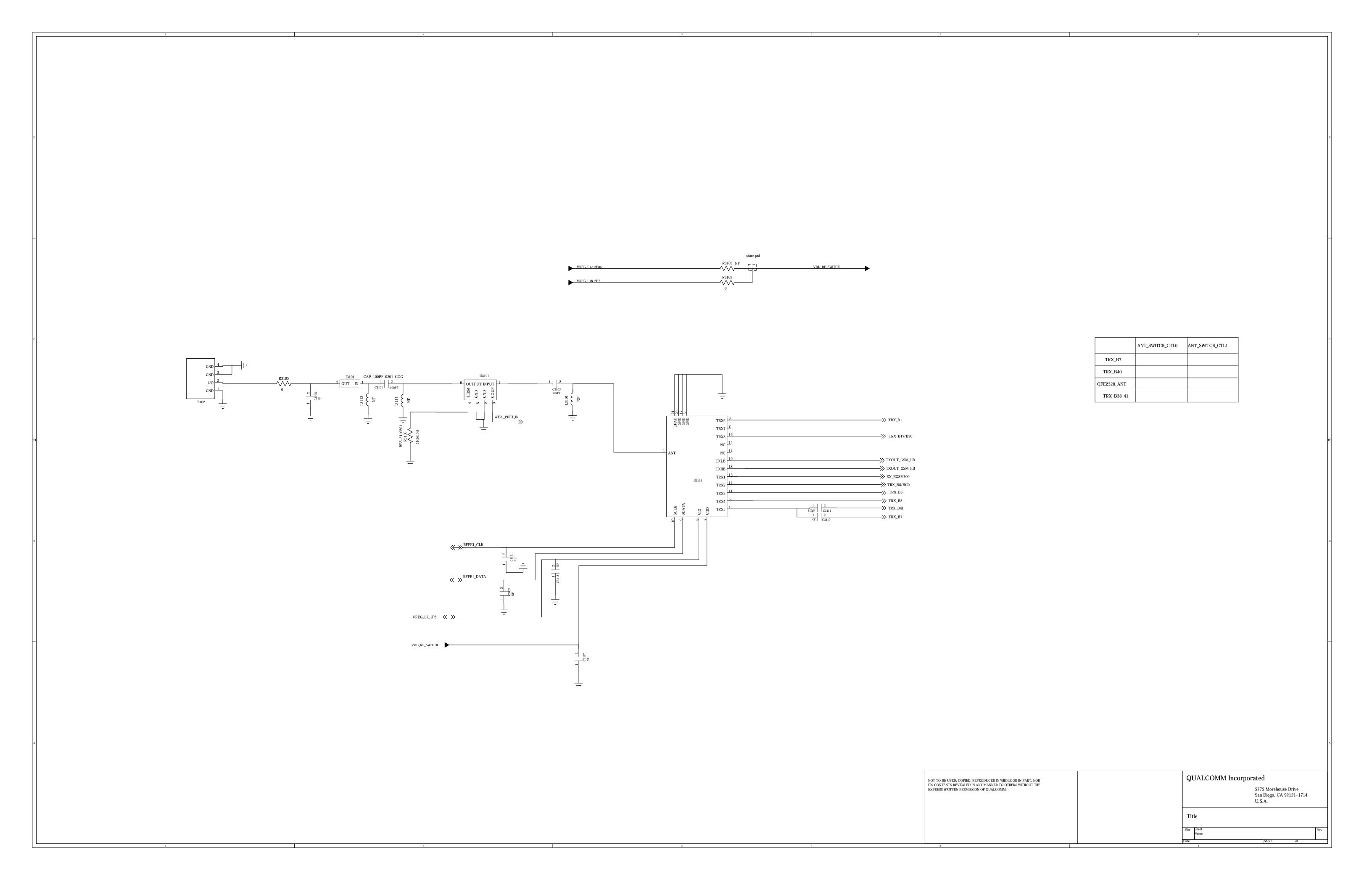
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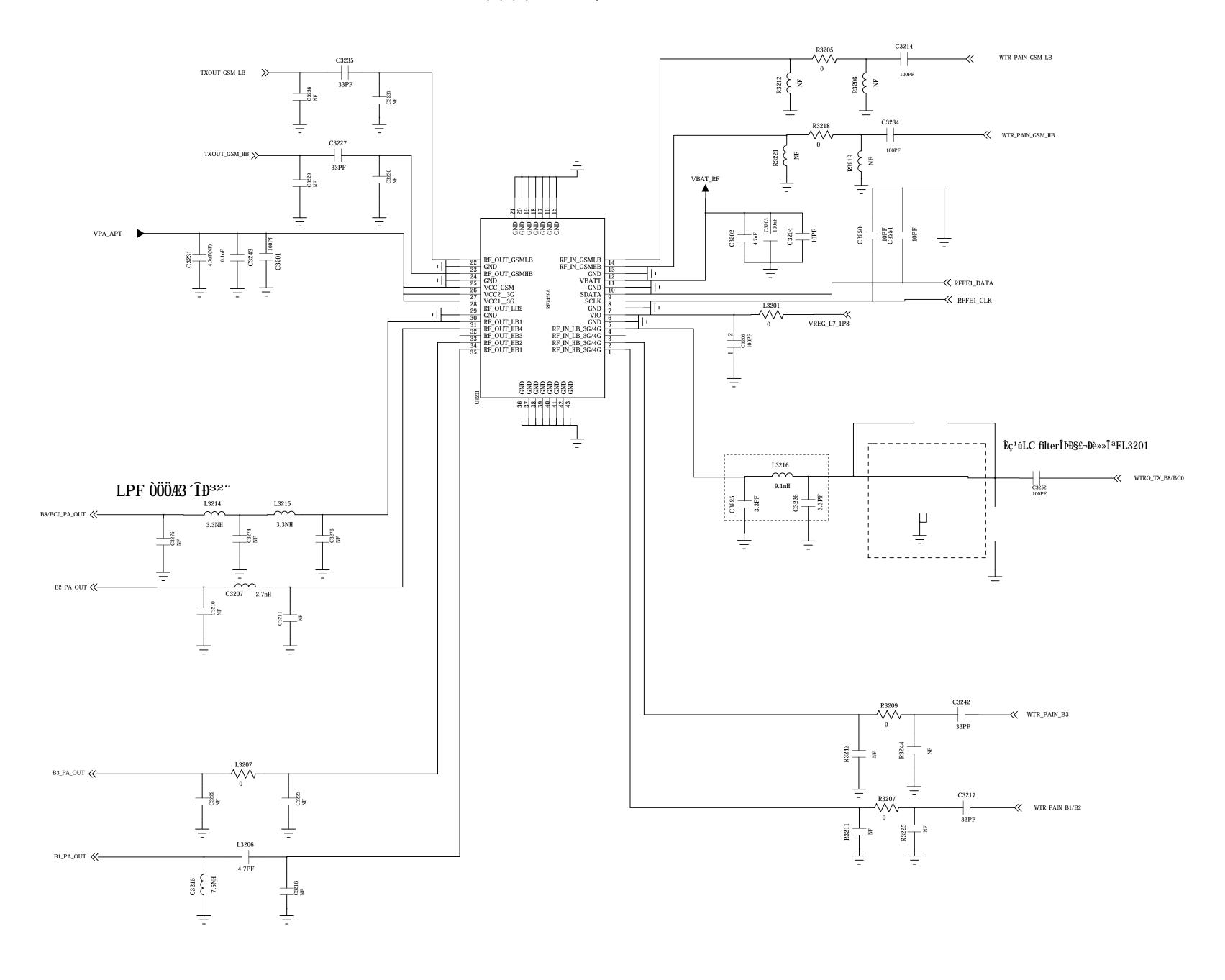
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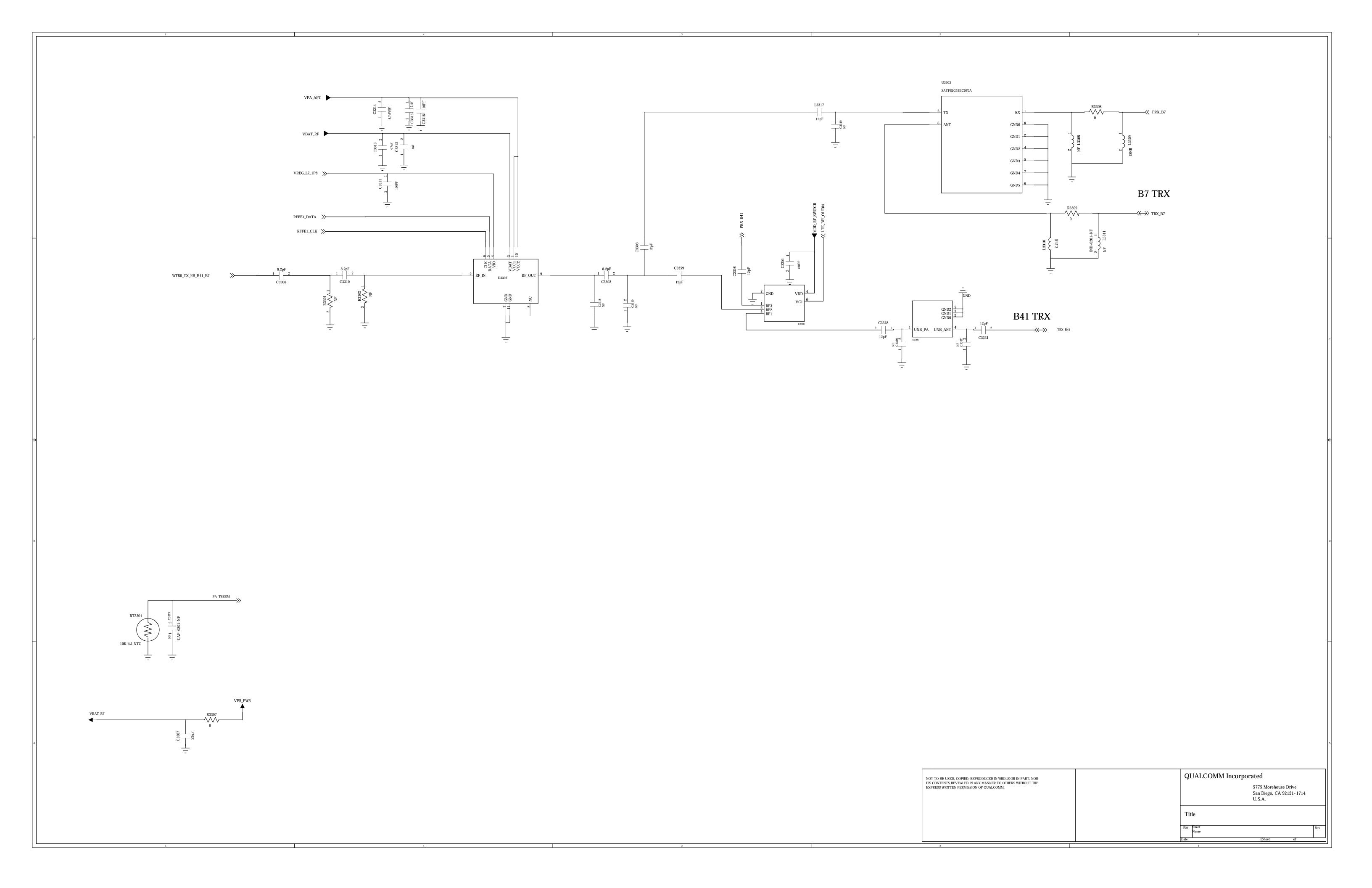




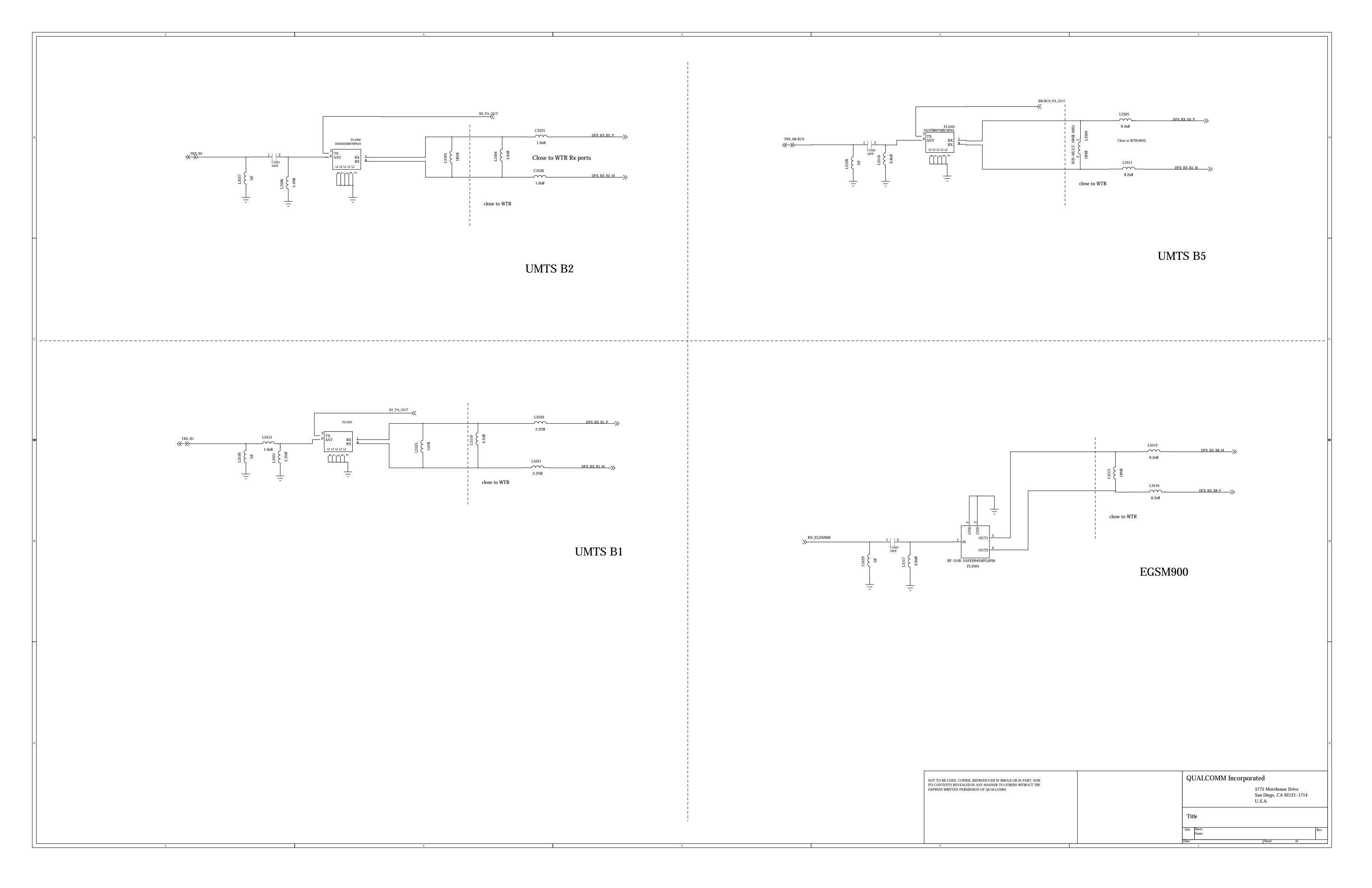
#### MMMB PA: B1,2,3,5,17 2G HB, 2G LB

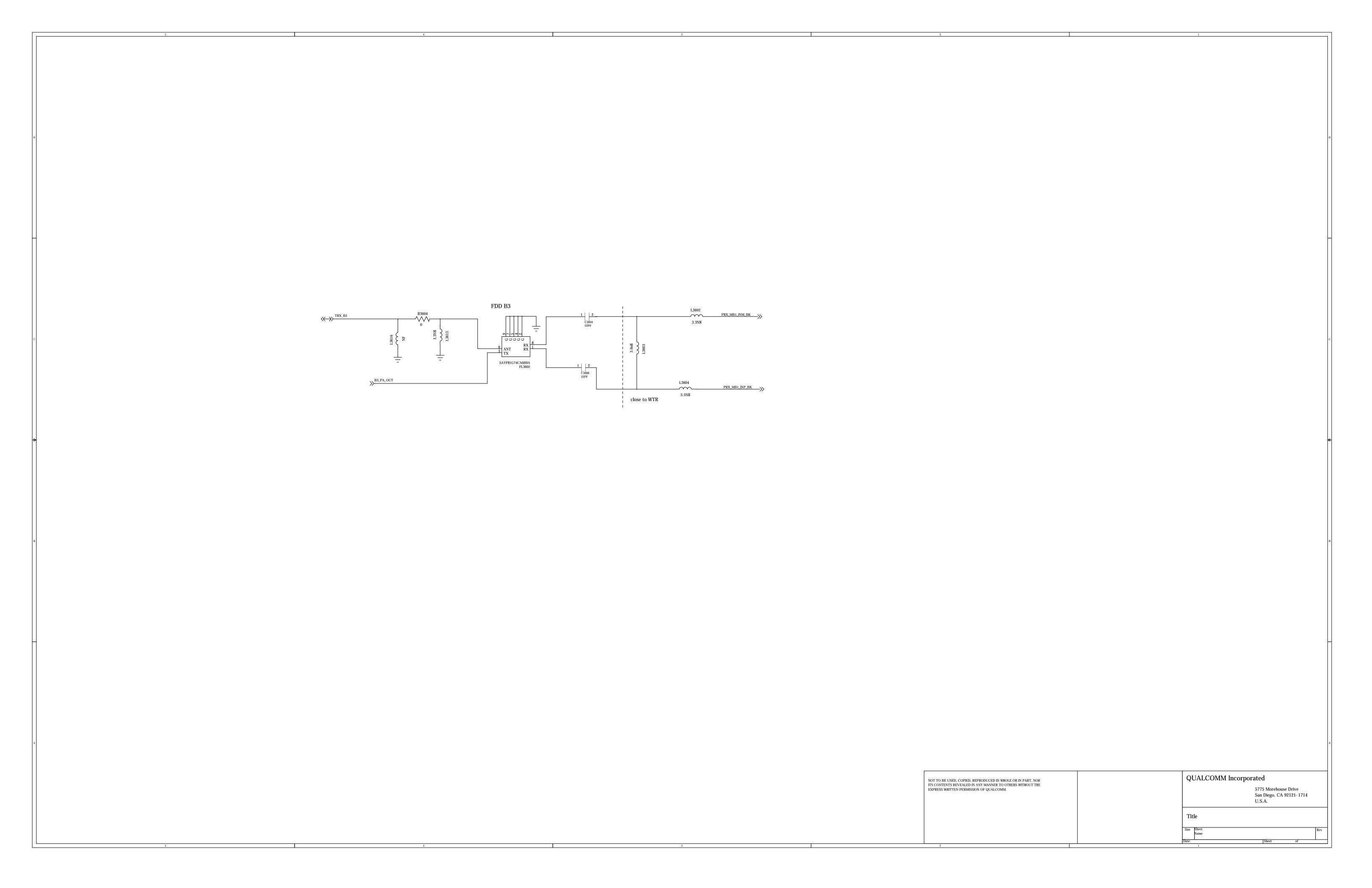


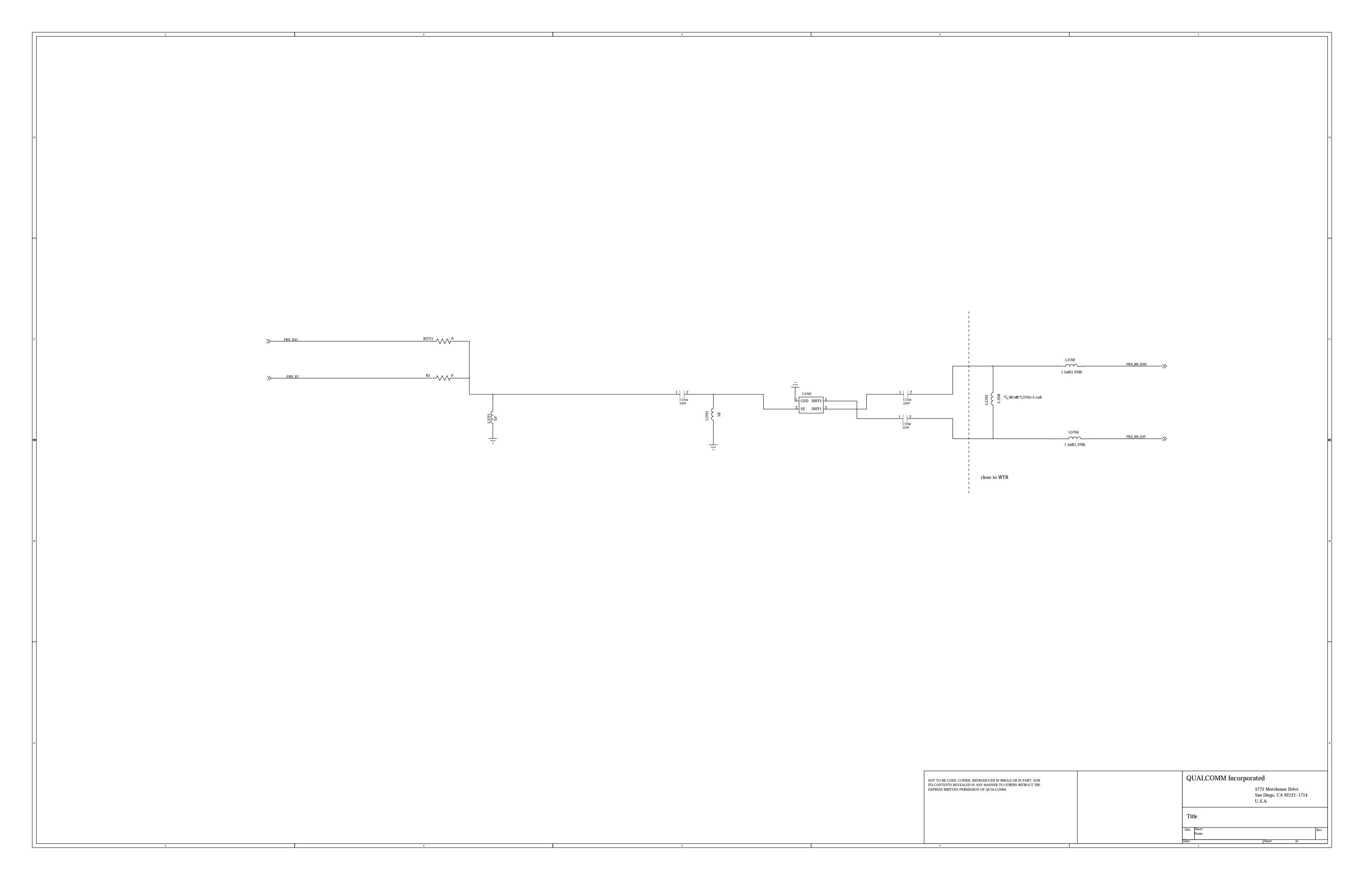
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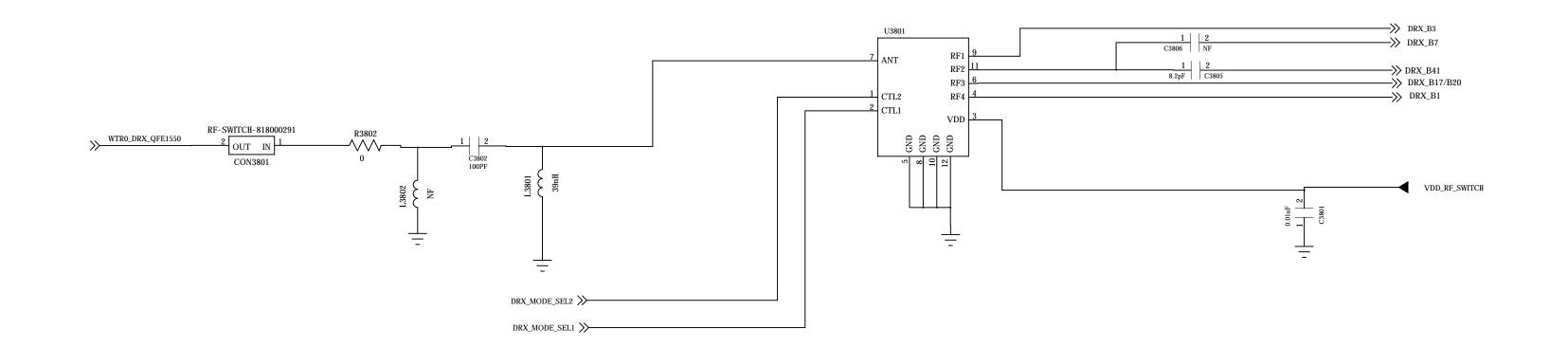


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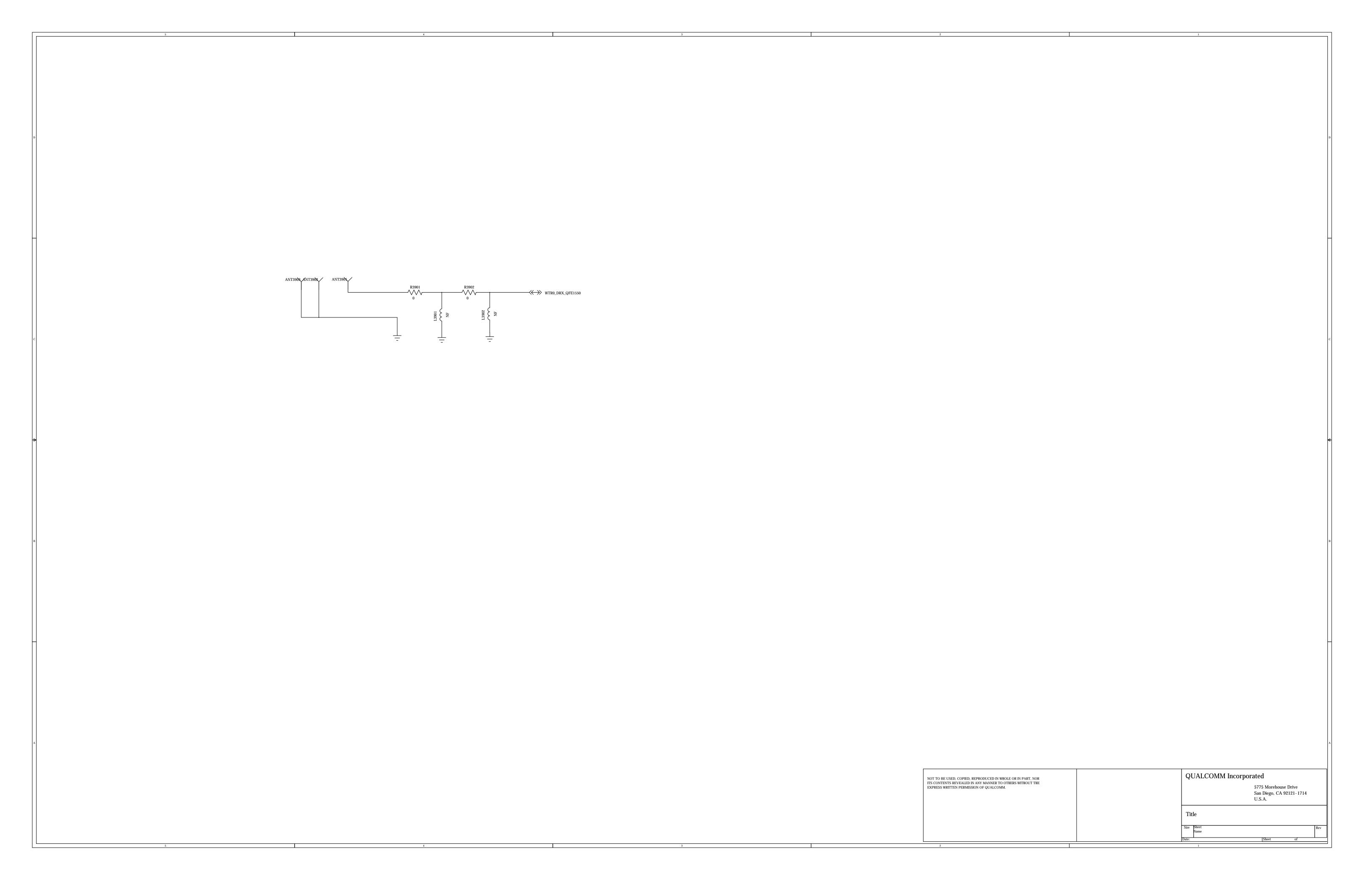


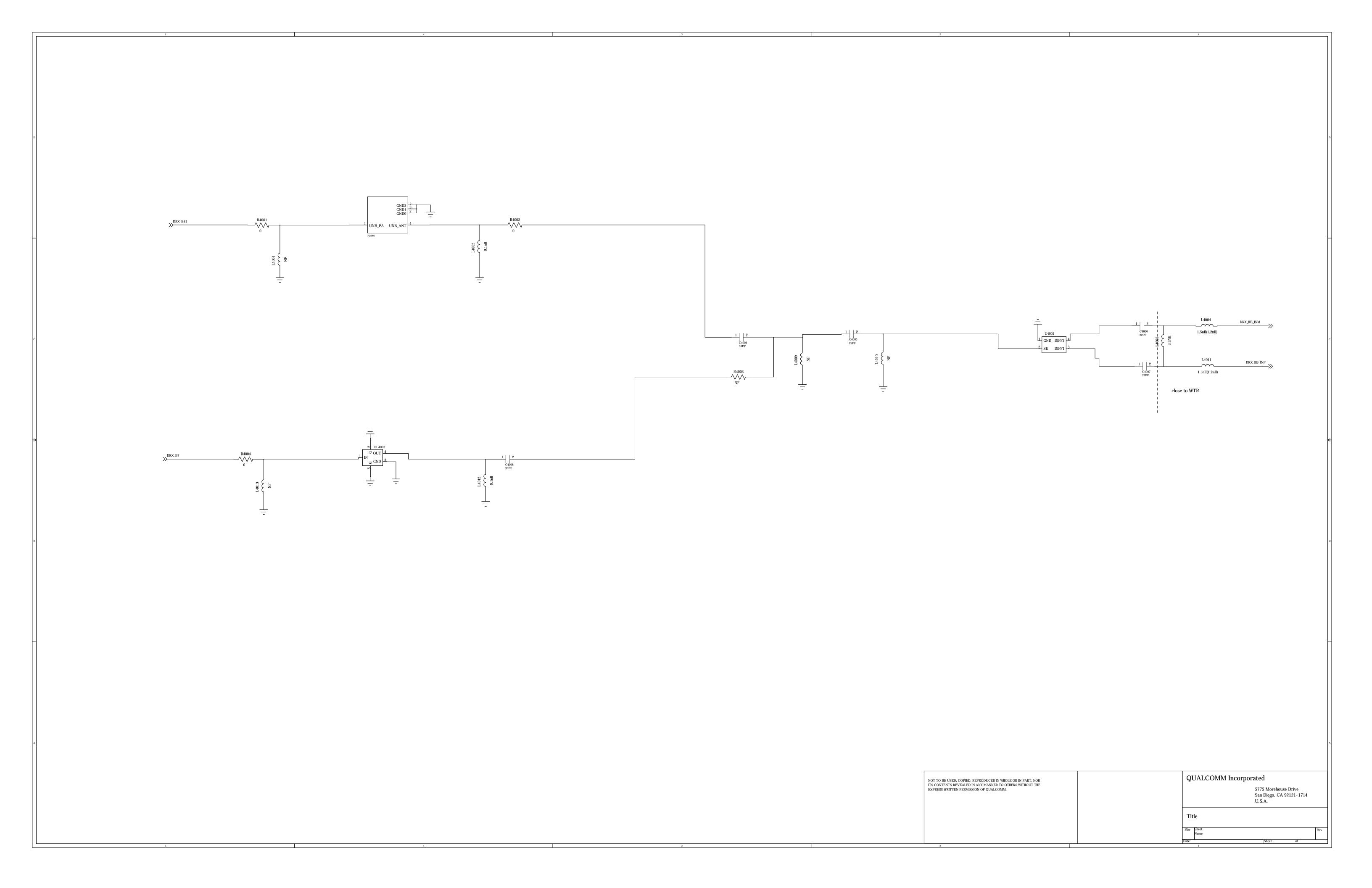


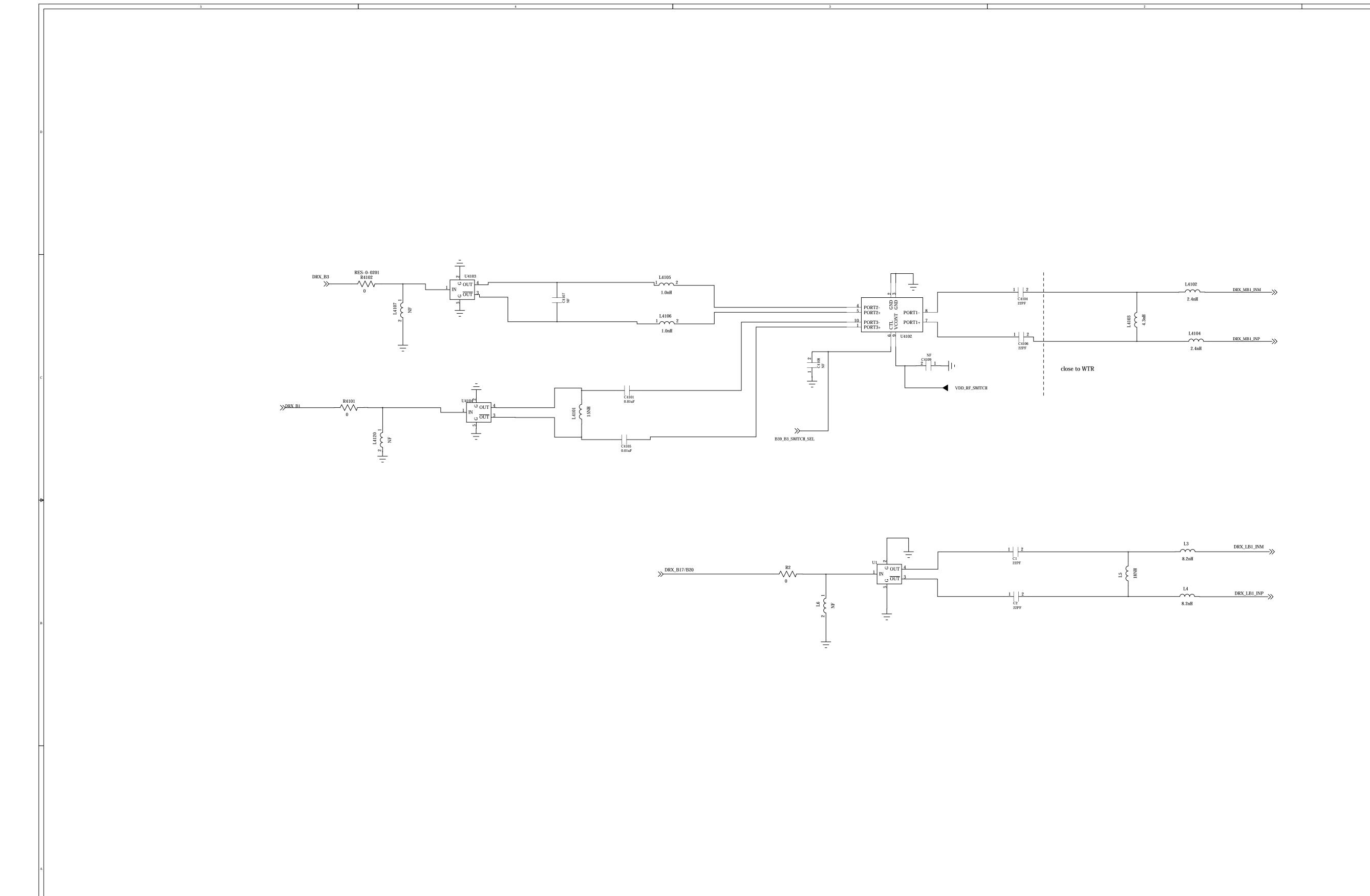
## RX BAND SELECT LOGIC

	PORT	DRX_MODE_SEL0	DRX_MODE_SEL1	DRX_MODE_SEL2
DRX_B7	RF1			
DRX_B40	RF2			
DRX_B41	RF3			
DRX_B39_B1	RF4			
DRX_B3	RF6			

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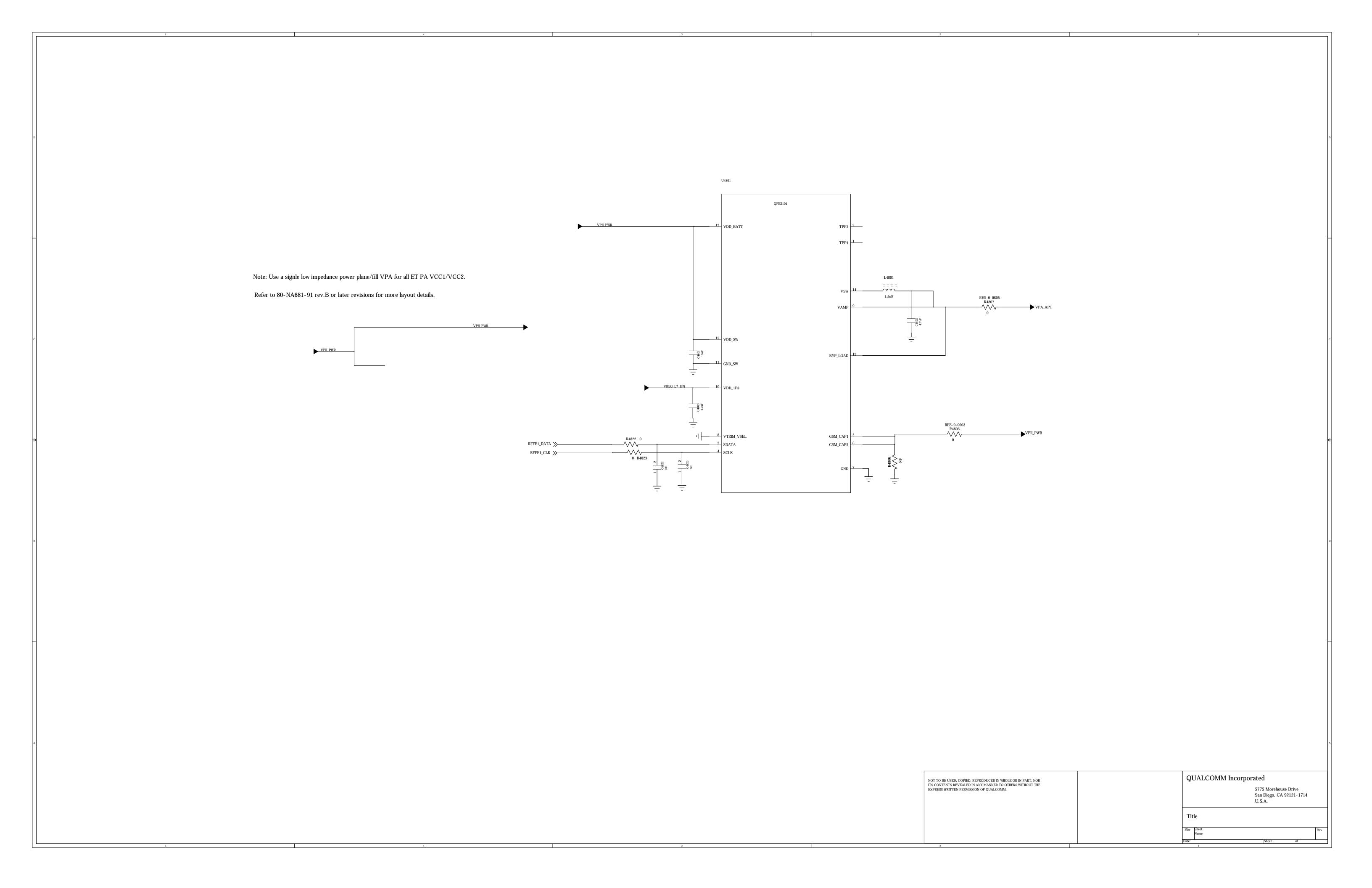
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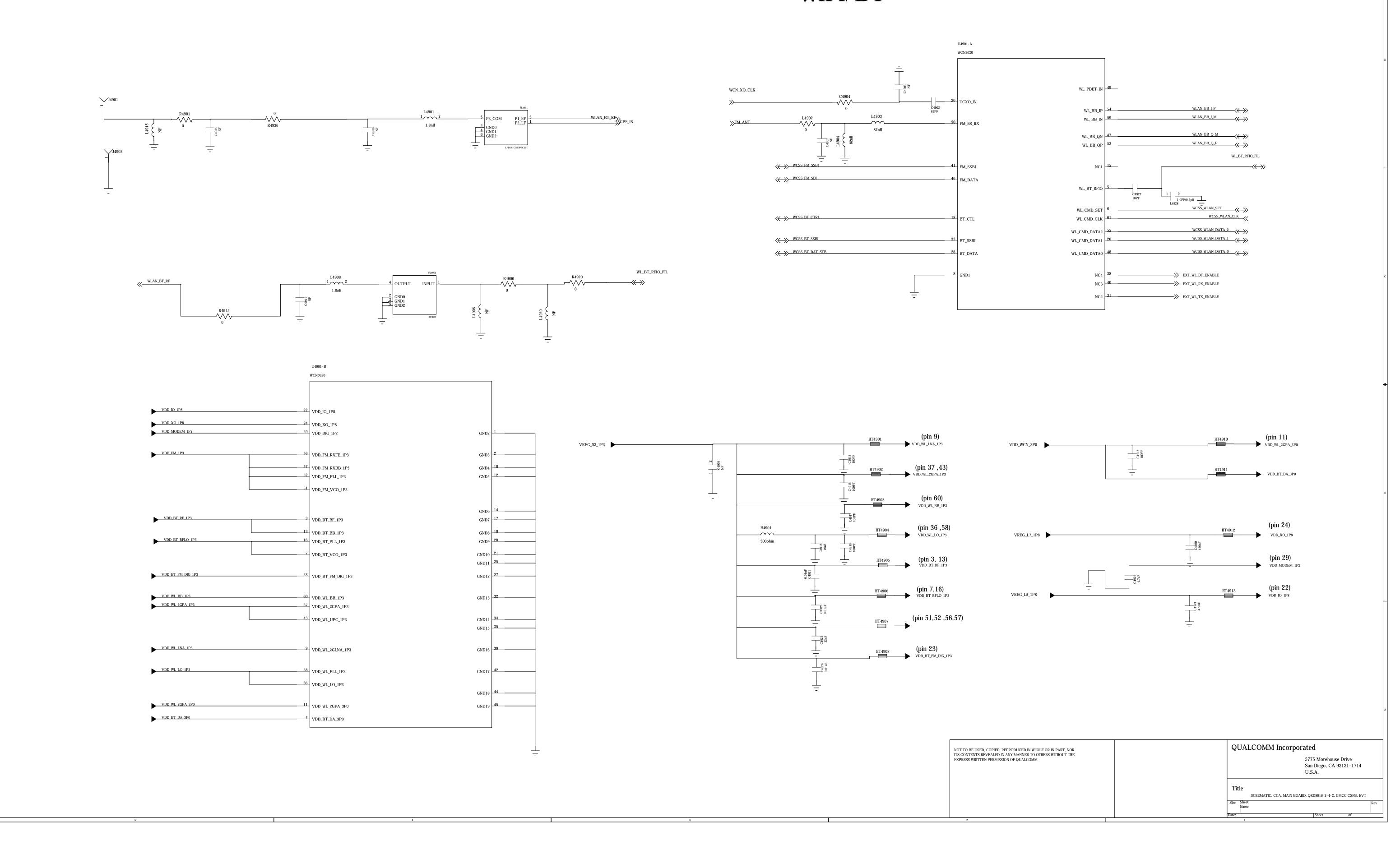
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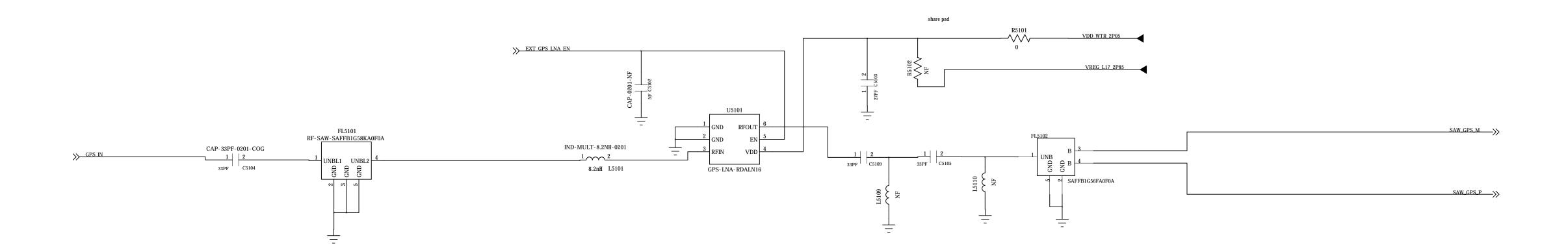


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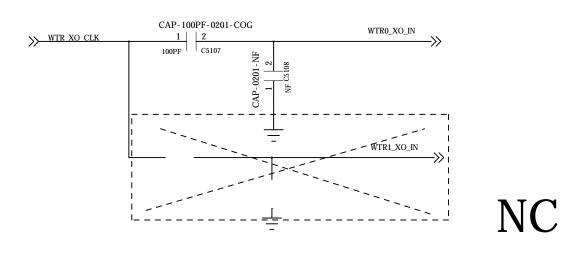


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## GPS



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