

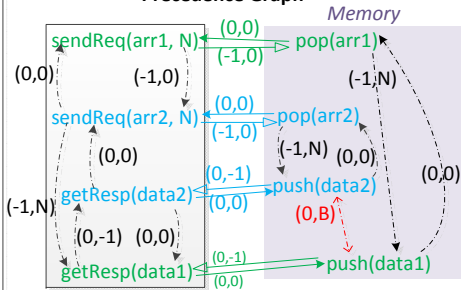
Transformation to Burst Mode

```
For i = 0 to N:
  data1 = arr1[i]
  data2 = arr2[i]
  ...
```

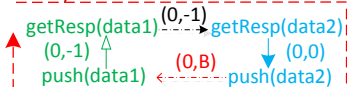
```
sendReq(arr1, N)
sendRea(arr2, N)
For i = 0 to N:
  getResp(data1)
  getResp(data2)
  ...
```

↔ DepM: Dependency due to head-of-line blocking for memory responses

Precedence Graph



Cycle for Artificial Deadlock



Weight of Cycle = $(0, B-2)$