

Transformation to Burst Mode

For $i = 0$ to N :

...

$\text{data1} = \text{arr1}[i]$

$\text{data2} = \text{arr2}[i]$

...



$\text{send_req}(\text{arr1}, N)$

$\text{send_req}(\text{arr2}, N)$

For $i = 0$ to N :

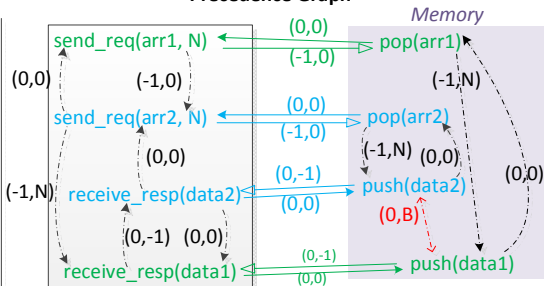
...

$\text{receive_resp}(\text{data1})$

$\text{receive_resp}(\text{data2})$

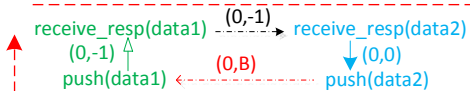
...

Precedence Graph



$\leftarrow \cdots \rightarrow$ DepM: Dependency due to head-of-line blocking for memory responses

Cycle for Artificial Deadlock



Weight of Cycle = $(0, B-2)$