

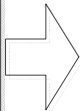
Process

Loop:

INS A

data = *dataPtr

...



Schedule

...

₍₀₎INS A

₍₁₎INS A

₍₂₎INS A ₍₀₎send_req(dataPtr)

₍₃₎INS A ₍₁₎send_req(dataPtr)

₍₄₎INS A ₍₂₎send_req(dataPtr)

₍₅₎INS A ₍₃₎send_req(dataPtr)

₍₄₎send_req(dataPtr) ₍₀₎receive_resp(data)

₍₅₎send_req(dataPtr) ₍₁₎receive_resp(data)

...

Memory

send_req(dataPtr)

₍₃₎

receive_resp(data)

₍₀₎

₍₋₁₎ Req_fifo

₍₋₁₎ Resp_fifo

₍₀₎

pop(dataPtr)

₍₋₁₎

push(data)

₍₀₎

Cycle for Artificial Deadlock

receive_resp ₍₃₎ → receive_resp

↑ ₍₋₁₎
push

↓ ₍₋₁₎
pop

Sum of edge weight = (0)

Precedence Graph