

1: add dst, src1, src2

2: cmp.lt P1, dst, modulus

3: (P1) sel modulus, 0x0, modulus

4: add dst, dst, (-)modulus

**(a) Compiler-generated assembly**

1: add dst, src1, src2

2: cmp.lt P1, dst, modulus

3: (P1) add dst, dst, (-)modulus

**(b) Hand-crafted assembly**