

Tasks

FE

VO

DPR

Match

Navi

...

Thread

SLAM

Inter Robot

Move base

...

ROS nodes

INCAME
Interface

CNN part

Backend

Control/Other

Instructions

HLS/RTL

C++/Python

Hardware
(MPSoC)

Interruptible
DPU

Special
Logic

PL (FPGA)

ScratchPad
memory

PS (CPU)

