Assignment - 3

Microprocessor and microcontroller · (ETEE-310)

①— How many address lines are used to identify an IO port in peripheral and in memory mapped IO method? Also explain the function of latch used at the O/P.

②— Draw a diagram to interface 2 chips of 8K ROM and 2 chips of 8K RAM consecutively with μP 8086.

③— What are the different operating modes of 8255? Discuss how to determine the control word for 8255.

④— Write a short note on programming 8255 in mode 2.

⑤— What is the difference between 8253 and 8254?

⑥— Explain the various modes of operation of 8254 programmable interval timer.

⑦— What are the major limitations of 8085 interrupt scheme? How are the limitations overcome by 8259? Explain with the help of 8259 block diagram.

⑧— Write a short note on 8279 Keyboard/display interface.

⑨— Draw the Block diagram of 8251 and explain each block.

⑩— Draw the interface of 8251 with 8086 in IO mapped IO mode.