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Exp. no	Experiment Name	Date of performance	Date of checking	Marks	Signature
1.	Realize all gates by verifying their truth tables.	31 – 08 - 2020	31 – 08 - 2020		
2.	To verify the truth tables of all logical gates (AND, OR, NOT, NAND, NOR, XOR, XNOR) using NAND gate only.	02 – 09 - 2020	02 – 09 - 2020		
3.	To verify the truth tables of all logical gates (AND, OR, NOT, NAND, NOR, XOR, XNOR) using NOR gate only.	09 – 09 - 2020	09 – 09 - 2020		
4.	To realize the circuit for Half Adder and Full Adder using logic gates.	30 – 09 - 2020	30 – 09 - 2020		
5.	To realize the circuit for Half Subtractor and Full Subtractor using logic gates.	07 – 10 - 2020	07 – 10 - 2020		
6	To realize priority encoder using basic Gates.	21 – 10 - 2020	21 – 10 - 2020		
7.	To realize binary to gray and gray to binary code converter.	28 – 10 - 2020	28 – 10 - 2020		
8.	To realize 2 bit Magnitude Comparator.	11 – 11 - 2020	11 – 11 - 2020		
9.	To realize 4-Bit Binary to BCD Convertor.	17 – 11 - 2020	17 – 11 - 2020		
10.	To realize Multiplexer and Demultiplexer using only NAND gates.	25 – 11 - 2020	25 – 11 - 2020		
11.	To realize J-K flip flop using logic gates or by using kit.	02 – 12 - 2020	02 – 12 - 2020		