



GNU Toolchain for Microchip AVR8 Embedded Processors

Introduction

The AVR 8-bit GNU Toolchain (3.7.0.1796) supports all AVR 8-bit devices. The AVR 8-bit Toolchain is based on the free and open-source GCC compiler. The toolchain includes compiler, assembler, linker and binutils (GCC and Binutils), Standard C library (AVR-libc) and GNU Debugger (GDB).

Table of Contents

Inti	roduct	tion	1
1.	Insta	Ilation Instructions	3
	1.1.	System requirements	
		1.1.1. Hardware requirements	
	1.2.	Downloading, Installing and Upgrading	
		1.2.1. Downloading/Installing on Windows	3
		1.2.2. Downloading/Installing on Linux and Mac	3
	1.3.	1.2.3. Upgrading from previous versions	
		•	
2.	Tools	set Background	
	2.1.	Component Versions	
	2.2. 2.3.	Compiler	
	2.4.	C Library	
	2.5.	Debugging	6
	2.6.	Source Code	6
3.	Bugs	and New Features	7
	3.1.	Notable Bugs Fixed	
	3.2.	Known Issues	7
4.	Supp	ported Devices	8
5.	Cont	act Information and Disclaimer 1	0
	5.1	Disclaimer	1 (



1. Installation Instructions

1.1 System requirements

1.1.1 Hardware requirements

- Minimum processor Pentium 4, 1GHz
- Minimum 512 MB RAM
- Minimum 500 MB free disk space

AVR 8-bit GNU Toolchain has not been tested on computers with less resources, but may run satisfactorily depending on the number and size of the projects and the user's patience.

1.1.2 Software Requirements

- Windows 2000, Windows XP, Windows Vista, Windows 7 (x86 or x86-64) or Windows 8 (x86 or x86-64)
- AVR 8-bit GNU Toolchain is not supported on Windows 98, NT or ME.
- The toolchain should work on the Linux distributions Fedora, RedHat Enterprise, Arch Linux and Ubuntu for both 32-bits and 64-bits architecture. AVR 8-bit GNU Toolchain may very well work on other distributions. However those are untested and unsupported.

1.2 Downloading, Installing and Upgrading

The AVR8 GNU toolchain provided by Microchip is available for download and install in one of the following ways.

1.2.1 Downloading/Installing on Windows

- . If you want to try the AVR8 GNU toolchain alone, you can download it from Microchip's website
- If you want to try the AVR8 GNU Toolchain along with Atmel Studio, you can download and install Atmel Studio 7 or (newer) which will also install the AVR8 GNU toolchain. See Atmel Studio release notes for more details.

1.2.2 Downloading/Installing on Linux and Mac

For Linux and Mac, the AVR8 GNU Toolchain is available as a tar.gz archive which can be extracted using the tar utility. In order to install, simply extract to the location from where you want to run it from. Linux and Mac builds are available from Microchip's website.

1.2.3 Upgrading from previous versions

If the AVR8 GNU Toolchain is installed by Atmel Studio installation, refer Atmel Studio documentation to upgrade.

If the toolchain is installed separately using one of the (Windows, Linux, Mac) installers, upgrading is not supported. You can install the new package side-by-side of the old package and use it.

1.3 Layout

Listed below are some directories you might want to know about.

`<install dir>` = The directory where you installed AVR 8-bit GNU Toolchain.

<install dir>\bin

The AVR software development programs. This directory should be in your `PATH` environment variable. This includes:

- GNU Binutils
- GCC
- GDB



- <install_dir>\avr\lib avr-libc libraries, startup files, linker scripts,and stuff.
- <install_dir>\avr\include avr-libc header files for AVR 8-bit.
- <install_dir>\avr\include\avr header files specific to the AVR 8-bit MCU. This is where, for example, #include <avr/io.h> comes from.
- <install_dir>\lib
 GCC libraries, other libraries, headers and stuff.
- <install_dir>\libexecGCC program components
- <install_dir>\doc Various documentation.



2. Toolset Background

AVR 8-bit GNU Toolchain is a collection of executable, open source software development tools for the Microchip AVR 8-bit series of microcontrollers. It includes the GNU GCC compiler for C and C++.

2.1 Component Versions

GCC: 7.3.0

binutils: 2.26.20160125

avr-libc: "2.0.0"

gdb: 7.8

2.2 Compiler

The compiler is the GNU Compiler Collection, or GCC. This compiler is incredibly flexible and can be hosted on many platforms, it can target many different processors/operating systems (back-ends), and can be configured for multiple different languages (front-ends).

The GCC included in AVR 8-bit GNU Toolchain is targeted for the AVR 8-bit microcontroller and is configured to compile C or C++.

CAUTION: There are caveats on using C++. See the avr-libc FAQ. C++ language is not fully supported and has some limitations. libstdc++ is unsupported.

Because this GCC is targeted for the AVR 8-bit MCUs, the main executable that is created is prefixed with the target name: `avr-gcc` (with '.exe' extension on MS Windows). It is also referred to as AVR GCC.

`avr-gcc` is just a "driver" program only. The compiler itself is called `cc1.exe` for C, or `cc1plus.exe` for C+ +. Also, the preprocessor `cpp.exe` will usually automatically be prepended with the target name: `avr-cpp`. The actual set of component programs called is usually derived from the suffix of each source code file being processed.

GCC compiles a high-level computer language into assembly, and that is all. It cannot work alone. GCC is coupled with another project, GNU Binutils, which provides the assembler, linker, librarian and more. Since 'gcc' is just a "driver" program, it can automatically call the assembler and linker directly to build the final program.

2.3 Assembler, Linker, Librarian and More

GNU Binutils is a collection of binary utilities. This also includes the assembler, as. Sometimes you will see it referenced as GNU as or gas. Binutils includes the linker, ld; the librarian or archiver, ar. There are many other programs included that provide various functionality.

Note that while the assembler uses the same mnemonics as proposed by Microchip, the "glue" (pseudo-ops, operators, expression syntax) is derived from the common assembler syntax used in Unix assemblers, so it is not directly compatible to Microchip AVR assembler source files.

Binutils is configured for the AVR target and each of the programs is prefixed with the target name. So you have programs such as:

- avr-as: The Assembler.
- avr-ld: The Linker.
- avr-ar: Create, modify, and extract from archives (libraries).
- avr-ranlib: Generate index to archive (library) contents.
- avr-objcopy: Copy and translate object files.
- avr-objdump: Display information from object files including disassembly.
- avr-size: List section sizes and total size.
- avr-nm: List symbols from object files.
- avr-strings: List printable strings from files.
- avr-strip: Discard symbols.



- avr-readelf: Display the contents of ELF format files.
- avr-addr2line: Convert addresses to file and line.
- avr-c++filt: Filter to demangle encoded C++ symbols.
- avr-gdb: GDB, the GNU debugger, allows you to see what is going on `inside' another program targeted to AVR, while it executes.

See the binutils user manual for more information on what each program can do.

2.4 C Library

avr-libc is the Standard C Library for AVR 8-bit GCC. It contains many of the standard C routines, and many non-standard routines that are specific and useful for the AVR 8-bit MCUs.

In addition to avr-libc libraries, Host IO library (libhostio.a) is integrated to this toolchain. This Host IO library allows allows the target to use the host's file system and console I/O to perform various avr I/O operations.

NOTE: The actual library is currently split into two main parts, libc.a and libm.a, where the latter contains mathematical functions (everything mentioned in <math.h>, and a bit more). Also, there are additional libraries which allow a customization of the printf and scanf function families. avr-libc contains documentation on how to use (and build) the entire toolset, including code examples. The avr-libc user manual also contains the FAQ on using the toolset.

2.5 Debugging

Atmel Studio provides a debugger and also provides simulators for the parts that can be used for debugging as well. Note that `Atmel Studio` is currently free to the public, but it is not Open Source. The GNU debugger is now shipped along with the toolchain.

2.6 Source Code

AVR8 GNU Toolchain uses modified source code from GCC, Binutils and AVR-LibC. The source code and the build scripts used for building the packaged binaries are available in Microchip's website.

Please refer to the README for the instructions on how to use the supplied script to build the toolchain.



3. Bugs and New Features

3.1 Notable Bugs Fixed

Issue #AVRTC-870:

Updated eeprom is ready for avrxmega3 devices with new NVM control registers.

Issue #AVRTC-871:

An intermittent segmentation fault has been corrected.

Issue #AVRTC-872:

FUSE MEMORY SIZE updated for ATtiny4/5/9/10/20/40 devices.

Issue #AVRTC-876:

Enabled clock prescale get/set functions for ATmega324PB and ATmega328PB devices.

Issue #XC8-1796:

Programs that exceeded that available RAM were not detected by the compiler in some situations, resulting in a runtime code failure.

Issue #XC8-1822:

Loop optimization causes an internal compiler error on Windows.

Issue #XC8-1826:

Unused volatile memory access (SFR reads, for e.g.) was optimized away in certain cases. This broke code that relied on the access for its side effects.

Issue #XC8-1739:

Fix PR 24564 - link fails for some rcalls/rjmps with wraparound.

Issue #XC8-1889:

Fix PR 24571 - Relaxation does not shorten imp or call to target at pc-relative range boundary

3.2 Known Issues

Issue #AVRTC-731:

For AVRTINY architecture, libgcc implementation has some known limitations. Standard C / Math library implementation is very limited or not present.

Issue #AVRTC-732:

Program memory images beyond 128KBytes are supported by the toolchain, subject to the limitations mentioned in "3.17.4.1 EIND and Devices with more than 128 Ki Bytes of Flash" at http://gcc.gnu.org/onlinedocs/gcc/AVR-Options.html

Issue #AVRTC-733:

Named address spaces are supported by the toolchain, subject to the limitations mentioned in "6.16.1 AVR Named Address Spaces" at http://gcc.gnu.org/onlinedocs/gcc/Named-Address-Spaces.html#AVR%20Named %20Address%20Spaces



4. Supported Devices

avr2

Most of the AVR8 devices are supported by this toolchain. Users can get new devices support from Microchip Device Family Packs (DFP). Download DFPs from here¹.

Using DFPs with this toolchain:

- Download DFP which has required device support. (e.g. ATmega328PB is part of ATmega Series DFP.)
- Unzip downloaded *.atpack file to packs directory (e.g. /home/packs/).
- Invoke avr-gcc with additional option -B to tell gcc where to look for device specific information and -I for device header include path.

e.g. avr-gcc -mmcu=atmega328pb -B /home/packs/Atmel.ATmega_DFP.1.0.86/gcc/dev/atmega328pb/ -I / home/packs/Atmel.ATmega_DFP.1.0.86/include/

at90s2343 attiny22 attiny26	at90s4414 at90s4433 at90s4434	at90s8515 at90c8534 at90s8535
ottinu4212	attinu0F	attinu 07
attiny441 attiny44a attiny441 attiny84 attiny84a attiny25 attiny45	attiny85 attiny261 attiny261a attiny461 attiny861 attiny861a attiny43u	attiny87 attiny48 attiny88 attiny828 attiny841 at86rf401
at76c711		
at43usb320		
at90usb82 at90usb162 atmega8u2	atmega16u2 atmega32u2 attiny167	attiny1634
atmega48a atmega48p atmega48pa atmega48pb atmega88 atmega88a atmega88p	atmega88pa atmega8515 atmega8535 atmega8hva at90pwm1 at90pwm2	at90pwm2b at90pwm3 at90pwm3b at90pwm81
atmega168pb atmega169 atmega169p atmega169pa atmega16hvb atmega16hvbrevb atmega16m1 atmega16u4 atmega32a atmega32 atmega32	atmega329a atmega329p atmega3290 atmega3290a atmega3290p atmega3290pa atmega32c1 atmega32m1 atmega32u4 atmega32u6 atmega406	atmega649p atmega6490 atmega16hva2 atmega32hvb atmega6490a atmega6490p atmega64c1 atmega64m1 atmega64hve atmega64hve2 atmega64ffr2
	attiny22 attiny26 attiny4313 attiny44 attiny44a attiny441 attiny84 attiny85 attiny45 at76c711 at43usb320 at90usb82 at90usb162 atmega8u2 atmega48p atmega48p atmega48p atmega48pb atmega48pb atmega88 atmega88 atmega88 atmega88 atmega88p atmega169p atmega169p atmega169p atmega16pp atmega16pp atmega16pp atmega16hvb atmega16hvb atmega16hvb atmega16m1 atmega16u4 atmega32a atmega32	attiny22 attiny26 attiny26 attiny4313 attiny44 attiny44a attiny261a attiny441 attiny461 attiny84 attiny461a attiny85 attiny85 attiny861 attiny85 attiny861 attiny85 attiny861 attiny85 attiny861 attiny85 attiny861 attiny45 attiny45 attiny45 attiny45 attiny461 attiny861 attiny45 attiny861a attiny45 attiny47 attiny861 attiny47 attiny861 attiny47 attiny861 attiny47 attiny861 attiny43u attiny461 attiny861 attiny861 attiny861 attiny861 attiny861 attiny661 attiny661 attiny661 attiny681 attiny681 attiny861 attiny461 attiny861 attiny461 attiny861 attiny461 attiny861 attiny461 attiny46

¹ http://packs.download.atmel.com/



atmega16a atmega161	atmega324a atmega324p	atmega64 atmega64a	atmega644rfr2 atmega32hvbrevb
atmega162	atmega324pa	atmega640	at90can32 at90can64
atmega163 atmega164a	atmega325 atmega325a	atmega644 atmega644a	at90can64 at90pwm161
atmega164p	atmega325p	atmega644p	at90pwm216
atmega164pa	atmega325pa	atmega644pa	at90pwm316
atmega165	atmega3250	atmega645	at90scr100
atmega165a atmega165p	atmega3250a atmega3250p	atmega645a atmega645p	at90usb646 at90usb647
atmega165pa	atmega3250pa	atmega6450	at94k
atmega168	atmega328	atmega6450a	m3000
atmega168a	atmega328p	atmega6450p	
atmega168p	atmega328pb	atmega649	
atmega168pa	atmega329	atmega649a	
avr51	atra a si a 1 201	ativa a si a 1 2 Oufa 1	a+00aa = 1.20
atmega128 atmega128a	atmega1281 atmega1284	atmega128rfa1 atmega128rfr2	at90can128 at90usb1286
atmega1280	atmega1284p	atmega1284rfr2	at90usb1287
avr6	3 1	3	
atmega2560	atmega2561	atmega256rfr2	atmega2564rfr2
avrxmega2			
atxmega8e5	atxmega32a4	atxmega16a4u	atxmega32e5
atxmega16a4	atxmega32c3	atxmega16c4	
atxmega16d4 atxmega16e5	atxmega32d3	atxmega32a4u	
· ·	atxmega32d4	atxmega32c4	
avrxmega3	attim: .41 C	atticus 017	attinu 221 C
attiny212	attiny416	attiny817	attiny3216
attiny212 attiny214	attiny417	attiny1614	attiny3216 attiny3217
attiny212			
attiny212 attiny214 attiny412	attiny417 attiny814	attiny1614 attiny1616	
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3	attiny417 attiny814 attiny816 atxmega64a3u	attiny1614 attiny1616 attiny1617 atxmega64b1	attiny3217 atxmega64c3
attiny212 attiny214 attiny412 attiny414 avrxmega4	attiny417 attiny814 attiny816	attiny1614 attiny1616 attiny1617	attiny3217
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3	attiny417 attiny814 attiny816 atxmega64a3u	attiny1614 attiny1616 attiny1617 atxmega64b1	attiny3217 atxmega64c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1	attiny417 attiny814 attiny816 atxmega64a3u	attiny1614 attiny1616 attiny1617 atxmega64b1	attiny3217 atxmega64c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u	attiny1614 attiny1616 attiny1617 atxmega64b1	attiny3217 atxmega64c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128a3 atxmega128d3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192a3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128a3 atxmega128d3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192a3 atxmega192d3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192a3 atxmega192d3 atxmega256a3	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192a3 atxmega192d3 atxmega192d3 atxmega256a3 avrxmega7	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u atxmega128b1	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u atxmega192c3	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192d3 atxmega192d3 atxmega192d3 atxmega256a3 avrxmega7 atxmega128a1 avrtiny attiny4	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u atxmega128a1u attiny9	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u atxmega192c3 atxmega128a4u attiny20	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192d3 atxmega192d3 atxmega192d3 atxmega7 atxmega128a1 avrtiny attiny4 attiny5	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u atxmega128a1u atxmega128a1u	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u atxmega192c3 atxmega192c3	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192d3 atxmega192d3 atxmega192d3 atxmega256a3 avrxmega7 atxmega128a1 avrtiny attiny4	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u atxmega128b1 atxmega128a1u attiny9 attiny10	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u atxmega192c3 atxmega128a4u attiny20 attiny40	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3
attiny212 attiny214 attiny412 attiny414 avrxmega4 atxmega64a3 atxmega64d3 avrxmega5 atxmega64a1 avrxmega6 atxmega128a3 atxmega128d3 atxmega192d3 atxmega192d3 atxmega192d3 atxmega7 atxmega128a1 avrtiny attiny4 attiny5	attiny417 attiny814 attiny816 atxmega64a3u atxmega64a4u atxmega64a1u atxmega256a3b atxmega256a3bu atxmega256d3 atxmega128a3u atxmega128a1u attiny9	attiny1614 attiny1616 attiny1617 atxmega64b1 atxmega64b3 atxmega128b3 atxmega128c3 atxmega128d4 atxmega192a3u atxmega192c3 atxmega128a4u attiny20	attiny3217 atxmega64c3 atxmega64d4 atxmega256a3u atxmega256c3 atxmega384c3



5. Contact Information and Disclaimer

Users of AVR 8-bit GNU Toolchain are also welcome to discuss on the AVRFreaks website forum for AVR Software Tools.

5.1 Disclaimer

AVR 8-bit GNU Toolchain is distributed free of charge for the purpose of developing applications for Microchip AVR processors. AVR 8-bit GNU Toolchain comes without any warranty.





Microchip Technology

2355 West Chandler Blvd., Chandler, Arizona, USA **T:** (+1)(480) 792-7200 **F:** (+1)(480) 792-7277 | 85224-6199

www.microchip.com

© 2022 Microchip Technology Inc. / Rev.: 42372A-MCU-05/2022

Microchip®, Microchip logo and combinations thereof, Enabling Unlimited Possibilities®, AVR®, tinyAVR®, XMEGA®, megaAVR®, and others are registered trademarks or trademarks of Microchip Technology Inc. in U.S. and other countries. Windows®, and others, are registered trademarks of Microsoft Corporation in U.S. and or other countries. Other terms and product names may be trademarks of others.

DISCLAIMER: The information in this document is provided in connection with Microchip products. No license, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Microchip products. EXCEPT AS SET FORTH IN THE MICROCHIP TERMS AND CONDITIONS OF SALES LOCATED ON THE MICROCHIP WEBSITE, MICROCHIP ASSUMES NO LIABILITY WHATSOEVER AND DISCLAIMS ANY EXPRESS, IMPLIED OR STATUTORY WARRANTY RELATING TO ITS PRODUCTS INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. IN NO EVENT SHALL MICROCHIP BE LIABLE FOR ANY DIRECT, INDIRECT, CONSEQUENTIAL, PUNITIVE, SPECIAL OR INCIDENTAL DAMAGES (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS AND PROFITS, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) ARISING OUT OF THE USE OR INABILITY TO USE THIS DOCUMENT, EVEN IF MICROCHIP HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. Microchip makes no representations or warranties with respect to the accuracy or completeness of the contents of this document and reserves the right to make changes to specifications and products descriptions at any time without notice. Microchip does not make any commitment to update the information contained herein. Unless specifically provided otherwise, Microchip products are not suitable for, and shall not be used in, automotive applications. Microchip products are not intended, authorized, or warranted for use as components in applications intended to support or sustain life.

SAFETY-CRITICAL, MILITARY, AND AUTOMOTIVE APPLICATIONS DISCLAIMER: Microchip products are not designed for and will not be used in connection with any applications where the failure of such products would reasonably be expected to result in significant personal injury or death ("Safety-Critical Applications") without an Microchip officer's specific written consent. Safety-Critical Applications include, without limitation, life support devices and systems, equipment or systems for the operation of nuclear facilities and weapons systems. Microchip products are not designed nor intended for use in military or aerospace applications or environments unless specifically designated by Microchip as military-grade. Microchip products are not designed nor intended for use in automotive applications unless specifically designated by Microchip as automotive-grade.