Pin	Signal	Channel	Pin	Signal	Channel	Pin	Signal	Channel
50	GND[1]		33	DAC5	Ch. 20	17	GND[1]	
49	$\overline{\text{DAC5}}$	Ch. 20	32	$\overline{\text{DAC4}}$	Ch. 19	16	DAC4	Ch. 19
48	DAC3	Ch. 18	31	DAC2	Ch. 17	15	$\overline{\text{DAC3}}$	Ch. 18
47	$\overline{\text{DAC2}}$	Ch. 17	30	$\overline{\text{DAC1}}$	Ch. 16	14	DAC1	Ch. 16
46	Reserved (do not connect)		29	ADC5	Ch. 15	13	GND[1]	
45	$\overline{\text{ADC5}}$	Ch. 15	28	$\overline{\text{ADC4}}$	Ch. 14	12	ADC4	Ch. 14
44	ADC3	Ch. 13	27	ADC2	Ch. 12	11	$\overline{\text{ADC3}}$	Ch. 13
43	$\overline{\text{ADC2}}$	Ch. 12	26	$\overline{\text{ADC1}}$	Ch. 11	10	ADC1	Ch. 11
42	Reserved (do not connect)		25	DIG_IO10	Ch. 10	9	GND[1]	
41	$\overline{\text{DIG\_IO10}}$	Ch. 10	24	$\overline{\text{DIG\_IO9}}$	Ch. 9	8	DIG_IO9	Ch. 9
40	DIG_IO8	Ch. 8	23	DIG_IO7	Ch. 7	7	$\overline{\text{DIG\_IO8}}$	Ch. 8
39	$\overline{\text{DIG\_IO7}}$	Ch. 7	22	$\overline{\text{DIG\_IO6}}$	Ch. 6	6	DIG_IO6	Ch. 6
38	Reserved (do not connect)		21	DIG_IO5	Ch. 5	5	GND[1]	
37	$\overline{\text{DIG\_IO5}}$	Ch. 5	20	$\overline{\text{DIG\_IO4}}$	Ch. 4	4	DIG_IO4	Ch. 4
36	DIG_IO3	Ch. 3	19	DIG_IO2	Ch. 2	3	$\overline{\text{DIG\_IO3}}$	Ch. 3
35	$\overline{\text{DIG\_IO2}}$	Ch. 2	18	$\overline{\text{DIG\_IO1}}$	Ch. 1	2	DIG_IO1	Ch. 1
34	Reserved (do not connect)					1	GND[1]	

1.

Common ground for all channels, internally connected to system ground.

The $\overline{\text{DIG\_IOx}}$ reference lines are connected to GND on the DS2655M1.