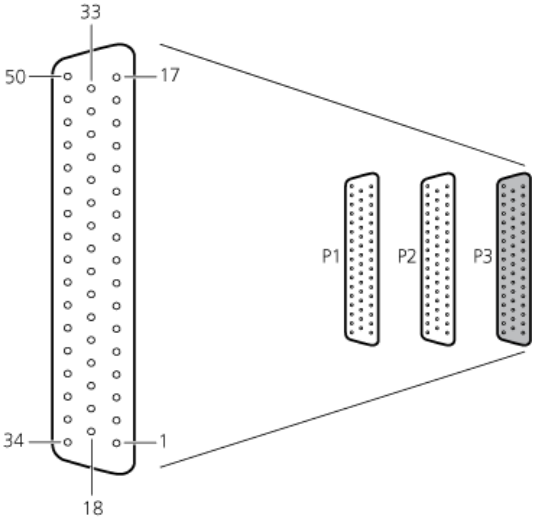

Pin	Signal	Pin	Signal	Pin	Signal
50	GND[1)]	33	Resistance Out 2 Ch. 5 Signal	17	GND[1)]
49	Resistance Out 2 Ch. 5 Reference	32	Resistance Out 2 Ch. 4 Reference	16	Resistance Out 2 Ch. 4 Signal
48	Resistance Out 2 Ch. 3 Signal	31	Resistance Out 2 Ch. 2 Signal	15	Resistance Out 2 Ch. 3 Reference
47	Resistance Out 2 Ch. 2 Reference	30	Resistance Out 2 Ch. 1 Reference	14	Resistance Out 2 Ch. 1 Signal
46	Reserved (do not connect)	29	Analog Out 8 Ch. 3 Signal	13	GND[1)]
45	Analog Out 8 Ch. 3 Reference	28	Analog Out 8 Ch. 2 Reference	12	Analog Out 8 Ch. 2 Signal
44	Analog Out 8 Ch. 1 Signal	27	Analog Out 6 Ch. 8 Signal	11	Analog Out 8 Ch. 1 Reference
43	Analog Out 6 Ch. 8 Reference	26	Analog Out 6 Ch. 7 Reference	10	Analog Out 6 Ch. 7 Signal
42	Reserved (do not connect)	25	Analog Out 6 Ch. 6 Signal	9	GND[1)]
41	Analog Out 6 Ch. 6 Reference	24	Analog Out 6 Ch. 5 Reference	8	Analog Out 6 Ch. 5 Signal
40	Analog Out 6 Ch. 4 Signal	23	Analog Out 6 Ch. 3 Signal	7	Analog Out 6 Ch. 4 Reference
39	Analog Out 6 Ch. 3 Reference	22	Analog Out 6 Ch. 2 Reference	6	Analog Out 6 Ch. 2 Signal
38	Reserved (do not connect)	21	Analog Out 6 Ch. 1 Signal	5	GND[1)]
37	Analog Out 6 Ch. 1 Reference	20	Analog Out 9 Ch. 4 Reference	4	Analog Out 9 Ch. 4 Signal
36	Analog Out 9 Ch. 3 Signal	19	Analog Out 9 Ch. 2 Signal	3	Analog Out 9 Ch. 3 Reference
35	Analog Out 9 Ch. 2 Reference	18	Analog Out 9 Ch. 1 Reference	2	Analog Out 9 Ch. 1 Signal
34	Reserved (do not connect)			1	GND[1)]

1.

To improve signal integrity, it is recommended to use all the GND pins in parallel. Each GND pin can carry a maximum of 1 A_{RMS}.