CCPC-CORE-001 Simulink Module Blocks

This document contains the logical block representations of the CCPC Core 001 processor. Each module is a conceptual Simulink block ready to be visualized, simulated, and executed in deployment environments (Replit, MATLAB Simulink, or custom visual orchestrators).

Block Definitions:

- DigitalMe Kernel: Identity & Wallet-Verifier

- Pane Logic Engine: Decision Filters (Ethical, Emotional, Role-Based)

- License Gateway: Issue & Validate Licensed Access

- Transformer Layer: Recommend actions, stack flows, and UI/UX route maps

- ESG Validator: Analyze impact on People, Planet, Profits (3P++)

- Realm Scanner: Validate alignment of transaction to realm health

- Time-Aware Scheduler: Checks urgency vs wisdom trade-offs

- Replit Router Bridge: Exports interface-level control to EmpireOS Web UI