Introducing logical spacetime

John D.H. Pritchard *

November 7, 2022

Abstract

Semiconductor system design would benefit from a spacetime foundation. A nanosecond spans a centimeter with a common relativistic frame. The individual inertial reference frames of each electron in the square centimeter of an integrated system are logically coherent to a nanosecond "clop".

1 Logical reference frame

A VLSI paradigm assures the coherency of an integrated logical spacetime. The inertial reference frames of individual electrons are not temporal as independent. They are not related individualistically. However, their number and distance has a temporal relation. And that relationship has a degree of individualism. The paradigm must assure temporal coherence.

An operational clock cycle ("clop") is the principal concept of unification. The integration of relativistic particle-waves to the performance of work requires a coherent conception. The *clop* enables that coherence by introducing temporal quantization to the physical reference frame.

2 Logical abstraction

This logical spacetime introduces the "clop" as a term of abstraction. The temporal object of operation brings clock frequency into a design frame of reference to support spatiotemporal type systems.

*@syntelos, logicalexistentialism@gmail.com