

Introducing logical spacetime

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Abstract

Semiconductor system design would benefit from a spacetime foundation. A nanosecond spans a centimeter with a common relativistic frame. The individual inertial reference frames of each electron in the square centimeter of an integrated system are logically coherent to a nanosecond “clop”.

formance of work requires a temporal coherence. The *clop* enables the coherence that establishes information work.

1 Integrated logical spacetime

The VLSI design ensures the coherency of an integrated logical spacetime. The inertial reference frames of individual electrons are not temporal as independent. They are not related individualistically. However, their number and distance has a temporal relation. And that relationship has a degree of individualism. The VLSI design must assure temporal coherence.

An operational clock cycle (“clop”) is the principal concept of unification. The integration of relativistic particle-waves to the per-

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