(\* Following taken directly from the IEC 61131.3 draft standard \*)

(\*

\* An IEC 61131-3 IL and ST compiler.

\*

\* Based on the

\* FINAL DRAFT - IEC 61131-3, 2nd Ed. (2001-12-10)

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\*)

FUNCTION\_BLOCK HYSTERESIS

(\* Boolean hysteresis on difference \*)

(\* of REAL inputs, XIN1 - XIN2 \*)

VAR\_INPUT XIN1, XIN2, EPS : REAL; END\_VAR

VAR\_OUTPUT Q : BOOL := 0; END\_VAR

IF Q THEN IF XIN1 < (XIN2 - EPS) THEN Q := 0; END\_IF ;

ELSIF XIN1 > (XIN2 + EPS) THEN Q := 1 ;

END\_IF ;

END\_FUNCTION\_BLOCK