



(1) TDM CONFIGURATION:

For Default configuration R27, R28, R28 should be assembled
For Alternative configuration R23, R24, R25 should be assembled

C71 needs to be assembled for use of default TDM with DCX81 chip part# DCX81AC0BFAE3ENC (pre MFTO). For use with DCX81AC0BFAE3FNC C71 (47pF) is not needed.

(2) QSPI FLASH VOLTAGE SELECTION:

Schematic shows usage of 1.8V QSPI Flash
For usage of 3V QSPI Flash please refer to DCX81 QSPI application note.

Note: QSPI Flash must support suspend/resume feature. The flash density should be at least 8Mb.

(3) UART / SPI CONFIGURATION:

For backward compatibility to old UART (SW flow control) configuration R26 and R32 need to be assembled.

For new UART (HW flow control) configuration R30, R31, R33 and R34 need to be assembled.

For SPI interface configuration R31, R33, R34 and R35 need to be assembled.

* VCC,VCC1 and VCC2 are the same voltage sources.
The separation made due to Layout constraints.

