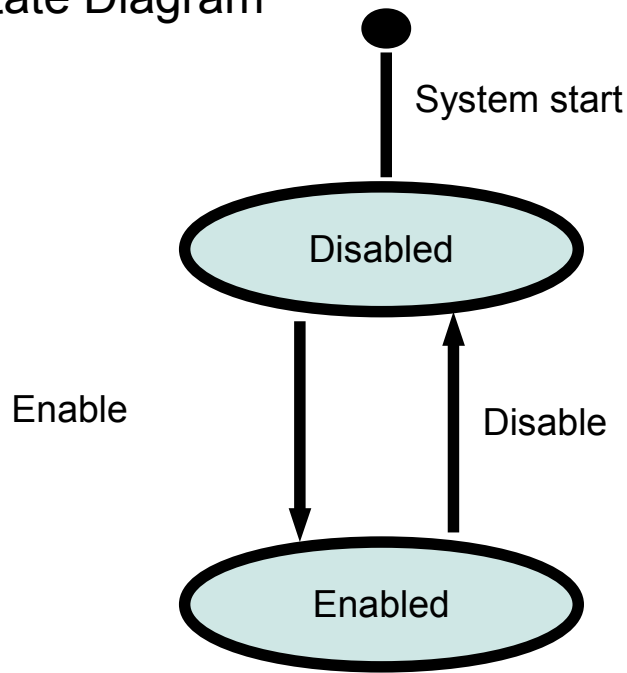


Engineer State Diagram



Enabled Sub-state Diagram

