	_4	Renaming \(\sigma \)						
Icache	Bpred	BTBuf	Icache		RF			
		Itlb	_3					
Dcache		InstS	InstScheduler					
		1	Mul	ALUs		FPU		
Dcache		Dtlb Mul InstDecoder			er			
Beache		Bpred	ALUs	LUs InstDecoder Itlb InstBuf				
Renaming	Dtlb Itlb InstBuf							
						InstScheduler		
		FPU				BTBuf	RF	