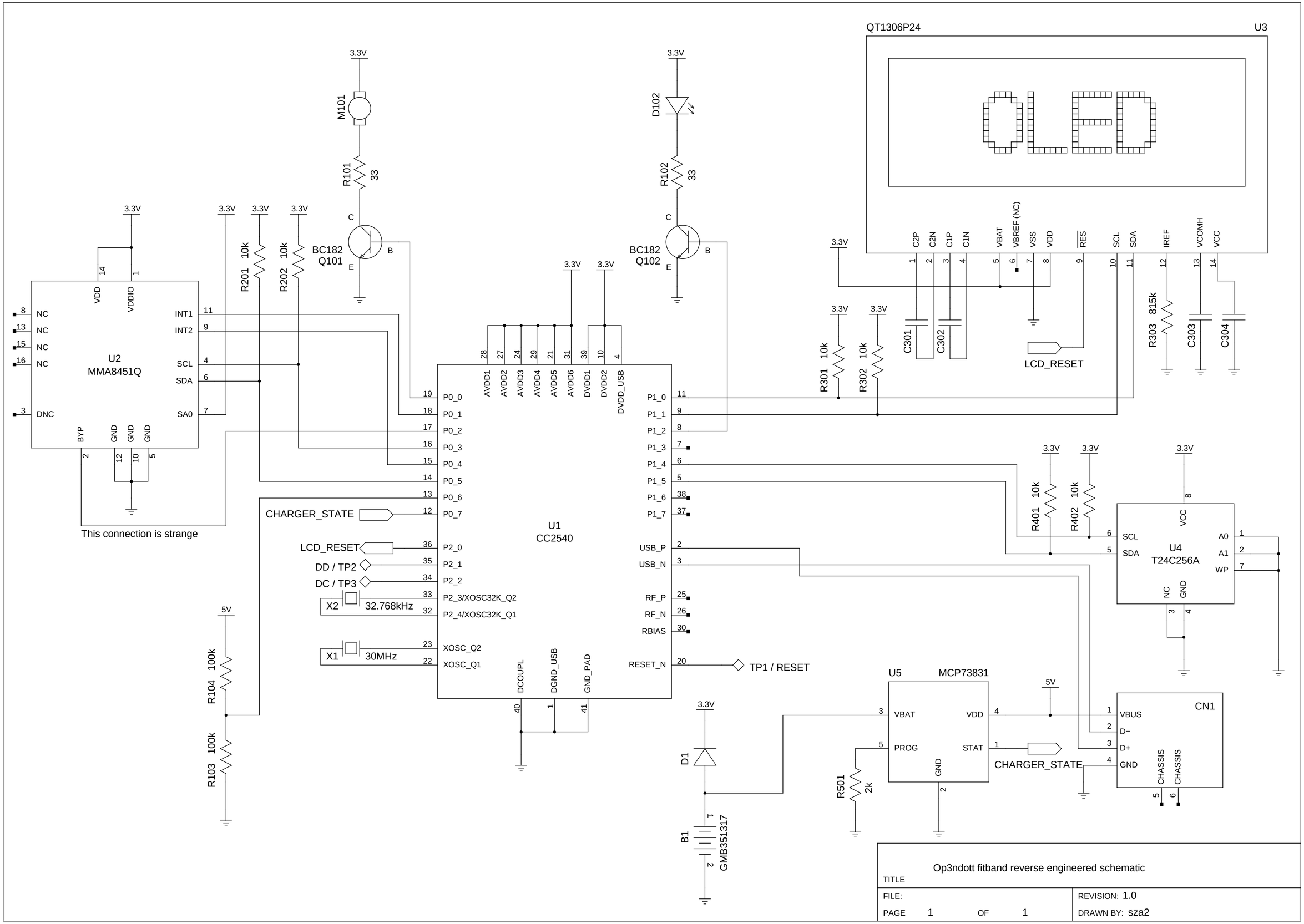
QT1306P24
U3
OLED
C2P
C2N
C1P
C1N
VBAT
VBREF (NC)
VSS
VDD
RES
SCL
SDA
IREF
VCOMH
VCC
C301
C302
LCD_RESET
R303 815k
C303
C304
R301 10k
R302 10k
3.3V
M101
R101 33
D102
R102 33
BC182
Q101
BC182
Q102
R201 10k
R202 10k
U2
MMA8451Q
NC
DNC
VDD
VDDIO
INT1
INT2
SCL
SDA
SA0
BYP
GND
This connection is strange
U1
CC2540
AVDD1
AVDD2
AVDD3
AVDD4
AVDD5
AVDD6
DVDD1
DVDD2
DVDD_USB
P0_0
P0_1
P0_2
P0_3
P0_4
P0_5
P0_6
P0_7
P1_0
P1_1
P1_2
P1_3
P1_4
P1_5
P1_6
P1_7
CHARGER_STATE
LCD_RESET
DD / TP2
DC / TP3
P2_0
P2_1
P2_2
P2_3/XOSC32K_Q2
P2_4/XOSC32K_Q1
X2
32.768kHz
X1
30MHz
XOSC_Q2
XOSC_Q1
USB_P
USB_N
RF_P
RF_N
RBIAS
RESET_N
TP1 / RESET
DCOUPL
DGND_USB
GND_PAD
R401 10k
R402 10k
U4
T24C256A
A0
A1
WP
NC
GND
5V
R104 100k
R103 100k
D1
B1
GMB351317
U5
MCP73831
VBAT
VDD
PROG
STAT
R501 2k
CHARGER_STATE
CN1
VBUS
D−
D+
CHASSIS
Op3ndott fitband reverse engineered schematic
TITLE
FILE:
REVISION: 1.0
PAGE 1 OF 1
DRAWN BY: sza2