### RESEARCH NOTE TEMPLATE

# EDITED BY SHIXI CHEN

The Hong Kong University of Science and Technology

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### Chapter 1

### Your Chapter One

#### 1.1 Your Section One

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Q: How can I place a continuous discussion thread that tracks my thinking process?

A: Write your thinking in a Q&A format inside a  $\beta \in \{formal\} \setminus \{formal\} \in A$ :

#### 1.1.1 Your Subsection

Ensuring a low enough bit error rate (BER) at the receiver requires that the received signal must exceed the minimum detectable power of the receiver. This requirement can be expressed mathematically as

$$P_{mod} - L_{IO} - L_{IC} \ge P_{min,det}, \qquad [dBm] \quad (1.1)$$

where  $P_{mod}$  represents the signal power immediately after modulation,  $L_{IO}$  represents the coupling loss at chip boundaries, and  $L_{IC}$  represents the on-chip loss due to devices such as waveguides, fibers, and micro-ring resonators (MRRs). The parameter  $P_{min,det}$  denotes the minimum detectable power, which is by definition the receiver's sensitivity and is expressed in decibels relative to one milliwatt (dBm).

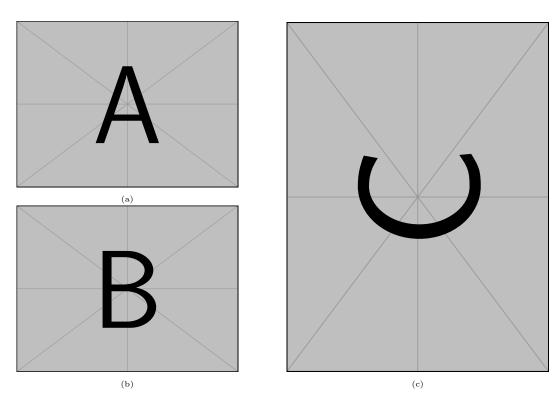


Fig. 1.1. Explanation of the figure presentation. (a) Evidence A. (b) Evidence B. (c) Evidence C.



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**Object2** is the focus of this paragraph. Lorem ipsum dolor sit amet, consectetuer adipiscing elit. Etiam lobortis facilisis sem. Nullam nec mi et neque pharetra sollicitudin. Praesent imperdiet mi nec ante. Donec ullamcorper, felis non sodales commodo, lectus velit ultrices

Parameter	Dynamic Power	Static Power	Note
core@4GHz + L1 I/D Cache	$1.50185~{ m W}$	$0.131346 \; \mathrm{W}$	
512KB L2 Slice	Read: $0.0226595 \text{ nJ/op}$	$0.0238705~{ m W}$	
	Write: $0.0226595 \text{ nJ/op}$		
4MB L3 Slice	Read: $0.080728 \text{ nJ/op}$	0.189787  W	McPAT 7nm
	Write: $0.080728 \text{ nJ/op}$		
router	0.09  pJ/bit	$0.11~\mathrm{W}$	
buffer	0.064  pJ/bit	$0.0002 \ \mathrm{W}$	
intra-die elink (16-Byte wide, uni-)	0.0768  pJ/bit	$0.017 \ \mathrm{W}$	
inter-die elink	1.02  pJ/bit	0 W, no report	[5]
inter-chip elink <sup>a</sup>	4.71  pJ/bit	0 W, no report	OEIL
memory controller	$0.0391783~{ m W}$	$0.00194085~{ m W}$	McPAT
memory	0.24 W	0.075 W	MICRON DDR4 8GB @(9% read, 3%write)

TABLE I ELECTRICAL POWER MODEL

augue, a dignissim nibh lectus placerat pede. Vivamus nunc nunc, molestie ut, ultricies vel, semper in, velit. Ut porttitor. Praesent in sapien. Lorem ipsum dolor sit amet, consectetuer adipiscing elit. Duis fringilla tristique neque. Sed interdum libero ut metus. Pellentesque placerat. Nam rutrum augue a leo. Morbi sed elit sit amet ante lobortis sollicitudin. Praesent blandit blandit mauris. Praesent lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

#### 1.1.2 Literature

[1–5] has reported promising results on phenomena A, B, C while [5] provides more evidence to support the hypothesis. Particularly, [5, Sec. III] explains their relationship in great details and proposes a new model called WWW.

Title	Type	Date	Group	Page
	Note1	2008	Boney96	2.1.2
⊚A very very very very very very very very	Note2	2009	MG	2.1.2
©A very very very very very very very [3]	Note3	2010	HK	2.1.2
○A very very very very very very very [4]	Note4	2011	Pan	2.1.2
②A very very very very very very very [5]	Note5	2012	Shixi	2.1.2

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Authors: Dana Vantrease, Robert Schreiber, Matteo Monchiero, Moray McLaren, Norman P. Jouppi, Marco Fiorentino, Al Davis, Nathan Binkert, Raymond G. Beausoleil, Jung Ho Ahn [1]

<sup>&</sup>lt;sup>a</sup> Highly dependent on length, working frequency, etc. Value derived from OEIL's default device parameters, check the manual for details.

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**①** 

Authors: Dana Vantrease, Robert Schreiber, Matteo Monchiero, Moray McLaren, Norman P. Jouppi, Marco Fiorentino, Al Davis, Nathan Binkert, Raymond G. Beausoleil, Jung Ho Ahn [2]

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#### Algorithm 1 My algorithm

```
1: procedure MyProcedure
         stringlen \leftarrow length of string
        i \leftarrow patlen
 3:
 4: top:
 5:
        if i > stringlen then return false
        j \leftarrow patlen
 6:
 7: loop:
        if string(i) = path(j) then
 8:
 9:
             j \leftarrow j - 1.
             i \leftarrow i-1.
10:
             goto loop.
             close;
12:
13:
        i \leftarrow i + \max(delta_1(string(i)), delta_2(j)).
14:
        goto top.
```

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### Chapter 2

### Your Chapter Two

#### 2.1 Your Section One

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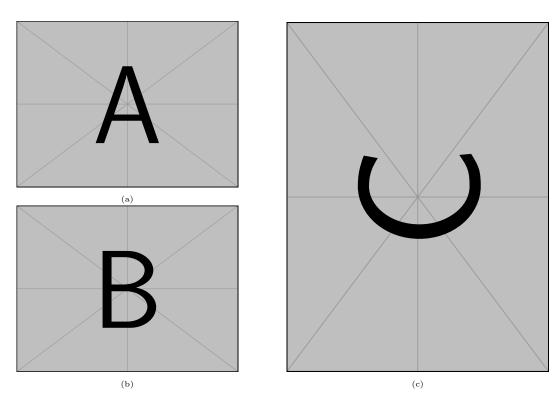


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### Bibliography

- [1] Boney, L., Tewfik, A.H., and Hamdy, K.N., "Digital Watermarks for Audio Signals," *Proceedings of the Third IEEE International Conference on Multimedia*, pp. 473-480, June 1996.
- [2] Goossens, M., Mittelbach, F., Samarin, A LaTeX Companion, Addison-Wesley, Reading, MA, 1994.
- [3] Kopka, H., Daly P.W., A Guide to LaTeX, Addison-Wesley, Reading, MA, 1999.
- [4] Pan, D., "A Tutorial on MPEG/Audio Compression," *IEEE Multimedia*, Vol.2, pp.60-74, Summer 1998.
- [5] Shixi, "A Better Way to Include Bibliography is to Use Zotera," *IEEE*, Vol.2, pp.60-74, Summer 2021.

## Appendix A

# An Appendix

TABLE I CLASSIFICATION

Entry3 Entry4	Single	Multiple			
Entry1	$\mathbf{LPS}$				
Entry2		<b>C</b> a	SD	$\mathbf{FD}$	
			Subsubsec-		
GS			tion1		
GS			Subsubsec-		
			tion5		
RS		Subsubsec-			
163		tion2			
D p		Subsubsec-		Subsubsec-	
		tion3		tion4	

<sup>&</sup>lt;sup>a</sup> Your comments on a <sup>b</sup> Your comments on b

## Appendix B

## Another Appendix

B.1 A Subection in Appendix

Appendix C

My Blog 🗷