

# Heterogeneous Multiprocessor System on Chip Design

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***Abstract***—Processors architecture size and maximum frequencies are hitting their limits. To further improve performance, multi-core systems are required.

***Keywords***—Processor design; Network-on-chip

## I. INTRODUCTION

Our ReCOP? mmhmmm.  
Our ASP/ANI? Perfection.

## II. CONCLUSION

Gold amongst trash.