

APPROVAL SHEET

承 认 书

客户名称 Customer	
产品型号 Part NO.	HY010001
产品内容 Product type	Mode: Transmissive type .Normally black. TFT LCD Module LCD Module: Graphic 80RGB*160Dot-matrix
备注栏 Remarks	□ APPROVAL FOR SEPCIFICATIONS ONLY ■ APPROVAL FOR SEPCIFICATIONS AND SAMPLE
客户确认签章 Signature by Customer:	

PREPARED BY	CHECKED BY	APPROVED BY



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1. General Description

HY010001 is a 80*160 dots matrix a-TFT LCD module. It has a TFT panel composed of 240 sources and160gates. The LCM can be easily accessed by micro-controller

2. Features

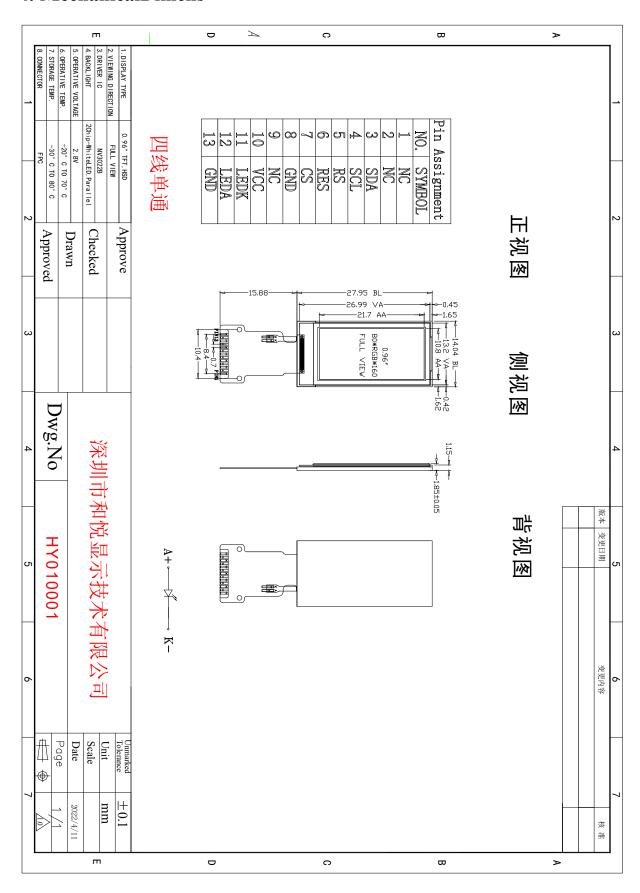
D: 1 M 1	Transmissive
Display Mode	a-TFT
Display Format	Graphic 80RGB*160 Dot-matrix
Input Data	SPI
Viewing Direction	ALL VIEW (IPS)
Diplay Driver IC	NV3022B

3. Mechanical Specification

Item	Specifications	Unit			
Dimensional autline	14.04(W)*27.95H)*1.85(T)				
Dimensional outline	(FPC not include)	mm			
Resolution	80RGB*160	dots			
Display Area	10.8(W)*21.696(V)	mm			
Pixel size	0.135(W)*0.1356(H)	mm			



4. Mechanical Dimens





5. Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Supply voltage	V	-0.3	4.8	V	-
Operating temperature	Top	-20	70	${\mathbb C}$	-
Storage temperature	T _{STR}	-30	80	${\mathbb C}$	-

6. Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Logic	V _{CC}	-	1.65	1.8	3.3	V
Innut Voltage	H level	T _{IH}		0.7*IOVCC	-	IOVCC	N/
Input Voltage	L level	T _{IL}	-	Vss	-	0.2* IOVCC	V

7. INCELL Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	V_{LED}	-	3.2	-	V
LED module current	V_{LED}	-	20	-	mA
INCELL Surface Luminance ★1	Ls	-	-	-	Cd/m³
INCELL Surface brightness uniform ★2	L_{D}	-	-	-	%

★ 1Test condition is:

(a) Center point on active area.

(b)Best Contrast.

★2Uniform measure condition:

- (1) Measure 9 point. Measure location show below;
- (2)Uniform=(Min. brightness/Max. brightness)*100%
- (3)Best Contrast.



8. Module Function Description

8.1Pin Descriptions

Pin No.	Symbol	Functional	Notes
1	NC	NC	
2	NC	NC	
3	SDA	data input and output	
4	SCL	clock input	
5	RS	Display data/command selection pin in 4-line serial interface	
6	RES	LCM_RST	
7	CS	Chip Select Pin	
8	GND	Power Ground	
9	NC	NC	
10	VCC	VI0_PMU	-
11	LEDK	LCD_LEDK	
12	LEDA	LCD_LEDA	
13	GND	Power Ground	

8.2Timing characteristics.

Refer to IC datasheet

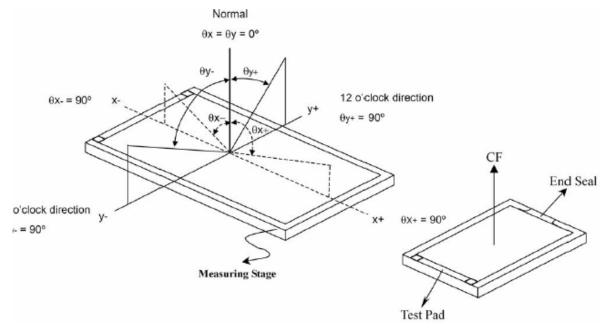


9. Electro-optical Characteristics

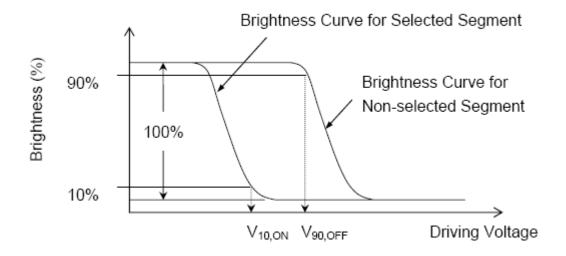
Item	Symbol	Conditions	Tem	p	Min.	Typ.	Max.	Unit	Note
D T'	T_R	$\theta = \Phi = 0$	25℃	7	-	TBD	TBD	msec	NOTE2
Response Time	T_{F}				-	TBD	TBD	-	NOTEZ
Viewing Angle Range	$\Phi = 0^{\circ} (6")$	$\Phi = 90^{\circ} (3^{\circ})$	$\Phi = 90^{\circ}(3")$		=180°(12")	$\Phi = 270^{\circ}$	(9")	NOTE3
θ (25°C) CR≥10	TBD	TBD		TE	BD		TBD		NOTE3

The above "viewing angle" is the measuring position with the largest contrast ratio. Not for good image quality. Viewing direction for good image quality is 12 O'clock.

- •For panel only
- Electro-Optical Characteristics Test Method



$$Vop = (V_{10, ON} + V_{90, OFF})/2$$





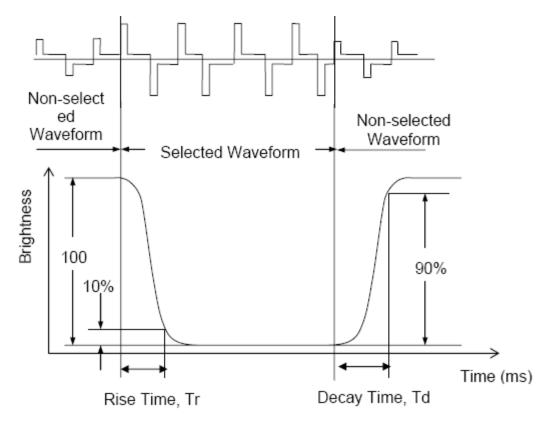
.Note2.Definition

of

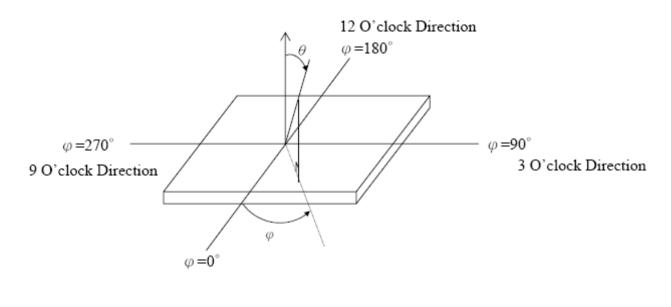
Optical

Response

Time:



.Note3.Definition of Viewing Angle θ and Φ :



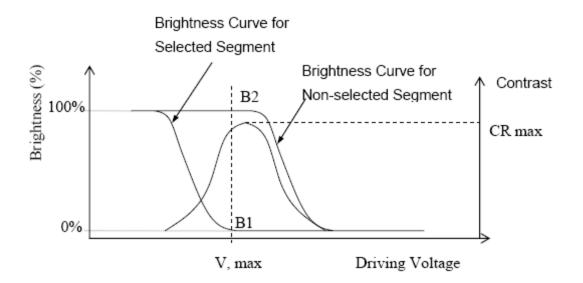
A

6 O'clock Direction



Note4.Definition of Contrast ratio (CR):

CR = Brightness of Non-selected Segment (B2)
Brightness of Selected Segment (B1)



10. Reliability

10.1Mtbf

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal

10.2Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	70°C*48Hrs	。No Defect Of Operational
2	Low Temperature Non-Operating Test	-20℃*48Hrs	Function In Room
3	High Temperature/Humidity Non Operating	60°C*90%RH*48Hrs	Temperature Are Allowable
3	Test	00 С 190%КП 148HIS	。IDD of LCM in Pre-and
4	High Temperature Operating Test	70°C*48Hrs	Post-Test Should Follow
5	Low Temperature Operating Test	-20℃*48Hrs	Specification
6	Thermal Shock Test	-20 °C (30Min) ↔70 °C (30Min) *10CYCLES	

Notes:

- 1. Judgments should be made after exposure in room temperature for two hours.
- 2. The distill water is used for the high temperature/humidity test.
- 3. The sample above is individually for every reliability tests condition.



11.Inspection standards

1.AQL(Acceptable Quality Level)

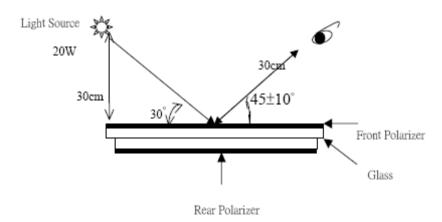
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000 ± 200 .(Darkroom's lux: 100 ± 50), About an angle of incidence 30, a distance of 30 cm with an angle of 45 degree to check the products without uncovering the film!

(As shown below)



3.Inspection item and criteria

3.1 Visual inspection criterion in immobility

3.1.1Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	 Linear cracks panel Reject Nonlinear crack contrast by limited sample 	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) b≤1/3Pin width(non bonding area) [Accept] 2)bonding area≤0.5mm [Accept]	A: Length, b: Width
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) b≤1/3of effective length for bonding electrode [Accept]	a: length, b: Width, c: Thickness



	ну різріаў		
			T
	Pin-side,non-conductive	1)Damage area don't touch the ITO	a: Length, b: Width c: Thickness
	area damaged	(Inclueling contraposition mark,	// 111
	(minor defect)	except scribing mark)	
		【Accept】	
5		$2)C < T b \le BM1/3 \text{ of width}$	
3		【Accept】	T a 4
		3)c=T	a
		b not touch the seal glue	c ·
		【Accept】	
		4)a disregards	
	Non-pin-side damage	c <t< td=""><td>c: Thickness b: width of</td></t<>	c: Thickness b: width of
	(minor defect)	1)b exceeds 1/3Bm	
		【Reject】	
6		c=T	■ BM 內緣
0		b not touch the seal glue	
		【Reject】	
			damage

3.1.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
		$W \leq 0.03$ mm	disregard	note2: disregard if out of AA
	Fiber, glass	$0.03 \text{mm} < W \le 0.05 \text{mm};$	2	← τ →
1	cratch, polarizer scratch/folded	L≦2.0mm	2	
	(minor defect)	$0.05 \text{mm} < W \le 0.08 \text{mm};$	1	V
	(illillor defect)	L≦2.0mm	1	W
		W>0.08mm;L>2.0mm	0	W
	Polarizer bubble, concave and convex	φ ≤ 0.1mm	disregard	note1: $\Phi = (L+W)/2$, L:Length,
2		0.1 mm $< \phi \le 0.15$ mm	2	W :Width
2		0.15 mm $< \phi \le 0.2$ mm	1	note2:disregard if out of AA
	(minor defect)	0.2mm< φ	0	
		φ ≤ 0.10mm	disregard	note2:disregard if out of AA
	Black dots, dirty dots,	0.10 mm $< \phi \le 0.15$ mm	2	
3	impurities, eye winker	0.15 mm $< \phi \le 0.2$ mm	1	<u> </u>
	(minor defect)	0.2mm< φ	0	ϕ
4	Polarizer prick	φ ≤ 0.1mm	disregard	note1: $\Phi = (L+W)/2$, L=Length,



(minor def	ct) $0.1 \text{mm} < \phi \le 0.15 \text{mm}$	3	W=Width
	φ>0. 20mm	0	note2:the distance between two dots>5mm

3.1.3FPC

NO	Defect item	Criteria		Remark
	Copper screen peel	Copper screen pe	eel	
1	(minor defect)	【Reject】		
1				
2	No release tape or peel	No release tape or peel		
2		【Reject】		
	Dirty dot and impurity of FPC	Specification	Allowable	Note1: Cannot have stride
3	for customer using side	Φ ≦ 0.25mm	2	ITO impurities
	(minor defect)	Ф>0. 25	0	

3.1.4Black tape &Mara tape

NO	Defect item	Criteria	Remark
	FPC or H/S black tape	1. shift spec:	LCD
		1) glue to the polarize	
		【Reject】	↓ <u>×</u>
1	(minor defect)	2) IC bare 【Reject】	y1
1		2. left-and-right spec:	
		1)exceed of FPC edge or	Mara tape
		H-S edge 【Reject】	x1
		2) IC bare 【Reject】	Heat Seal
2	No black tape	No black tape	
	(major defect)	【Reject】	
3	Tape position mistake	Not by engineering drawing	
3	(minor defect)		
	Mara tape defect	Peel before pulling the	
4	(minor defect)	protecting film	
		【Reject】	

3.1.5Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon	Uncover the ITO and circuit area	note: compared by engineering
	(major defect)	【Reject】	



2	Taffy glue	1.Uncover the reveal copper area [Reject]	note: if customer has special
	(major defect)	2.Cover layer 0.3mm(Min)~3.0mm(Max)	requirement, refer to the technical
		【Reject】	document
			3.0mm(Max)
3	Depth of glue covering	Depth of glue covering overtop front	Except of the special requirement
	(major defect)	Polarizer [Reject]	

3.2Electrical criteria

3.2E	lectrical criteria			
NO	Defect item	Criteria		Remark
1	No display	No display		
	(major defect)	【Reject】		
2	Missing line	Missing line		
	(major defect)	【Reject】		
3	Seg-com light and dark	Seg-com light and dark	ND filter 29	% test
	(major defect)	【Reject】		
4	No display in immobility	No display in immobility		
	(major defect)	【Reject】		
5	Flicker of Pattern	Flicker of Pattern		
	(major defect)	【Reject】		
6	Mura	ND filter 2%test		
	(major defect)			
7	Over current	Over current		
	(major defect)	【Reject】		
8	Voltage out of specification	Voltage out of		
	(major defect)	specification		
		【Reject】		
9	Pattern blur, error code	Pattern blur, error code		
	(major defect)	【Reject】		
10	Dark light, Flicker	Dark light, Flicker		
	(major defect)	【Reject】		
11	Black/white dots \ Dirty	Specification	Allowable	Note1:disregard if out of AA
	dots, eye winker	φ ≦ 0.15mm	disregard	¥
	(major defect)	0.15 mm $< \phi \le 0.25$ mm	2	\bigcirc $\downarrow \phi$
		0.25 mm $< \phi \le 0.3$ mm	1	←→
		0.3mm< φ	0	φ
12	Fiber, glass crutch, Polarizer	W ≤ 0.03mm	disregard	Note1:L: Length, W: Width
	scratch/folded	$0.03 \text{mm} < W \le 0.0.05 \text{mm}$	2	Note2: disregard if out of AA
	(major defect)	L≤3.0mm	2	← τ →
		0.05mm <w≤0.1mm< td=""><td>1</td><td>^</td></w≤0.1mm<>	1	^
		L≤3.0mm	1	V X
		W>0.1mm;L>3.0mm	0	w



12.Precautions for using LCD modules.

12.1 Safety

- (1)Do mot swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

12.2Srorang Conditions

- (4)Store the panel or module in a dark place where the temperature is $23\pm5^{\circ}$ C and the humidity is below 45 $\pm 20\%$ RH.
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.
- (7) Do not place the module near organics solvents or corrosive gases.
- (8))Do not crush, shake, or jolt the module.

12.3 Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle if very carefully.
- (11) Do not give external shock.
- (12) DO not apply excessive force on the surface.
- (13) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

12.4Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.

13.Factory

FACTORY NAME:

FACTORY ADDRESS:

FACTORY PHONE:

14. Revision history

Version Revise record Dat	æ
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V1.0 Original version 2021-04-14