## Gábor Szilágyi

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Address 1032 Budapest, Kiscelli utca 16, 7/40

Birth 1999.05.18.



#### Education

## Master's Degree in Electrical Engineering

Budapest University of Technology and Economics

Department of Broadband Infocommunications and Electromagnetic Theory FPGA Based Systems (auxiliary specialization)

## Bachelor's Degree in Electrical Engineering

Budapest University of Technology and Economics

Department of Broadband Infocommunications and Electromagnetic Theory

# 2018–2022

2022-2024

## **Work Experience**

#### **Hardware Intern**

Silicon Laboratories Hungary kft.

2021–2022

- RF measurements at the on-site antenna chamber, measurement automation in Python
- Soldering tasks (mostly fine-tuning of matching/filter networks)
- PCB design in Altium, Schematic/BOM management

#### **Hardware Intern**

2023-2024

thyssenkrupp Components Technology Hungary kft.

- Worst case analysis of automotive electronics
- Complete PCB design in Altium
- Temperature chamber measurements

#### Technical skills

Programming languages

 $C, C++, MATLAB, \LaTeX$  Python, Rust

OTHER

Git, Vim, Linux, Altium, LTspice, Amateur Radio lincense

#### Languages

**Hungarian** – native

English - C1

German - A1

## Social competencies

- I consider myself very empathic
- Experience as the team leader of the HA5KFU amateur radio club in the Simonyi Károly Szakkollégium
- I can work efficiently both in teams and alone