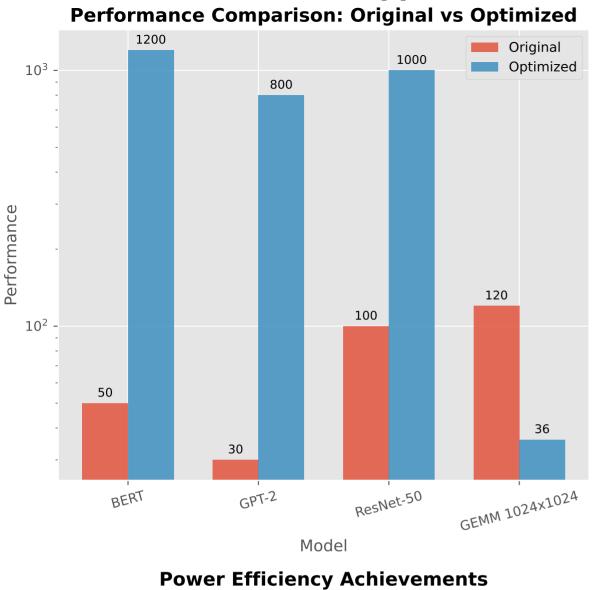
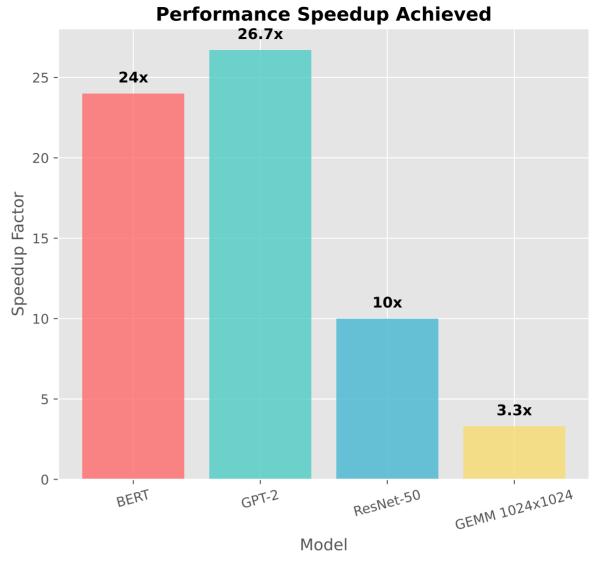
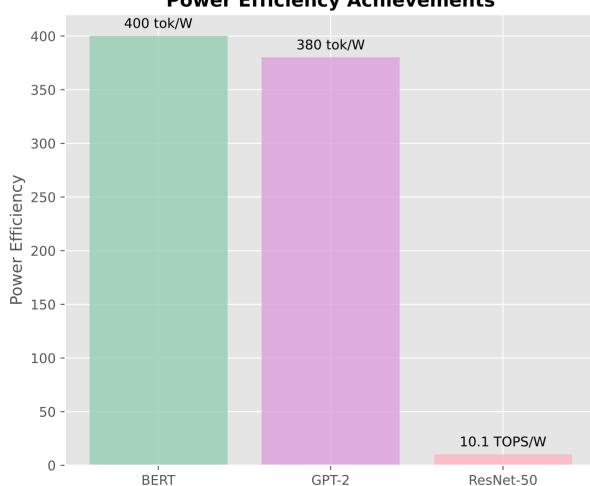
Apple Silicon Integration Performance Results







Model



