

# Workshop in Vivado and VHDL

This workshop is about getting a very basic understanding on how to create a project, simulate code and programming the programmable logic. It is also okay to use the workshop to get better at navigating in Vivado and get an overview on how the IDE is working.

Suggestion for workshop activities:

- Create a new project
- Create a block design
- Add a source file (available on blackboard)
- Create a VDL wrapper
- Make in and outputs external
- Simulate added source and play around with the simulation tool
- Create bit file
- Look into schematics of implemented hardware
- Program programmable logic.