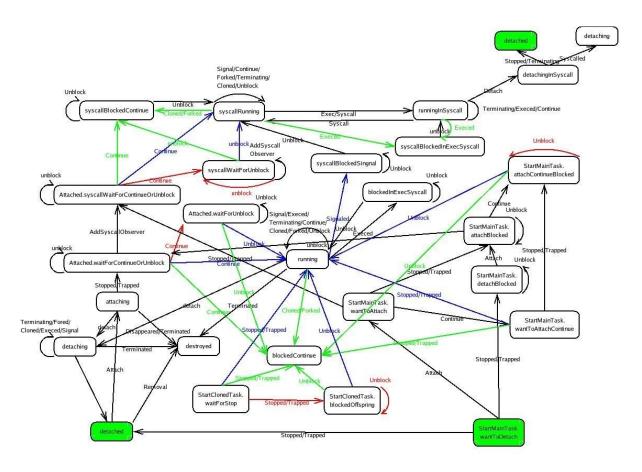
Lecture 8

Finite State Machine FSM



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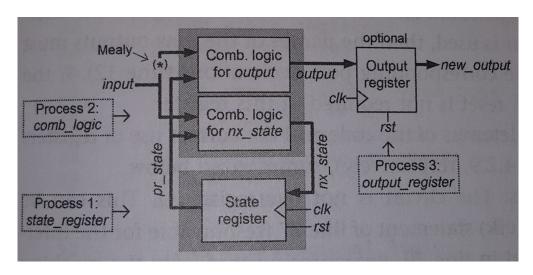
Machine type

Moore

The input, if it exist, in connected only to the logic block that computes the next state

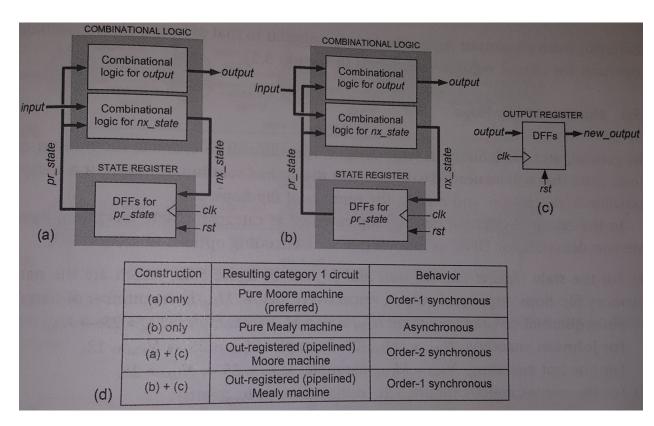
Mealy

The input is connected to both logic block, that is, for the next state and for the actual output.



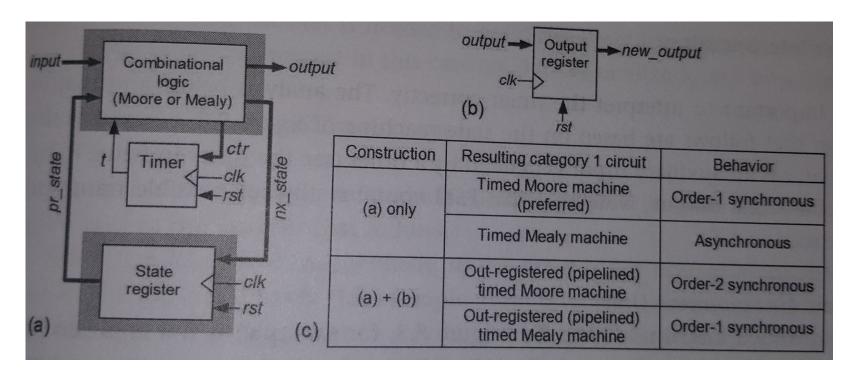


Output dependent only on input and state State dependent on previous state and input



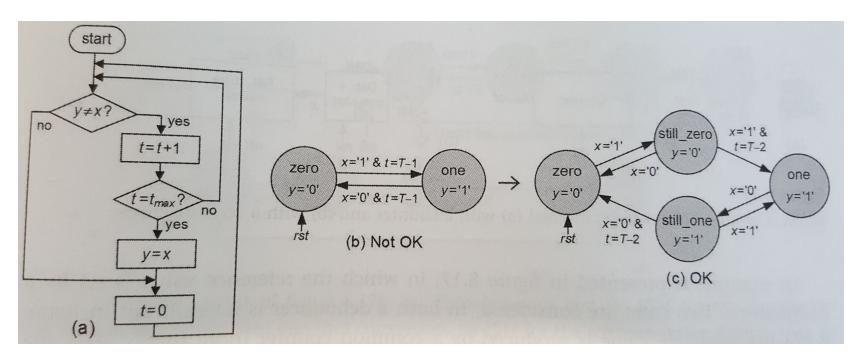


Output dependent only on input and state
State dependent on previous state, input and timer



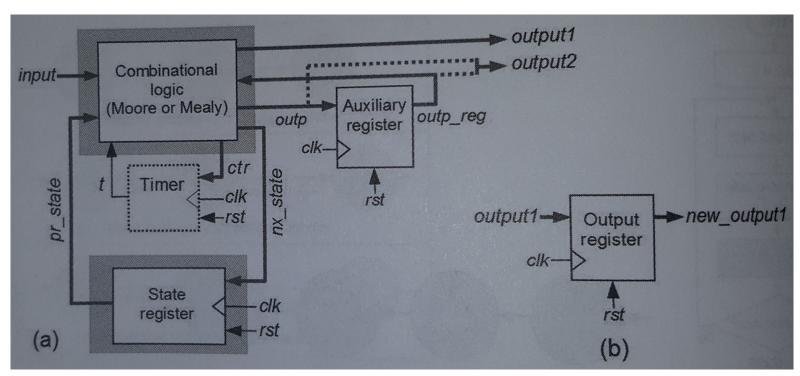


Example of switch debouncer cat 2 FSM





Output dependent on input, state and previous output State dependent on previous state, input, output. (timer is optional)





Example of memory writer cat 3 FSM

