	500	00 i	reg	qis	ter	·list	for devices with KE firmware from	V3.0)5 (che	ck th	e ins	stalled version by reading registe	r 191)
				(,			, , ,	,
				(0×03		single register (0x06)							
o		×			2)	single register (0x06)							
ModBus address (dec)		ModBus address (hex)		registers	(0×0)	ter (es	SJ		
SS		SS	01)	regi	coil ((gist				bytes	registers		
ldre		ldre	(0x		သ မ	e le				ni r	reg		
sac		s ac	Read coils (0x01)	holding	Write single	ngle			oe O	Data length in	r of		
Bus		Bus	d C	ų p	e si	is a		SS	Data type	ı leı	Number		
Vod		lod	Rea	Read	Vrite	Write	Description	Access)ata)ate	Jun	Data	Example or description
0	0x0	0000	ш	Х	>	> >	Device class		uint(16)	2	1	Dala	29 = PSI 5000
1		0001		X			Device type	R		40		ASCII	PSI 5040-40
21 41		0015		X			Manufacturer Manufacturer address	R R		40 40	20	ASCII ASCII	
61		003D		X			Manufacturer ZIP code	R		40		ASCII	
81 101		0051		X			Manufacterer phone number Manufacturer website	R		40		ASCII ASCII	
121		0079		Х			Nominal voltage	R		4		Floating point number IEEE754	40
123 125	_	007B 007D		X		-	Nominal current Nominal power	R R		4		Floating point number IEEE754 Floating point number IEEE754	40 640
131	_	0083		X			Article no.	R		40		ASCII	05100406
151		0097		х			Serial no.	R	char	40		ASCII	1234567890
171 191		00AB 00BF		X		х	User text Firmware version (KE)	RW R	char char	40 40		ASCII ASCII	V3.04 10.05.2017
211	0x0			X			Firmware version (KE) Firmware version (HMI)	R		40		ASCII	V2.05 23.01.2017
231	0x0	00E7		х			Firmware version (DR)	R	char	40	20	ASCII	V1.0.20 23.03.2017
400	J nyn	0192					Damata wada	DW.	:+/40)	2	4	Coils : Remote	0.0000#: 0.5500
402 405	4	0195	x		X	\dashv	Remote mode DC output		uint(16) uint(16)	2	_	Coils : Remote Coils : Output/input	0x0000 = off; 0xFF00 = on 0x0000 = off; 0xFF00 = on
407	7 0x0)197	Х		Х		Condition of DC output after PF alarm	RW	uint(16)	2	1	Coils : Condition	0x0000 = off; 0xFF00 = auto
408		0198 019A		Х		х	Condition of DC output after power ON	RW W	uint(16)	2		Reg : Condition	0xFFFF = off; 0xFFFE = Restore
410 411	4)19A	X		X	+	Restart of the device (warm start) Acknowledge alarms	W	\ · /	2		Coils : Restart Coils : Alarms	0xFF00 = execute 0xFF00 = acknowledge
416	0x0)1A0	Х		Х		Analog interface: Reference voltage (pin VREF)	RW	uint(16)	2	1	Coils : VREF	0x0000 = 10V; 0xFF00 = 5V
417	_)1A1	Х		Х	\perp	Analog interface: REM-SB level	RW		2		Coils : REM-SB Level	0x0000 = normal; 0xFF00 = inverted
418 425)1A2)1A9	X		X	+	Analog interface: REM-SB action Condition of DC output after leaving remote	RW	uint(16) uint(16)	2		Coils : REM-SB Action Coils : Condition	0x0000 = DC off; 0xFF00 = DC on/off 0x0000 = off; 0xFF00 = auto
432	0x0)1B0			Х		Reset device to factory settings	RW	uint(16)	2	1	Coils : Trigger	0xFF00 = trigger reset
440	0x0)1B8		х		х	Analog interface: Pin 14 configuration	RW	uint(16)	2	1	Reg: Alarms 1	0x0000 = OVP (default); 0x0001 = OCP;
													0x0001 = OCP; 0x0002 = OPP;
													0x0003 = OVP + OCP;
													0x0004 = OVP + OPP; 0x0005 = OCP + OPP;
		450						514	/.				0x0006 = OVP + OCP + OPP
441	0x0)1B9		Х		х	Analog interface: Pin 6 configuration	RW	uint(16)	2	1	Reg: Alarms 2	0x0000 = OT + PF (default); 0x0001 = OT:
- 440		45.4						514	/.40			D 011 D0	0x0002 = PF
442	0x0	1BA		Х		х	Analog interface: Pin 15 configuration	RW	uint(16)	2	1	Reg: Status DC	0x0000 = CV; 0x0001 = DC on/off
500	_)1F4		х		х	Set voltage value	RW	(- /	2		0x0000 - 0xD0E5 (0 - 102%)	Voltage value (for translation see programming guide)
501)1F5		X		X	Set current value		uint(16)	_		0x0000 - 0xD0E5 (0 - 102%)	Current value (for translation see programming guide)
	0x0			X		Х	Device state		uint(16) uint(32)			0x0000 - 0xD0E5 (0 - 102%) Bit 0- 4: Control location	Power value (for translation see programming guide) 0x00 = free; 0x01 = local; 0x02 = remote; 0x03 = USB; 0x04
												Dit E . Confin mode	= analog; 0x06 = Ethernet
												Bit 5 : Config mode Bit 7 : DC output/input state	0 = off; 1 = active 0 = off; 1 = on
												Bit 9-10 : Regulation mode	00 = CV; 01 = CR; 10 = CC; 11 = CP
												Bit 11 : Remote	0 = off; 1 = on
												Bit 14 : Remote sensing	0 = off; 1 = on 0 = none; 1 = active
													0 - Holle, 1 - active
												Bit 15 : Alarms Bit 16 : OVP	0 = none; 1 = active
												Bit 16 : OVP Bit 17 : OCP	0 = none; 1 = active
												Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP	0 = none; 1 = active 0 = none; 1 = active
												Bit 16 : OVP Bit 17 : OCP	0 = none; 1 = active
		450										Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input
	7 0x0 8 0x0			X		X	Actual voltage Actual current		uint(16)	2		Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%)	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide)
507 508 509	0x0 0x0	1FC 1FD		x x x		x x	Actual voltage Actual current Actual power	R	uint(16) uint(16) uint(16)	2 2 2	1	Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input
508 509 520	0x0 0x0 0x0	1FC 1FD 0208		X X			Actual current Actual power Count of OV alarms since power up	R R R	uint(16) uint(16) uint(16)	2 2 2	1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%)	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count
508 509 520 521	0x0 0 0x0 0 0x0 0 0x0	1FC 1FD 0208 0209		x x x			Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up	R R R	uint(16) uint(16) uint(16) uint(16)	2 2 2	1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count Count
508 509 520	0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0	1FC 1FD 208 209 20A 20B		X X			Actual current Actual power Count of OV alarms since power up	R R R R	uint(16) uint(16) uint(16)	2 2 2	1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%)	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count
508 509 520 521 522 523 524	0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0	11FC 11FD 0208 0209 020A 020B 020C		x x x x x x		x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up	R R R R R	uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16)	2 2 2	1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF	0 = none; 1 = active Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count Count Count
508 509 520 521 522 523 524 550	3 0x0 0x0 0x0 0x0 0x0 0x0 0x0 0x0	11FC 11FD 1208 1209 120A 120B 120C 1226		x x x x x x x		x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP)	R R R R R R	uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16)	2 2 2	1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual power (for translation see programming guide) Actual power (for translation see programming guide) Count
508 509 520 521 522 523 524	0x0 0x0	11FC 11FD 0208 0209 020A 020B 020C		x x x x x x		x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up	R R R R R R R R R	uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16)	2 2 2	1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF	0 = none; 1 = active Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count Count Count
508 509 520 521 522 523 524 550 553	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229		x x x x x x x x		X X X	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP)	R R R R R R R R R R R R R R R R R R R	uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16)	2 2 2	1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual power (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count Count OVP threshold (for translation see programming guide) OCP threshold (for translation see programming guide)
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229 122C 1221		x x x x x x x x x		x x x x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm	R R R R R R RW RW RW	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual current (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count Count OVP threshold (for translation see programming guide) OCP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OND (DR translation see programming guide) ORD (DR translation see programming guide) ORD (DR translation see programming guide) ORD (DR translation see programming guide)
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229 122C 1221		x x x x x x x x x		x x x x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP)	R R R R R R RW RW RW	uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16) uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%)	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229 122C 1221		x x x x x x x x x		x x x x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm	R R R R R R RW RW RW	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229 122C 1221		x x x x x x x x x		x x x x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm	R R R R R R RW RW RW	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 1208 1209 120A 120B 120C 1226 1229 122C 1221		x x x x x x x x x x		x x x x x x x x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm	R R R R R R RW RW RW	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 12208 12209 1220A 1220B 1220C 1222C 12	1	x x x x x x x x x	1	x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm	R R R R R R R R R R R R R R R R R R R	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 5	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 - 102%) Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual power (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count OVP threshold (for translation see programming guide) OCP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OVD (over translation see programming guide) OVD (over translation see programming guide) OVD (over translation see programming guide) OVE (over translation see programming over tran
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 12208 12209 1220A 1220B 1220C 1222C 12		x x x x x x x x x x x x x x x x x x x	1	x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm Recall set 1	R R R R R R R R R R R R R R R R R R R	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 5	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 - 102%)	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 12208 12209 1220A 1220B 1220C 1222C 12	1	x x x x x x x x x x x x x x x x x x x	<u> </u>	x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm Recall set 1	R R R R R R R R R R R R R R R R R R R	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 5	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 - 102%) Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual power (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count OVP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OVO000 = off; 0x0001 = Restore Voltage value (for translation see programming guide) Current value (OVP) (for translation see programming Overcurrent value (OVP) (for translation see programming Always 0x0000 ↓ Voltage value (for translation see programming guide) Current value (for translation see programming guide) Current value (for translation see programming guide) Current value (for translation see programming guide) Overvoltage value (for translation see programming guide)
508 509 520 521 522 523 524 550 553 556 577	3 0x0 0	11FC 11FD 12208 12209 1220A 1220B 1220C 1222C 12	1	x x x x x x x x x x x x x x x x x x x		x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm Recall set 1	R R R R R R R R R R R R R R R R R R R	uint(16)	2 2 2 2 2 2 2 2 2 2 2	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 5	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 - 102%) Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = none;
508 509 520 521 522 523 524 550 553 577 7100	3 0x0 0	11FC 11FD 1208 1209 1200 1200 1200 1200 1220 1220 1220	1	x x x x x x x x x x x x x x x x x x x		x	Actual current Actual power Count of OV alarms since power up Count of OC alarms since power up Count of OP alarms since power up Count of OT alarms since power up Count of PF alarms since power up Overvoltage protection threshold (OVP) Overcurrent protection threshold (OCP) Overpower protection threshold (OPP) Zustand DC-Ausgang nach OT-Alarm Recall set 1	R R R R R R R R R R R R R R R R R R R	uint(16)	2 2 2 2 2 2 2 2 2 2 10	1 1 1 1 1 1 1 1 1 1 1 1 5	Bit 16 : OVP Bit 17 : OCP Bit 17 : OCP Bit 18 : OPP Bit 19 : OT Bit 21 : Power fail Bit 30 : REM-SB 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF (0 - 125%) 0x0000 - 0xFFFF 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) 0x0000 - 0xE147 (0 - 110%) Reg: Condition Bytes 0-1: 0x0000 - 0xD0E5 (0 - 102%) Bytes 0-1: 0x0000 - 0xD0E5 (0 -	0 = none; 1 = active 0 = DC enabled; 1 = REM-SB disables DC output/input Actual voltage (for translation see programming guide) Actual power (for translation see programming guide) Count Count Count Count OVP threshold (for translation see programming guide) OCP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OPP threshold (for translation see programming guide) OVO000 = off; 0x0001 = Restore Voltage value (for translation see programming guide) Overvorltage value (OVP) (for translation see programming guide) Overcurrent value (OCP) (for translation see programming Always 0x0000 Voltage value (for translation see programming guide) Overvorltage value (OVP) (for translation see programming guide) Overvorltage value (for translation see programming guide) Overvorltage value (for translation see programming guide) Overvorltage value (OVP) (for translation see programming guide) Overvorltage value (OVP) (for translation see programming guide) Overvorltage value (OVP) (for translation see programming guide)
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