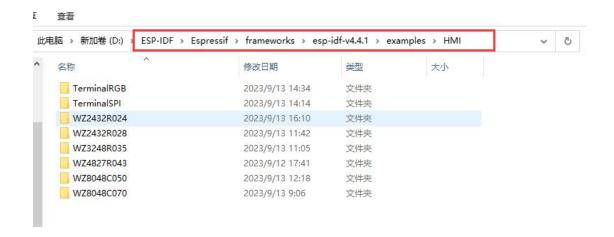
WZ8048C070 Use a tutorial

Place the downloaded project under the IDF directory (as shown below):



Let's first learn at the use of commands:

cd xxx---Moving to the xxx directory, xxx represents the name of the directory, for example: cd example

idf.py set-target esp32s3---Set the target chip for example: esp32s3

idf.py fullclean---Delete the entire build directory, including all the CMake configuration output files.

idf.py clean---It removes the building output files from the building directory and cleans up the entire project..

idf.py menuconfig---Configure the target chip

idf.py build---Compile a private code base

idf.py -p com3 flash---Download the program to the target chip

idf.py -p com3 flash monitor---Once compile burn and open monitoring

Now we open the terminal and go to the WZ8048C070 project catalog

```
D:\ESP-IDF\Espressif\frameworks\esp-idf-v4.4.1\examples\HMI)cd \\VZ8048C070
D:\ESP-IDF\Espressif\frameworks\esp-idf-v4.4.1\examples\HMI\\\Z8048C070>_
```

Now we have to empty the project idf.py fullclean once first, and then go into the configuration

```
D:\ESP-IDF\Espressif\frameworks\esp-idf-v4. 4. 1\examples\HMI\WZ8048C070>idf. py fullclean

Bxecuting action: fullclean

Done

ESP-IDF 4.4 CMD - 'D\ESP-IDREspressif\idf_cmd_init.bat' esp-idf_ab65b06dd9af15e42f388b601e9bc52 - python.exe 'D\ESP-IDREs... - \ X

Top)

SDK tool configuration -->

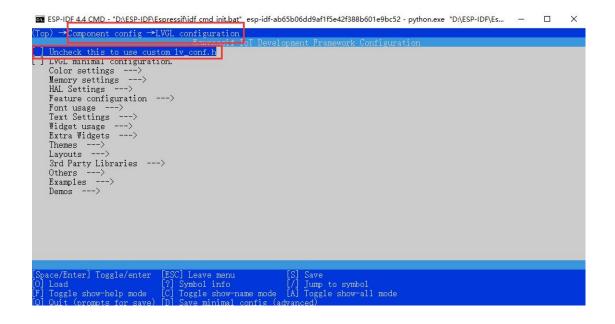
Build type --->
Application manager --->
Bootloader config --->
Security features --->
Serial flasher config --->
Arduino Configuration --->
Compiler options --->
Component config --->
Component config --->
Compatibility options --->

Compatibility options --->

Space/Enter] Toggle/enter [ESC] Leave menu [S] Save [J] Jump to symbol [J] Jump to symbol [C] Toggle show-help mode [O] Toggle show-name mode [A] Toggle show-all mode [O] Toggle show-all mode [O] Toggle show-name mode [A] Toggle show-all mode [O] Toggle show-all mode [O] Toggle show-name mode [A] Toggle show-all mode [O] Toggle show-all mode [O] Toggle show-all mode [O] Toggle show-name mode [A] To
```

Now modify the options by following the following steps:

ESP-IDF 4.4 CMD - "D:\ESP-IDF\Espressif\idf_cmd_init.bat" esp-idf-ab6	5b06dd9af1f5e42f388b601e9bc52 - python.exe "D:\ESP-IDF\Es	_ 🗆	X
(Top) → Partition Table	P. C. Si		
Partition Table (Custom partition table CSV)> (huse app.csv) Custom partition CSV file (0x8000) Offset of partition table [*] Generate an MD5 checksum for the partition table	pment Framework Configuration		
[Space/Enter] Toggle/enter [ESC] Leave menu [0] Load [?] Symbol info [F] Toggle show-help mode [C] Toggle show-name mode [Q] Quit (prompts for save) [D] Save minimal config (ad	[S] Save [/] Jump to symbol [A] Toggle show-all mode vanced)		
From the Add Carlo State of the	Flore J.D. (4) F. (4) (200 legg, old, F2, and because JDA FD JDD F.		V
ESP-IDF 4.4 CMD - "D:\ESP-IDF\Espressif\idf cmd initbat" esp-idf-ab6! (Top) → Component config → ESP32S3-Specific		- 0	×
CPU frequency (160 MHz)>	oment Framework Configuration		
Cache config *] Support for external, SPI-connected RAM			
SPI RAM config>			
Enable Ultra Low Power (ULP) Coprocessor Make exception and panic handlers JTAG/OCD aware			
[*] Hardware brownout detect & reset Brownout voltage level (2.44V)>			
Timers used for gettimeofday function (RTC and high- RTC clock source (Internal 150kHz RC oscillator)			
(1024) Number of cycles for RTC_SLOW_CLK calibration (2000) Extra delay in deep sleep wake stub (in us)			
<pre>L] No Binary Blobs [] Place RTC_DATA_ATTR and RTC_RODATA_ATTR variables in</pre>	nto RTC fast memory segment		
[] Use fixed static RAM size			
[Space/Enter] Toggle/enter [ESC] Leave menu	[S] Save		
[O] Load [?] Symbol info	[/] Jump to symbol [A] Toggle show-all mode		
[Q] Quit (prompts for save) [D] Save minimal config (ad	vanced)		
ESD IDE 4.4 CMD. "DAESD IDDEEnvosiAidf and init hat" and idd abo	5b06dd0.sf1f5.d2f299b601.c0bc52putbop.ovo_"DNECD_IDDEc		~
ESP-IDF 4.4 CMD - "D:\ESP-IDF\Espressif\idf_cmd_init.bat" esp-idf-ab6 (Top) → Component config → ESP32S3-Specific → Support f.		- 0	×
Mode (QUAD/OCT) of SPI RAM chip in use (Octal Mode)	PSRAM)>		
Type of SPIKAM chip in use (Auto-detect)> PSRAM Clock and CS IO for ESP32S3>			
[] Cache fetch instructions from SPI RAM			
Set RAM clock speed (80MHz clock speed)> [*] Initialize SPI RAM during startup			
[] Ignore PSRAM when not found SPI RAM access method (Make RAM allocatable using m	alinc() as well)>		
[*] Run memory test on SPI RAM initialization (16384) Maximum malloc() size, in bytes, to always put			
[] Try to allocate memories of WiFi and LWIP in SPIRAM (32768) Reserve this amount of bytes for data that spec	firstly. If failed, allocate internal memory		
Control sind amount of bytes for data that spec	2223427, needs to be in ban of internal memory		
[Space/Enter] Toggle/enter [ESC] Leave menu	[S] Save		
[O] Load [?] Symbol info	[/] Jump to symbol [A] Toggle show-all mode		



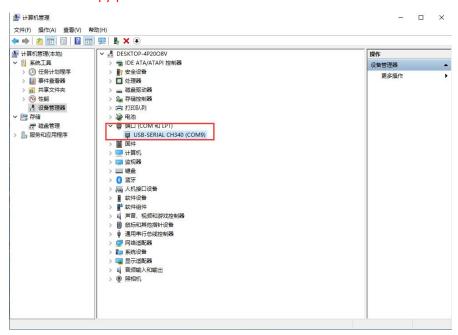
Save the exit after setup, and then execute the idf.py build

```
ents/lvg1-3 D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/lwip D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedt1s D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components
```

Waiting for the compilation to complete, the following figure interface appears:

```
Text | Strict |
```

Perform the idf.py-p com9 flash



success!

```
Writing at 0x00072d8e... (34 %)
Writing at 0x00072d8e... (38 %)
Writing at 0x00098b7d... (38 %)
Writing at 0x00098b7d... (42 %)
Writing at 0x000ad811... (46 %)
Writing at 0x000ad811... (46 %)
Writing at 0x000ad813... (50 %)
Writing at 0x000b9990... (57 %)
Writing at 0x000b9990... (61 %)
Writing at 0x000cb9... (65 %)
Writing at 0x000cb9... (65 %)
Writing at 0x000cb345... (65 %)
Writing at 0x000cb79... (69 %)
Writing at 0x000d83d3... (30 %)
Writing at 0x000d83d5... (80 %)
Writing at 0x000d83d5... (80 %)
Writing at 0x000d805f... (84 %)
Writing at 0x000d805f... (88 %)
Writing at 0x000d805f... (88 %)
Writing at 0x000d805f... (92 %)
Writing at 0x000f46b0... (92 %)
Writing at 0x000f46b0... (92 %)
Writing at 0x000f40b0... (100 %)
Wrote 984224 bytes (410199 compressed) at 0x00010000 in 11.7 seconds (effective 673.4 kbit/s)...
Hash of data verified.
Compressed 3072 bytes to 105...
Writing at 0x00008000... (100 %)
Wrote 984228 bytes (105 compressed) at 0x00008000 in 0.1 seconds (effective 314.3 kbit/s)...
Hash of data verified.
Leaving...
Hash of data verified.
Leaving...
Hard resetting via RTS pin...
Done
```