





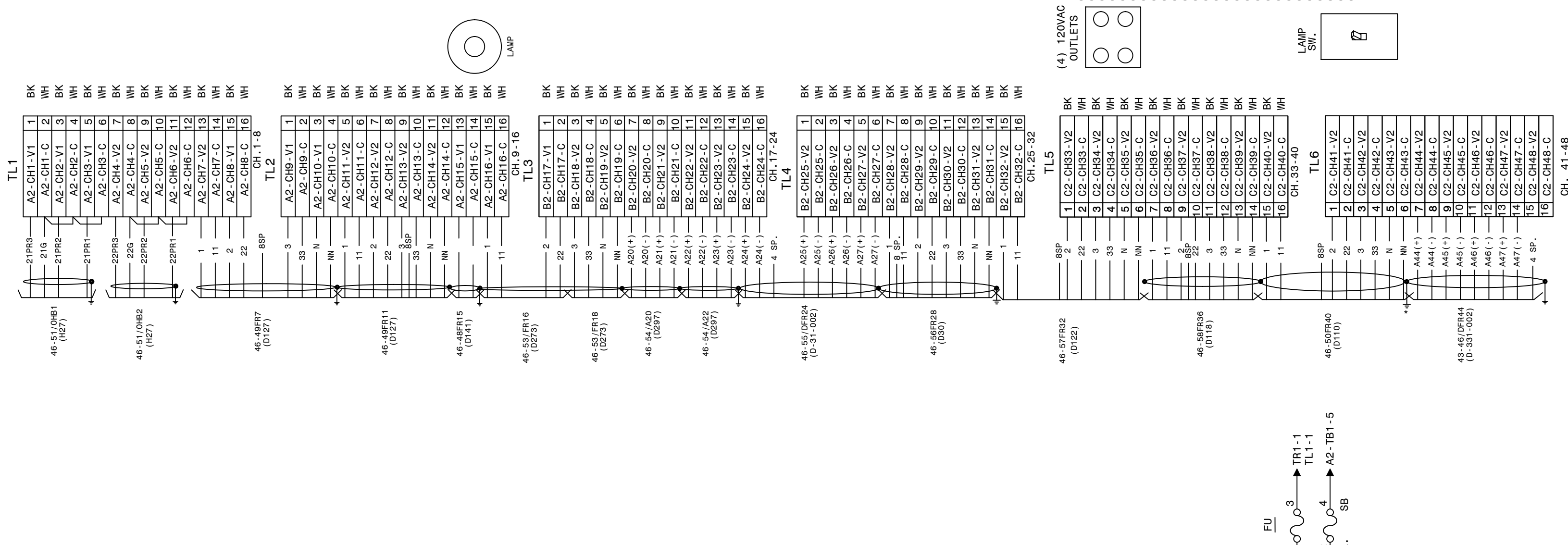




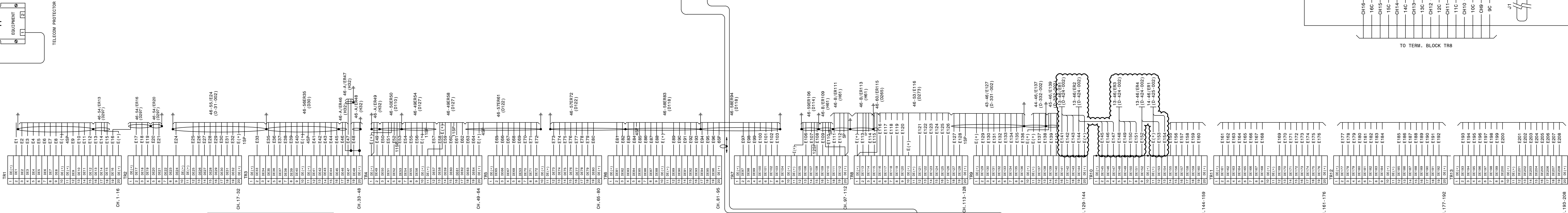




LEFT SIDE SHEET



RIGHT SIDE SHEET



CABLE ARRANGEMENT AND CONNECTION DIAGRAM

NOTE: ALL CABLES SHIELDED AND GROUNDING AT THIS END, UNLESS NOTED BY AN "X".

SIGNAL CONDITIONING UNITS (TYPICAL)

CONNECTION DETAILS (REAR VIEW)

- NOTES:
- ALL CAPACITORS ON TL6 ARE 0.1UF.
  - ADD-ON CHASSIS D2 WAS A 0 ANALOG, 64 EVENT POINT CHASSIS AND HAS BEEN REPLACED WITH A 16 ANALOG, 64 EVENT POINT CHASSIS BY THE FIELD, SHOWN FOR REFERENCE. FIELD TO VERIFY THE CONNECTIONS.
  - FOR THE NEW 16A, 64E CHASSIS, ONE 20 AND ONE 12 POLE STATES TERMINAL BLOCK HAVE BEEN INSTALLED UNDER CHASSIS D2 BY THE FIELD. FIELD TO VERIFY CONNECTIONS FROM CHASSIS D2 TO TERMINAL BLOCKS.

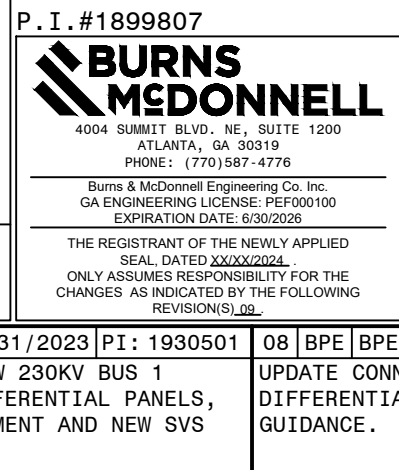
REFERENCES:

- 01-173-D132 DFR #1 F. R. CHANNEL & EVENT ASSIGNMENTS, PANEL #7  
01-173-D205 DFR #1 F. V. AND SECTIONS, PANEL #46  
01-173-D206 DFR #1 CABINET REAR VIEW, PANEL #46

INTERCONNECTING CABLES LIST

A12-J6 TO A2-J3	COMPUTER USB (A2) TO PRIMARY CHASSIS USB (B)
A12-J5 TO C2-J2	COMPUTER USB (A1) TO ADD-ON CHASSIS USB (B)
A12-J14 TO A2-J14	COMPUTER PC VOLTS TO PRIMARY CHASSIS
A12-J8 TO A2-J5	COMPUTER COM1 TO PRIMARY CHASSIS I/O PORT
A12-J13 TO A11 PORT 1-8	COMPUTER J13 TO A11 PORT DESIGNATION PER STATION
A12-J11 TO A2-J6	COMPUTER COM3 TO PRIMARY CHASSIS INT. GPS
A12-J1 TO B2-J3	A2 CHASSIS DSP TIMING (OUT) TO B2 CHASSIS DSP TIMING (IN)
A2-J2 TO B2-J2	A2 CHASSIS USB (A) TO B2 CHASSIS USB (B)
A2-J8 TO A10-IRIG-B	MODULATED IRIG-B CLOCK SOURCE
A2-J7 TO A7-J2	ALARM MODULE
B2-J1 TO C2-J2	B2 CHASSIS USB (A) TO C2 CHASSIS USB (B)
B2-J4 TO C2-J3	B2 CHASSIS DSP TIMING (OUT) TO C2 CHASSIS DSP TIMING (IN)
C2-J1 TO D2-J2	C2 CHASSIS USB (A) TO D2 CHASSIS USB (B)
C2-J4 TO D2-J3	C2 CHASSIS DSP TIMING (OUT) TO D2 CHASSIS DSP TIMING (IN)

ITEM	QTY	DESCRIPTION
A2	1	PRIMARY CHASSIS 16A, 64E
B2	1	ADD-ON CHASSIS 16A, 64E
C2	1	ADD-ON CHASSIS 16A, 64E
D2	1	ADD-ON CHASSIS 16A, 64E
A6	1	MONITOR/ KEYBOARD
A7	1	ALARM OUTPUT MODULE
A9	1	MODM (INTERNAL TO A12)
A12	1	COMPUTER
IL	1	INTERIOR LIGHT
CR	2	CONVENIENCE RECEPTACLE
LS	1	LIGHT SWITCH
TP	1	TELECOM PROTECTOR



AUTOCAD ELECTRICAL  
THIS DRAWING CONTAINS AUTOCAD ELECTRICAL ELEMENTS

P1:	01 [SLT] AJW	9/1/2006	P1:	02 [CP] TEB	3/13/2012	P1: 1402601	03 [BAK] TW	11/11/2013	P1: 1500701	04 [REL] LKJ	2/1/2016	P1: FCB	05 [BAS] JWH	USPP	3/27/2020	P1: 1616363	06 [JCM] JCM	2/6/2021	P1: 1855101	07 [BPE] BPE	BPE	05/31/2023	P1: 1930501	08 [BPE] BPE	BPE	08/01/2023	P1: 1930501	09 [EG] DN	AA	8/20/2024	P1: 1989807
UPDATE INT. L ARNPT PER USE AS-BUILTS.CHGD CABLE NAMES: 46-57FR00 TO 46-54FR20, 46-54FR32 TO 46-57FR32, 46-57ER13 TO 46-54ER13, 46-54ER61 TO 46-57ER61, 46-54ER72 TO 46-57ER72, ADDER COMM CBL CONN, ADDER CBL #4-46COM1, FC336 SWAP PHASES.																															
REMOVE CABLES 46-53FR16, 46-53ER2, ADD 46-53FR16, 46-53FR18, 46-53ER15, 46-53ER19, 46-53ER23 DUE TO PANEL #53 REPLACEMENT.																															
SHOWN FOR RECORD ONLY.																															
FC410 (CC): UPDATED STATUS POINT NAMES.																															
REMOVE CABLES 46-54FR20, 46-54ER13, ADD CABLES 46-54A200, 46-54A202, 46-54ER13, 46-54ER16, & 46-54ER20.																															
UPDATE CONNECTIONS FOR NEW 230KV BUS 1																															
UPDATE CONNECTIONS TO PRIMARY AND SECONDARY DIFFERENTIAL PANELS, NELSON LINE PANEL REPLACEMENT AND NEW SVS GUIDANCE.																															

GEORGIA POWER A SOUTHERN COMPANY		FACILITY NAME: MCGRAU FORD TS	
DRAWN: JLC		TITLE: 230KV DIGITAL FAULT RECORDER #1 CONNECTION DIAGRAM PANEL #46	
CHECKED: AJW	TYPE: WD	FACILITY #: 01-173	NUMBER: 71
DATE: 5/12/2005	SCALE: N.T.S.	BOM:	SHEET: REV: -001-08
ASCT FAC:		ALT DWG NUM:	