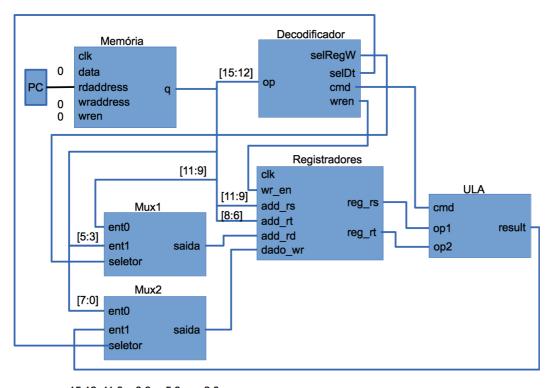
picro-processador

	OP	Instrução	Formato	Operação
0001	0000	Add	OP, rd, rs, rt	R[rd]=R[rs]+R[rt]
0010	0001	Sub	OP, rd, rs, rt	R[rd] = R[rs] - R[rt]
0011	0010	NEG	OP, rd, rs	R[rd]= - R[rs]
0100	0011	And	OP, rd, rs, rt	R[rd]= R[rs]& R[rt]
0101	0100	Or	OP, rd, rs, rt	R[rd]= R[rs] R[rt]
0110	0101	LRG	OP, rs,#conteudo	R[rs]= #conteúdo
0111	0110	RRG	OP, rs	PIO= R[rs]
1000	0111	BLT	OP, rs, #desvio	PC=(rs[7]==1?PC+#endereço:PC)
1001	1000	BGT	OP, rs, #desvio	PC=(rs[7]==0?PC+#endereço:PC)
1010	1001	BEQ	OP, rs#desvio	PC=(rs==0?PC+#endereço:PC)
1011	1010	BNE	OP, rs#desvio	PC=(rs!=0?PC+#endereço:PC)
1100	1111	JMP	OP, #endereço	PC=PC+#endereço
0000	1110	NOP	OP	PC=PC+1

CMD	Operação	Obs.
000	ADD	
001	SUB	
010	NEG	
011	OR	
100	AND	
111	TST_RS	



	15:12	11:9	8:6		5:3	2:0			
Tipo R	OP	RS	RT		RD				
	15:12	11:9	8						
Tipo I	OP	RS			ato				
	15:12		11:0						
Tipo J	OP	Endereço							

End.	CMD	OP		RS		RT				RD					OPC		
		OP			RS					Imediato			0				
0	RRG R1																
1	RRG R2																
3	RRG R3																
3	RRG R4																
<u>4</u> 5	LRG R1,1																
5	LRG R2,2																
6	LRG R3,3																
7	LRG R4,4																
8	RRG R1																
9	RRG R2																
10	RRG R3																
11	RRG R4																
12	Add R5,R1,R4																
13	Add R6,R3,R3																
14	Neg R1																
15	OR RO,R1,R2																
16	RRG R5																
17	RRG R6																
18	RRG R7																
19 20	RRG R0 SUB R0,R2,R1																
21	BLT R0,-1																
22	BGT R0,1																
23	NOP																
24	BEQ R0,-1																
25	BNE R0, 1																
26	JMP -1																
27	JMP -28																
28	, -																
29																	
30																	
31																	
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Máquina de Estados

