4190.308 **Computer Architecture**

Course Syllabus and Organization





Course Organization

- Lecture
 - · higher level concepts
- Homework Assignments
 - everv week
 - practice knowledge covered in class, small assignments
 - prerequisite to participate in the exam
- Projects
 - provide in-depth understanding of computer architecture aspects
 - · larger programming assignments
- Exams / Quizzes
 - mid-term and final, plus random quizzes here and there
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 test your understanding of computer architecture concepts & principles

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Course Logistics

Communication

eΤI

the main channel of communication is through the course website on eTL. Check that site often and regularly! We consider all information posted on eTL read within three weekdays.

Email/SMS

in certain cases we may use eTL's email/SMS functionality to transmit important information to you. All emails/SMS sent to your email address/phone number are considered read within one

→ make sure that your email address/phone number in eTL are correct. While you're at it, please also upload a current picture.

use the course email (comparch@csap.snu.ac.kr). You can Emails to

Instructor/TA expect an answer within one weekday.



Lectures

Tuesdays, Thursdays 15:30 – 16:45 in room 302-409 Time/I ocation

Material

http://newetl.snu.ac.kr/

"Computer Systems: A Programmer's Perspective Randal E. Bryant, David R. O'Hallaron, Textbook

2nd international edition, Pearson, 2011 (must have)

"Computer Organization and Design: The Hardware/Software Interface

David A. Patterson, John L. Hennessy 5th edition, Morgan Kaufmann, 2013

(for interested students)

Acknowledgements slides are based on the cs:app course at CMU

Projects

Submission

 Material on eTI

http://newetl.snu.ac.kr/

Teamwork unless stated otherwise, you must work alone on all

assignments and projects

Late Policy

5 grace days for the entire semester once grace days are used up, **20% penalty per day**

Tip: don't spend them all on the first project

follow the instructions in the assignment

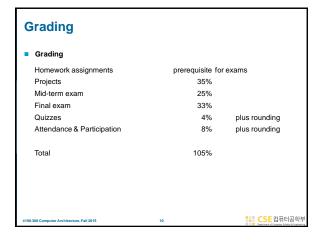
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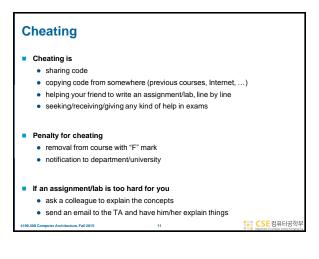


Homework Assignments No teamwork work alone on your homework assignments - paper handins: drop-off box in class and front of the CSAP lab (301-419) - electronic handins: per email to the TA Grading homework is checked, but not graded. Required number of submissions to participate in the - mid-term exam: 5 - final exam: 5 Late Policy homework must be submitted by the deadline.

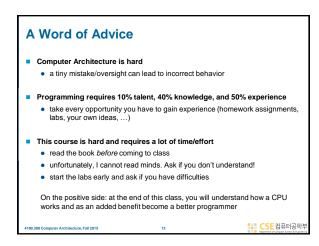
Exams Two exams mid-term final Test your understanding of computer architecture concepts & principles blindly memorizing stuff will not help. A lot of the questions will be based on the homework and projects. Exam logistics 75 minutes closed book one A4 page (front + back) of handwritten notes (original, no copy) allowed Again: you need to submit five (5) homework assignments in time to participate in an exam (both for the mid-term and the final exam)

Attendance & Participation Attendance • you are old enough to know that you should attend the lectures • without notice from you, we assume that you attend the class. If you are unable to attend a lecture, you must notify the TA until 15:45 of the same day. If you are found to be absent without notification, you get 0% in attendance & participation. • you are allowed to skip two lectures and still get 100% attendance • be on time Participation • very much encouraged • participation tracking: will try chocolate paper score, may have to find other means Attendance and participation are part of your grade • easy points, make sure not to miss them!









Week	Date	Lecture Topic	Homework (due)	Project
1	09/01 (Tue) 09/03 (Thu)	Introduction to Computer Architecture		Bomb Lab
2	09/08 (Tue) 03/10 (Thu)	The HW/SW Interface (ISA): Basic Operations The HW/SW Interface (ISA): Arithmetic & Control	HW#1	
3	09/15 (Tue) 09/17 (Thu)	The HW/SW Interface (ISA): Control flow structures The HW/SW Interface (ISA): Procedures and Calling Convention	HW #2	
4	09/22 (Tue) 09/24 (Thu)	Processor Architecture: the Y86 Instruction Set Architecture Processor Architecture: Logic Design	HW #3	
5	09/29 (Tue) 10/01 (Thu)	(public holiday (Chuseok) – no class) → 10/16 09:30-10:45 in 301-107 Processor Architecture: Sequential Implementation	HW #4	Processor Lab
6	10/06 (Tue) 10/08 (Thu)	Processor Architecture: Sequential Implementation Processor Architecture: Pipelining Basics	HW #5	
7	10/13 (Tue) 10/15 (Thu)	Processor Architecture: Pipelined Implementation The Memory Hierarchy: Introduction	HW #6	
8	10/20 (Tue) 10/22 (Thu)	Recitation Mid-term examination (class cancellation deadline: 10/21)	HW #7	



