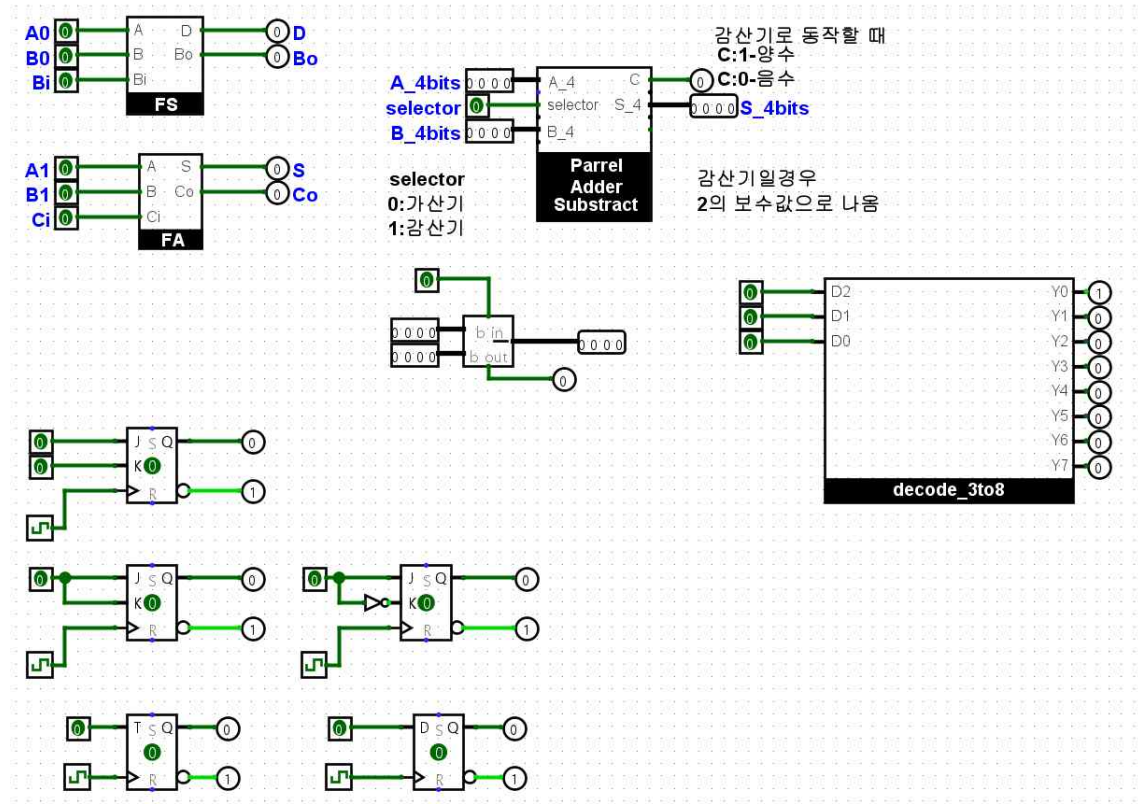
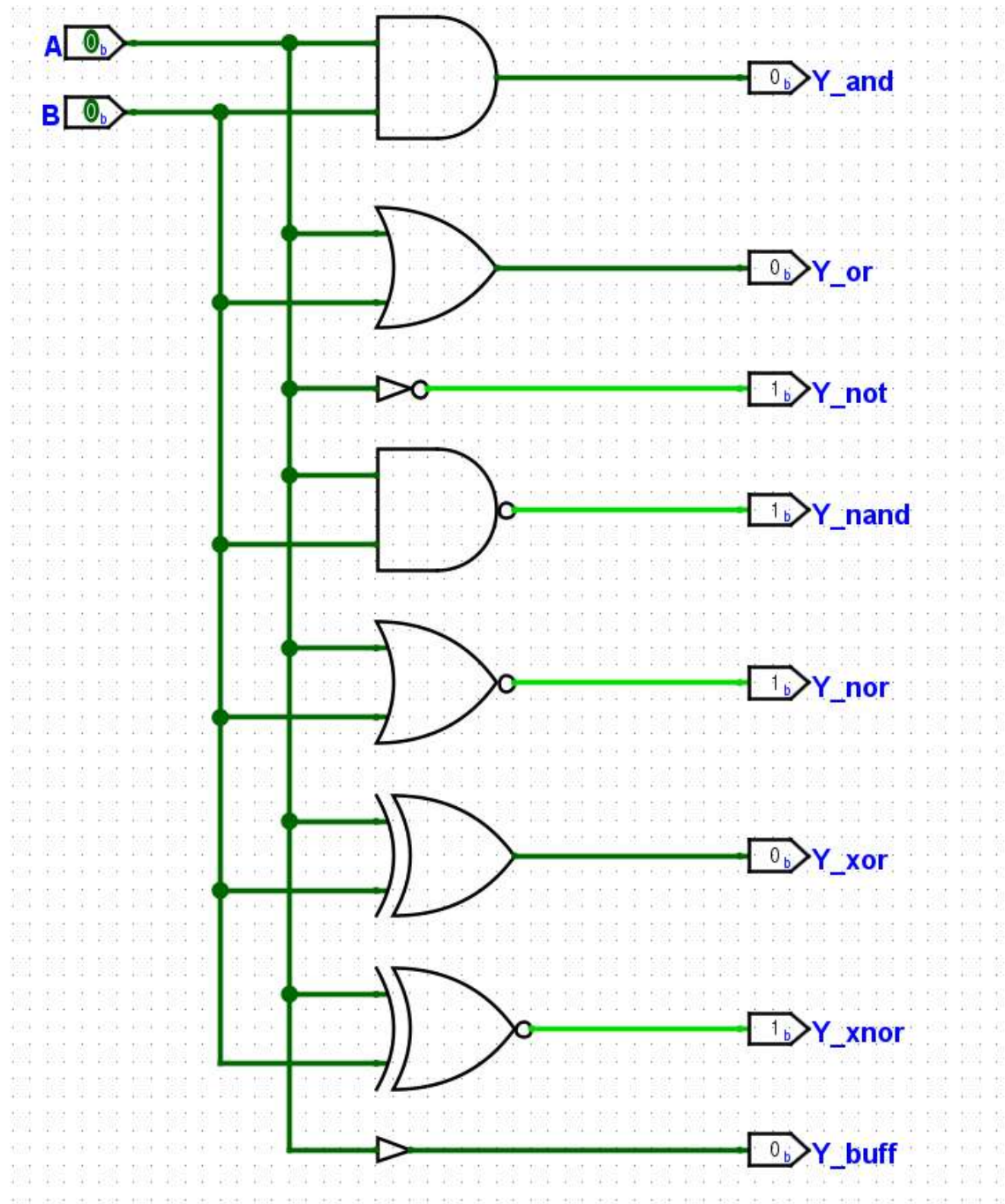


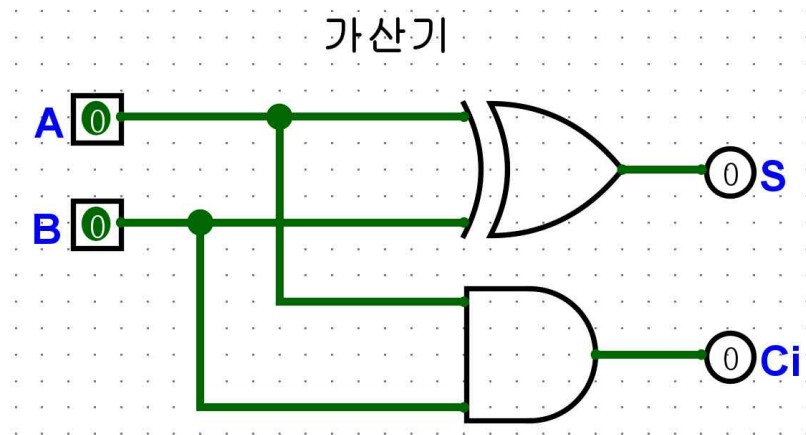
[01. 기본 실습]



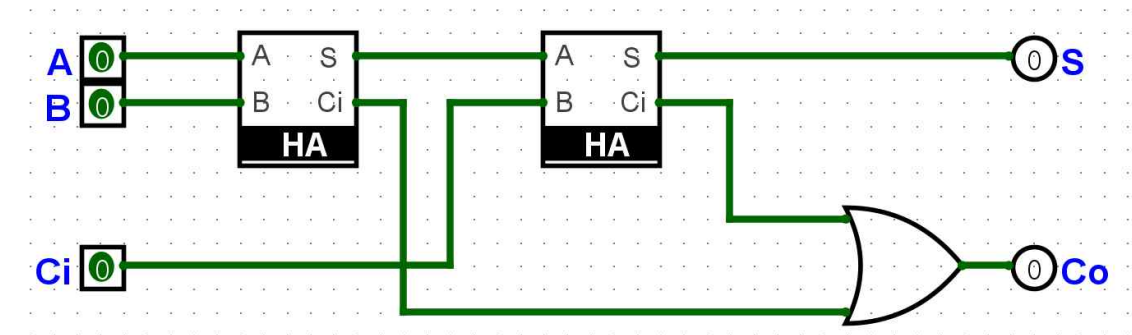
01. basic_gates



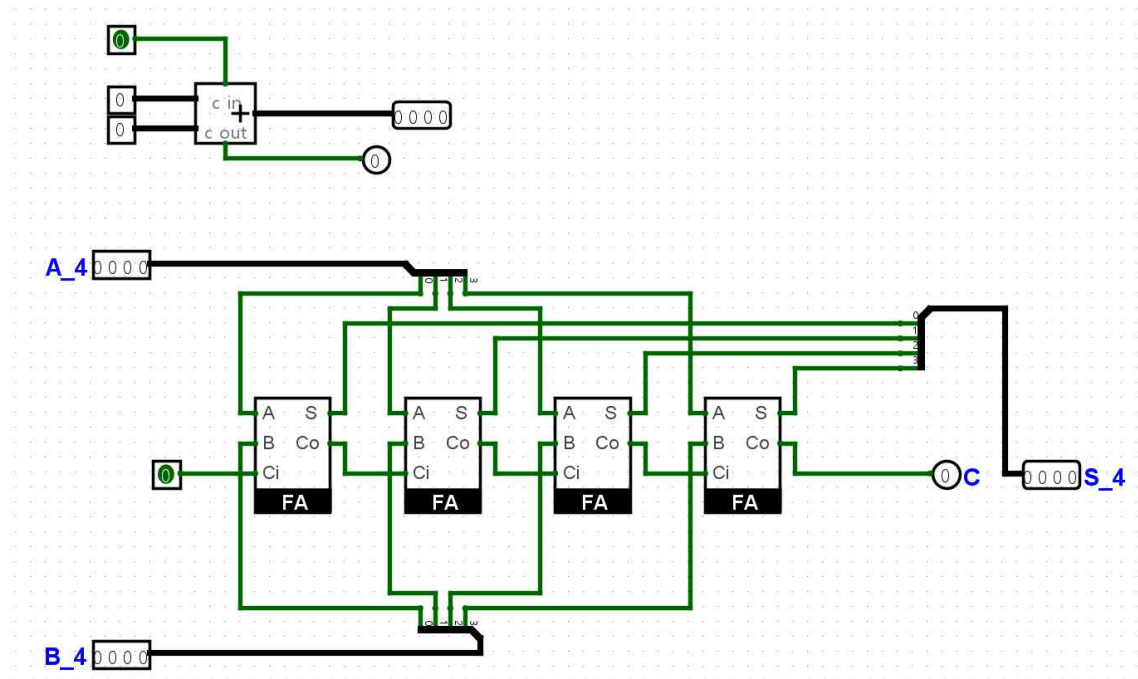
02. HA



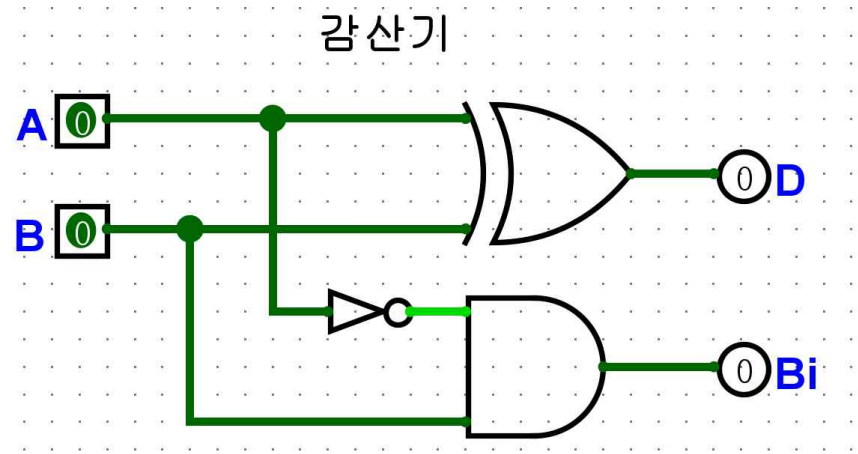
03. FA



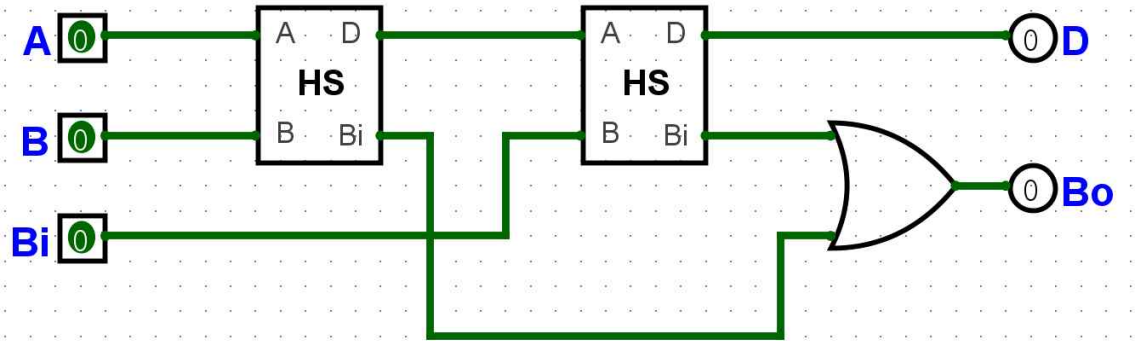
04. Adder_4bits



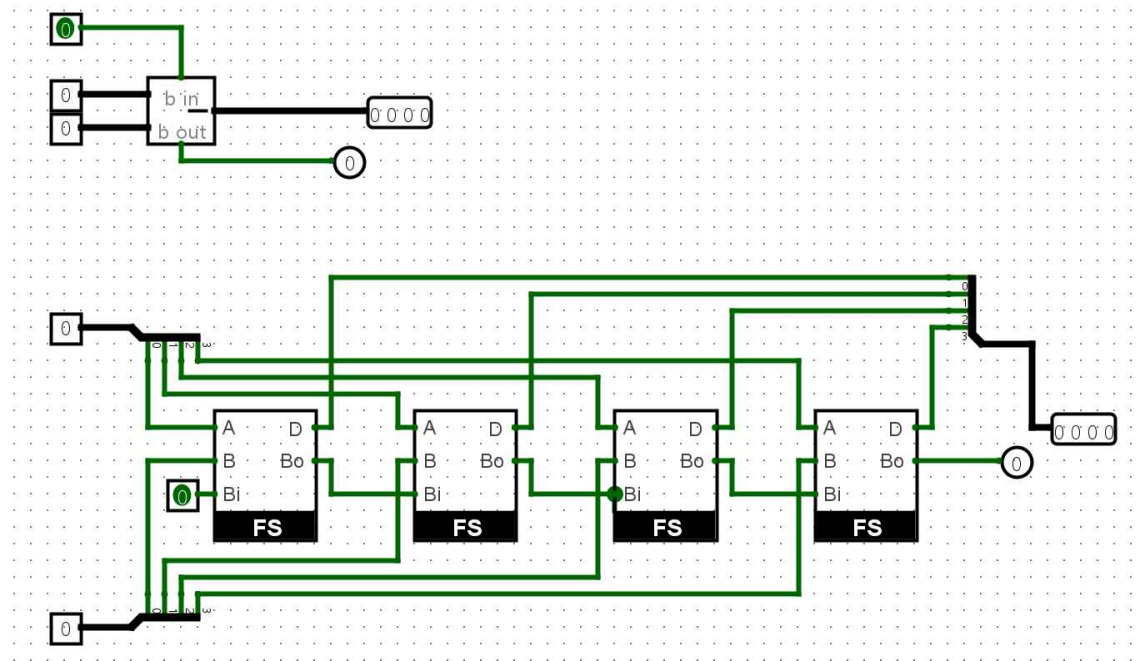
05. HS



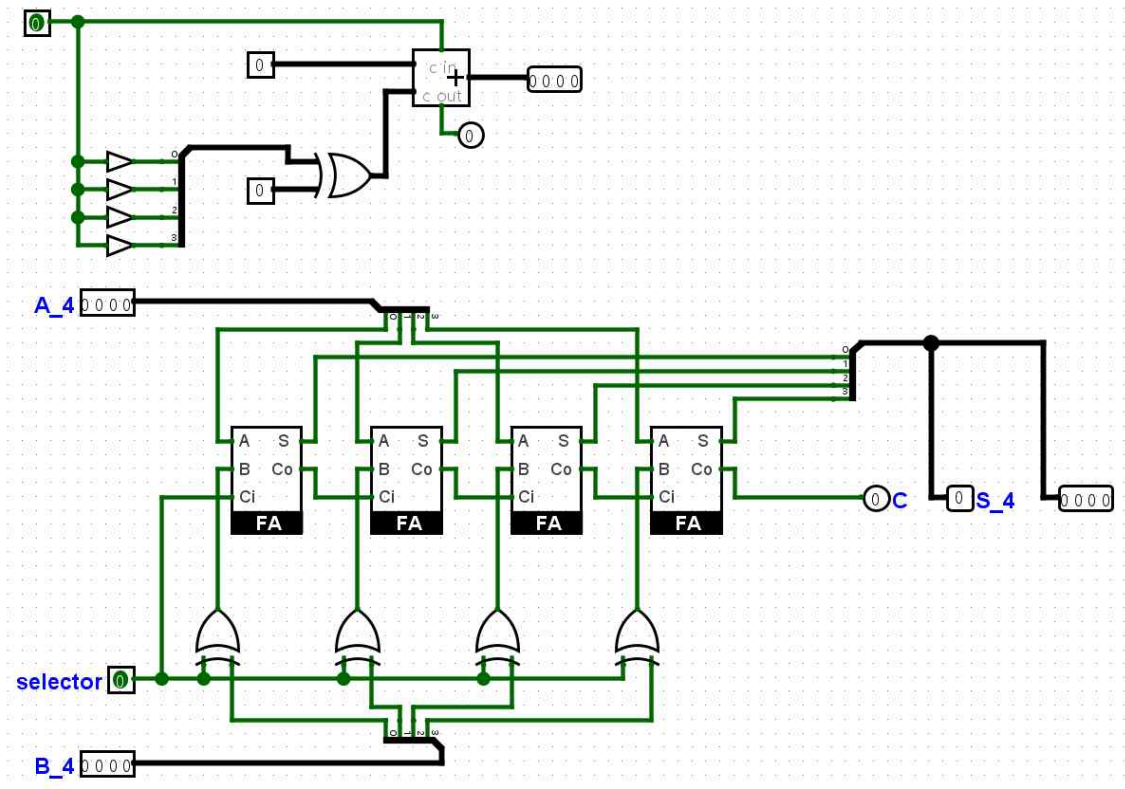
06. FS



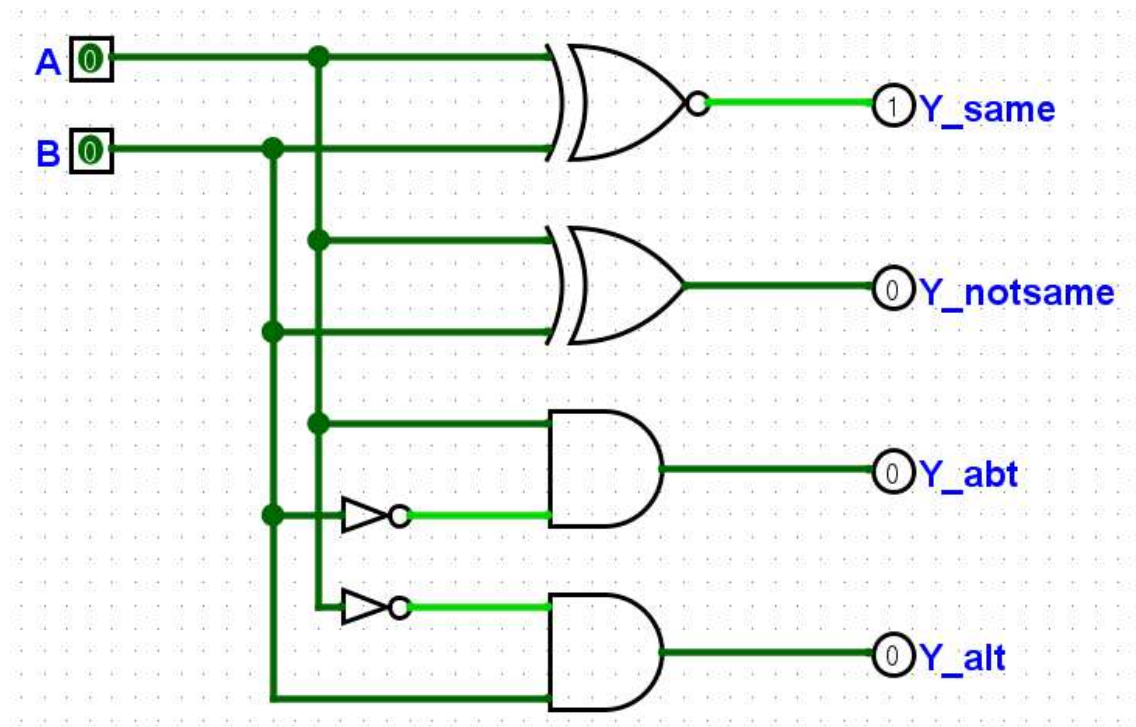
07. Substract_4bits



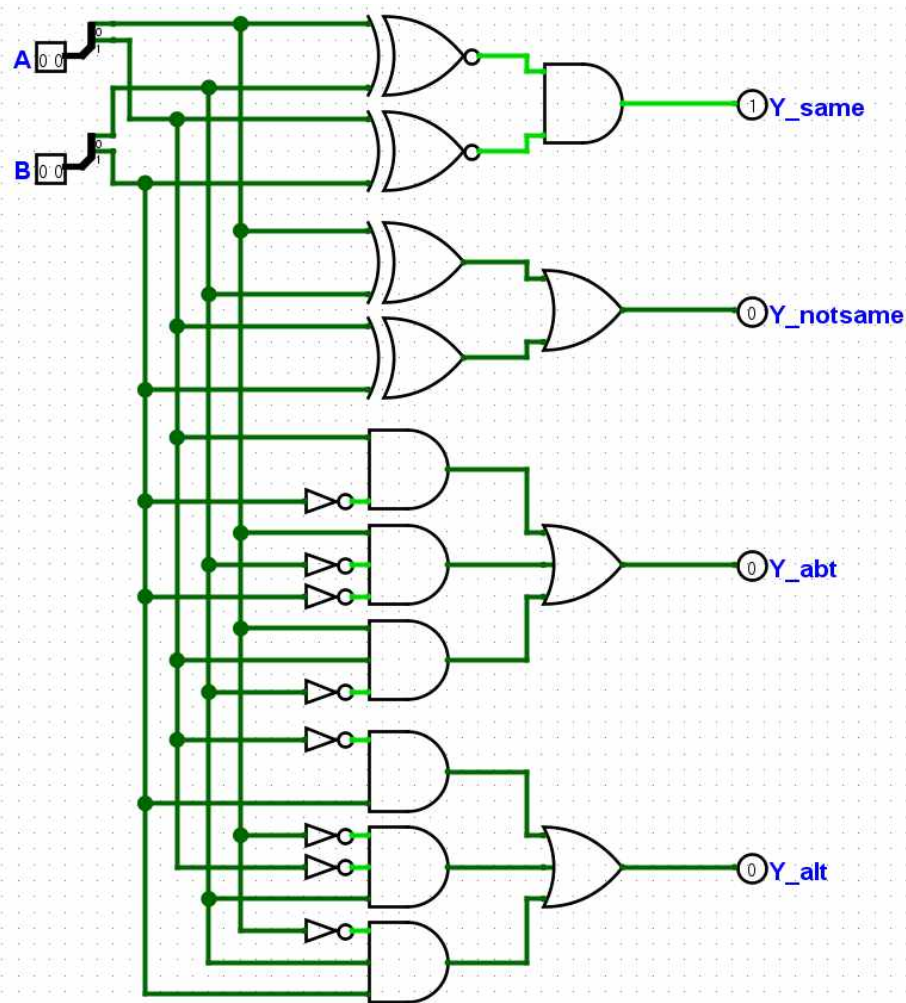
08. Parrel_Adder_Subtract



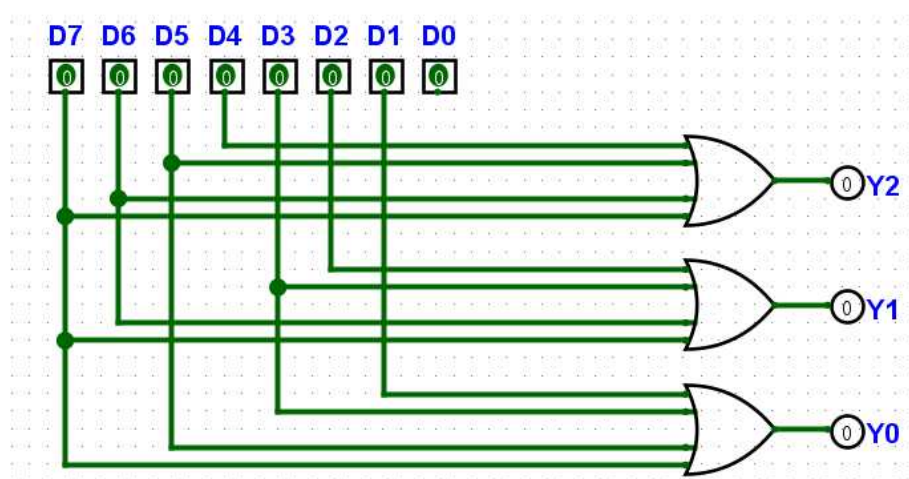
09. comp_1bit



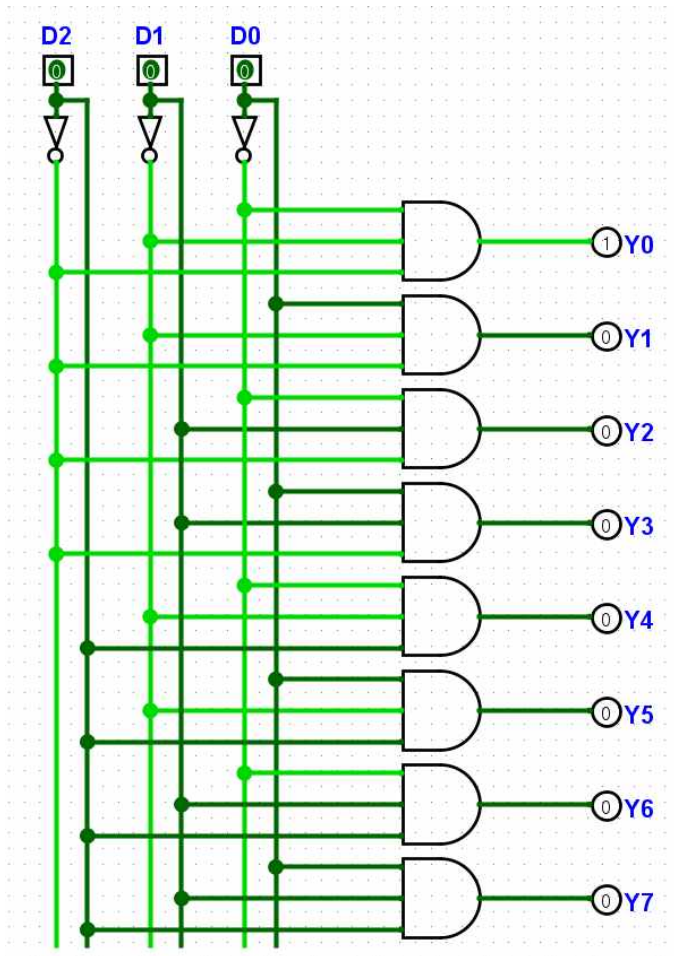
10. comp_2bits



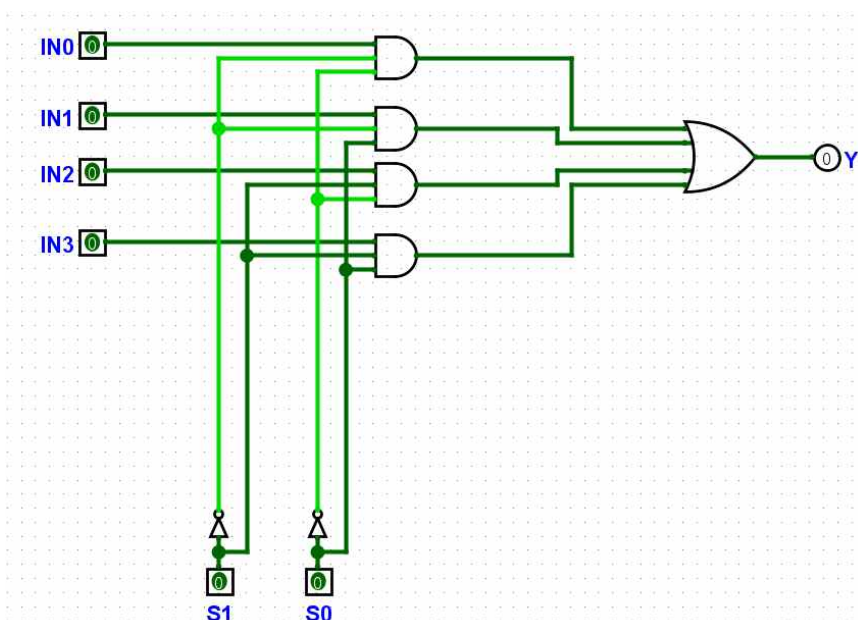
11. encode_8to3



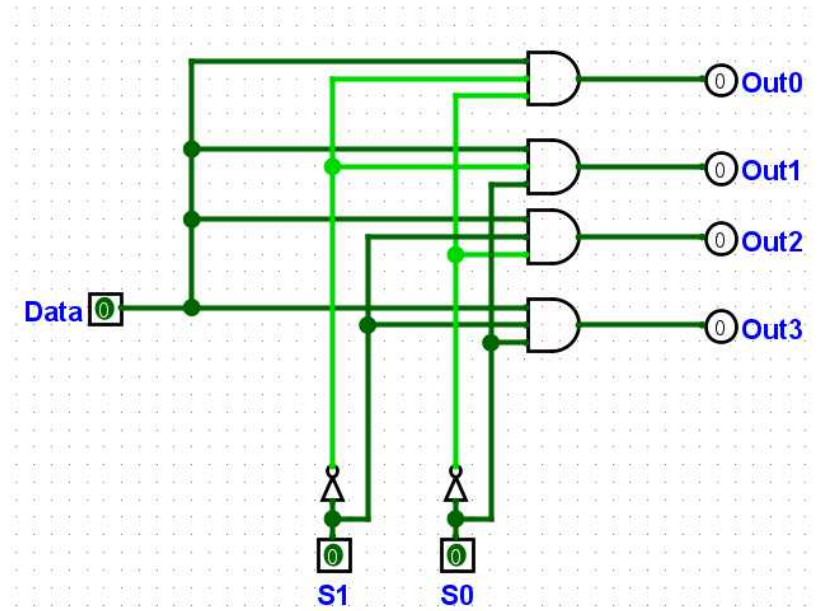
12. decode_3to8



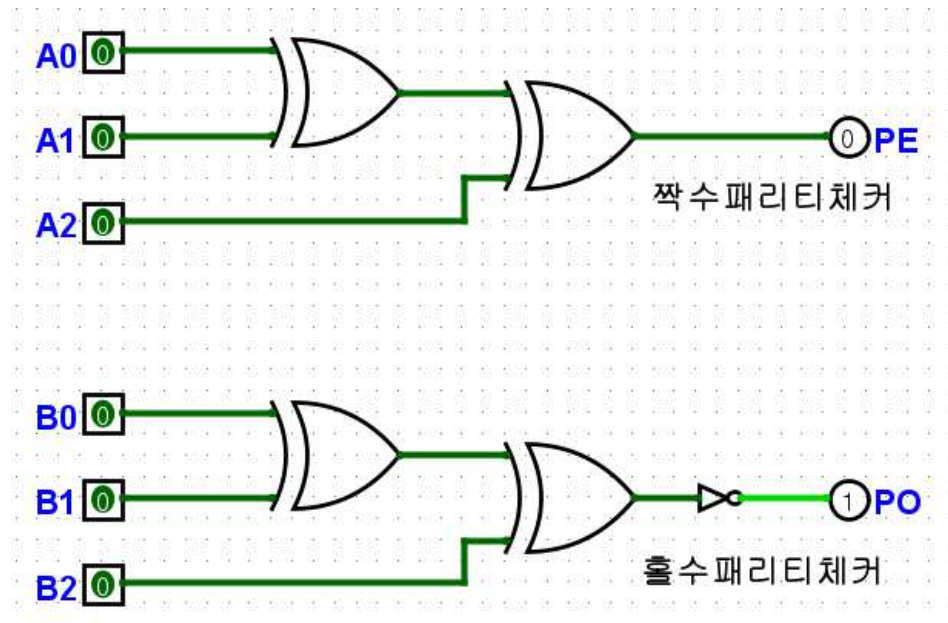
13. Mux



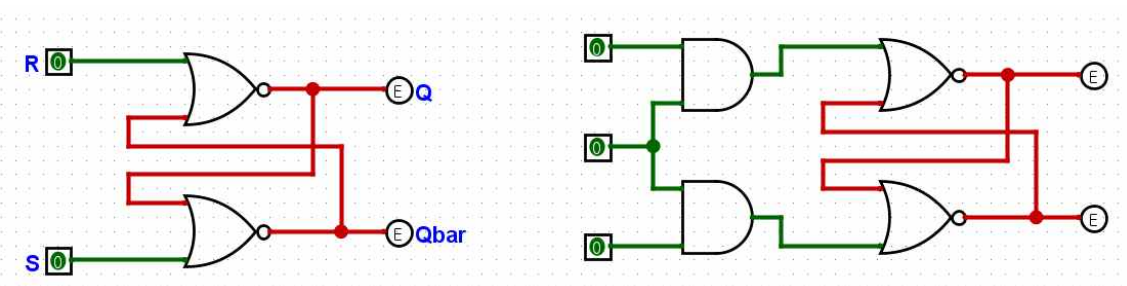
14. DeMux



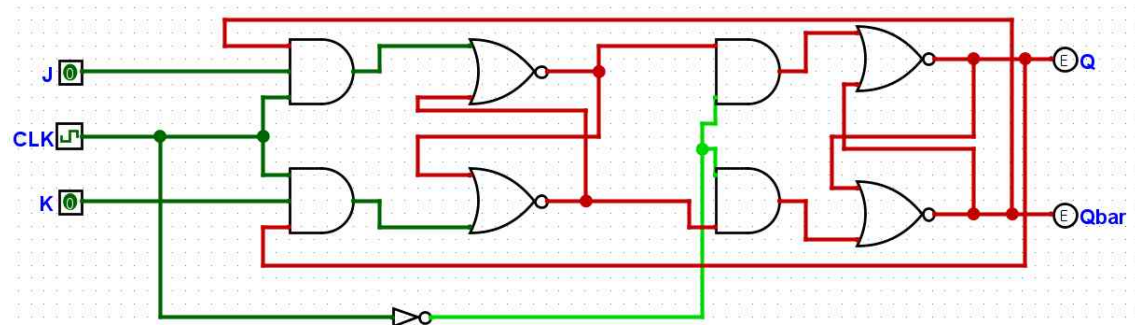
15. ParityCheck



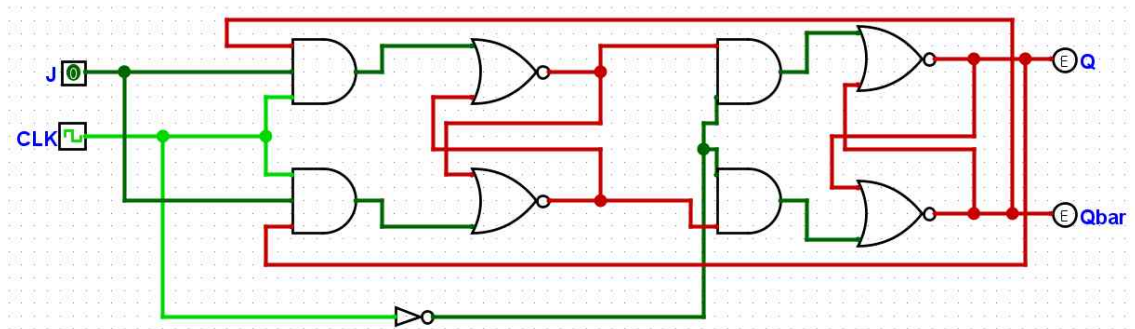
16. latchRS



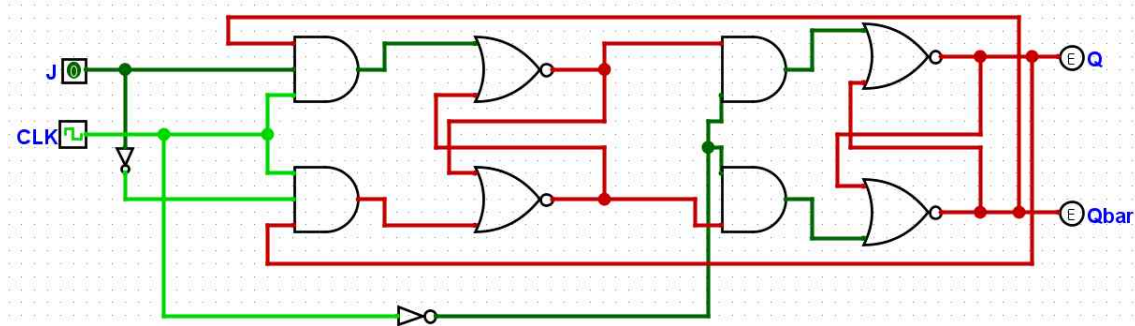
17. JK



18. T

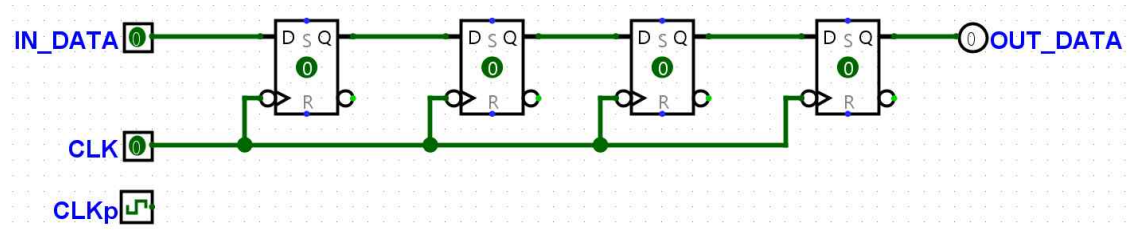


19. D

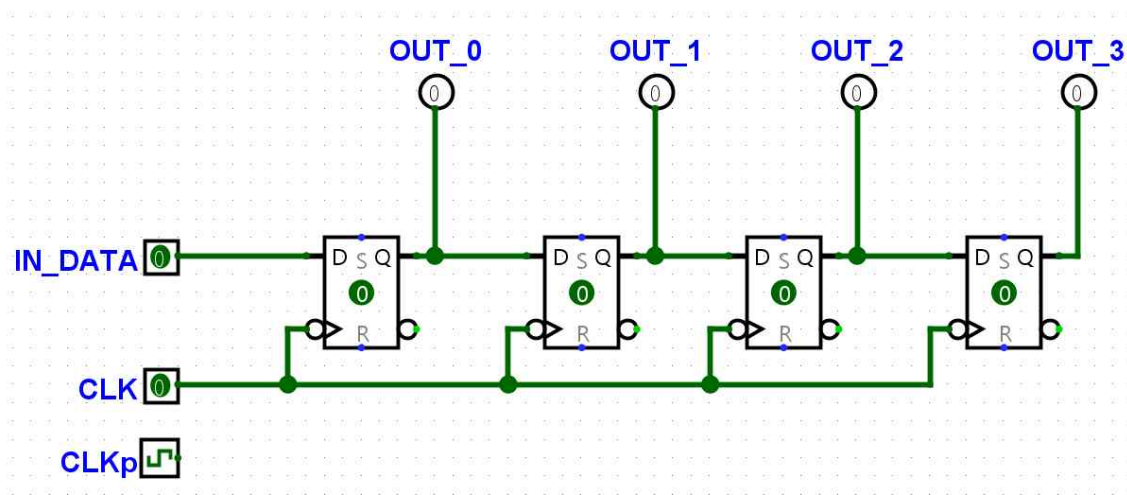


[02. 레지스터 실습]

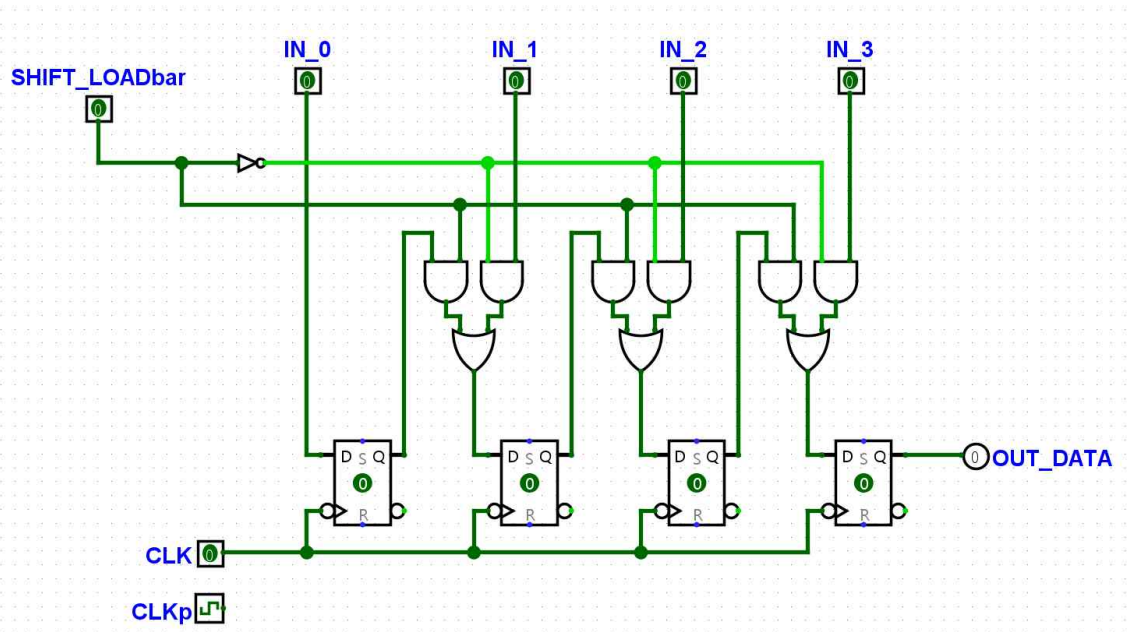
1. register_siso



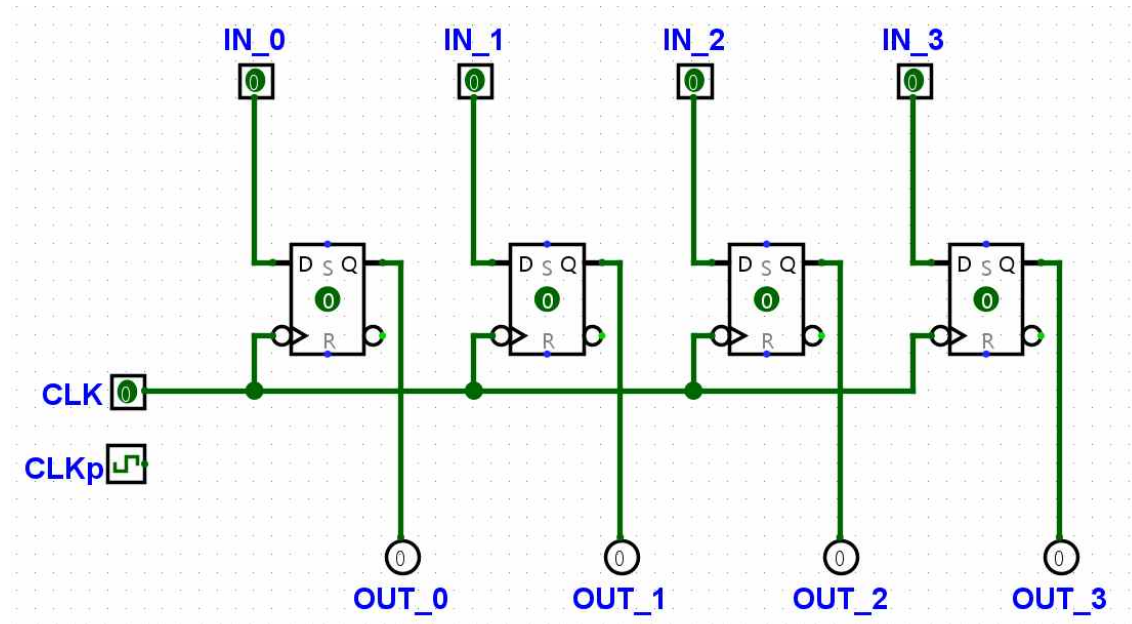
2. register_sipo



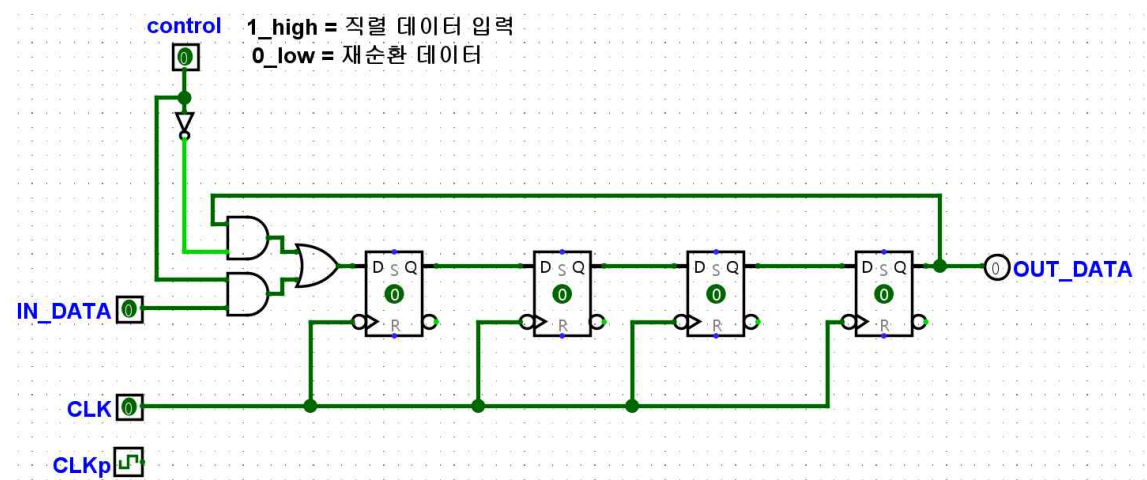
3. register_piso



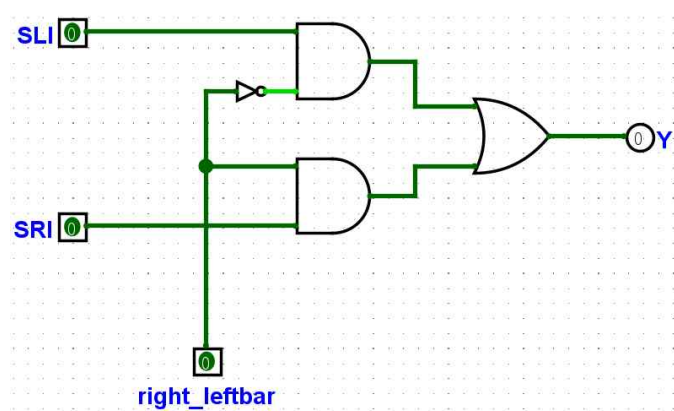
4. register_pipo



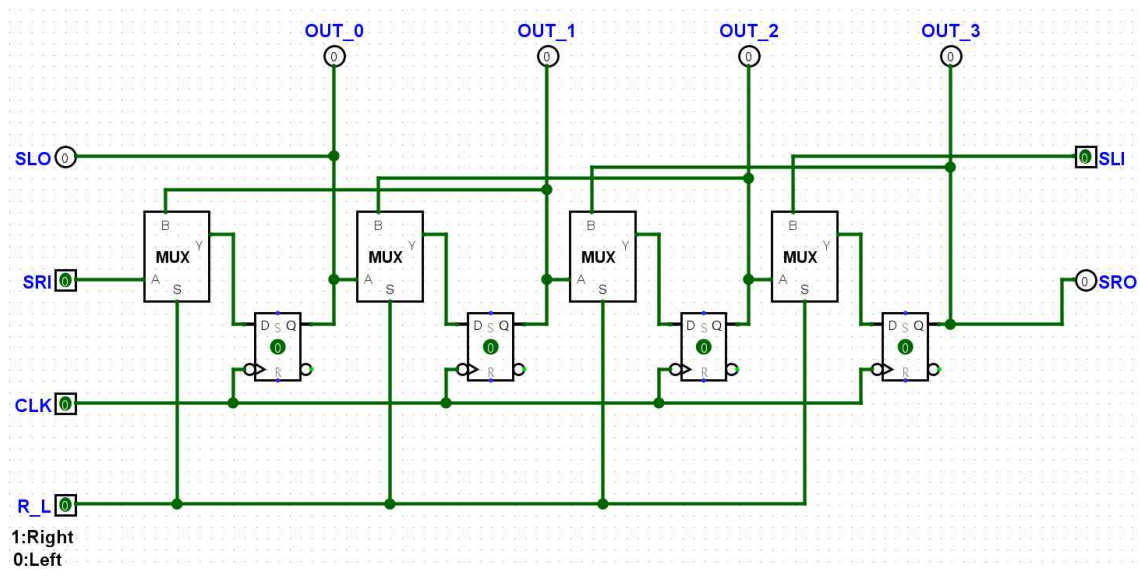
5. register_asis_recirc



6. select_MUX_2

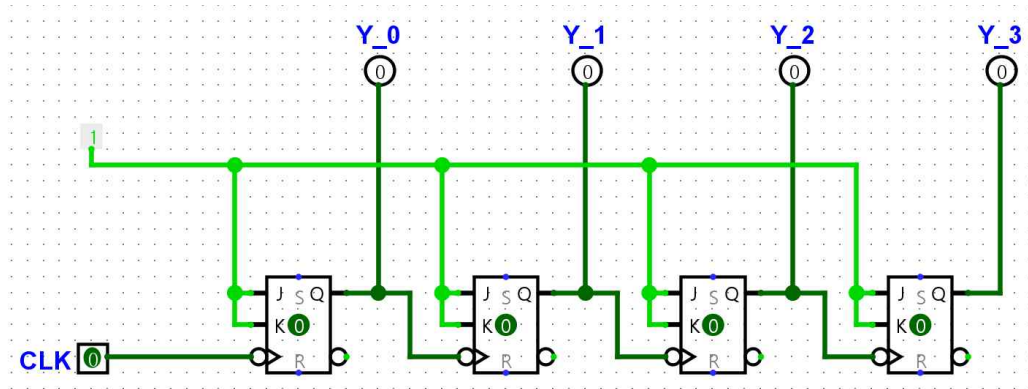


7. register_dual_RL_shift

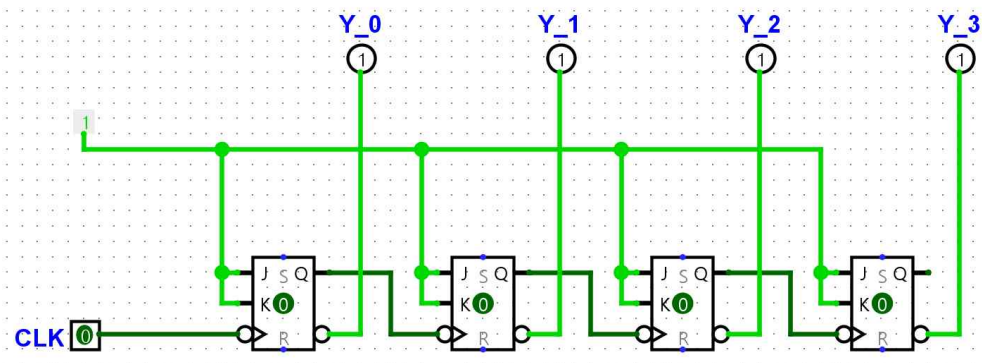


[03. 카운터 실습]

1. counter_async_up_4bits



2. counter_async_down_4bits



3. counter_sync_3bits

