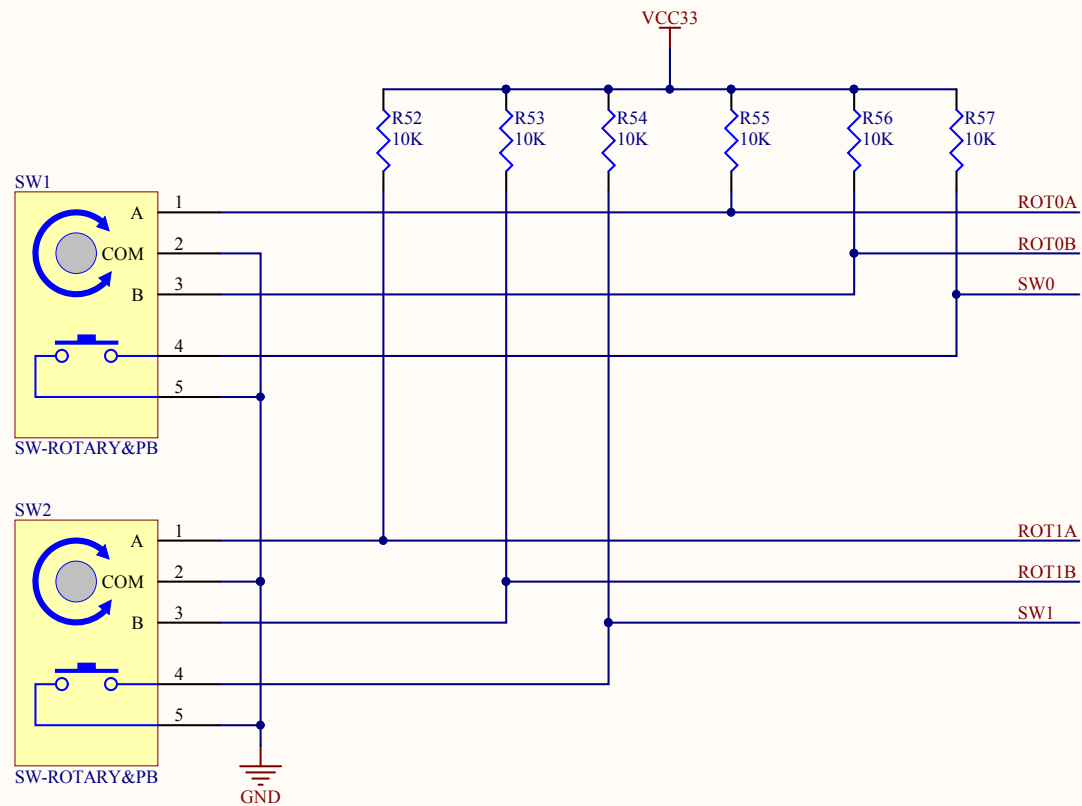
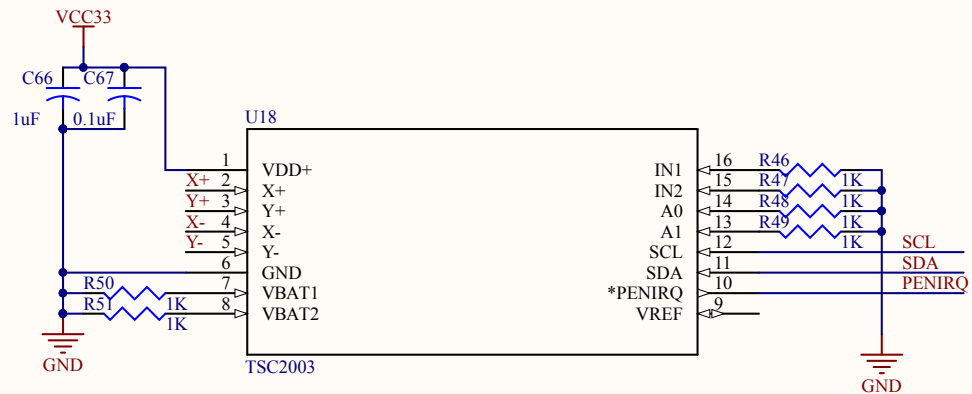
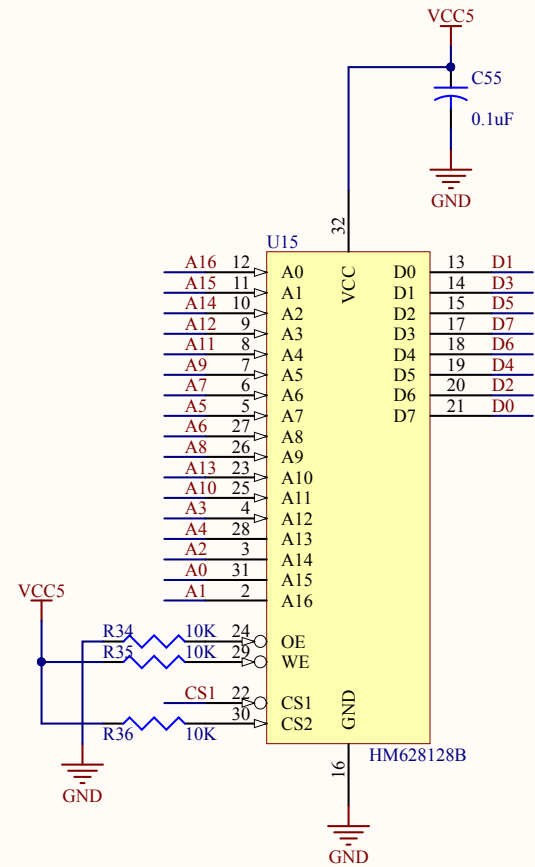
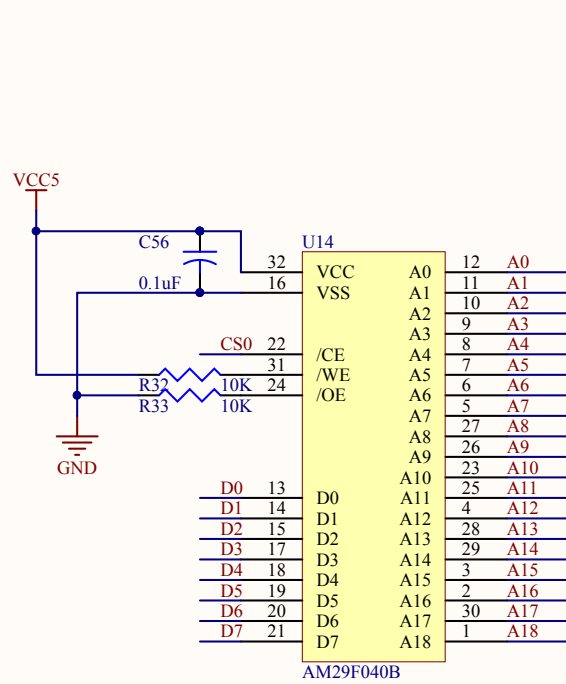


Title		
SoPC Oscilloscope - VRAM, Display		
Size	Number	Revision
A		*
Date:	3/12/2014	Sheet 1 of 1
File:	C:\Users\...\vram.SchDoc	Drawn By: Santiago Navonne



Title		
SoPC Oscilloscope - User Interface		
Size	Number	Revision
A		1.0
Date:	3/12/2014	Sheet 1 of 1
File:	C:\Users\...\user_input.SchDoc	Drawn By: Santiago Navonne



Title SoPC Oscilloscope - SRAM, ROM		
Size A	Number	Revision 1.0
Date: 3/12/2014	Sheet 1 of 1	
File: C:\Users\...\sram_rom.SchDoc	Drawn By: Santiago Navonne	

A

B

C

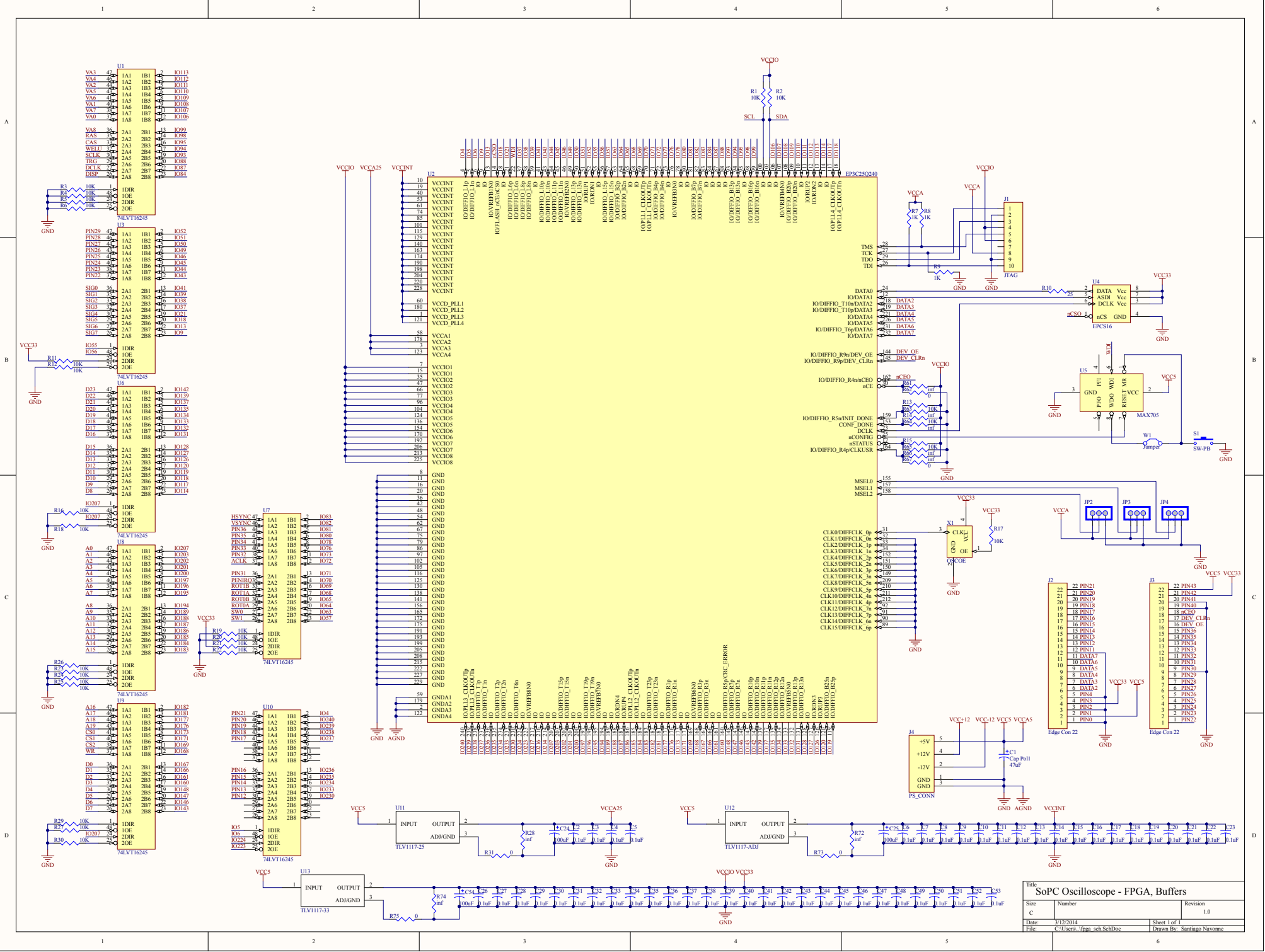
D

A

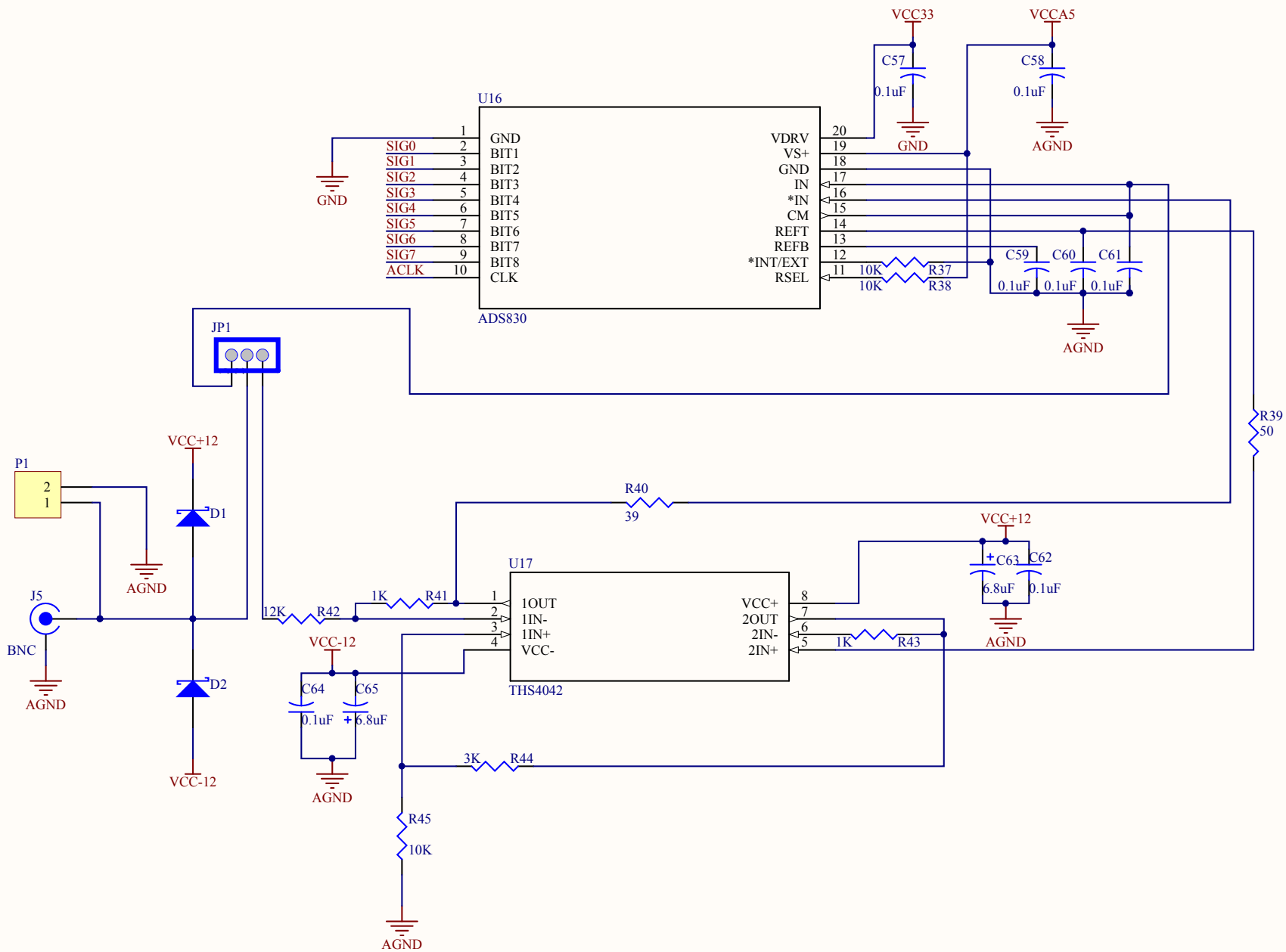
B

C

D



SoPC Oscilloscope - FPGA, Buffers		
Size	Number	Revision
C		1.0
Date:	3/12/2014	Sheet 1 of 1
File:	C:\Users\jipau\sch\SchDoc	Drawn By: Santiago Navonne



Title		
SoPC Oscilloscope - Analog Front End		
Size	Number	Revision
A		1.0
Date:	3/12/2014	Sheet 1 of 1
File:	C:\Users\...\analog_frontend.SchDoc	Drawn By: Santiago Navonne