General Data

SYMBOL	DEFINITION	DESCRIPTION	MIN	MAX	NOTES
t_{prop}		Propagation Delay		2ns	
t_{BIO}		Buffer Input/Output Delay		4.5ns	
t_{RAS}		RAS Pulse Width	60ns		
t_{RC}		Read/Write Cycle Time	104ns		
t_{RP}		RAS Precharge Time	40ns		
t_{CSH}		CAS Hold Time	45ns		
t_{RSH}		RAS Hold Time	15ns		
t_{RCD}		RAS to CAS Delay Time	15ns	42ns	
t_{CAS}		CAS Pulse Width	10ns	10000ns	
t_{AR}		Column Address Hold Time (from RAS)	50ns		
t_{RAD}		RAS to Column Address Delay Time	12ns		
t_{RAL}		Column Address to RAS Lead Time	30ns		
t_{ASR}		Row Address Setup	0ns		
t_{RAH}		Row Address Hold Time	10ns		
t_{ASC}		Column Address Setup Time	0ns		
t_{CAH}		Column Address Hold Time	10ns		
t_{TLS}		TRG Low Setup Time	0ns		
t_{TLH}		TRG Low Hold Time	10ns		

General Data continued...

SYMBOL	DEFINITION	DESCRIPTION	MIN	MAX	NOTES
t_{RSD}		RAS to First SC Delay Time	60ns		
t_{TRP}		TRG to RAS Precharge Time	40ns		
t_{TP}		TRG Precharge Time	20ns		
t_{TSD}		TRG to First SC Delay Time	10ns		