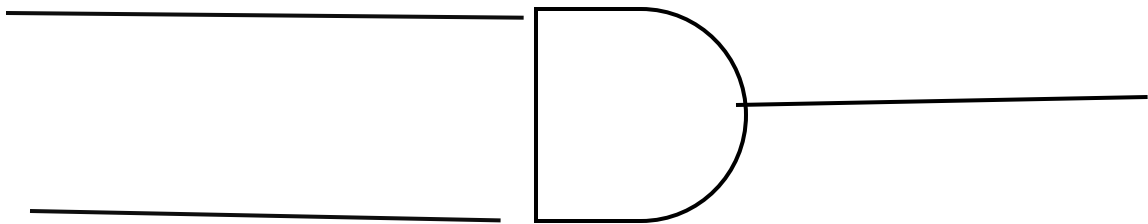


A	B	A AND B
0	0	0
0	1	0
1	0	0
1	1	1



A	B	A OR B
0	0	0
0	1	1
1	0	1
1	1	1



INPUT	OUTPUT	
A	NOT A	
0	1	
1	0	

