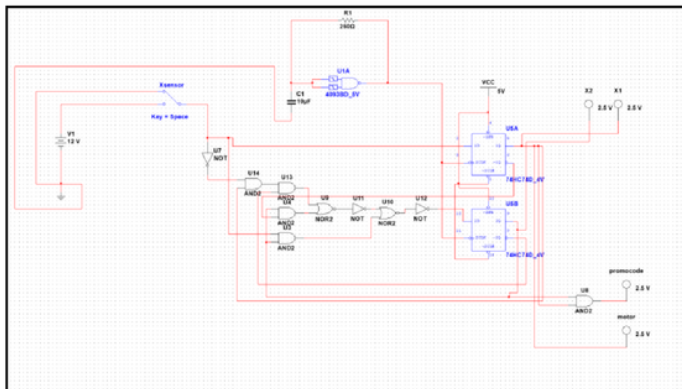
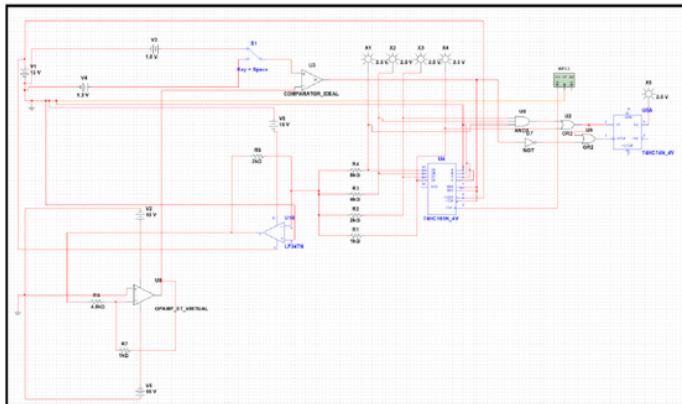


APPENDIX B – SCHEMATICS OF PROTOTYPE CIRCUIT

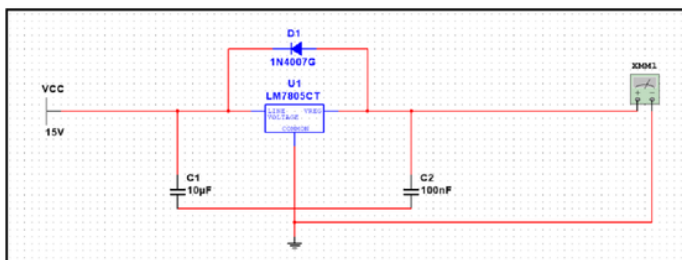
MULTISIM CIRCUITS



State machine and clocking circuit

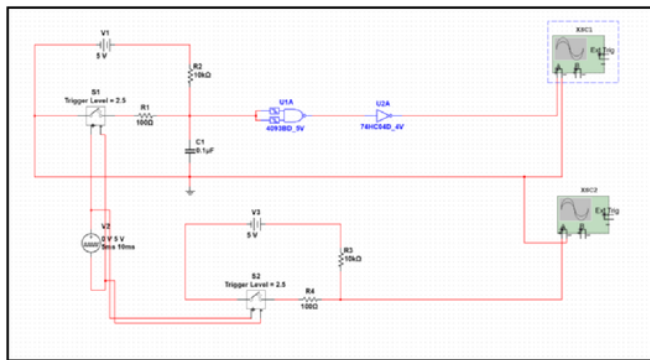


Analog-Digital converter circuit

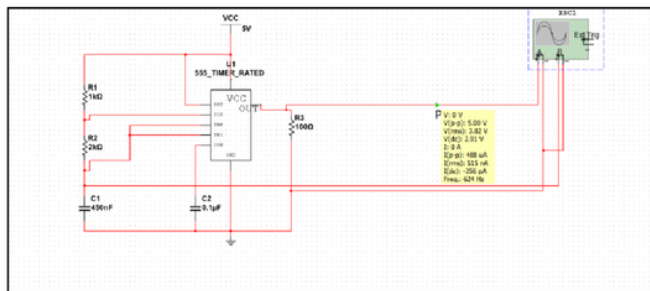


Power supply circuit

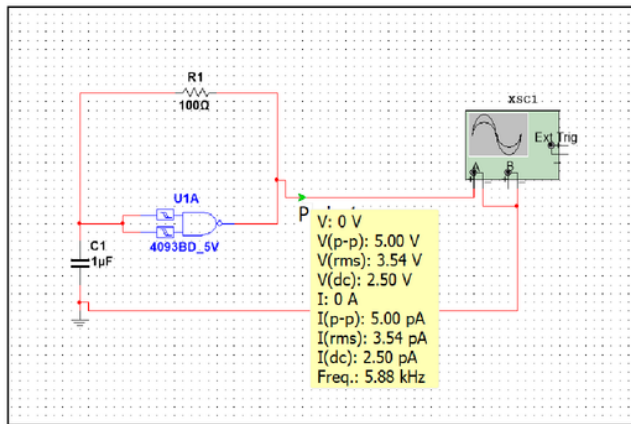
Deliverable 6: Final design report - Proreverse Enterprise – Team D



De-bouncing circuit.



555 Timer circuit.



Clocking Circuit.