CS 303 Logic and Digital System Design

InLab Assignment #03 - Section-C

Using the CircuitVerse simulator available at

https://circuitverse.org/simulator

simulate a 3-bit comparator. Inputs are to be entered using two 3-bit steppers, and you must have 3-bit output. Outputs should give results according to the situation(A < B, A = B, A > B). The comparator is to be implemented using basic gates.

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